



11AC40SISO_Ant1_5230



11AC40SISO_Ant0_5270



11AC40SISO_Ant1_5270



11AC40SISO_Ant0_5310



11AC40SISO_Ant1_5310



11AC40SISO_Ant0_5510



11AC40SISO_Ant1_5510



11AC40SISO_Ant0_5550



11AC40SISO_Ant1_5550



11AC40SISO_Ant0_5670



11AC40SISO_Ant1_5670



11AC40SISO_Ant0_5755



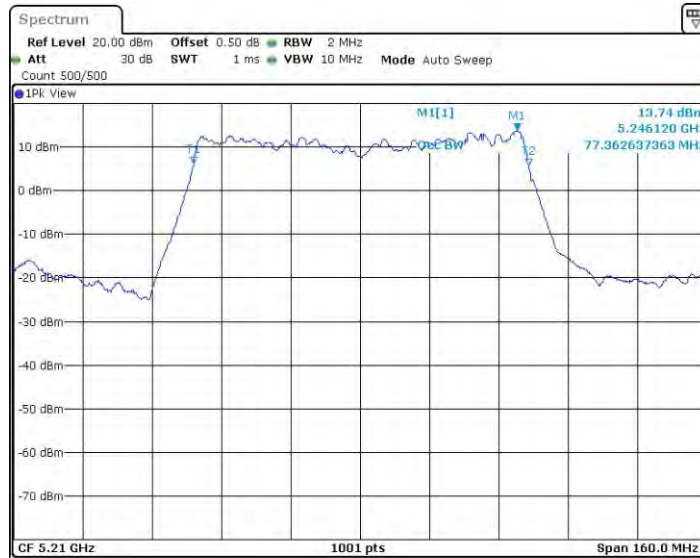
11AC40SISO_Ant1_5755



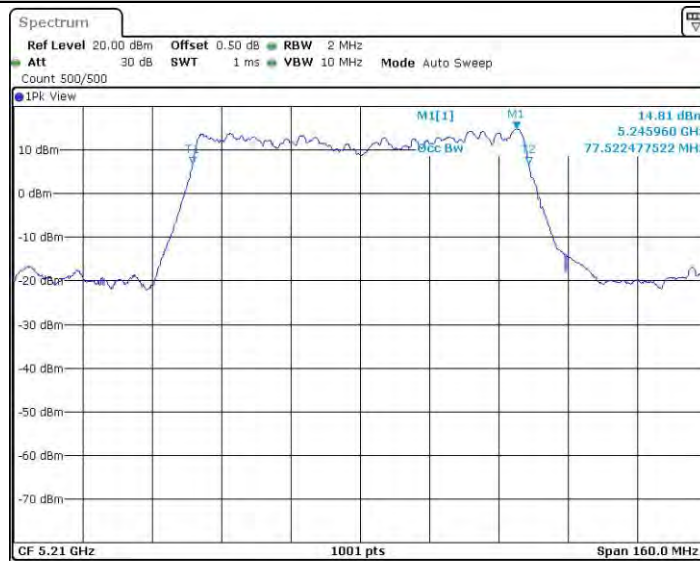
11AC40SISO_Ant0_5795



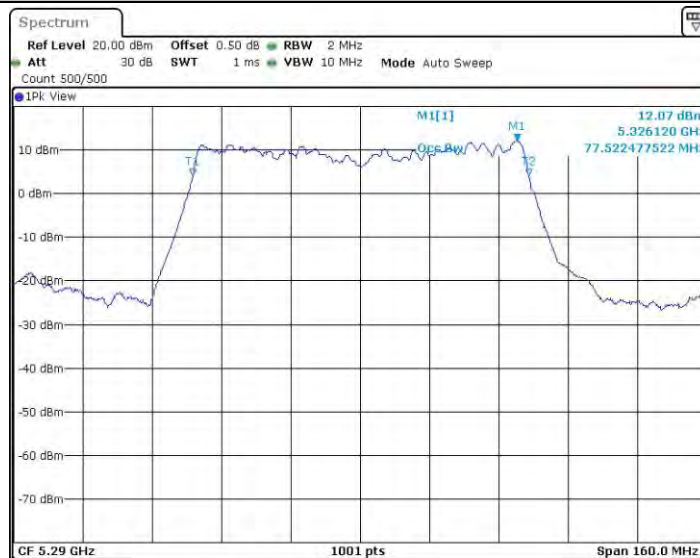
11AC40SISO_Ant1_5795



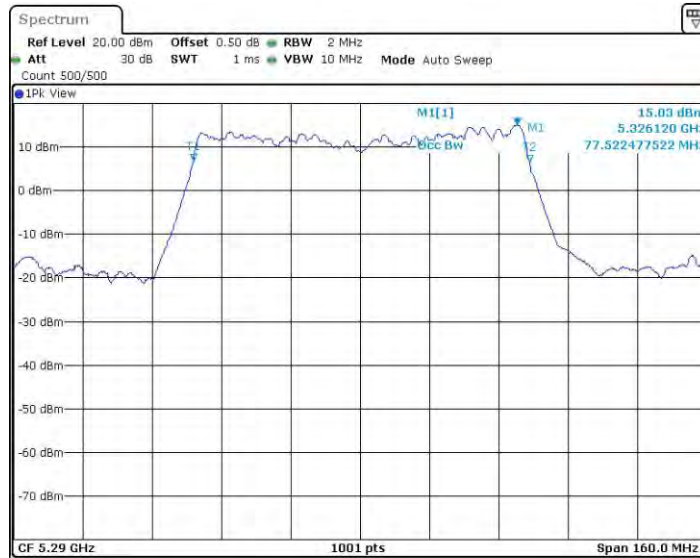
11AC80SISO_Ant0_5210



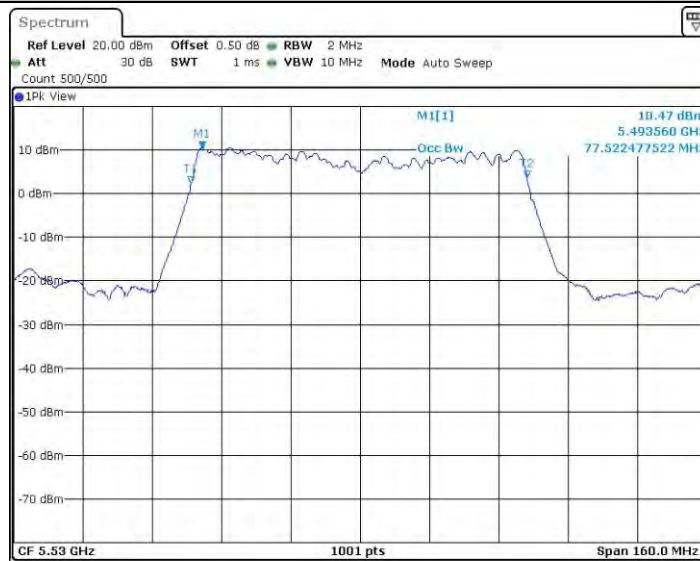
11AC80SISO_Ant1_5210



11AC80SISO_Ant0_5290



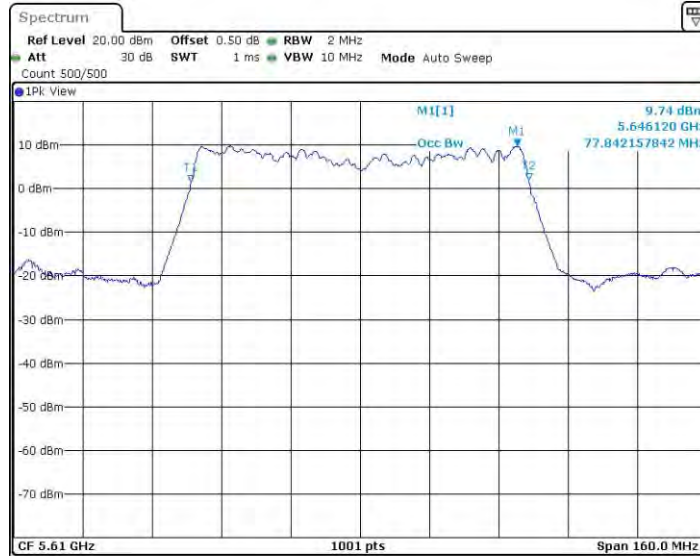
11AC80SISO_Ant1_5290



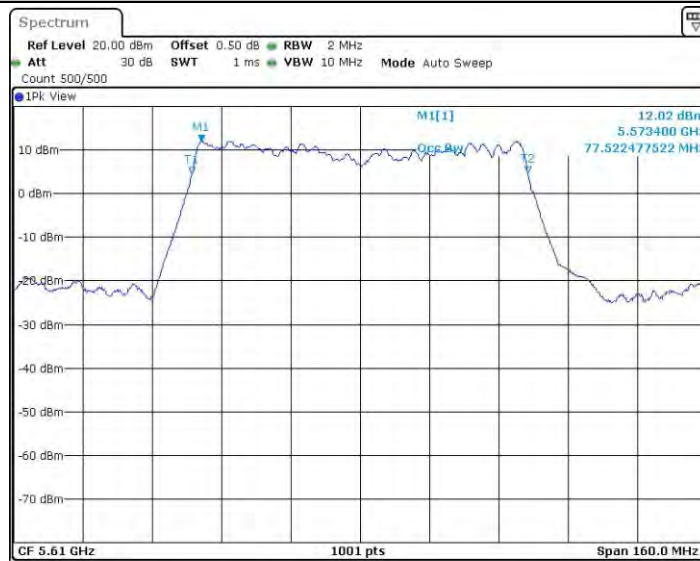
11AC80SISO_Ant0_5530



11AC80SISO_Ant1_5530



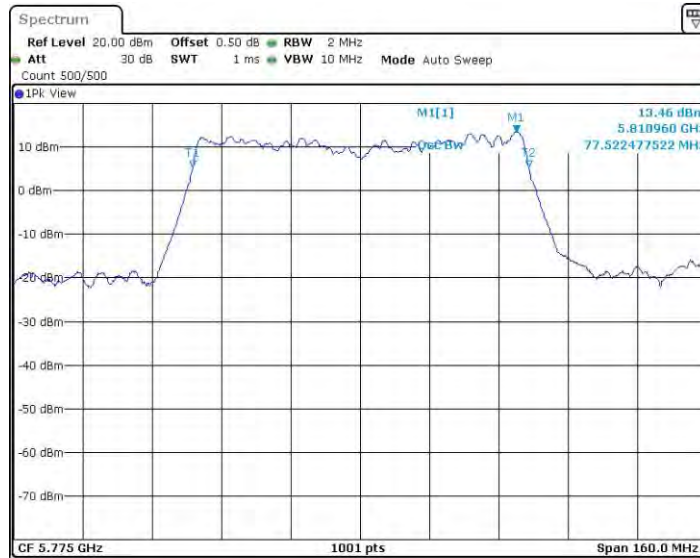
11AC80SISO_Ant0_5610



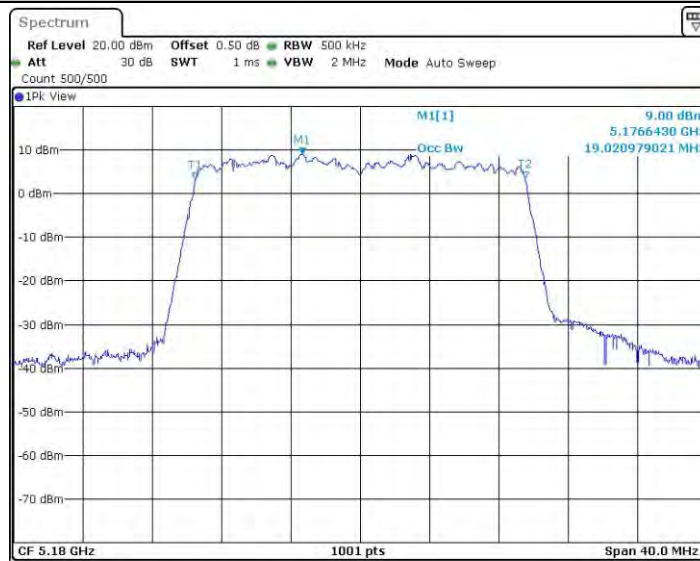
11AC80SISO_Ant1_5610



11AC80SISO_Ant0_5775



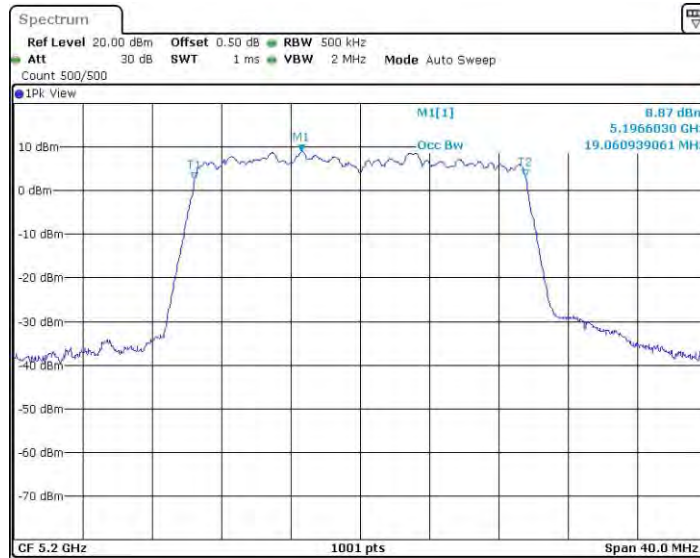
11AC80SISO_Ant1_5775



11AX20SISO_Ant0_5180



11AX20SISO_Ant1_5180



11AX20SISO_Ant0_5200



11AX20SISO_Ant1_5200



11AX20SISO_Ant0_5240



11AX20SISO_Ant1_5240



11AX20SISO_Ant0_5260



11AX20SISO_Ant1_5260



11AX20SISO_Ant0_5280



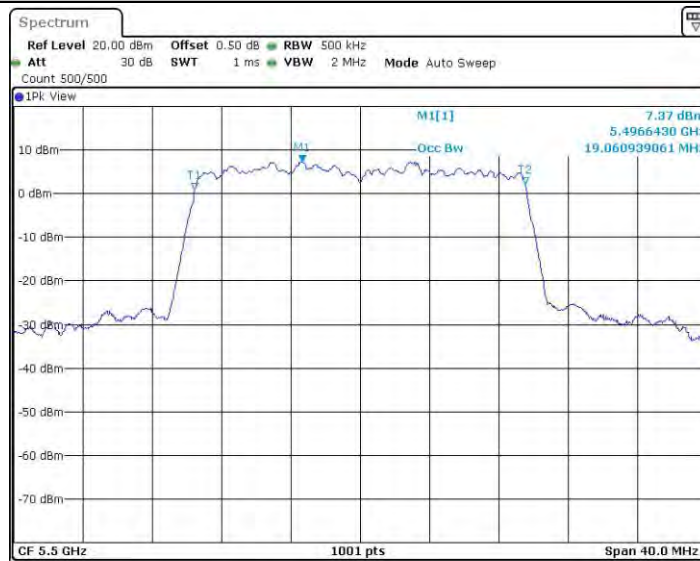
11AX20SISO_Ant1_5280



11AX20SISO_Ant0_5320



11AX20SISO_Ant1_5320



11AX20SISO_Ant0_5500



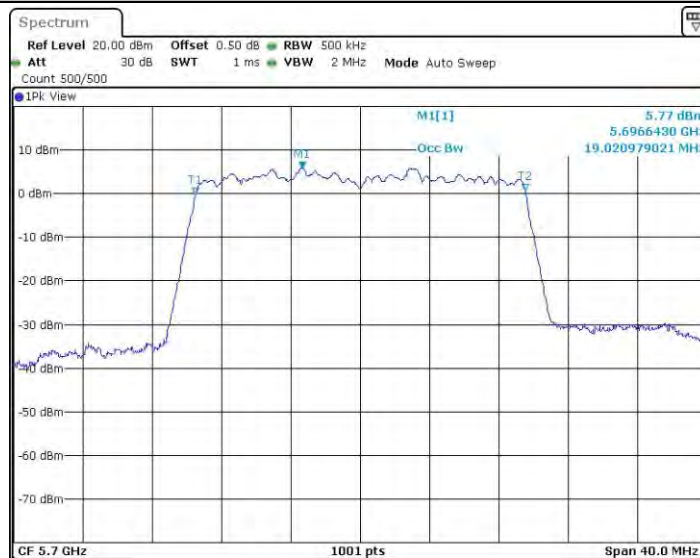
11AX20SISO_Ant1_5500



11AX20SISO_Ant0_5580



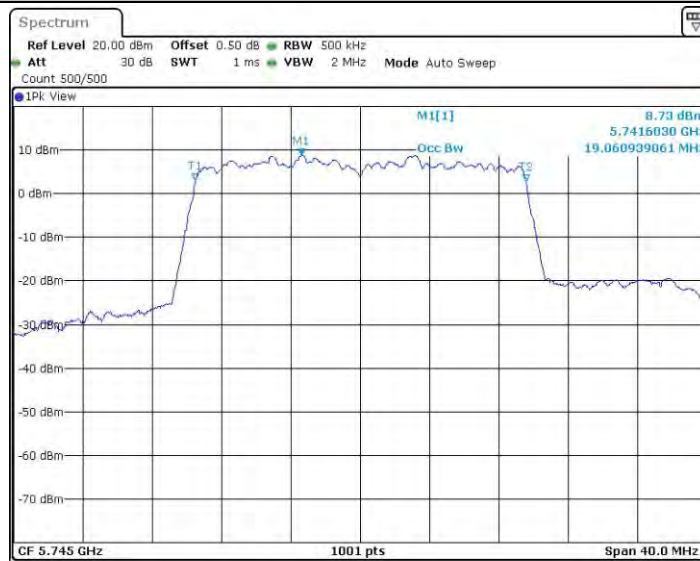
11AX20SISO_Ant1_5580



11AX20SISO_Ant0_5700



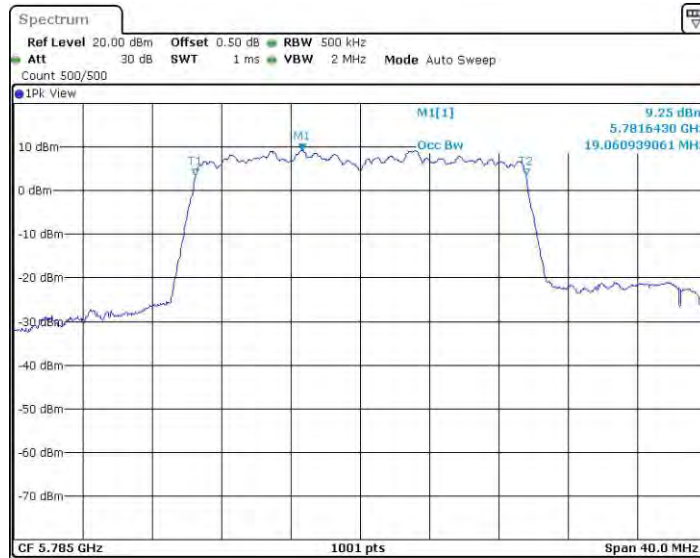
11AX20SISO_Ant1_5700



11AX20SISO_Ant0_5745



11AX20SISO_Ant1_5745



11AX20SISO_Ant0_5785



11AX20SISO_Ant1_5785



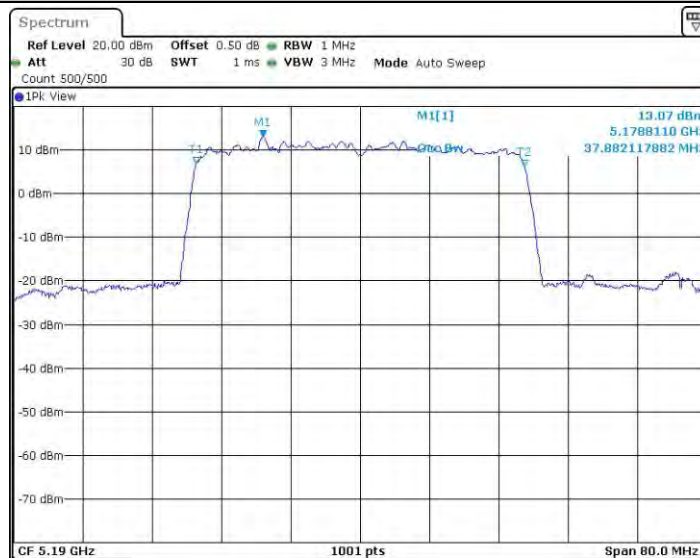
11AX20SISO_Ant0_5825



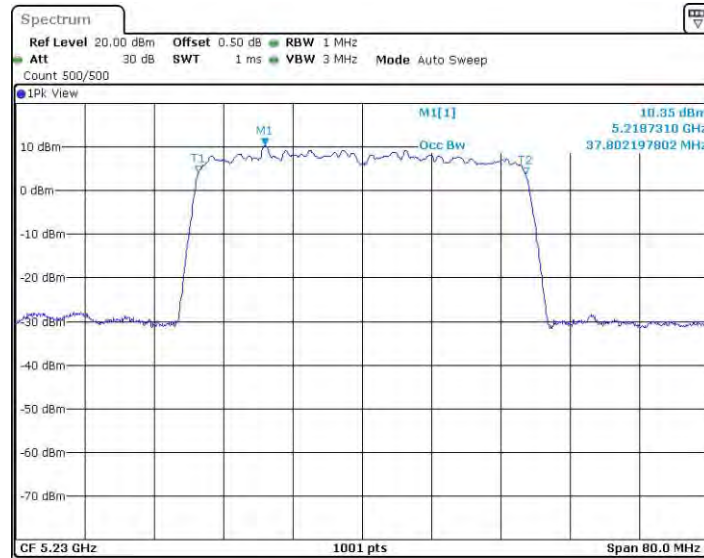
11AX20SISO_Ant1_5825



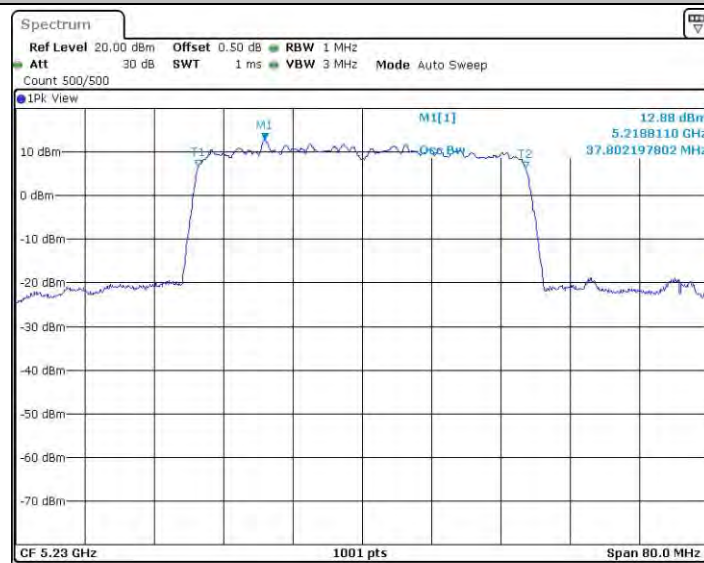
11AX40SISO_Ant0_5190



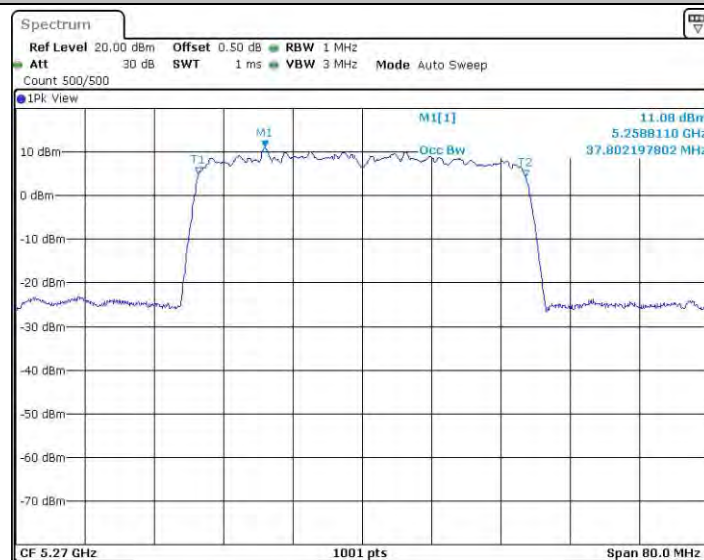
11AX40SISO_Ant1_5190



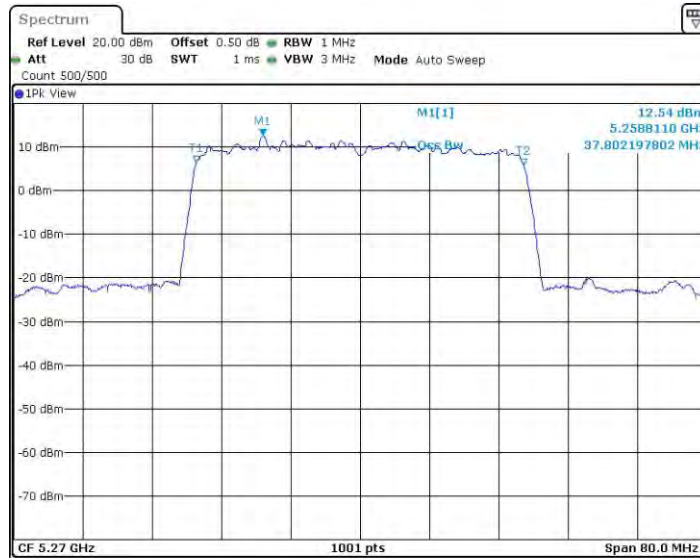
11AX40SISO_Ant0_5230



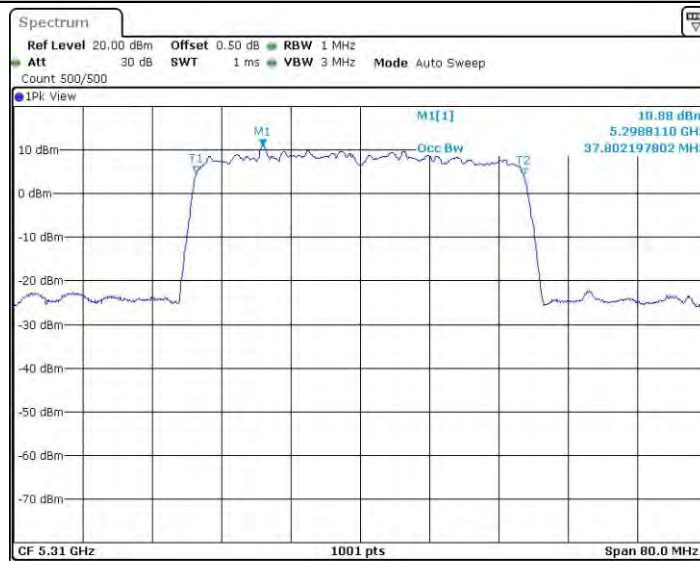
11AX40SISO_Ant1_5230



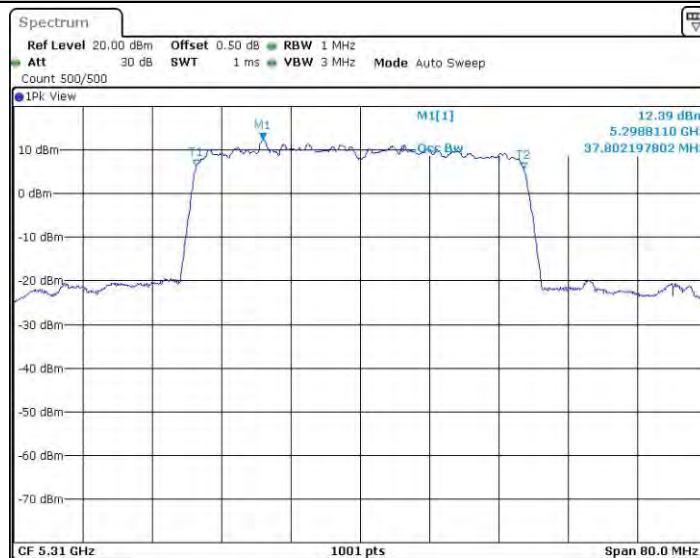
11AX40SISO_Ant0_5270



11AX40SISO_Ant1_5270



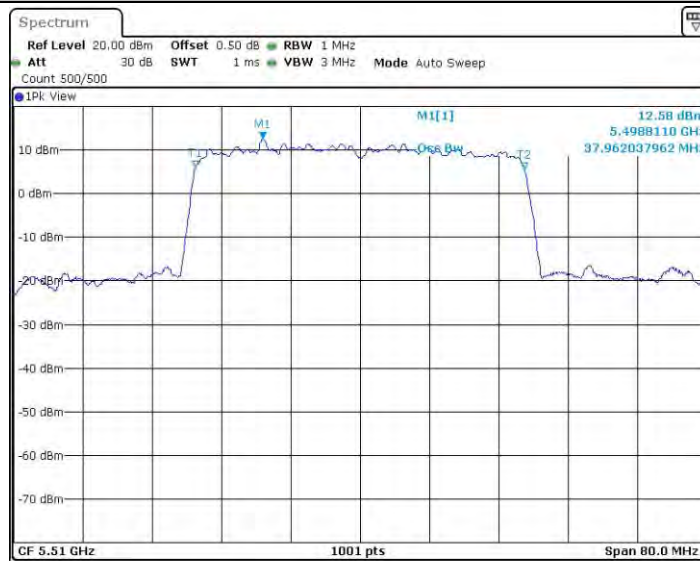
11AX40SISO_Ant0_5310



11AX40SISO_Ant1_5310



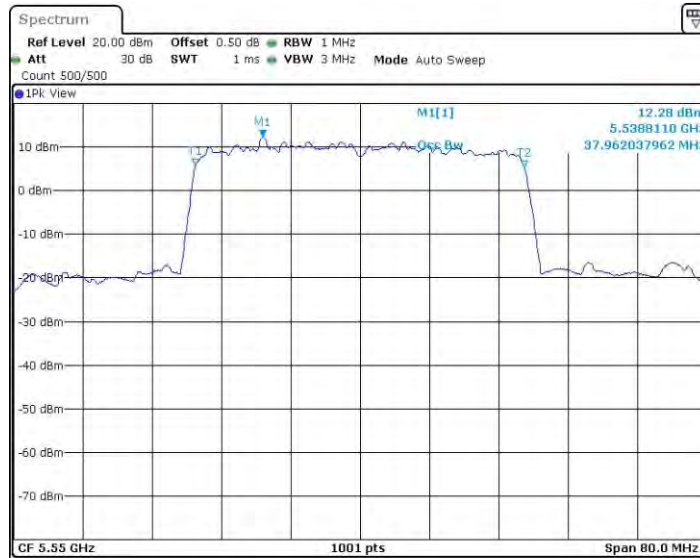
11AX40SISO_Ant0_5510



11AX40SISO_Ant1_5510



11AX40SISO_Ant0_5550



11AX40SISO_Ant1_5550



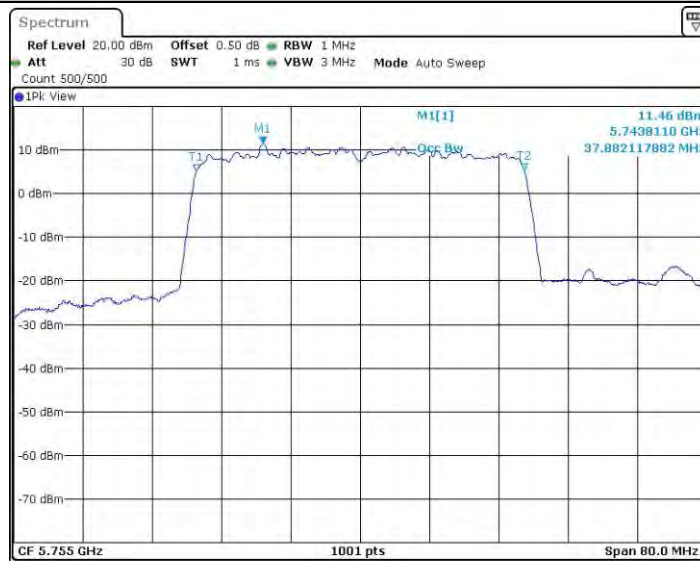
11AX40SISO_Ant0_5670



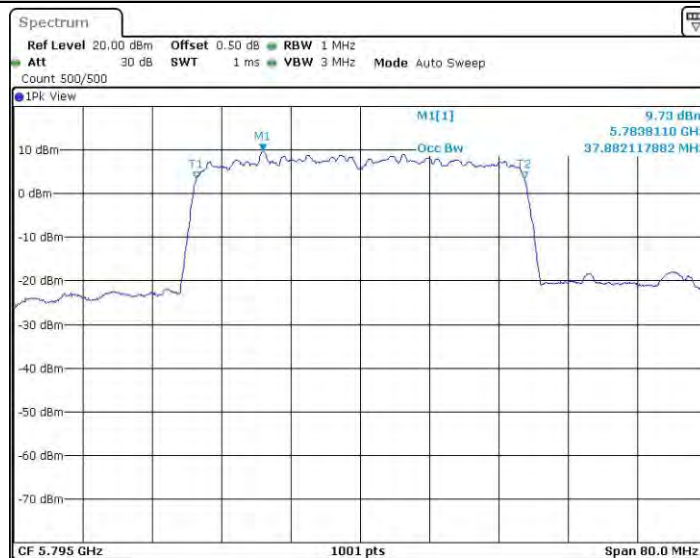
11AX40SISO_Ant1_5670



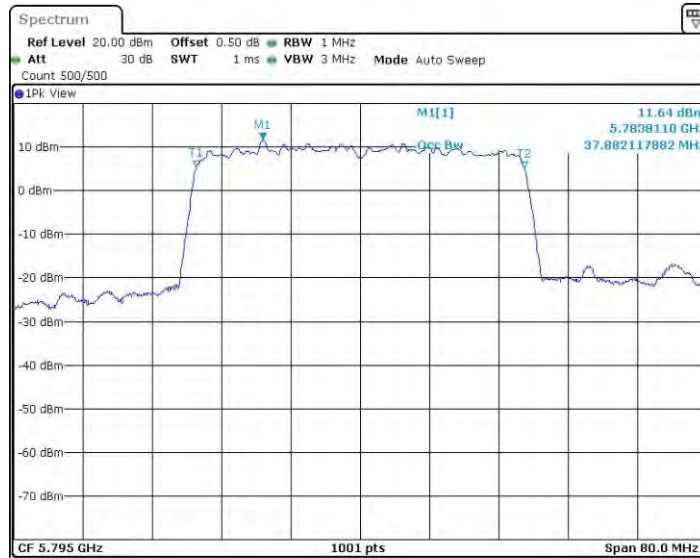
11AX40SISO_Ant0_5755



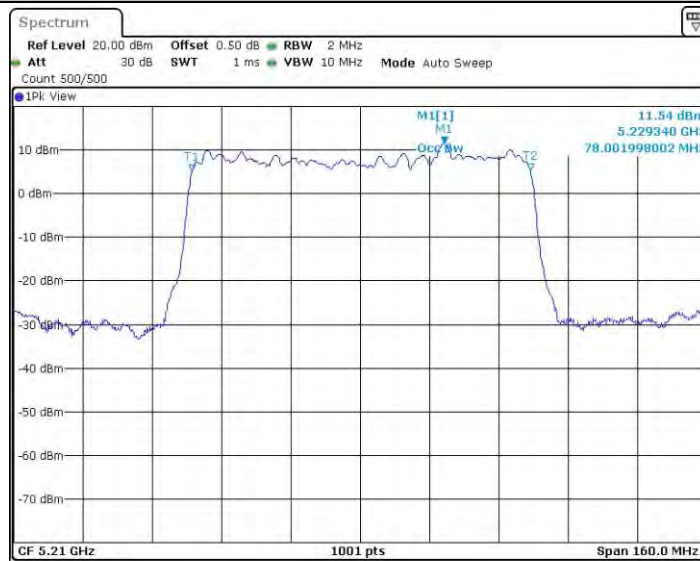
11AX40SISO_Ant1_5755



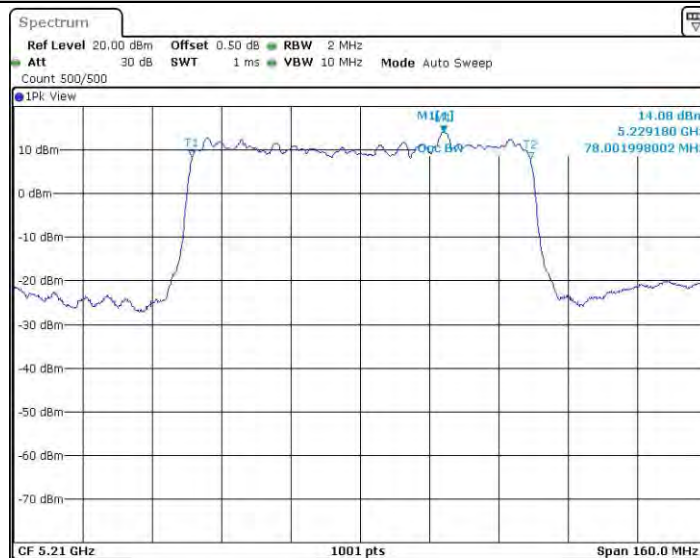
11AX40SISO_Ant0_5795



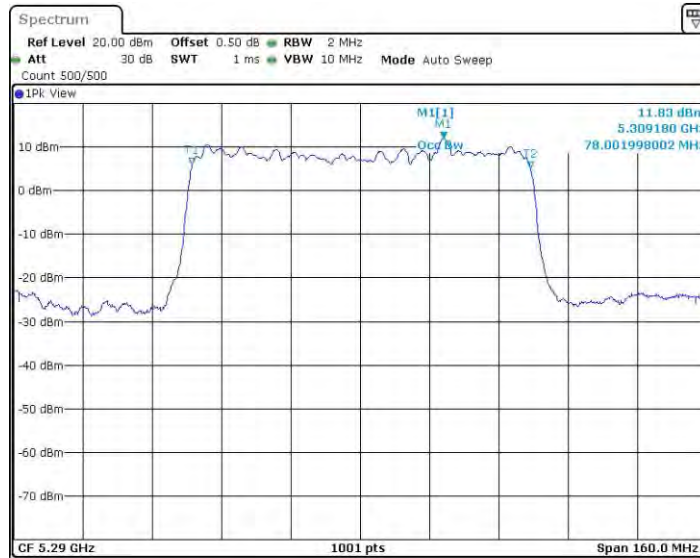
11AX40SISO_Ant1_5795



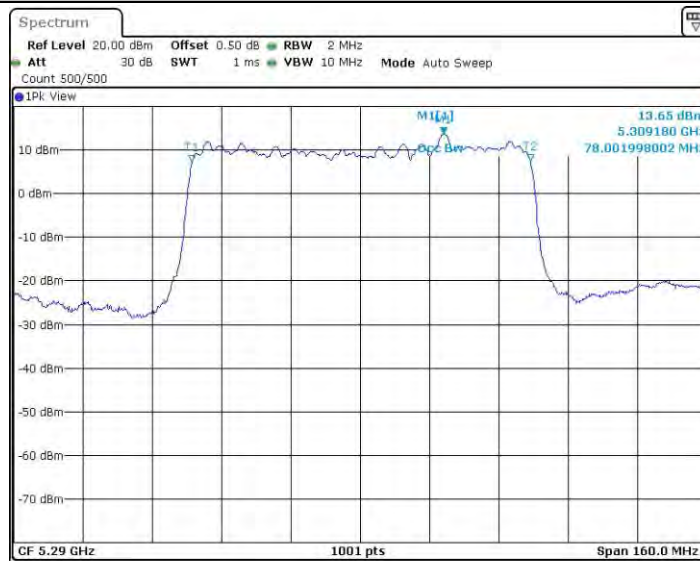
11AX80SISO_Ant0_5210



11AX80SISO_Ant1_5210



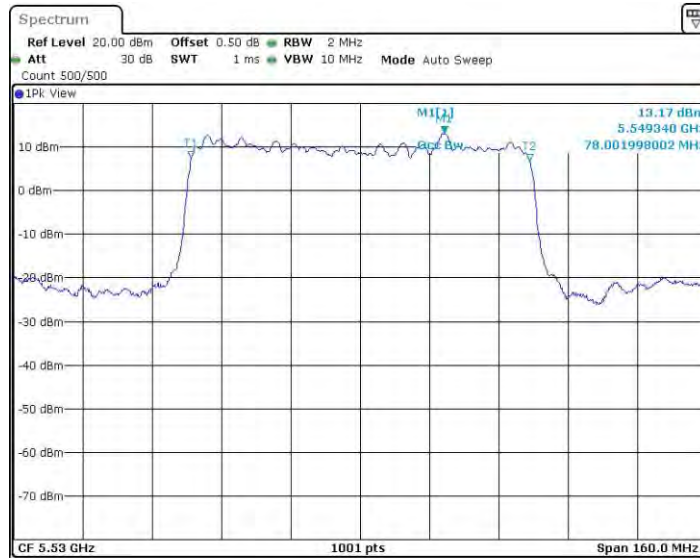
11AX80SISO_Ant0_5290



11AX80SISO_Ant1_5290



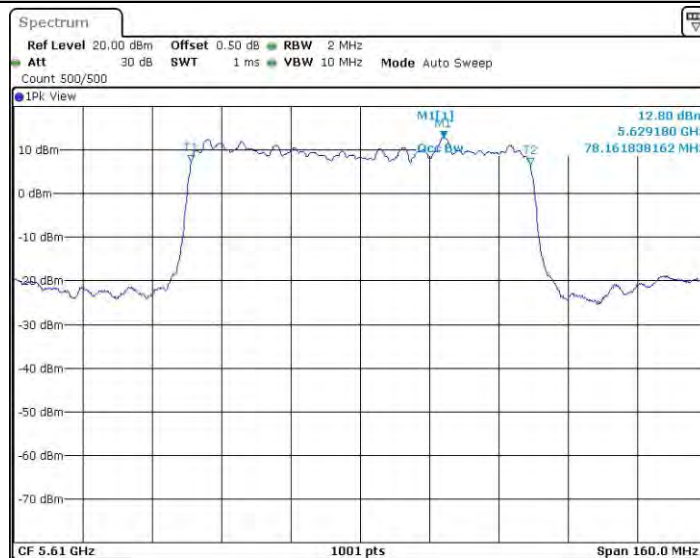
11AX80SISO_Ant0_5530



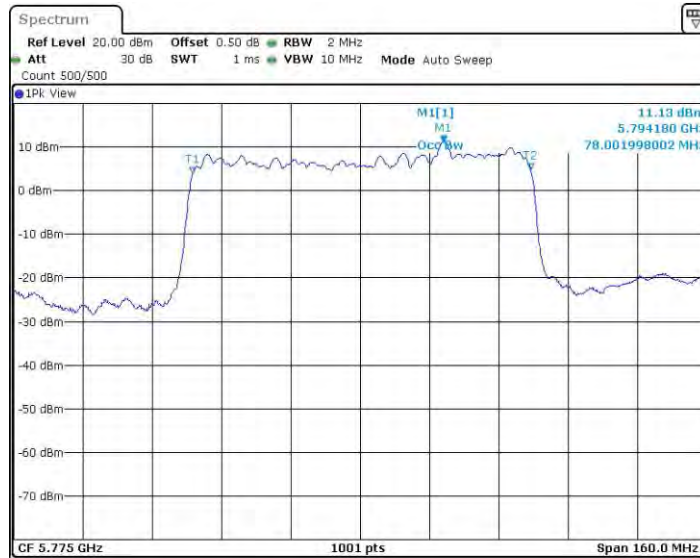
11AX80SISO_Ant1_5530



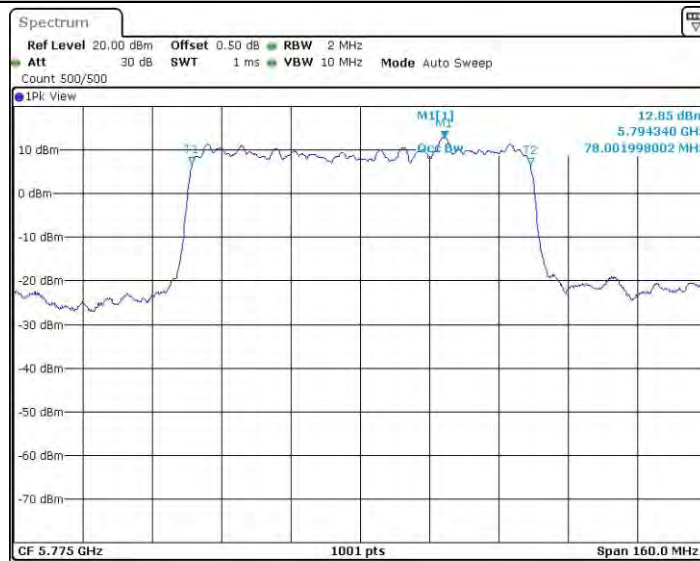
11AX80SISO_Ant0_5610



11AX80SISO_Ant1_5610



11AX80SISO_Ant0_5775



11AX80SISO_Ant1_5775



11N20MIMO_Ant0_5180

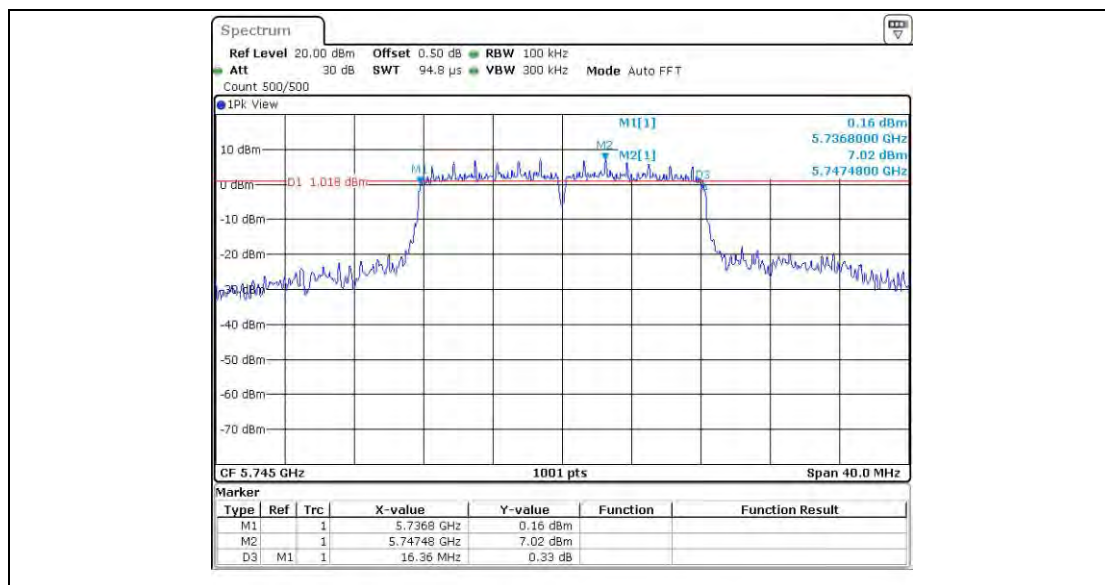
Appendix A3: Min emission bandwidth

Test Result

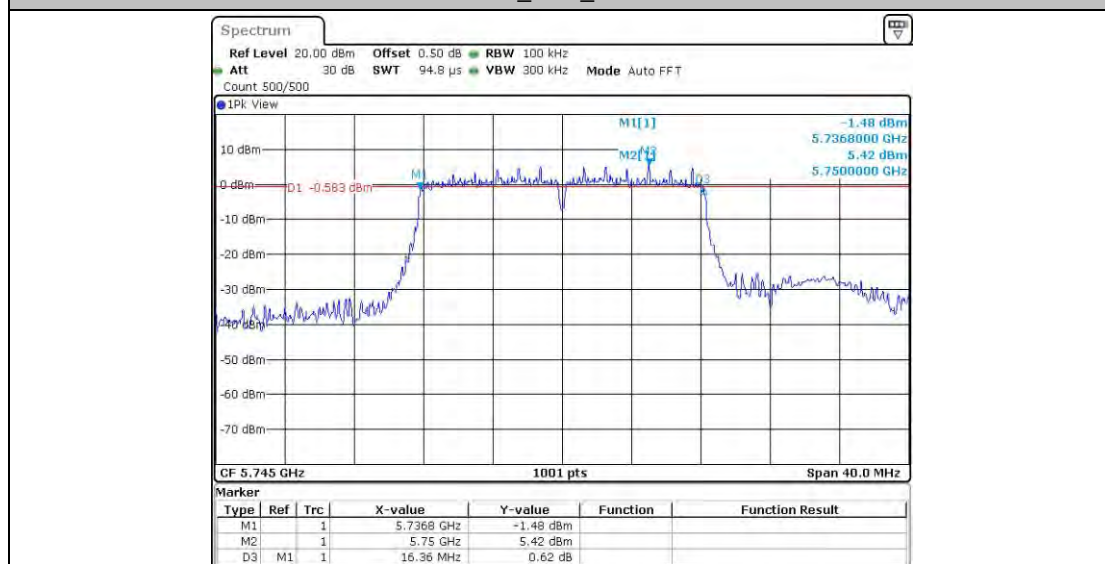
TestMode	Antenna	Channel	6db EBW [MHz]	FL[MHz]	FH[MHz]	Limit[MHz]	Verdict
11A	Ant0	5745	16.360	5736.800	5753.160	0.5	PASS
	Ant1	5745	16.360	5736.800	5753.160	0.5	PASS
	Ant0	5785	16.360	5776.800	5793.160	0.5	PASS
	Ant1	5785	16.360	5776.800	5793.160	0.5	PASS
	Ant0	5825	16.360	5816.800	5833.160	0.5	PASS
	Ant1	5825	16.360	5816.800	5833.160	0.5	PASS
11N20SISO	Ant0	5745	17.000	5736.160	5753.160	0.5	PASS
	Ant1	5745	17.640	5736.160	5753.800	0.5	PASS
	Ant0	5785	16.720	5776.440	5793.160	0.5	PASS
	Ant1	5785	17.320	5776.200	5793.520	0.5	PASS
	Ant0	5825	17.640	5816.160	5833.800	0.5	PASS
	Ant1	5825	17.640	5816.160	5833.800	0.5	PASS
11N40SISO	Ant0	5755	35.520	5737.160	5772.680	0.5	PASS
	Ant1	5755	35.680	5737.160	5772.840	0.5	PASS
	Ant0	5795	35.680	5777.160	5812.840	0.5	PASS
	Ant1	5795	35.680	5777.160	5812.840	0.5	PASS
11AC20SISO	Ant0	5745	17.000	5736.560	5753.560	0.5	PASS
	Ant1	5745	17.000	5736.560	5753.560	0.5	PASS
	Ant0	5785	16.720	5776.800	5793.520	0.5	PASS
	Ant1	5785	17.040	5776.520	5793.560	0.5	PASS
	Ant0	5825	16.960	5816.560	5833.520	0.5	PASS
	Ant1	5825	17.040	5816.520	5833.560	0.5	PASS
11AC40SISO	Ant0	5755	35.680	5737.320	5773.000	0.5	PASS
	Ant1	5755	35.840	5737.160	5773.000	0.5	PASS
	Ant0	5795	35.840	5777.160	5813.000	0.5	PASS
	Ant1	5795	35.840	5777.160	5813.000	0.5	PASS
11AC80SISO	Ant0	5775	76.160	5737.240	5813.400	0.5	PASS
	Ant1	5775	76.800	5736.600	5813.400	0.5	PASS
11AX20SISO	Ant0	5745	18.400	5735.720	5754.120	0.5	PASS
	Ant1	5745	17.640	5736.160	5753.800	0.5	PASS
	Ant0	5785	18.240	5775.720	5793.960	0.5	PASS
	Ant1	5785	17.000	5776.560	5793.560	0.5	PASS
	Ant0	5825	18.040	5815.720	5833.760	0.5	PASS
	Ant1	5825	17.040	5816.520	5833.560	0.5	PASS
11AX40SISO	Ant0	5755	37.680	5736.440	5774.120	0.5	PASS
	Ant1	5755	37.760	5736.360	5774.120	0.5	PASS
	Ant0	5795	37.760	5776.360	5814.120	0.5	PASS
	Ant1	5795	37.760	5776.360	5814.120	0.5	PASS
11AX80SISO	Ant0	5775	77.920	5735.960	5813.880	0.5	PASS

	Ant1	5775	77.920	5735.960	5813.880	0.5	PASS
--	------	------	--------	----------	----------	-----	------

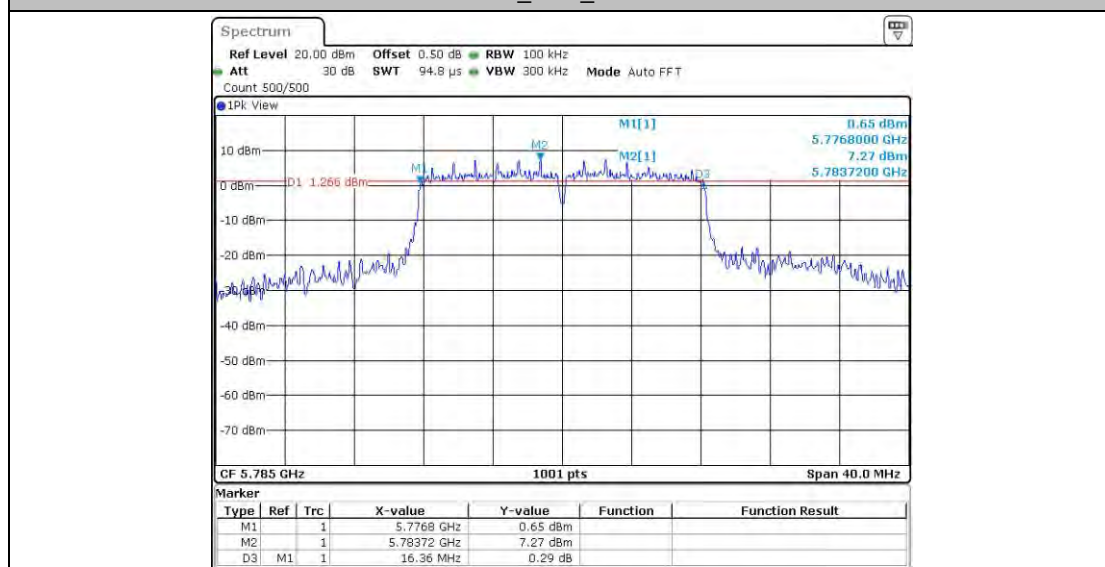
Test Graphs



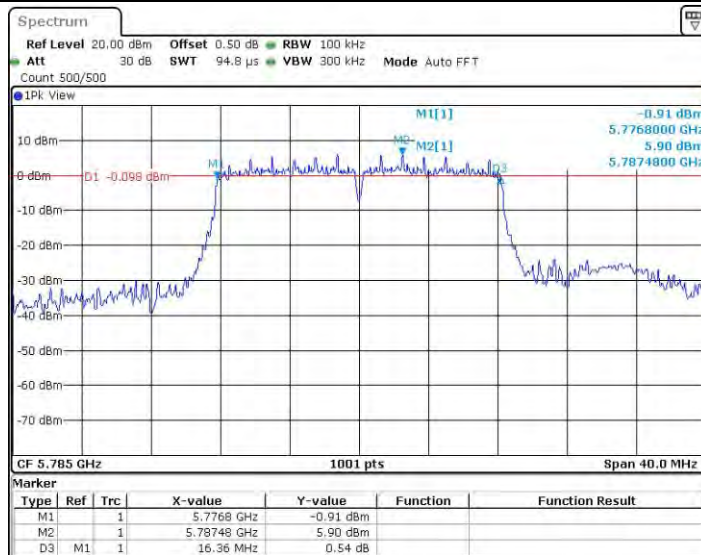
11A_Ant0_5745



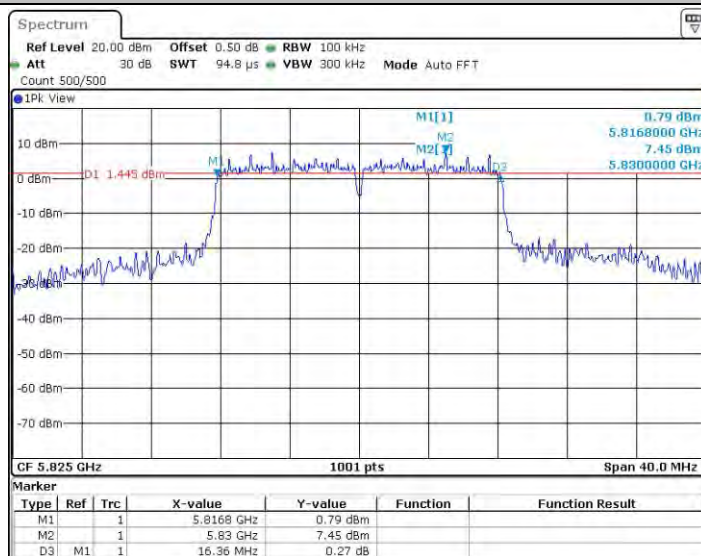
11A_Ant1_5745



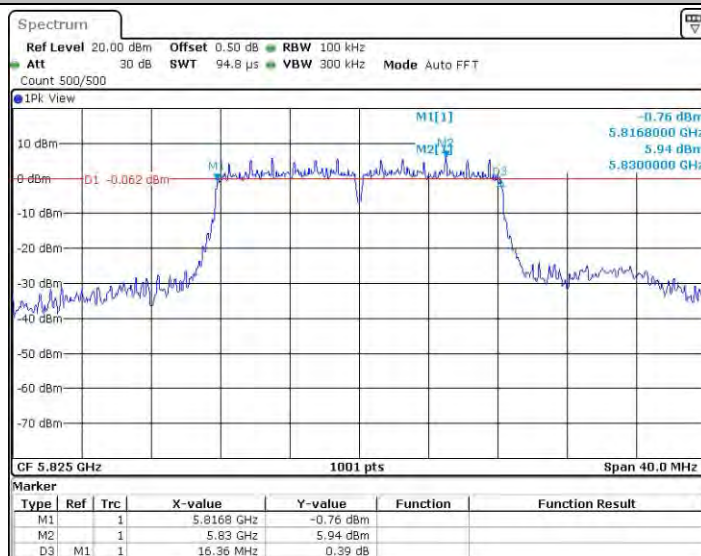
11A_Ant0_5785



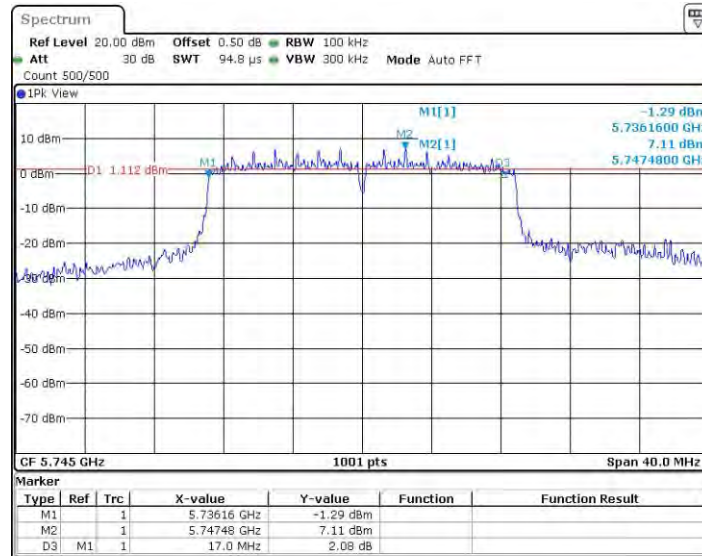
11A_Ant1_5785



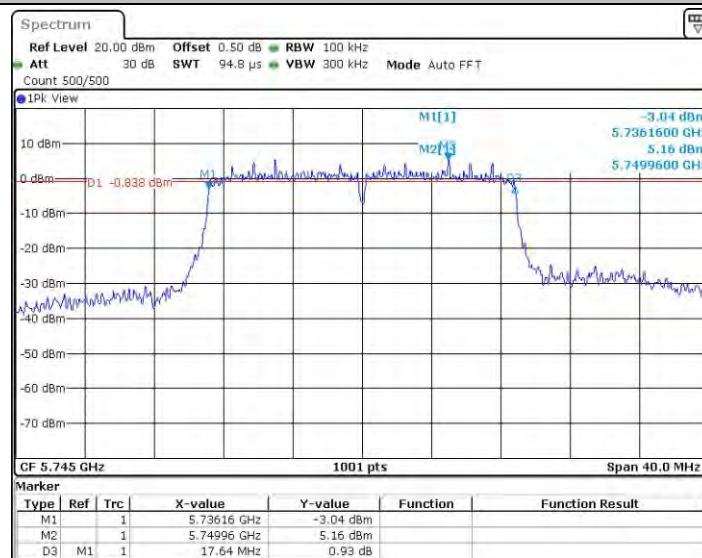
11A_Ant0_5825



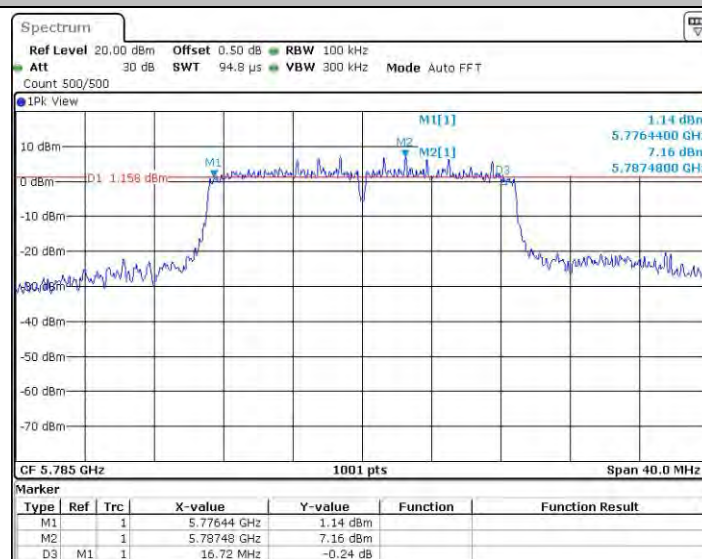
11A_Ant1_5825



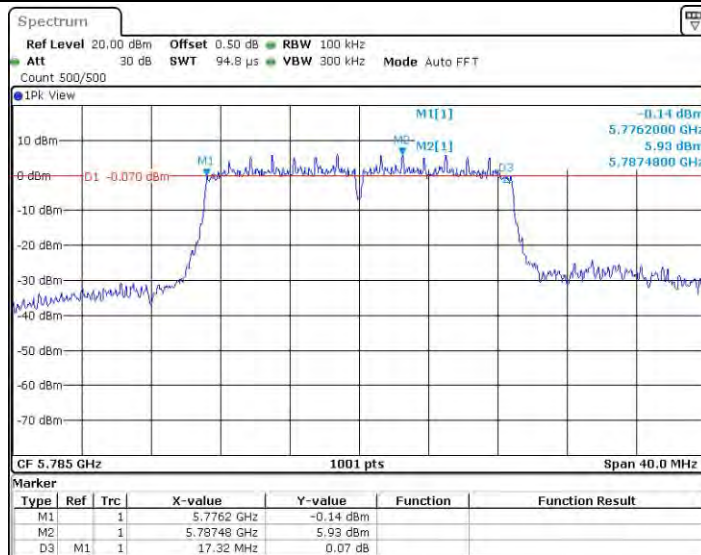
11N20SISO_Ant0_5745



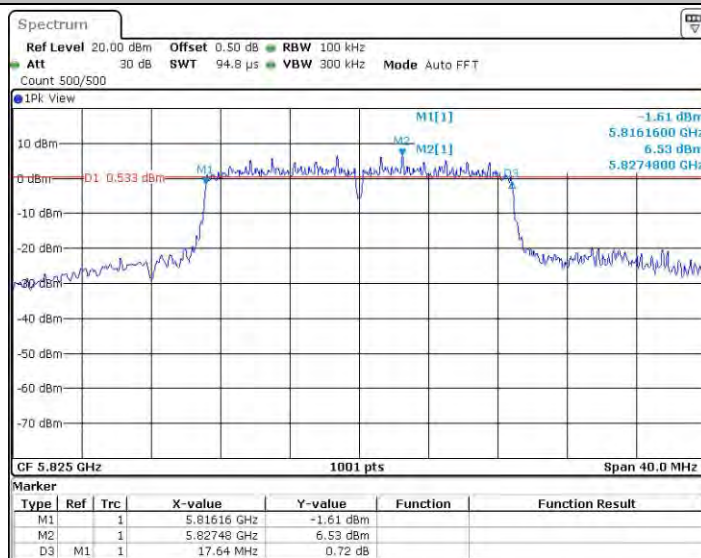
11N20SISO_Ant1_5745



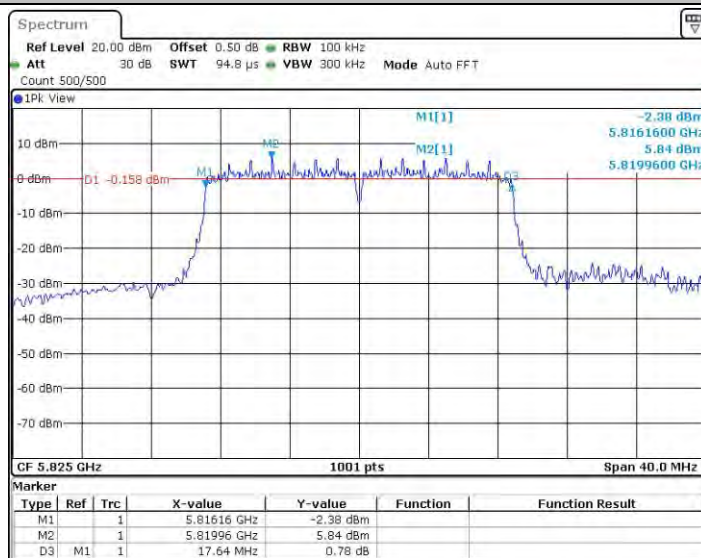
11N20SISO_Ant0_5785



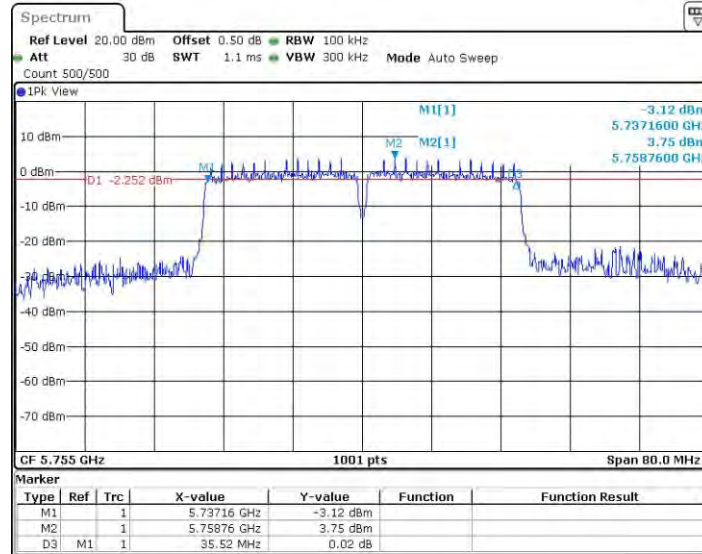
11N20SISO_Ant1_5785



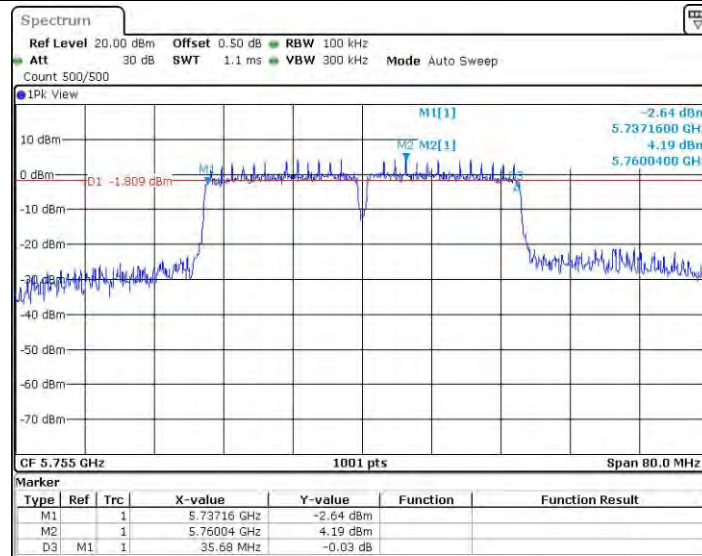
11N20SISO_Ant0_5825



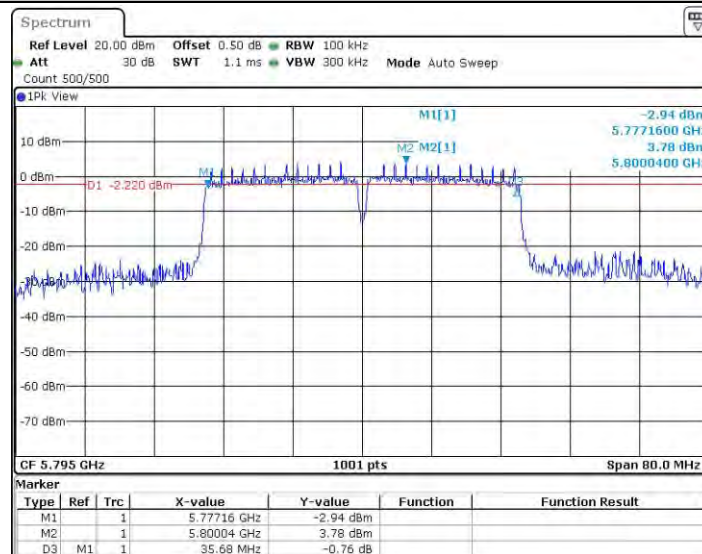
11N20SISO_Ant1_5825



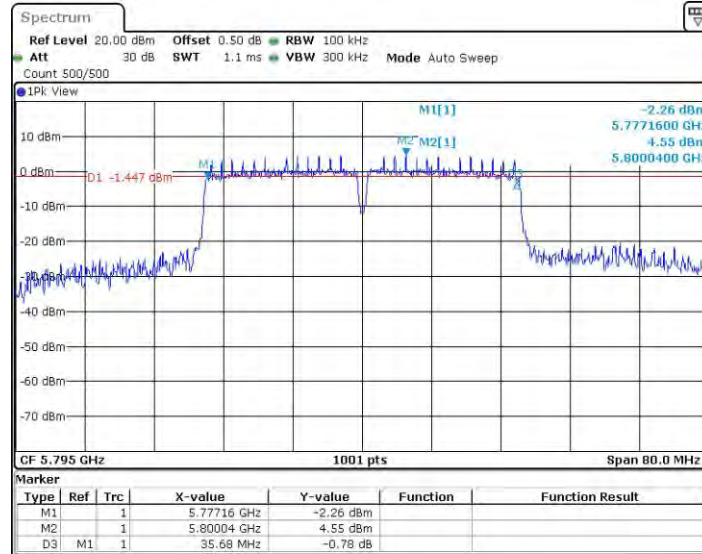
11N40SISO_Ant0_5755



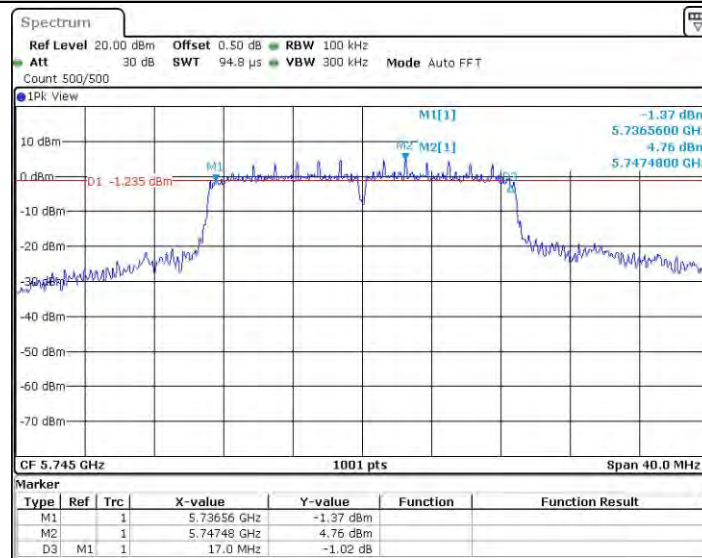
11N40SISO_Ant1_5755



11N40SISO_Ant0_5795



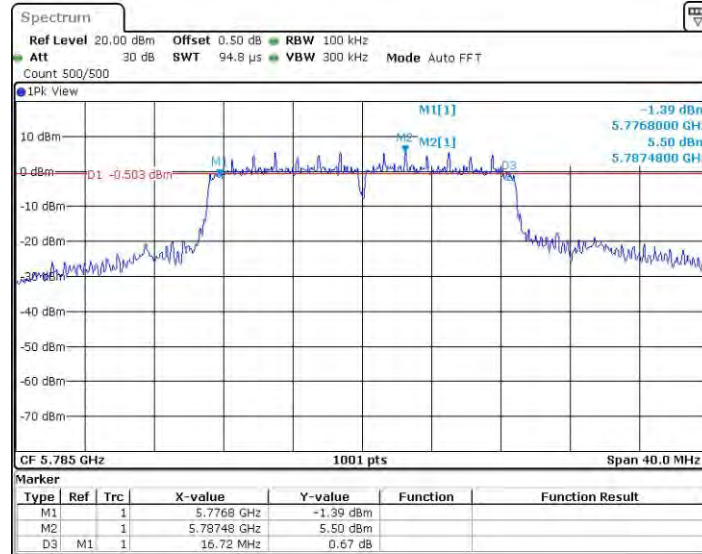
11N40SISO_Ant1_5795



11AC20SISO_Ant0_5745



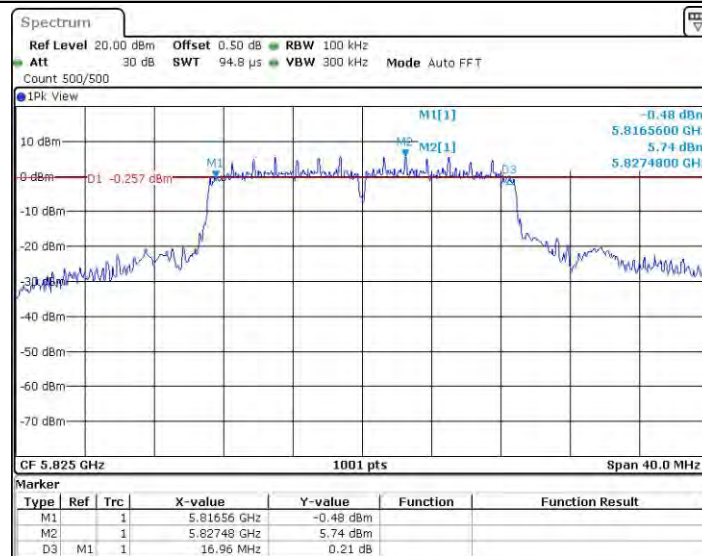
11AC20SISO_Ant1_5745



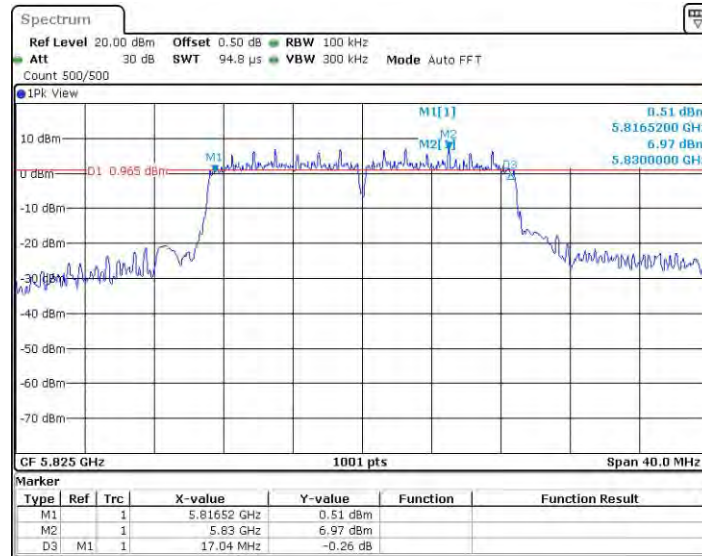
11AC20SISO_Ant0_5785



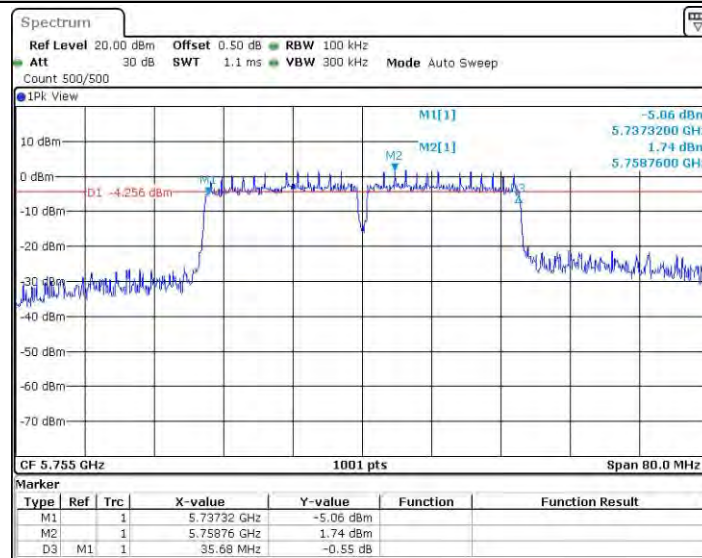
11AC20SISO_Ant1_5785



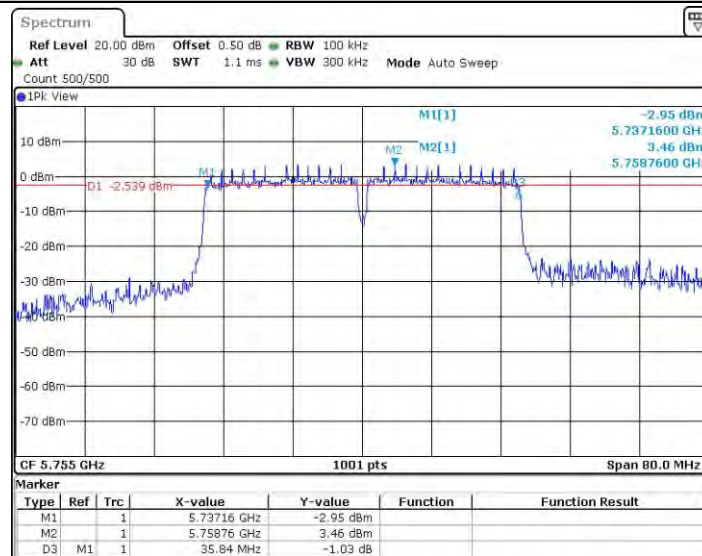
11AC20SISO_Ant0_5825



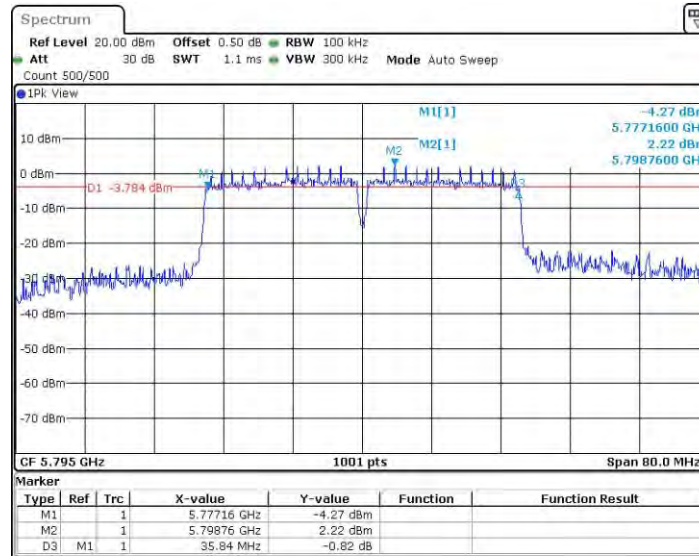
11AC20SISO_Ant1_5825



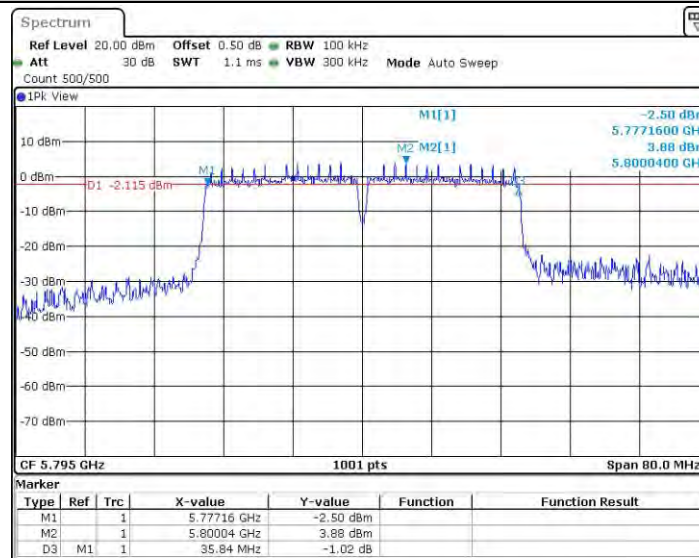
11AC40SISO_Ant0_5755



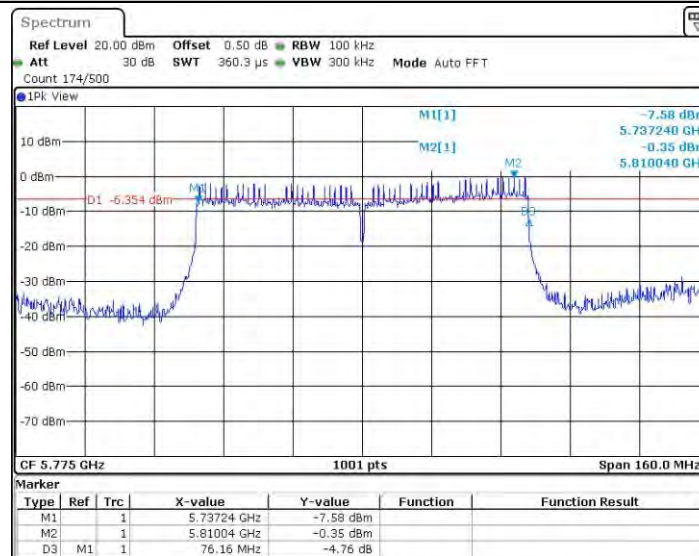
11AC40SISO_Ant1_5755



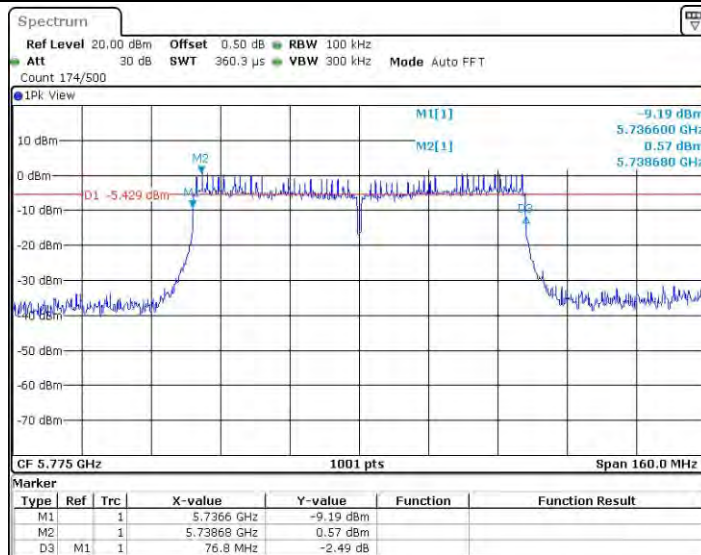
11AC40SISO_Ant0_5795



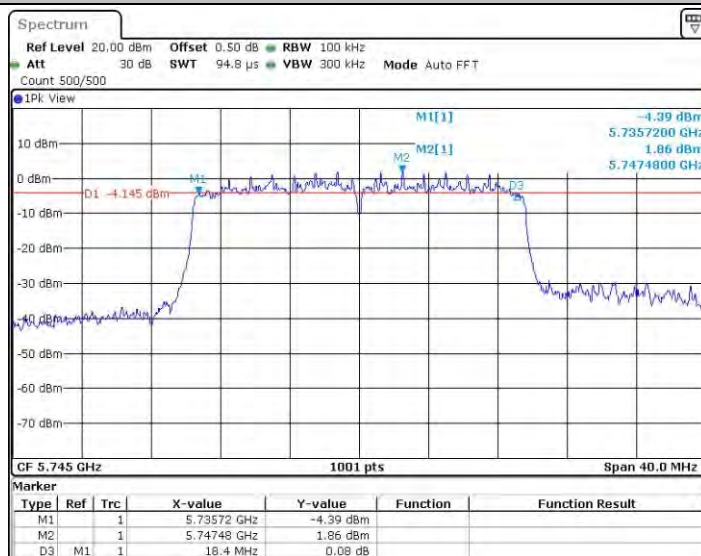
11AC40SISO_Ant1_5795



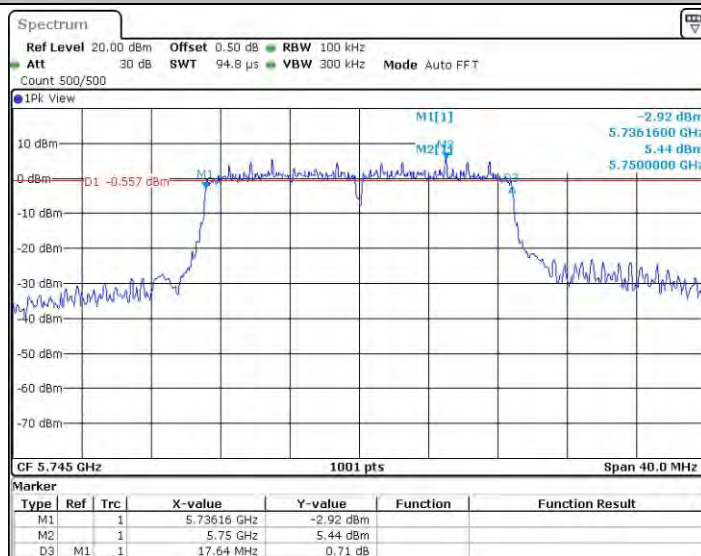
11AC80SISO_Ant0_5775



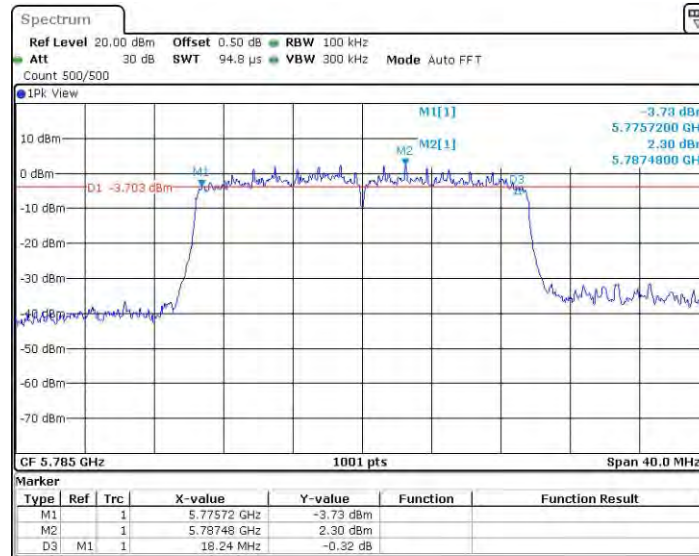
11AC80SISO_Ant1_5775



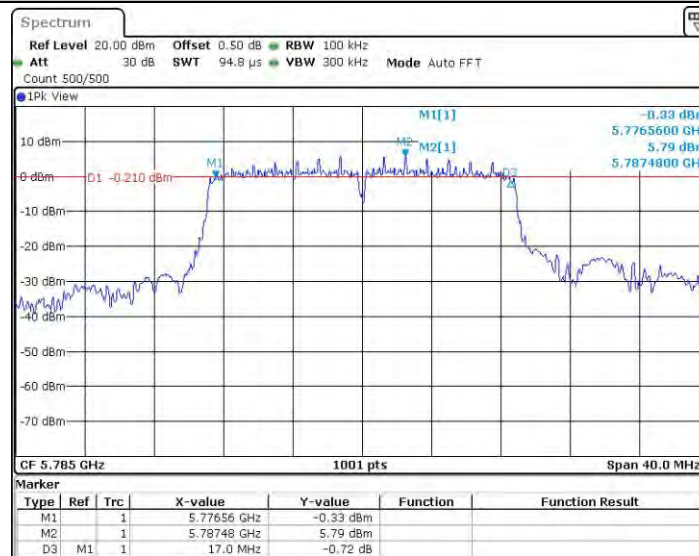
11AX20SISO_Ant0_5745



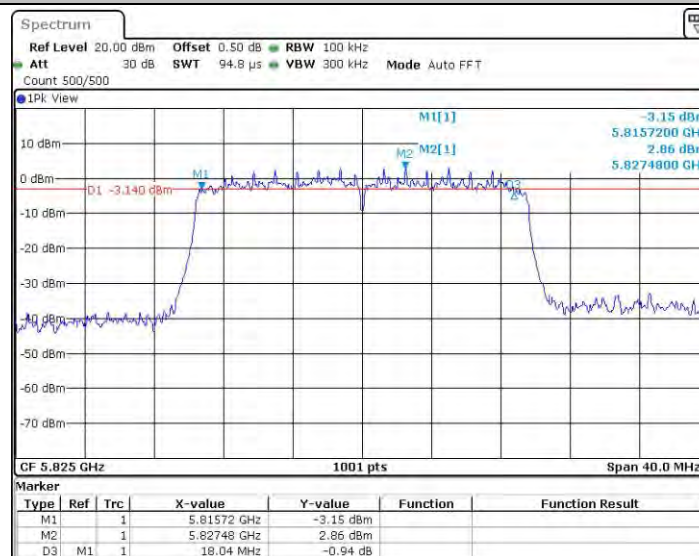
11AX20SISO_Ant1_5745



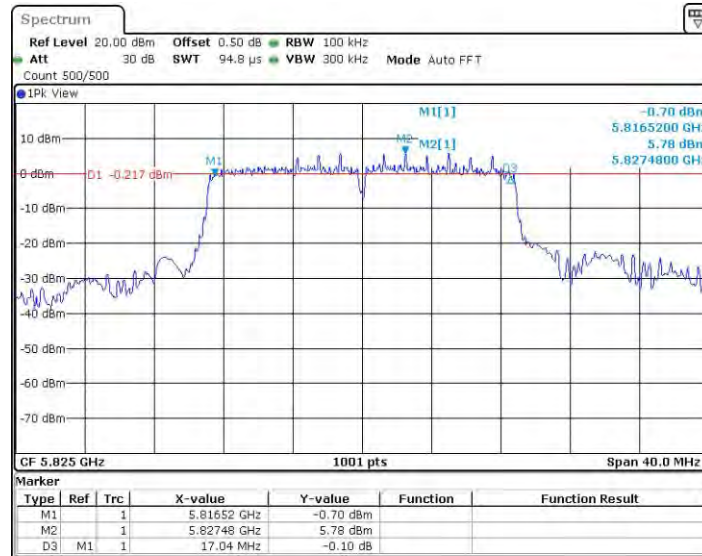
11AX20SISO_Ant0_5785



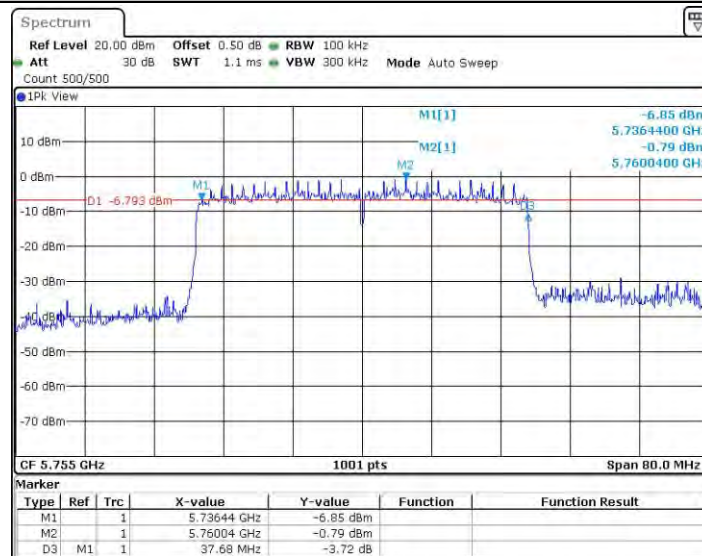
11AX20SISO_Ant1_5785



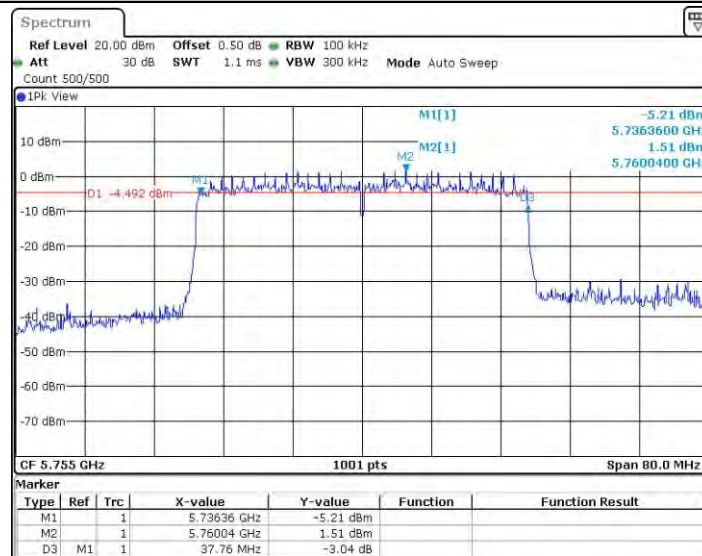
11AX20SISO_Ant0_5825



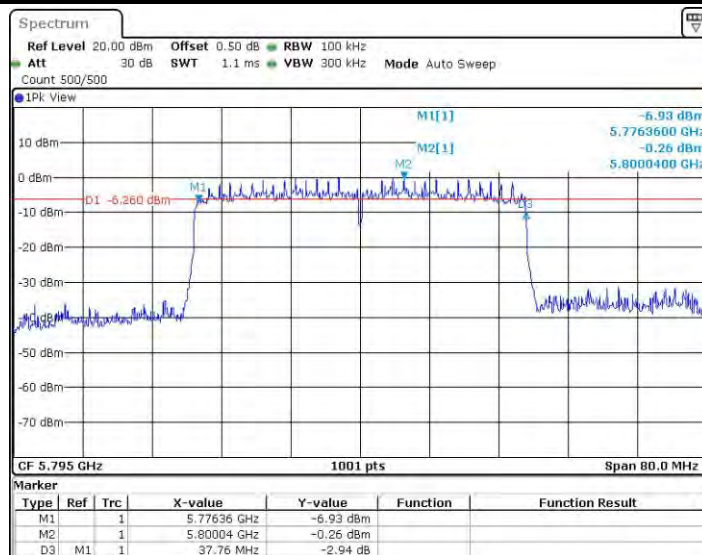
11AX20SISO_Ant1_5825



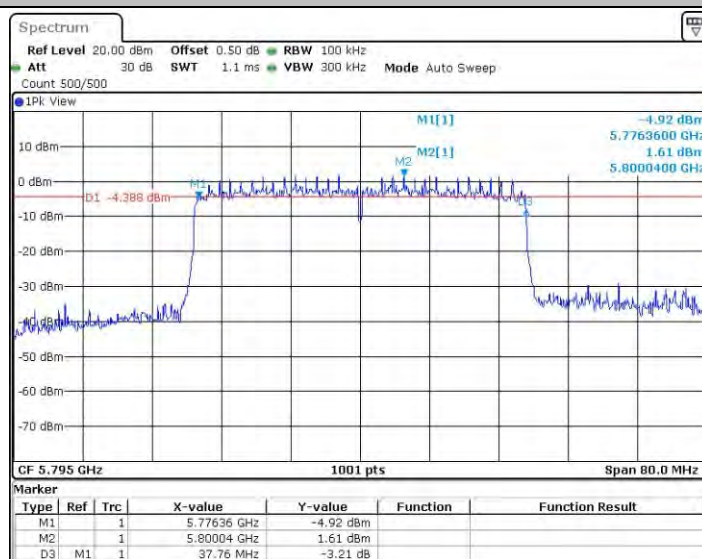
11AX40SISO_Ant0_5755



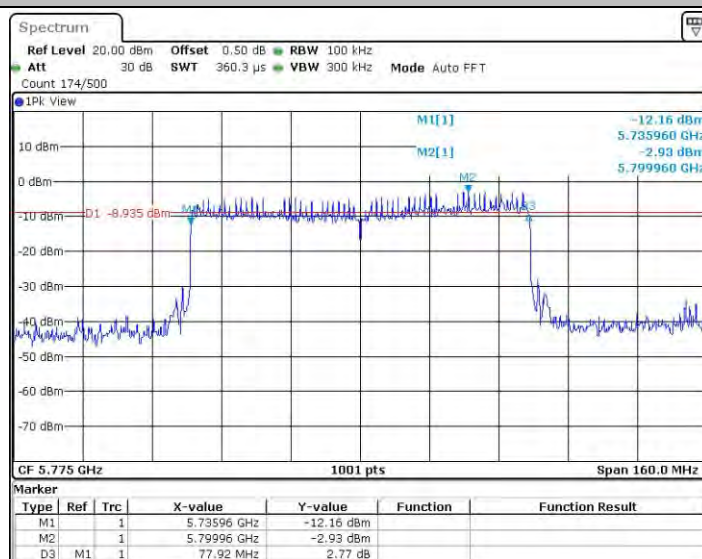
11AX40SISO_Ant1_5755



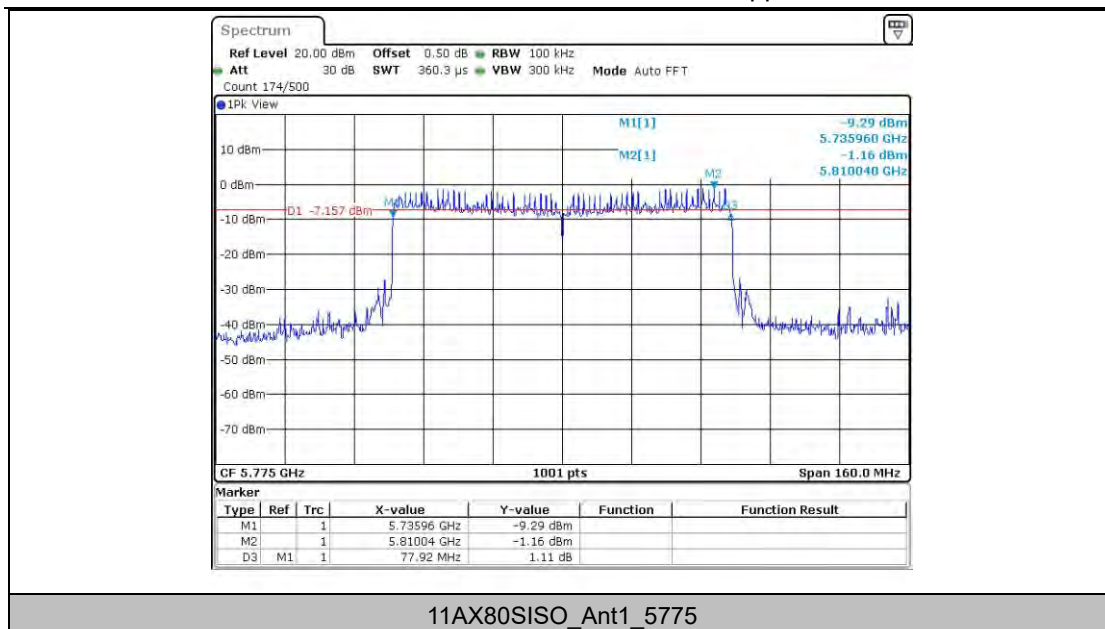
11AX40SISO_Ant0_5795



11AX40SISO_Ant1_5795



11AX80SISO_Ant0_5775



Appendix B1: Maximum average conducted output power

Test Result

TestMode	Antenna	Channel	Result	Limit	Verdict
11A	Ant0	5180	15.85	<=23.98	PASS
	Ant1	5180	16.65	<=23.98	PASS
	Ant0	5200	15.52	<=23.98	PASS
	Ant1	5200	16.43	<=23.98	PASS
	Ant0	5240	14.26	<=23.98	PASS
	Ant1	5240	15.90	<=23.98	PASS
	Ant0	5260	15.90	<=23.70	PASS
	Ant1	5260	16.11	<=23.63	PASS
	Ant0	5280	15.64	<=23.67	PASS
	Ant1	5280	15.80	<=23.64	PASS
	Ant0	5320	14.63	<=23.65	PASS
	Ant1	5320	14.93	<=23.64	PASS
	Ant0	5500	16.31	<=23.98	PASS
	Ant1	5500	15.68	<=23.64	PASS
	Ant0	5580	16.45	<=23.98	PASS
	Ant1	5580	14.98	<=23.87	PASS
	Ant0	5700	16.31	<=23.87	PASS
	Ant1	5700	15.52	<=23.64	PASS
	Ant0	5745	18.51	<=30	PASS
	Ant1	5745	16.70	<=30	PASS
	Ant0	5785	18.52	<=30	PASS
	Ant1	5785	17.16	<=30	PASS
	Ant0	5825	18.63	<=30	PASS
	Ant1	5825	17.33	<=30	PASS
11N20SISO	Ant0	5180	16.09	<=23.98	PASS
	Ant1	5180	16.87	<=23.98	PASS
	Ant0	5200	15.57	<=23.98	PASS
	Ant1	5200	16.67	<=23.98	PASS
	Ant0	5240	14.71	<=23.98	PASS
	Ant1	5240	16.21	<=23.98	PASS
	Ant0	5260	16.47	<=23.87	PASS
	Ant1	5260	16.28	<=23.86	PASS
	Ant0	5280	16.13	<=23.86	PASS
	Ant1	5280	15.86	<=23.88	PASS
	Ant0	5320	15.60	<=23.87	PASS
	Ant1	5320	15.01	<=23.88	PASS
	Ant0	5500	17.02	<=23.84	PASS
	Ant1	5500	15.67	<=23.87	PASS
	Ant0	5580	16.44	<=23.86	PASS
	Ant1	5580	15.37	<=23.85	PASS
	Ant0	5700	17.09	<=23.89	PASS

	Ant1	5700	15.42	<=23.88	PASS
	Ant0	5745	18.60	<=30	PASS
	Ant1	5745	16.73	<=30	PASS
	Ant0	5785	18.60	<=30	PASS
	Ant1	5785	17.13	<=30	PASS
	Ant0	5825	18.68	<=30	PASS
	Ant1	5825	17.33	<=30	PASS
11N40SISO	Ant0	5190	14.07	<=23.98	PASS
	Ant1	5190	15.74	<=23.98	PASS
	Ant0	5230	14.18	<=23.98	PASS
	Ant1	5230	15.48	<=23.98	PASS
	Ant0	5270	15.16	<=23.98	PASS
	Ant1	5270	15.11	<=23.98	PASS
	Ant0	5310	15.17	<=23.98	PASS
	Ant1	5310	14.96	<=23.98	PASS
	Ant0	5510	16.04	<=23.98	PASS
	Ant1	5510	14.93	<=23.98	PASS
	Ant0	5550	16.04	<=23.98	PASS
	Ant1	5550	14.88	<=23.98	PASS
	Ant0	5670	15.98	<=23.98	PASS
	Ant1	5670	14.55	<=23.98	PASS
	Ant0	5755	16.98	<=30	PASS
	Ant1	5755	17.03	<=30	PASS
	Ant0	5795	16.94	<=30	PASS
	Ant1	5795	17.30	<=30	PASS
11AC20SISO	Ant0	5180	15.99	<=23.98	PASS
	Ant1	5180	16.89	<=23.98	PASS
	Ant0	5200	15.35	<=23.98	PASS
	Ant1	5200	16.77	<=23.98	PASS
	Ant0	5240	14.43	<=23.98	PASS
	Ant1	5240	16.26	<=23.98	PASS
	Ant0	5260	16.41	<=23.98	PASS
	Ant1	5260	17.20	<=23.87	PASS
	Ant0	5280	15.46	<=23.90	PASS
	Ant1	5280	16.83	<=23.88	PASS
	Ant0	5320	14.98	<=23.87	PASS
	Ant1	5320	16.07	<=23.88	PASS
	Ant0	5500	16.04	<=23.98	PASS
	Ant1	5500	16.65	<=23.90	PASS
	Ant0	5580	17.12	<=23.98	PASS
	Ant1	5580	16.40	<=23.97	PASS
	Ant0	5700	16.57	<=23.98	PASS
	Ant1	5700	15.70	<=23.98	PASS
	Ant0	5745	18.73	<=30	PASS
	Ant1	5745	16.37	<=30	PASS
	Ant0	5785	18.73	<=30	PASS

	Ant1	5785	16.81	<=30	PASS
	Ant0	5825	17.97	<=30	PASS
	Ant1	5825	17.03	<=30	PASS
11AC40SISO	Ant0	5190	14.16	<=23.98	PASS
	Ant1	5190	15.78	<=23.98	PASS
	Ant0	5230	14.17	<=23.98	PASS
	Ant1	5230	15.49	<=23.98	PASS
	Ant0	5270	15.15	<=23.98	PASS
	Ant1	5270	15.13	<=23.98	PASS
	Ant0	5310	15.11	<=23.98	PASS
	Ant1	5310	14.95	<=23.98	PASS
	Ant0	5510	15.93	<=23.98	PASS
	Ant1	5510	14.94	<=23.98	PASS
	Ant0	5550	16.00	<=23.98	PASS
	Ant1	5550	14.87	<=23.98	PASS
	Ant0	5670	15.93	<=23.98	PASS
	Ant1	5670	14.56	<=23.98	PASS
	Ant0	5755	16.90	<=30	PASS
	Ant1	5755	16.06	<=30	PASS
	Ant0	5795	16.88	<=30	PASS
	Ant1	5795	16.42	<=30	PASS
11AC80SISO	Ant0	5210	14.09	<=23.98	PASS
	Ant1	5210	15.40	<=23.98	PASS
	Ant0	5290	15.06	<=23.98	PASS
	Ant1	5290	15.00	<=23.98	PASS
	Ant0	5530	14.86	<=23.98	PASS
	Ant1	5530	13.70	<=23.98	PASS
	Ant0	5610	15.04	<=23.98	PASS
	Ant1	5610	13.66	<=23.98	PASS
	Ant0	5775	16.71	<=30	PASS
	Ant1	5775	17.45	<=30	PASS
11AX20SISO	Ant0	5180	13.53	<=23.98	PASS
	Ant1	5180	12.63	<=23.98	PASS
	Ant0	5200	13.47	<=23.98	PASS
	Ant1	5200	12.43	<=23.98	PASS
	Ant0	5240	13.40	<=23.98	PASS
	Ant1	5240	12.08	<=23.98	PASS
	Ant0	5260	13.22	<=23.98	PASS
	Ant1	5260	14.81	<=23.86	PASS
	Ant0	5280	13.30	<=23.98	PASS
	Ant1	5280	14.70	<=23.90	PASS
	Ant0	5320	13.37	<=23.98	PASS
	Ant1	5320	14.58	<=23.87	PASS
	Ant0	5500	14.30	<=23.98	PASS
	Ant1	5500	14.60	<=23.88	PASS
	Ant0	5580	14.43	<=23.98	PASS

	Ant1	5580	14.44	<=23.88	PASS
	Ant0	5700	14.23	<=23.98	PASS
	Ant1	5700	14.43	<=23.86	PASS
	Ant0	5745	17.27	<=30	PASS
	Ant1	5745	16.10	<=30	PASS
	Ant0	5785	17.27	<=30	PASS
	Ant1	5785	16.25	<=30	PASS
	Ant0	5825	17.24	<=30	PASS
	Ant1	5825	16.31	<=30	PASS
11AX40SISO	Ant0	5190	14.56	<=23.98	PASS
	Ant1	5190	17.30	<=23.98	PASS
	Ant0	5230	14.47	<=23.98	PASS
	Ant1	5230	16.98	<=23.98	PASS
	Ant0	5270	15.23	<=23.98	PASS
	Ant1	5270	16.58	<=23.98	PASS
	Ant0	5310	14.94	<=23.98	PASS
	Ant1	5310	16.54	<=23.98	PASS
	Ant0	5510	13.94	<=23.98	PASS
	Ant1	5510	16.59	<=23.98	PASS
	Ant0	5550	13.64	<=23.98	PASS
	Ant1	5550	16.41	<=23.98	PASS
	Ant0	5670	12.65	<=23.98	PASS
	Ant1	5670	16.13	<=23.98	PASS
	Ant0	5755	13.46	<=30	PASS
	Ant1	5755	15.86	<=30	PASS
	Ant0	5795	14.16	<=30	PASS
	Ant1	5795	15.99	<=30	PASS
11AX80SISO	Ant0	5210	14.68	<=23.98	PASS
	Ant1	5210	17.34	<=23.98	PASS
	Ant0	5290	15.14	<=23.98	PASS
	Ant1	5290	16.77	<=23.98	PASS
	Ant0	5530	13.09	<=23.98	PASS
	Ant1	5530	16.45	<=23.98	PASS
	Ant0	5610	12.52	<=23.98	PASS
	Ant1	5610	16.50	<=23.98	PASS
	Ant0	5775	13.92	<=30	PASS
	Ant1	5775	16.05	<=30	PASS
11N20MIMO	Ant0	5180	12.15	<=23.98	PASS
	Ant1	5180	13.95	<=23.98	PASS
	total	5180	16.2	<=23.98	PASS
	Ant0	5200	11.90	<=23.98	PASS
	Ant1	5200	13.84	<=23.98	PASS
	total	5200	16.0	<=23.98	PASS
	Ant0	5240	11.27	<=23.98	PASS
	Ant1	5240	13.54	<=23.98	PASS
	total	5240	15.6	<=23.98	PASS

	Ant0	5260	14.01	<=23.85	PASS
	Ant1	5260	13.84	<=23.87	PASS
	total	5260	16.9	<=23.87	PASS
	Ant0	5280	14.09	<=23.89	PASS
	Ant1	5280	13.87	<=23.86	PASS
	total	5280	17.0	<=23.86	PASS
	Ant0	5320	13.96	<=23.88	PASS
	Ant1	5320	14.15	<=23.86	PASS
	total	5320	17.1	<=23.86	PASS
	Ant0	5500	12.53	<=23.83	PASS
	Ant1	5500	13.68	<=23.86	PASS
	total	5500	16.2	<=23.86	PASS
	Ant0	5580	11.11	<=23.86	PASS
	Ant1	5580	13.01	<=23.86	PASS
	total	5580	15.2	<=23.86	PASS
	Ant0	5700	12.55	<=23.85	PASS
	Ant1	5700	12.97	<=23.87	PASS
	total	5700	15.8	<=23.87	PASS
	Ant0	5745	11.92	<=30	PASS
	Ant1	5745	13.77	<=30	PASS
	total	5745	16.0	<=30	PASS
	Ant0	5785	11.61	<=30	PASS
	Ant1	5785	13.63	<=30	PASS
	total	5785	15.7	<=30	PASS
	Ant0	5825	11.21	<=30	PASS
	Ant1	5825	13.48	<=30	PASS
	total	5825	15.5	<=30	PASS
11N40MIMO	Ant0	5190	13.34	<=23.98	PASS
	Ant1	5190	13.13	<=23.98	PASS
	total	5190	16.2	<=23.98	PASS
	Ant0	5230	13.14	<=23.98	PASS
	Ant1	5230	14.67	<=23.98	PASS
	total	5230	17.0	<=23.98	PASS
	Ant0	5270	13.80	<=23.98	PASS
	Ant1	5270	14.38	<=23.98	PASS
	total	5270	17.1	<=23.98	PASS
	Ant0	5310	13.49	<=23.98	PASS
	Ant1	5310	13.78	<=23.98	PASS
	total	5310	16.5	<=23.98	PASS
	Ant0	5510	12.53	<=23.98	PASS
	Ant1	5510	13.47	<=23.98	PASS
	total	5510	16.0	<=23.98	PASS
	Ant0	5550	10.92	<=23.98	PASS
	Ant1	5550	13.54	<=23.98	PASS
	total	5550	15.4	<=23.98	PASS
	Ant0	5670	10.70	<=23.98	PASS

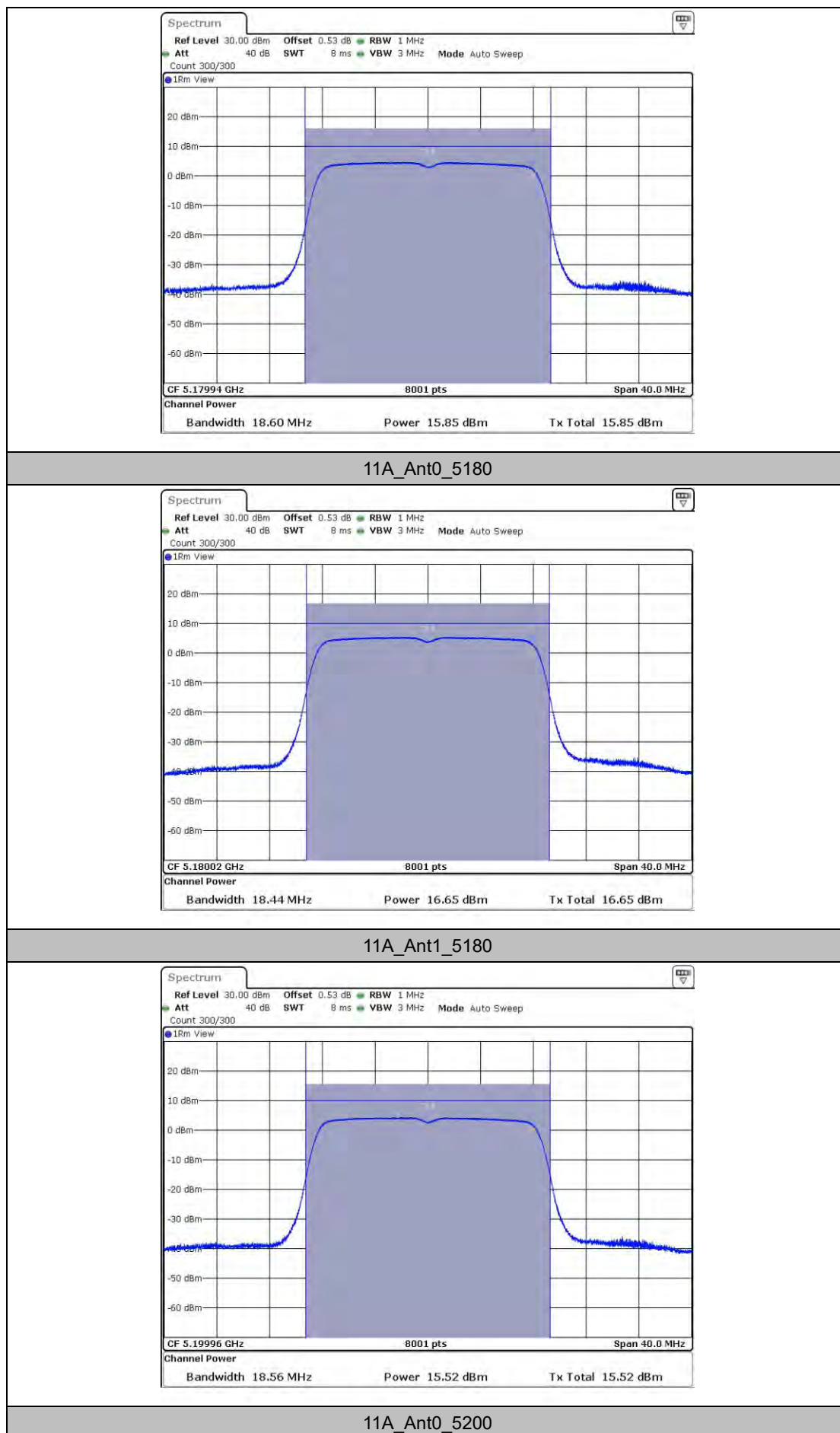
	Ant1	5670	13.32	<=23.98	PASS
	total	5670	15.2	<=23.98	PASS
	Ant0	5755	12.08	<=30	PASS
	Ant1	5755	14.56	<=30	PASS
	total	5755	16.5	<=30	PASS
	Ant0	5795	12.45	<=30	PASS
	Ant1	5795	14.63	<=30	PASS
	total	5795	16.7	<=30	PASS
11AC20MIMO	Ant0	5180	11.31	<=23.98	PASS
	Ant1	5180	13.25	<=23.98	PASS
	total	5180	15.4	<=23.98	PASS
	Ant0	5200	11.16	<=23.98	PASS
	Ant1	5200	13.01	<=23.98	PASS
	total	5200	15.2	<=23.98	PASS
	Ant0	5240	11.25	<=23.98	PASS
	Ant1	5240	12.74	<=23.98	PASS
	total	5240	15.1	<=23.98	PASS
	Ant0	5260	13.76	<=23.87	PASS
	Ant1	5260	13.85	<=23.86	PASS
	total	5260	16.8	<=23.86	PASS
	Ant0	5280	13.90	<=23.90	PASS
	Ant1	5280	13.72	<=23.87	PASS
	total	5280	16.8	<=23.87	PASS
	Ant0	5320	13.77	<=23.85	PASS
	Ant1	5320	13.67	<=23.86	PASS
	total	5320	16.7	<=23.86	PASS
	Ant0	5500	13.66	<=23.88	PASS
	Ant1	5500	13.77	<=23.87	PASS
	total	5500	16.7	<=23.87	PASS
	Ant0	5580	12.84	<=23.87	PASS
	Ant1	5580	11.30	<=23.87	PASS
	total	5580	15.1	<=23.87	PASS
	Ant0	5700	11.87	<=23.89	PASS
	Ant1	5700	13.43	<=23.84	PASS
	total	5700	15.7	<=23.84	PASS
	Ant0	5745	11.52	<=30	PASS
	Ant1	5745	14.76	<=30	PASS
	total	5745	16.4	<=30	PASS
	Ant0	5785	11.22	<=30	PASS
	Ant1	5785	14.95	<=30	PASS
	total	5785	16.5	<=30	PASS
	Ant0	5825	11.22	<=30	PASS
	Ant1	5825	14.99	<=30	PASS
	total	5825	16.5	<=30	PASS
11AC40MIMO	Ant0	5190	13.55	<=23.98	PASS
	Ant1	5190	13.48	<=23.98	PASS

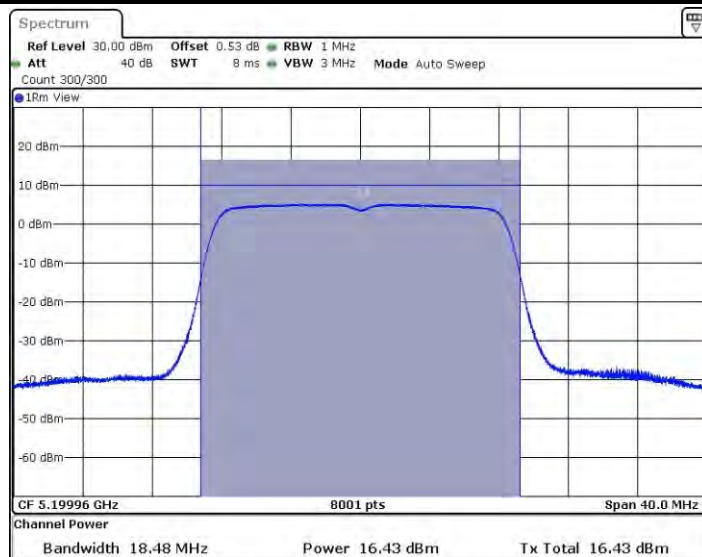
	total	5190	16.5	<=23.98	PASS
	Ant0	5230	13.38	<=23.98	PASS
	Ant1	5230	13.29	<=23.98	PASS
	total	5230	16.3	<=23.98	PASS
	Ant0	5270	14.16	<=23.98	PASS
	Ant1	5270	14.02	<=23.98	PASS
	total	5270	17.1	<=23.98	PASS
	Ant0	5310	14.08	<=23.98	PASS
	Ant1	5310	14.01	<=23.98	PASS
	total	5310	17.1	<=23.98	PASS
	Ant0	5510	13.96	<=23.98	PASS
	Ant1	5510	13.87	<=23.98	PASS
	total	5510	16.9	<=23.98	PASS
	Ant0	5550	13.92	<=23.98	PASS
	Ant1	5550	13.85	<=23.98	PASS
	total	5550	16.9	<=23.98	PASS
	Ant0	5670	12.99	<=23.98	PASS
	Ant1	5670	12.91	<=23.98	PASS
	total	5670	16.0	<=23.98	PASS
	Ant0	5755	12.32	<=30	PASS
	Ant1	5755	12.22	<=30	PASS
	total	5755	15.3	<=30	PASS
	Ant0	5795	12.04	<=30	PASS
	Ant1	5795	11.97	<=30	PASS
	total	5795	15.0	<=30	PASS
11AC80MIMO	Ant0	5210	13.18	<=23.98	PASS
	Ant1	5210	14.11	<=23.98	PASS
	total	5210	16.7	<=23.98	PASS
	Ant0	5290	13.41	<=23.98	PASS
	Ant1	5290	13.67	<=23.98	PASS
	total	5290	16.6	<=23.98	PASS
	Ant0	5530	13.49	<=23.98	PASS
	Ant1	5530	13.61	<=23.98	PASS
	total	5530	16.6	<=23.98	PASS
	Ant0	5610	12.05	<=23.98	PASS
	Ant1	5610	13.31	<=23.98	PASS
	total	5610	15.7	<=23.98	PASS
	Ant0	5775	10.54	<=30	PASS
	Ant1	5775	13.84	<=30	PASS
	total	5775	15.5	<=30	PASS
11AX20MIMO	Ant0	5180	11.58	<=23.98	PASS
	Ant1	5180	11.57	<=23.98	PASS
	total	5180	14.6	<=23.98	PASS
	Ant0	5200	11.14	<=23.98	PASS
	Ant1	5200	12.36	<=23.98	PASS
	total	5200	14.8	<=23.98	PASS

	Ant0	5240	11.25	<=23.98	PASS
	Ant1	5240	12.09	<=23.98	PASS
	total	5240	14.7	<=23.98	PASS
	Ant0	5260	11.82	<=23.98	PASS
	Ant1	5260	11.82	<=23.98	PASS
	total	5260	14.8	<=23.98	PASS
	Ant0	5280	12.00	<=23.98	PASS
	Ant1	5280	11.68	<=23.98	PASS
	total	5280	14.9	<=23.98	PASS
	Ant0	5320	11.94	<=23.98	PASS
	Ant1	5320	12.01	<=23.98	PASS
	total	5320	15.0	<=23.98	PASS
	Ant0	5500	11.92	<=23.98	PASS
	Ant1	5500	11.59	<=23.98	PASS
	total	5500	14.8	<=23.98	PASS
	Ant0	5580	11.29	<=23.98	PASS
	Ant1	5580	12.62	<=23.98	PASS
	total	5580	15.0	<=23.98	PASS
	Ant0	5700	11.00	<=23.98	PASS
	Ant1	5700	11.57	<=23.98	PASS
	total	5700	14.3	<=23.98	PASS
	Ant0	5745	13.85	<=30	PASS
	Ant1	5745	9.61	<=30	PASS
	total	5745	15.2	<=30	PASS
	Ant0	5785	13.88	<=30	PASS
	Ant1	5785	9.89	<=30	PASS
	total	5785	15.3	<=30	PASS
	Ant0	5825	13.97	<=30	PASS
	Ant1	5825	10.21	<=30	PASS
	total	5825	15.5	<=30	PASS
11AX40MIMO	Ant0	5190	13.58	<=23.98	PASS
	Ant1	5190	14.76	<=23.98	PASS
	total	5190	17.2	<=23.98	PASS
	Ant0	5230	13.29	<=23.98	PASS
	Ant1	5230	14.54	<=23.98	PASS
	total	5230	17.0	<=23.98	PASS
	Ant0	5270	13.55	<=23.98	PASS
	Ant1	5270	14.16	<=23.98	PASS
	total	5270	16.9	<=23.98	PASS
	Ant0	5310	14.15	<=23.98	PASS
	Ant1	5310	14.01	<=23.98	PASS
	total	5310	17.1	<=23.98	PASS
	Ant0	5510	14.07	<=23.98	PASS
	Ant1	5510	14.09	<=23.98	PASS
	total	5510	17.1	<=23.98	PASS
	Ant0	5550	13.42	<=23.98	PASS

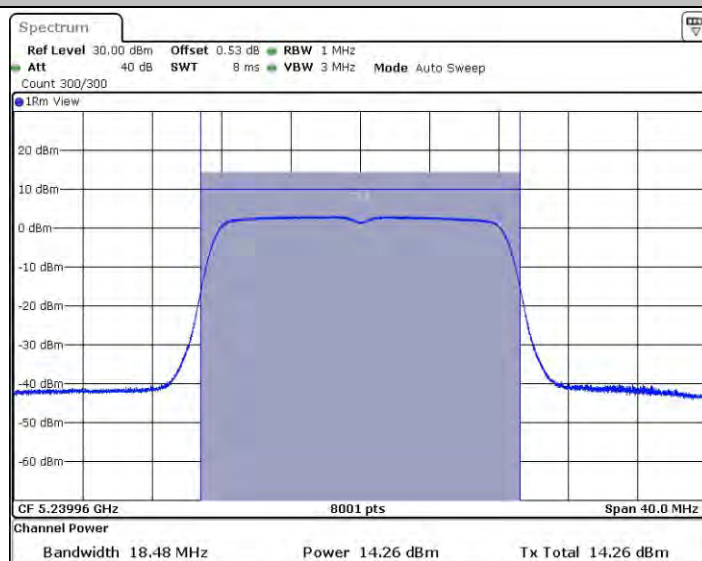
	Ant1	5550	10.74	<=23.98	PASS
	total	5550	15.3	<=23.98	PASS
	Ant0	5670	13.55	<=23.98	PASS
	Ant1	5670	13.62	<=23.98	PASS
	total	5670	16.6	<=23.98	PASS
	Ant0	5755	10.42	<=30	PASS
	Ant1	5755	13.88	<=30	PASS
	total	5755	15.5	<=30	PASS
	Ant0	5795	10.96	<=30	PASS
	Ant1	5795	13.96	<=30	PASS
	total	5795	15.7	<=30	PASS
11AX80MIMO	Ant0	5210	12.72	<=23.98	PASS
	Ant1	5210	13.63	<=23.98	PASS
	total	5210	16.2	<=23.98	PASS
	Ant0	5290	13.12	<=23.98	PASS
	Ant1	5290	13.01	<=23.98	PASS
	total	5290	16.1	<=23.98	PASS
	Ant0	5530	13.02	<=23.98	PASS
	Ant1	5530	12.97	<=23.98	PASS
	total	5530	16.0	<=23.98	PASS
	Ant0	5610	12.84	<=23.98	PASS
	Ant1	5610	12.78	<=23.98	PASS
	total	5610	15.8	<=23.98	PASS
	Ant0	5775	10.88	<=30	PASS
	Ant1	5775	13.97	<=30	PASS
	total	5775	15.7	<=30	PASS

Test Graphs

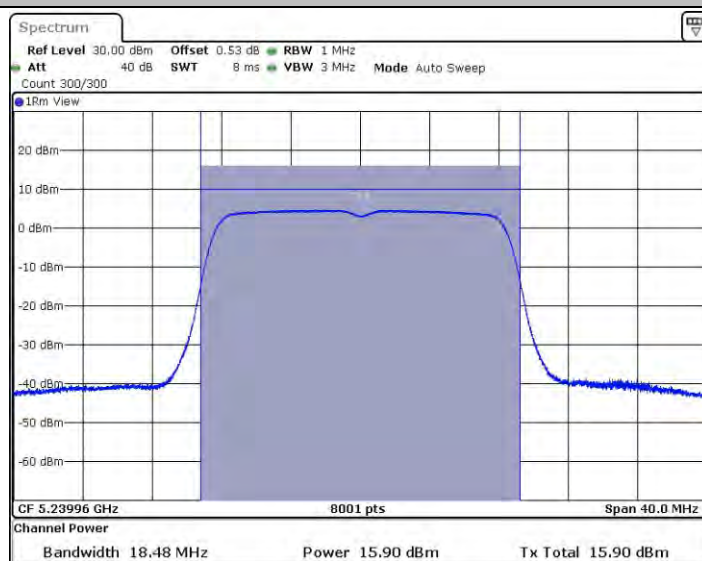




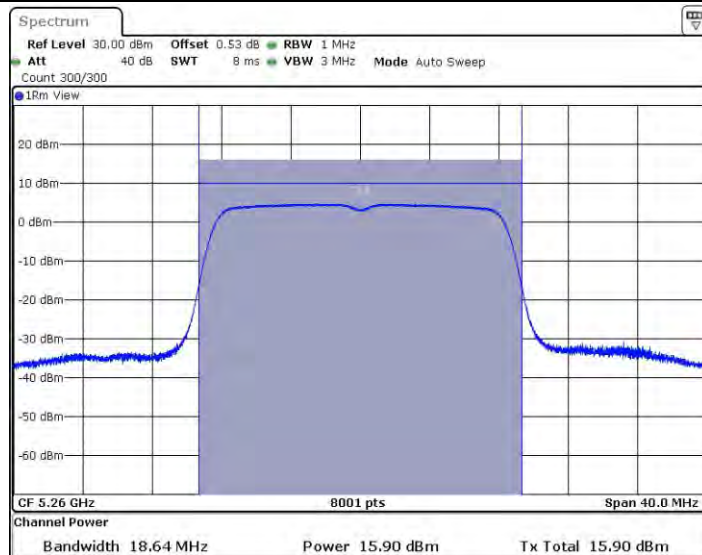
11A_Ant1_5200



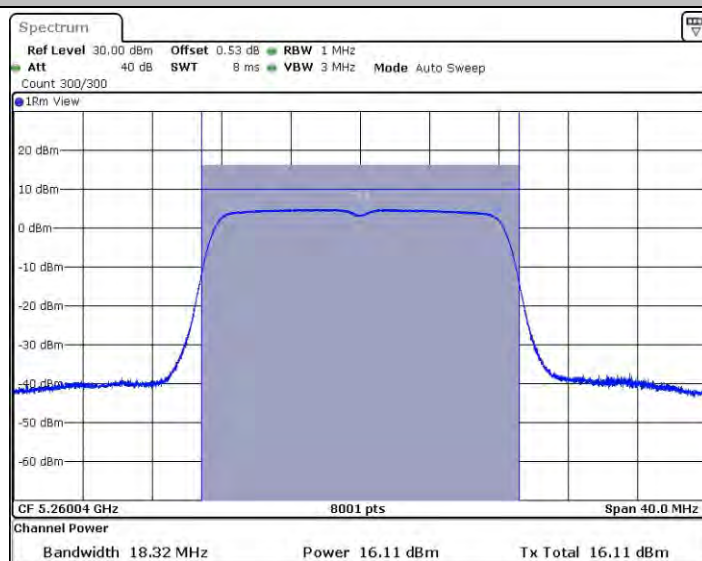
11A_Ant0_5240



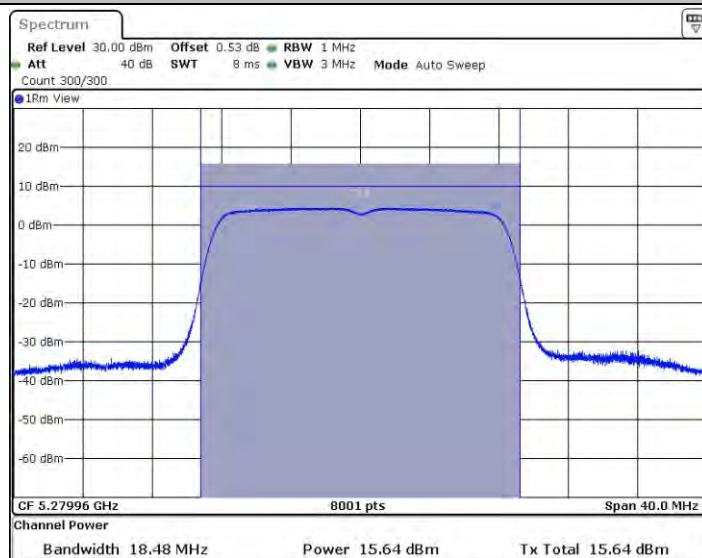
11A_Ant1_5240



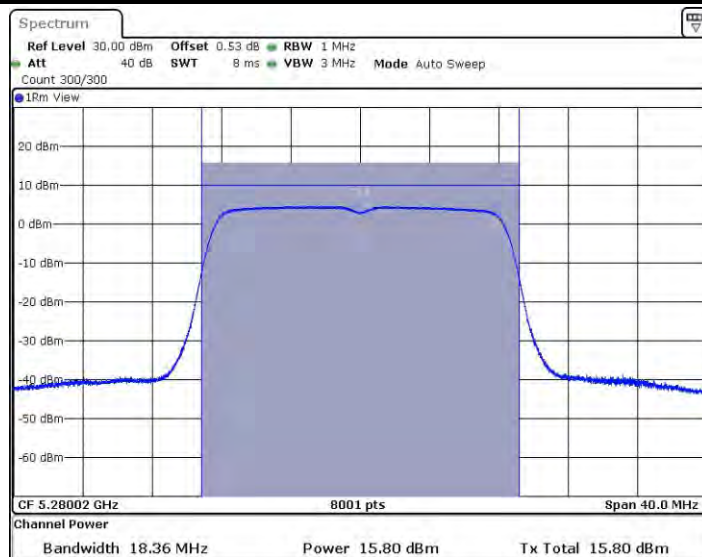
11A_Ant0_5260



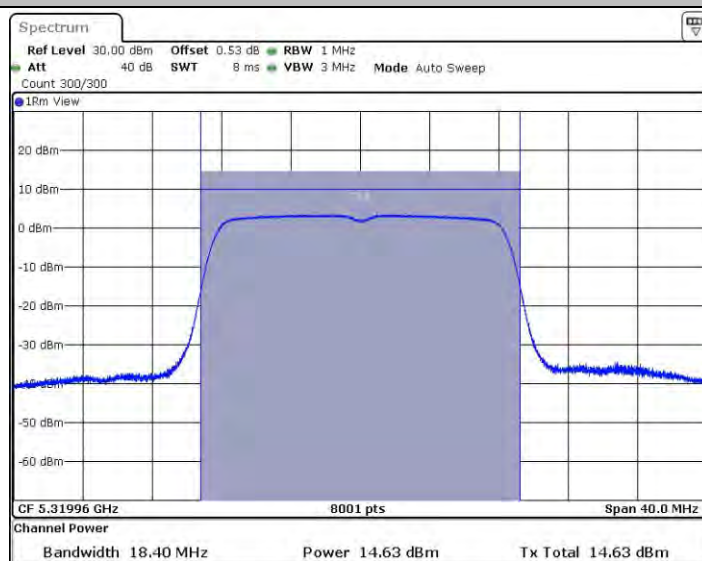
11A_Ant1_5260



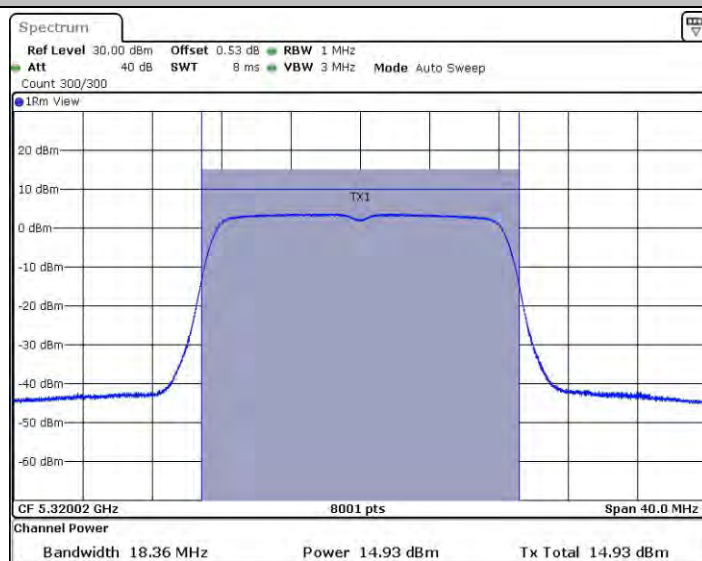
11A_Ant0_5280



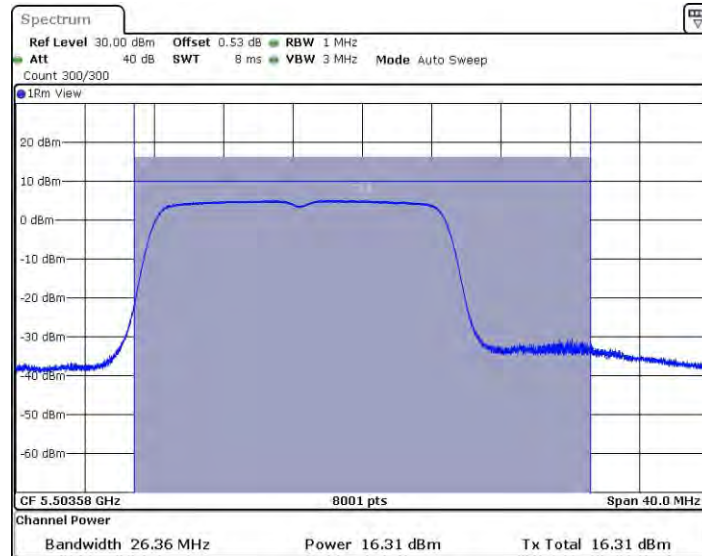
11A_Ant1_5280



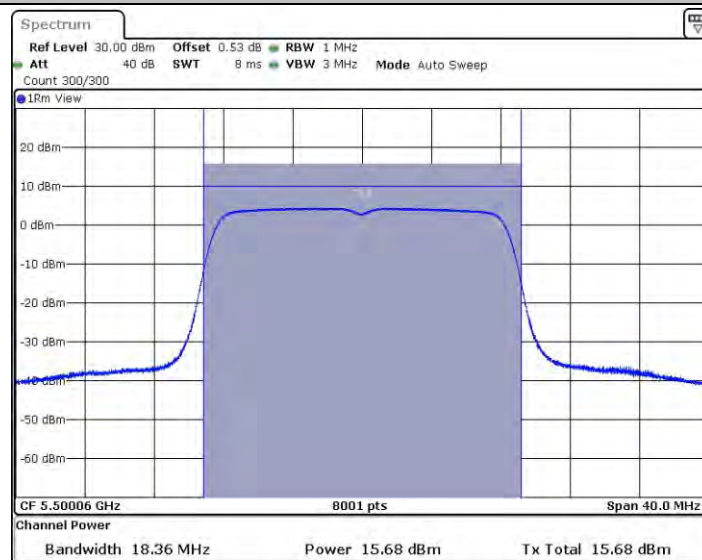
11A_Ant0_5320



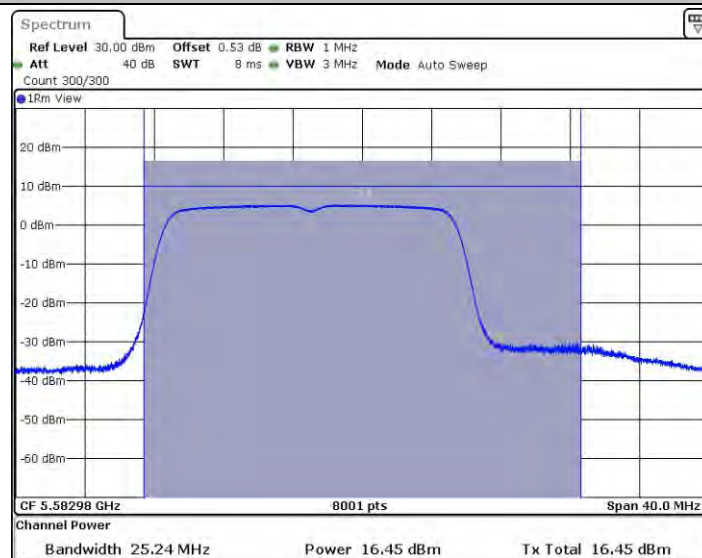
11A_Ant1_5320



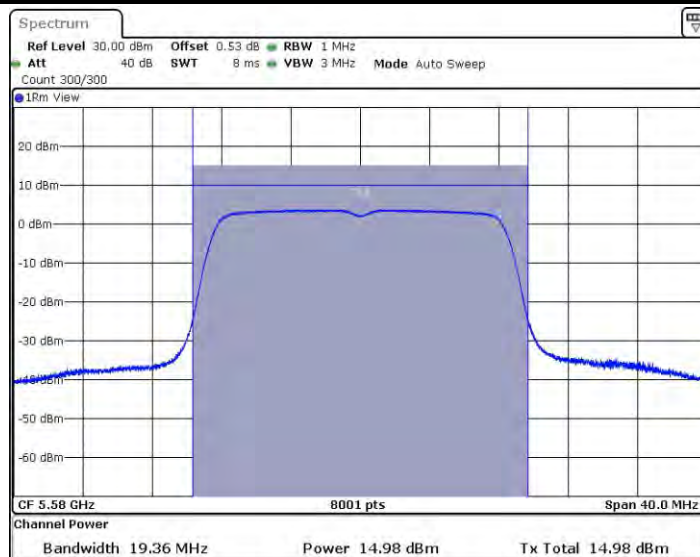
11A_Ant0_5500



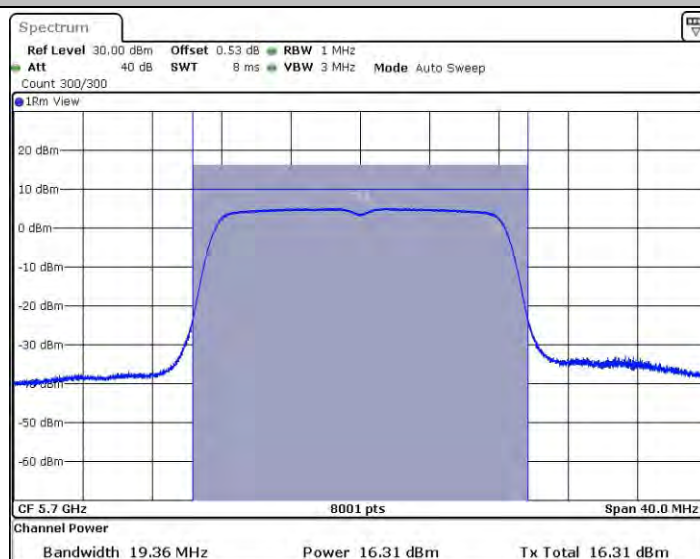
11A_Ant1_5500



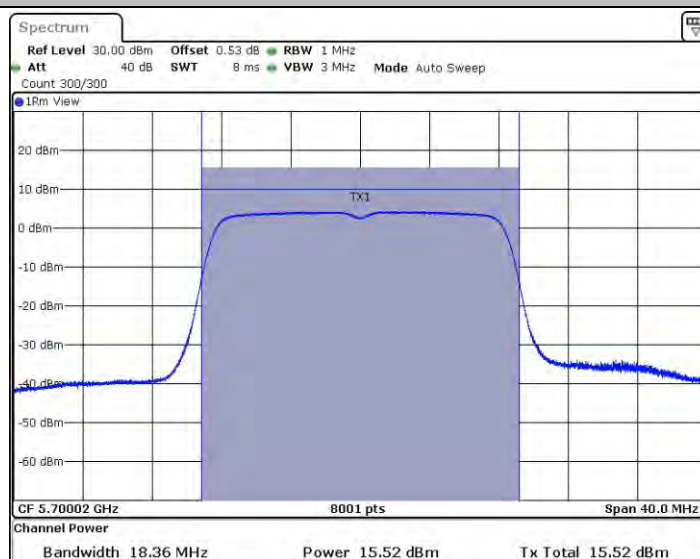
11A_Ant0_5580



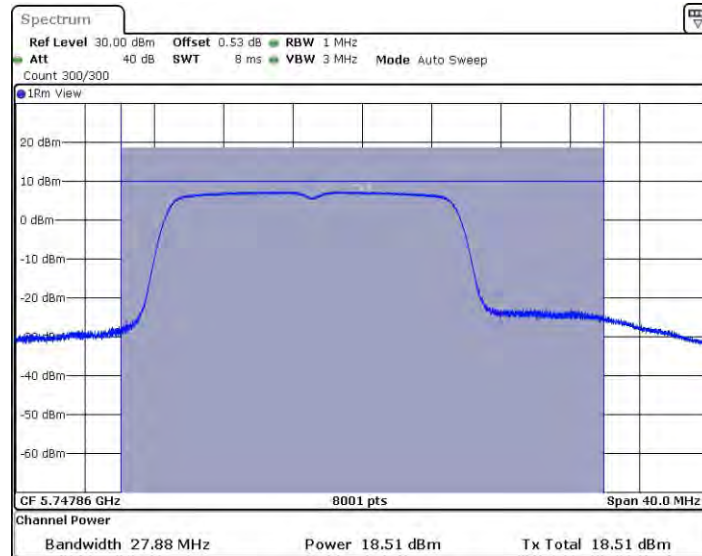
11A_Ant1_5580



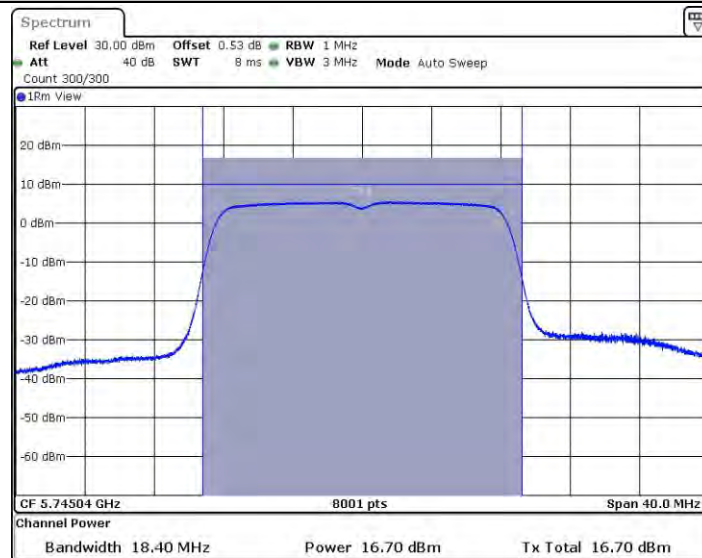
11A_Ant0_5700



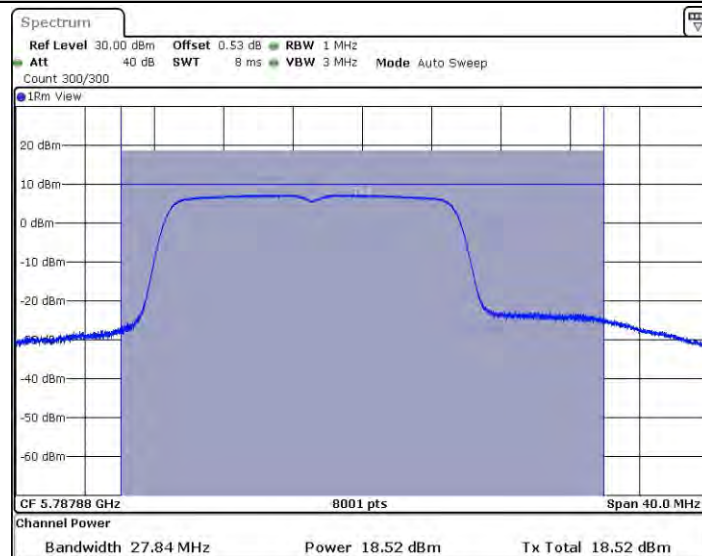
11A_Ant1_5700



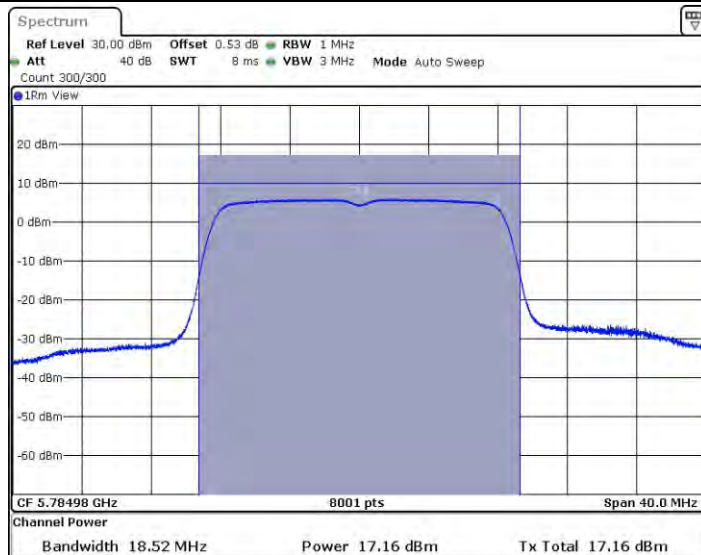
11A_Ant0_5745



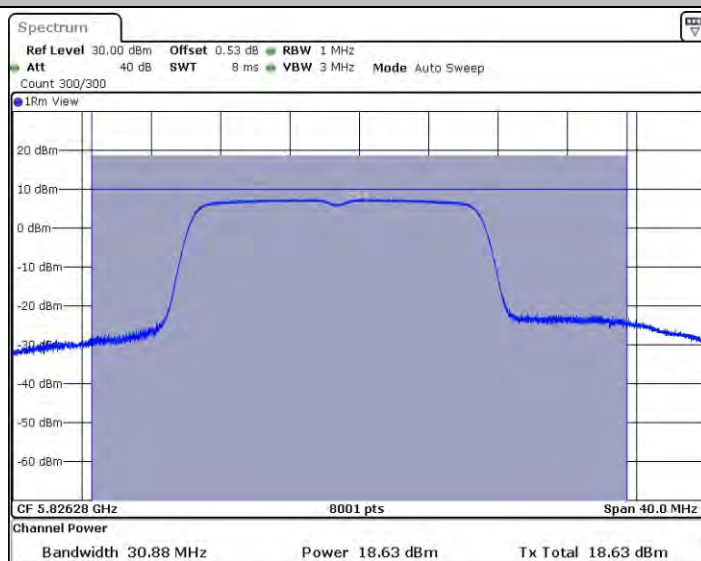
11A_Ant1_5745



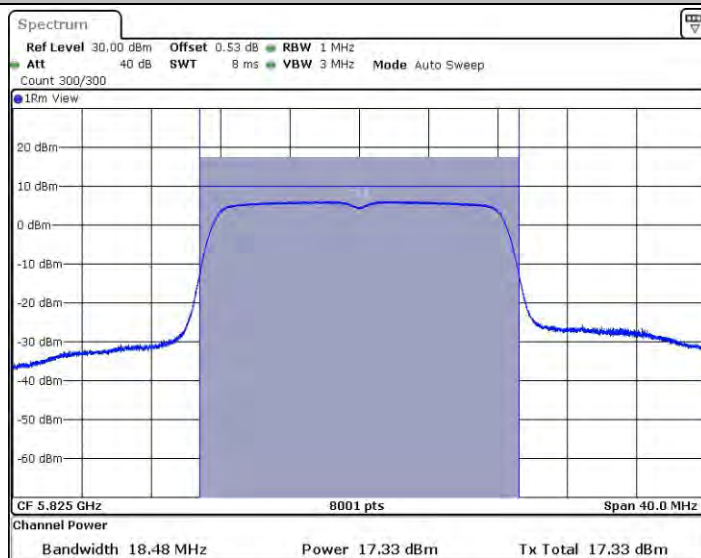
11A_Ant0_5785



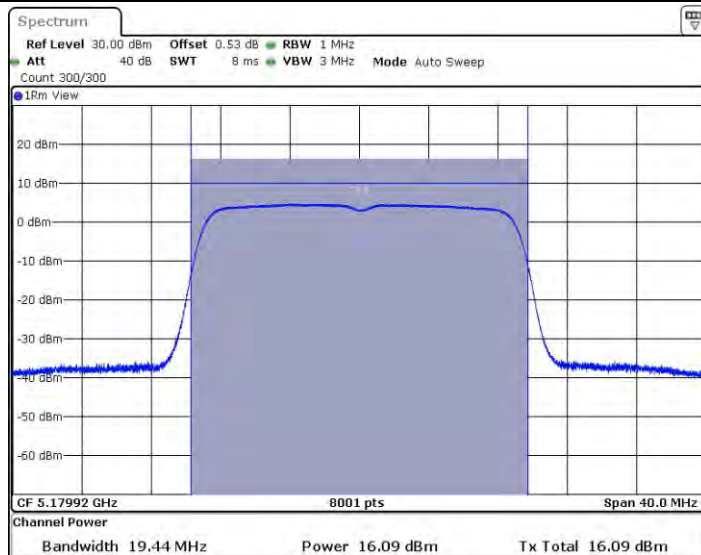
11A_Ant1_5785



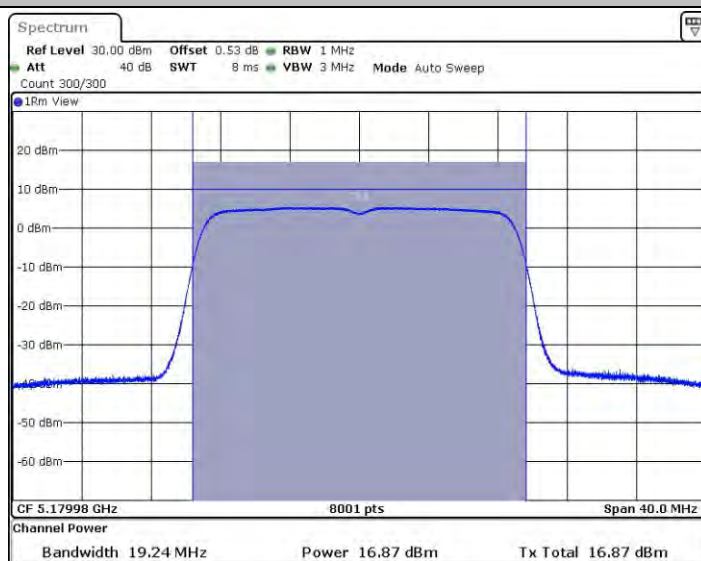
11A_Ant0_5825



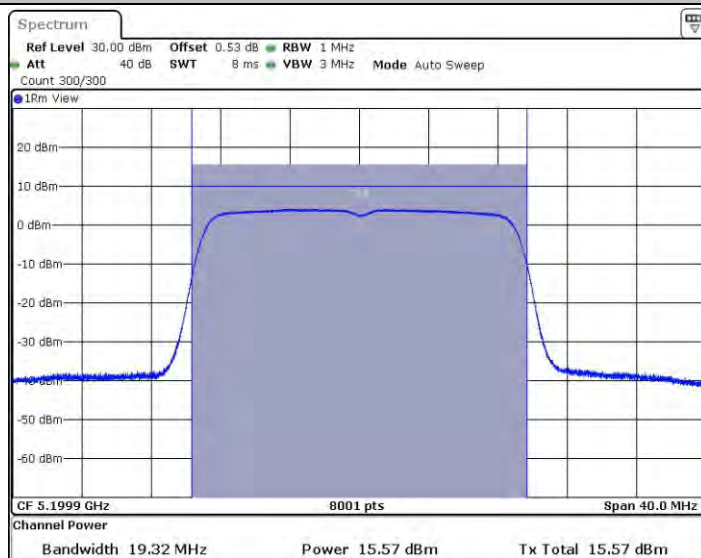
11A_Ant1_5825



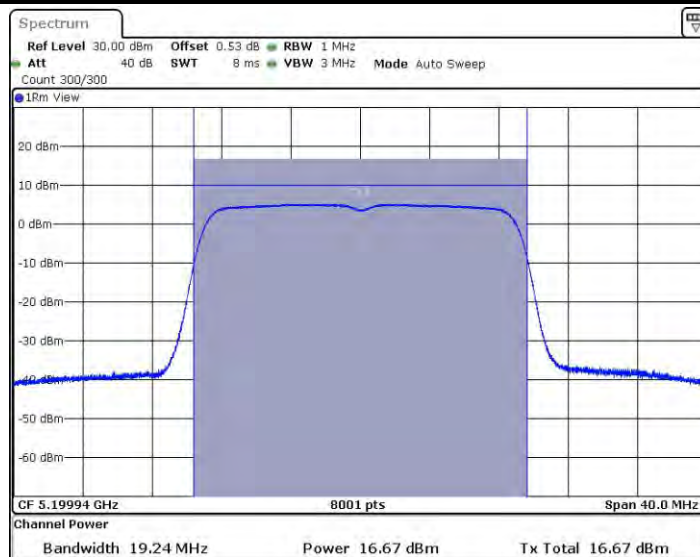
11N20SISO_Ant0_5180



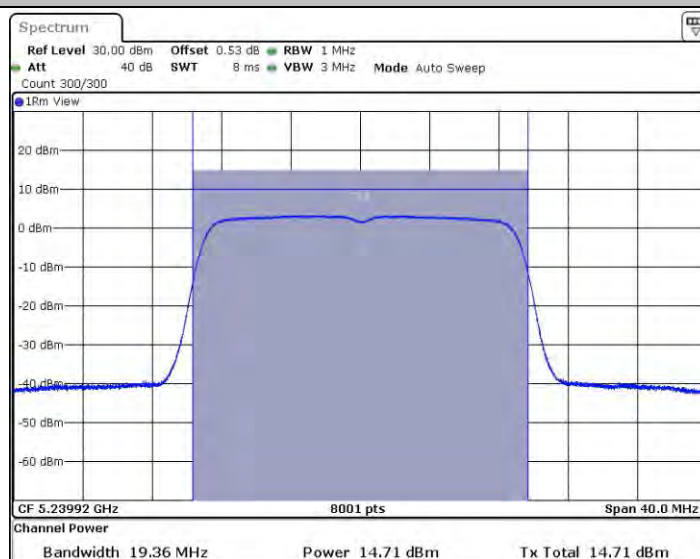
11N20SISO_Ant1_5180



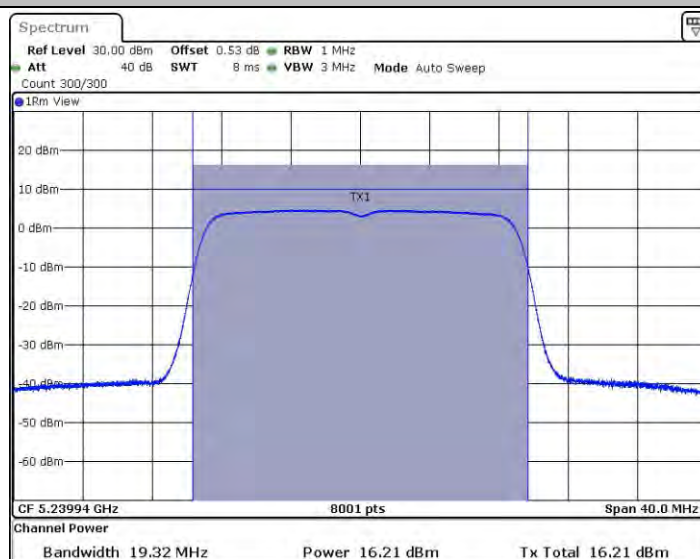
11N20SISO_Ant0_5200



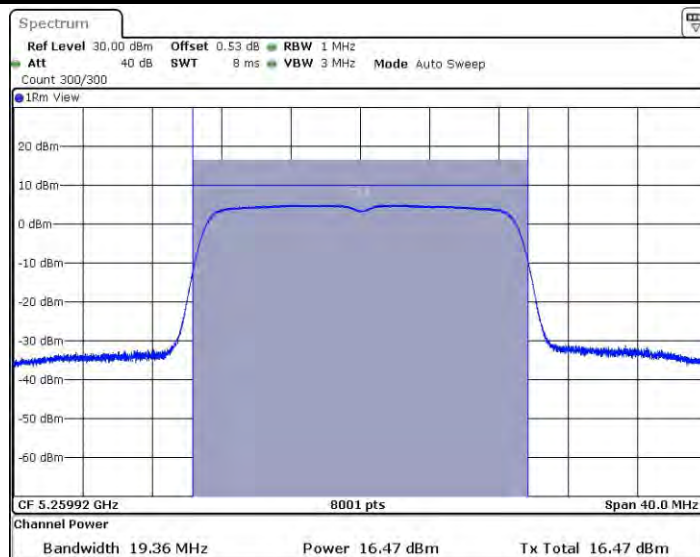
11N20SISO_Ant1_5200



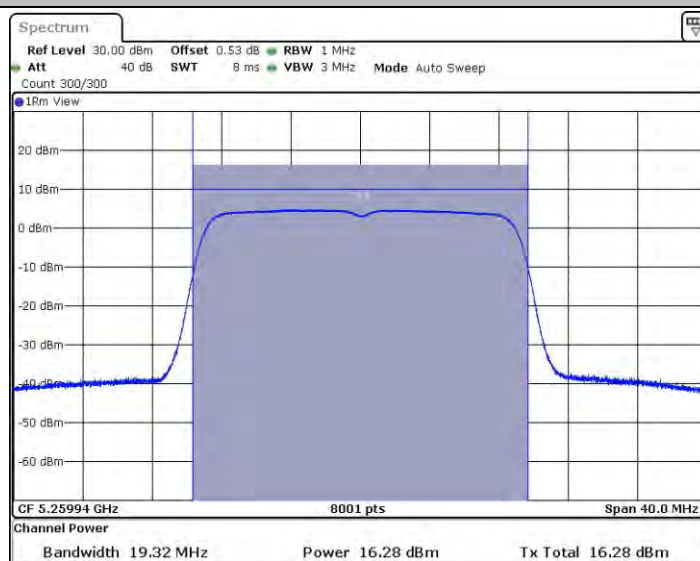
11N20SISO_Ant0_5240



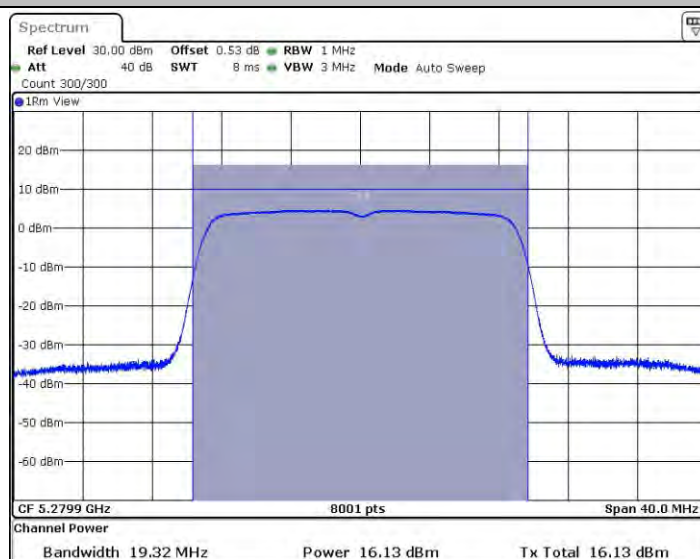
11N20SISO_Ant1_5240



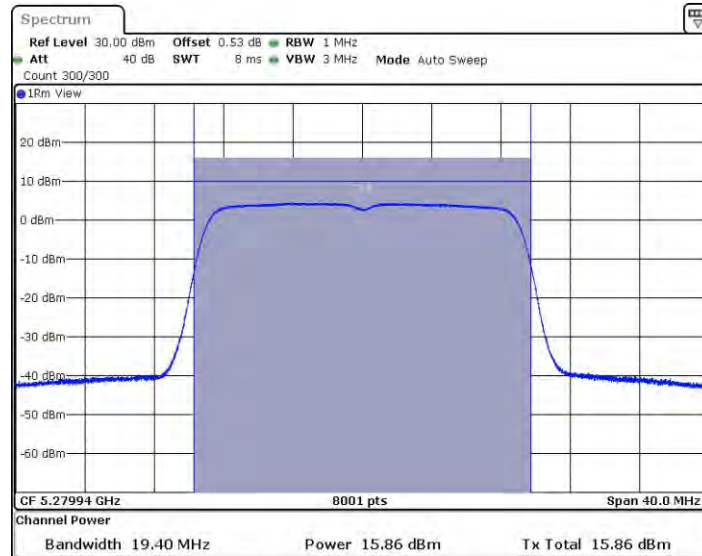
11N20SISO_Ant0_5260



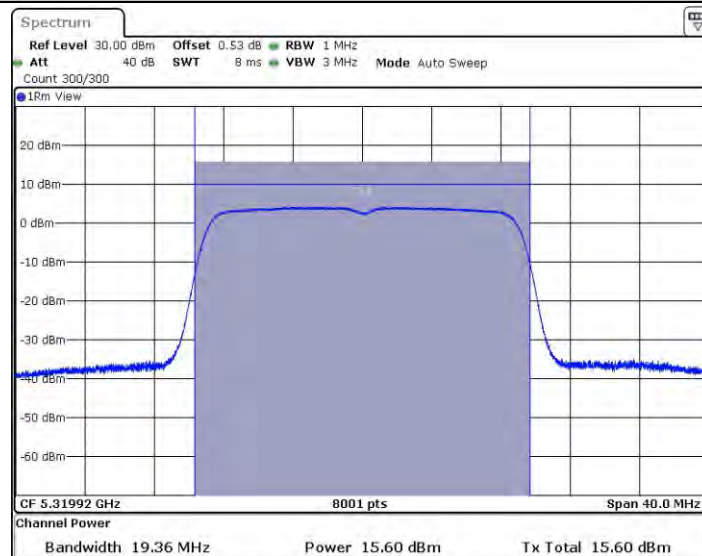
11N20SISO_Ant1_5260



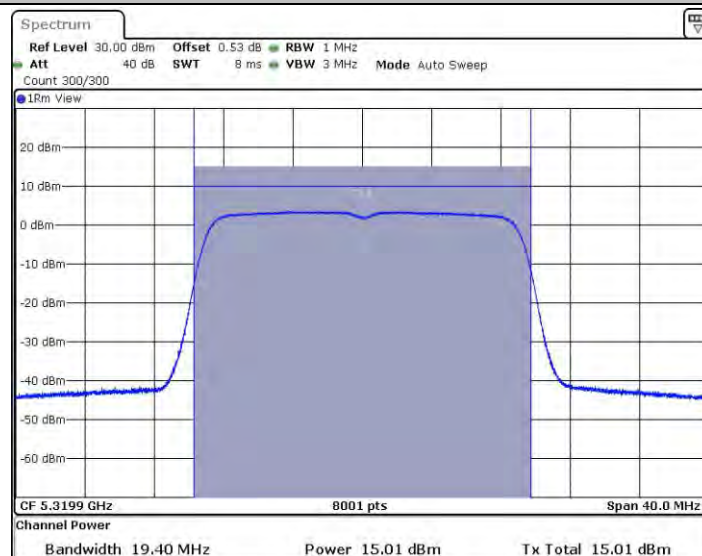
11N20SISO_Ant0_5280



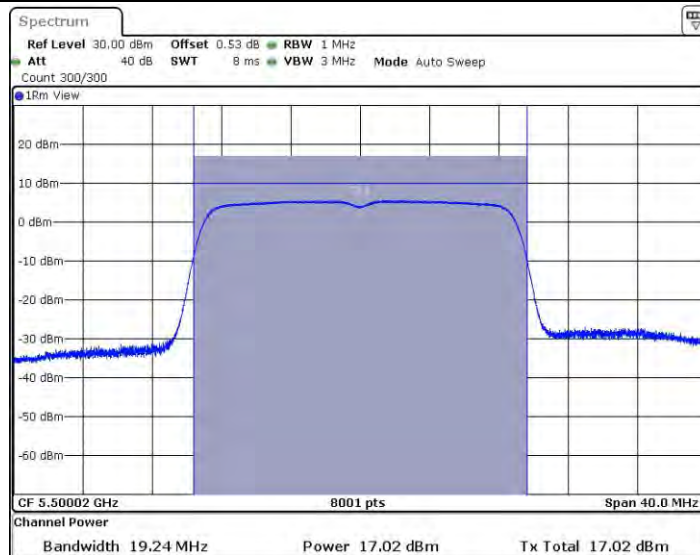
11N20SISO_Ant1_5280



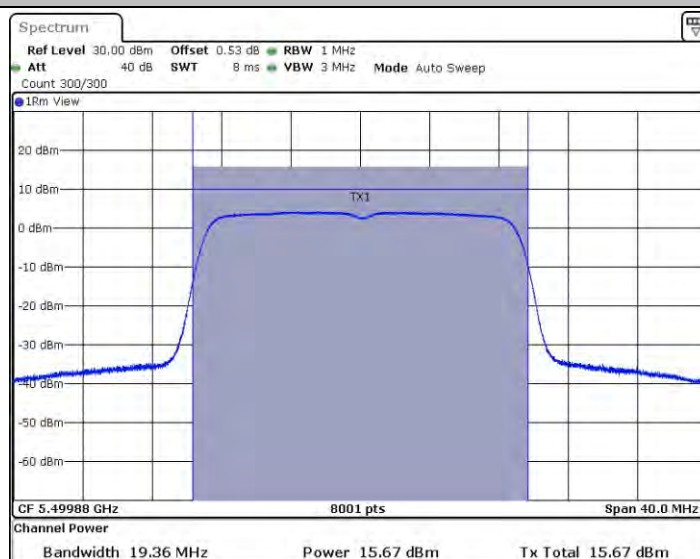
11N20SISO_Ant0_5320



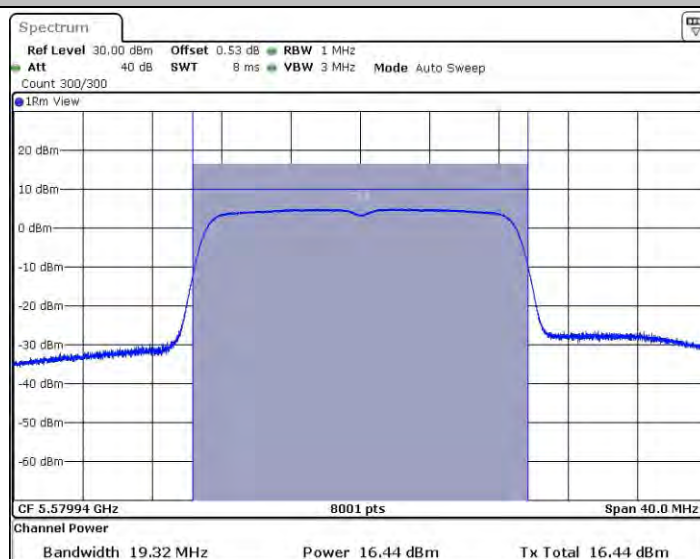
11N20SISO_Ant1_5320



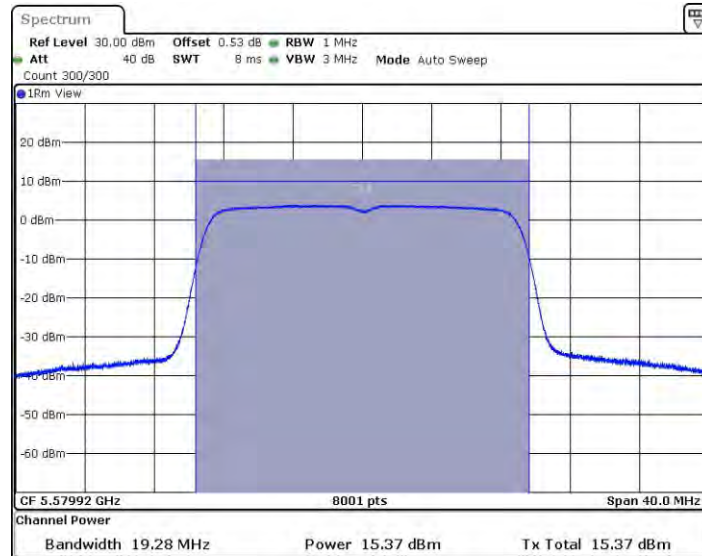
11N20SISO_Ant0_5500



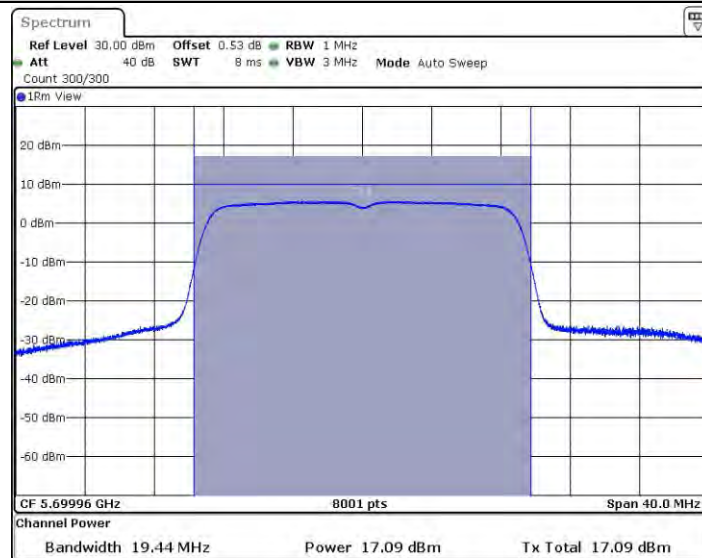
11N20SISO_Ant1_5500



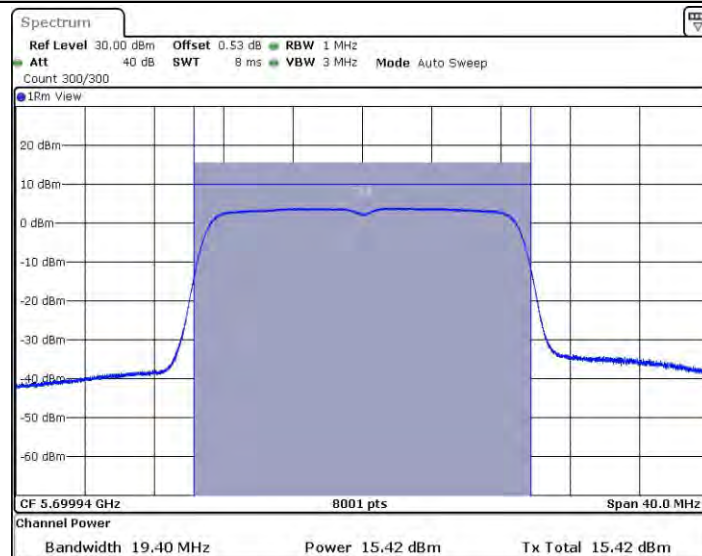
11N20SISO_Ant0_5580



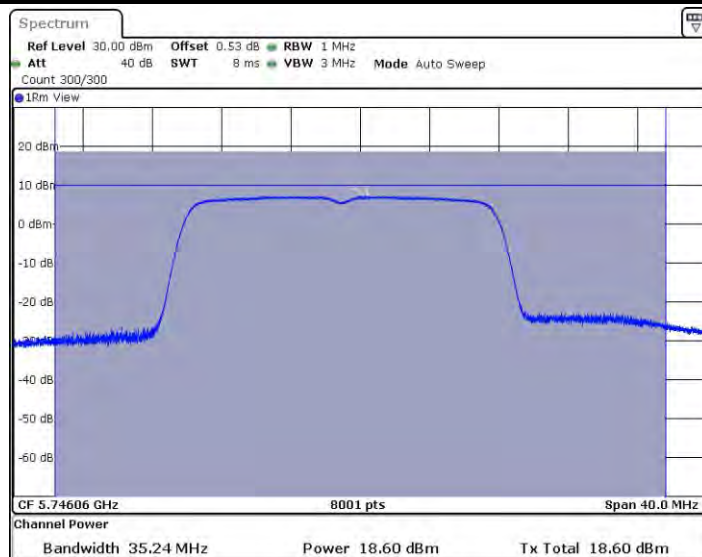
11N20SISO_Ant1_5580



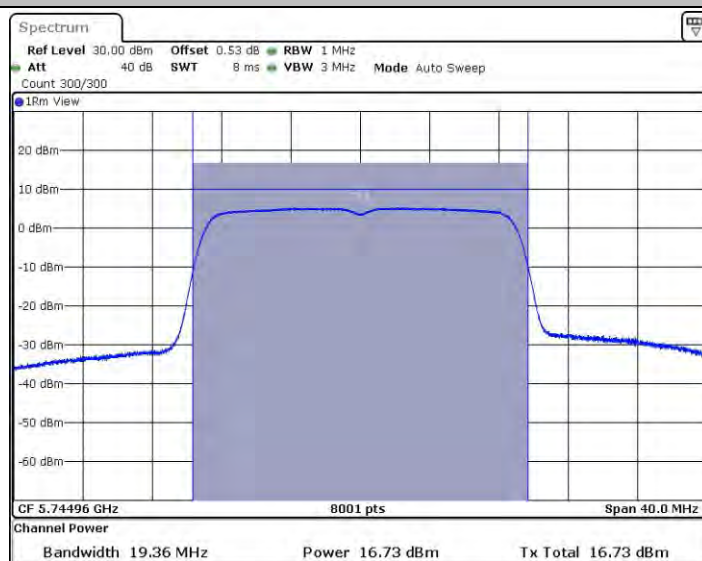
11N20SISO_Ant0_5700



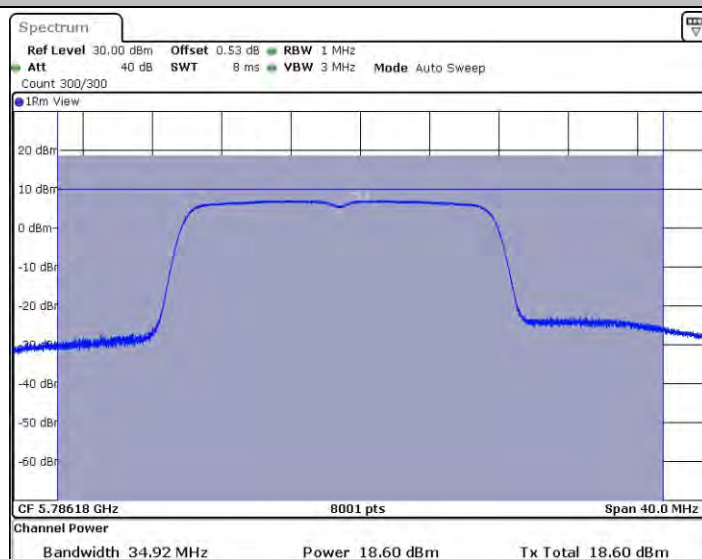
11N20SISO_Ant1_5700



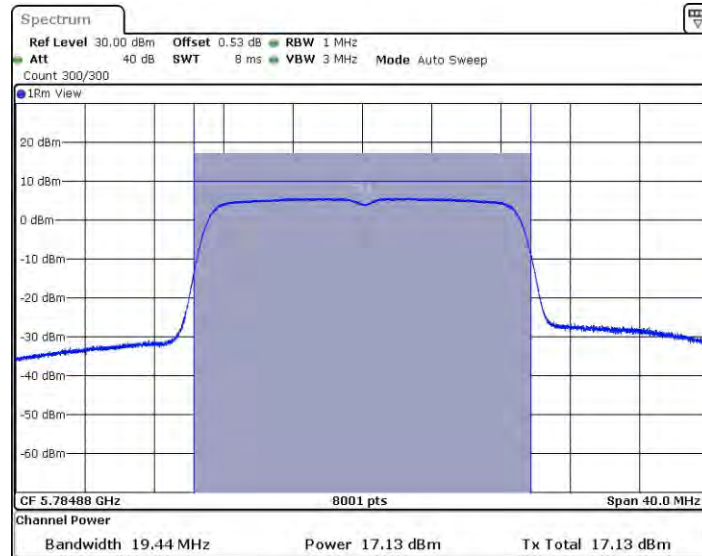
11N20SISO_Ant0_5745



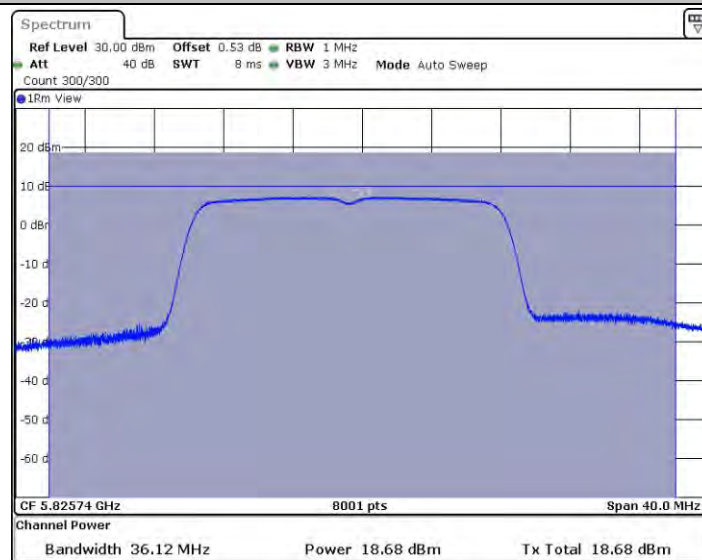
11N20SISO_Ant1_5745



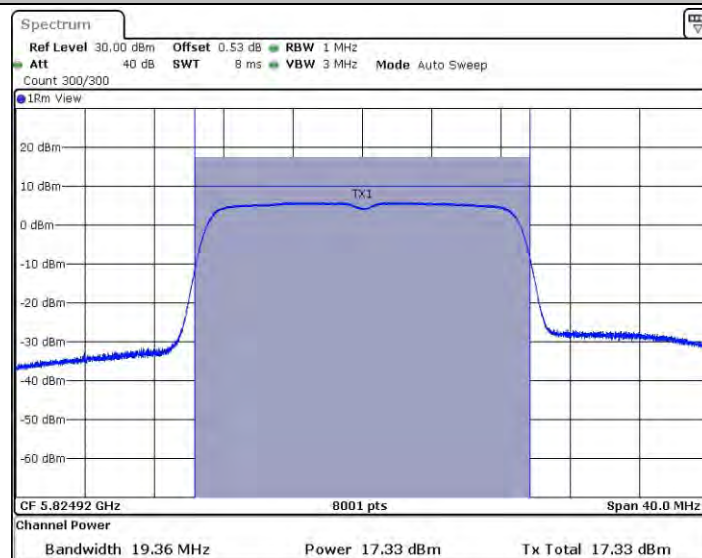
11N20SISO_Ant0_5785



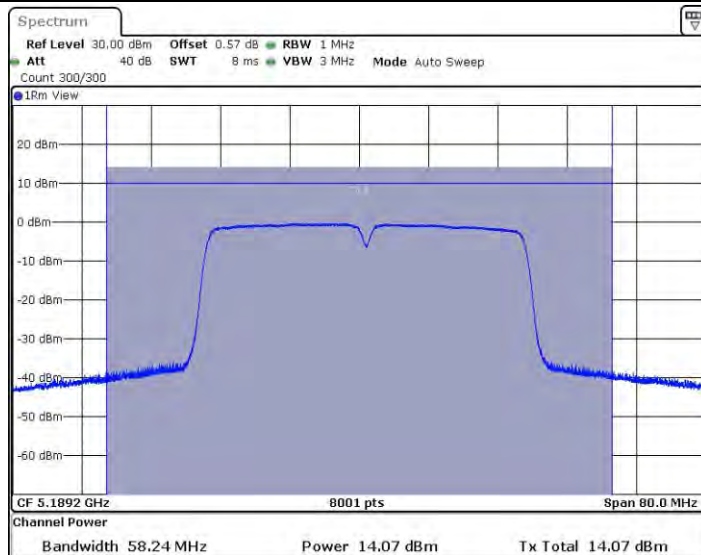
11N20SISO_Ant1_5785



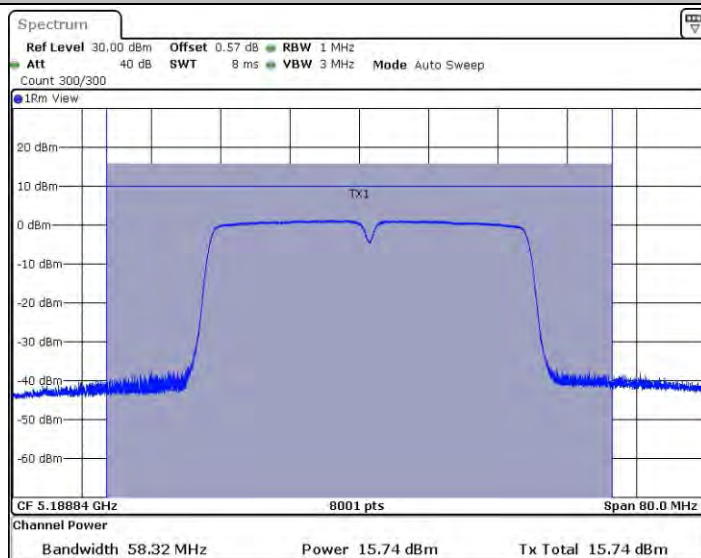
11N20SISO_Ant0_5825



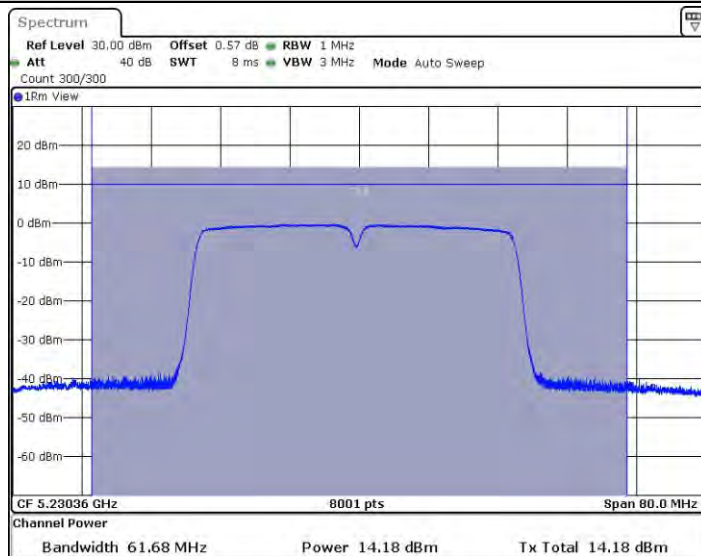
11N20SISO_Ant1_5825



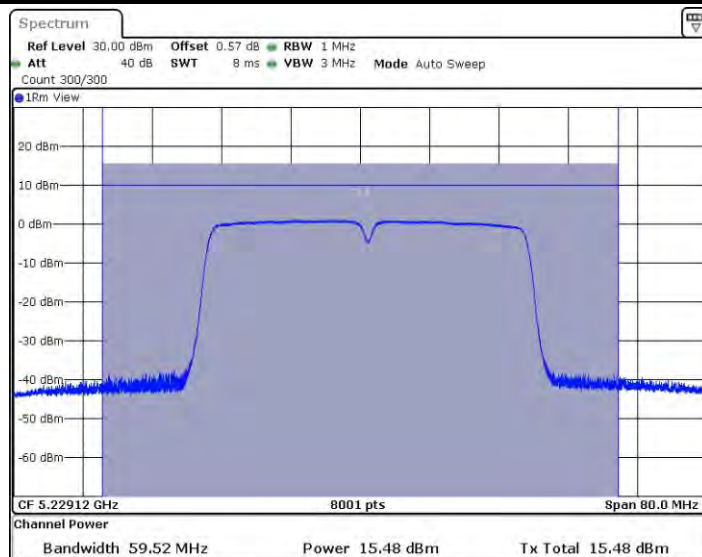
11N40SISO_Ant0_5190



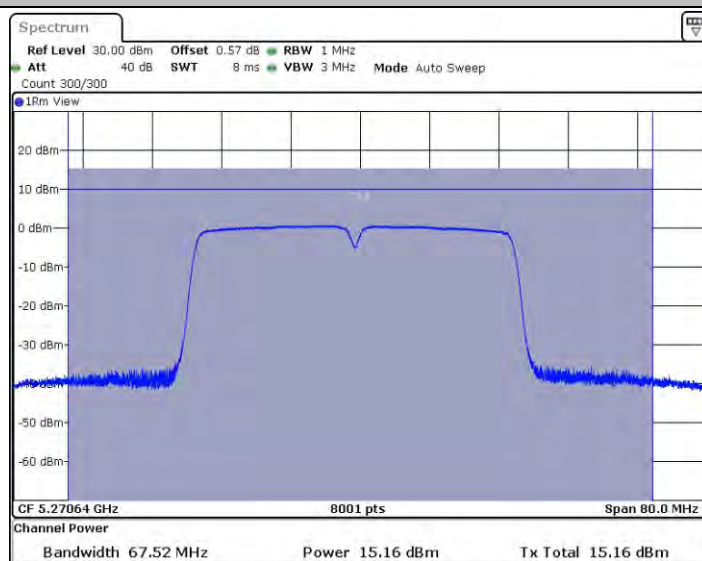
11N40SISO_Ant1_5190



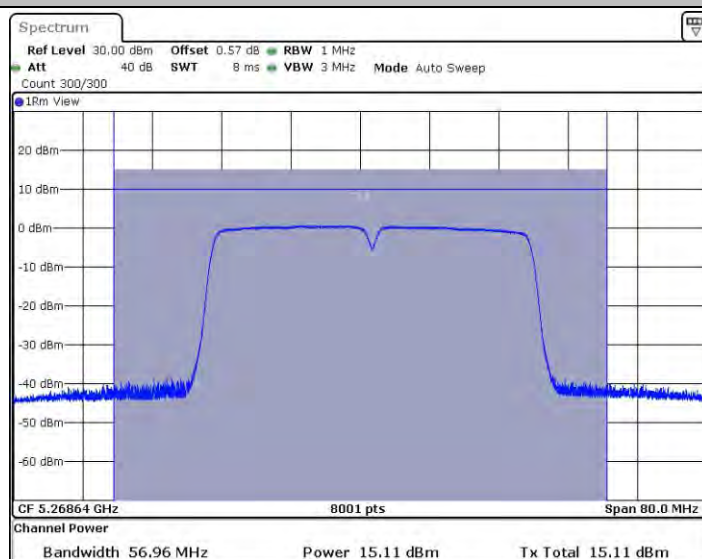
11N40SISO_Ant0_5230



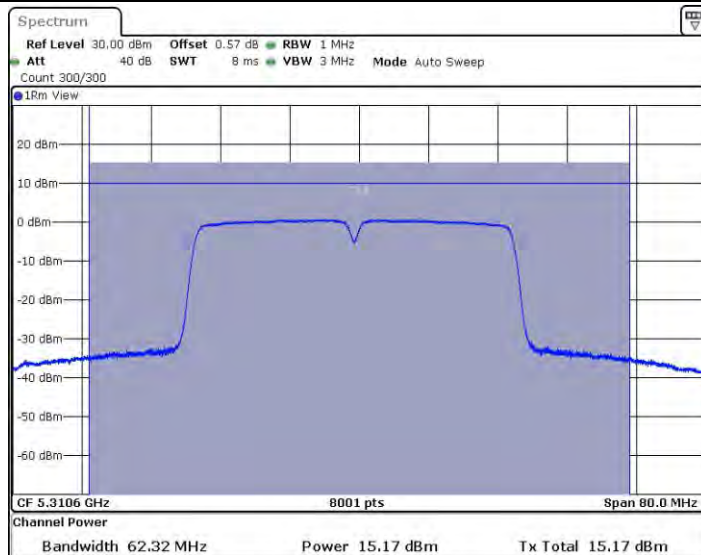
11N40SISO_Ant1_5230



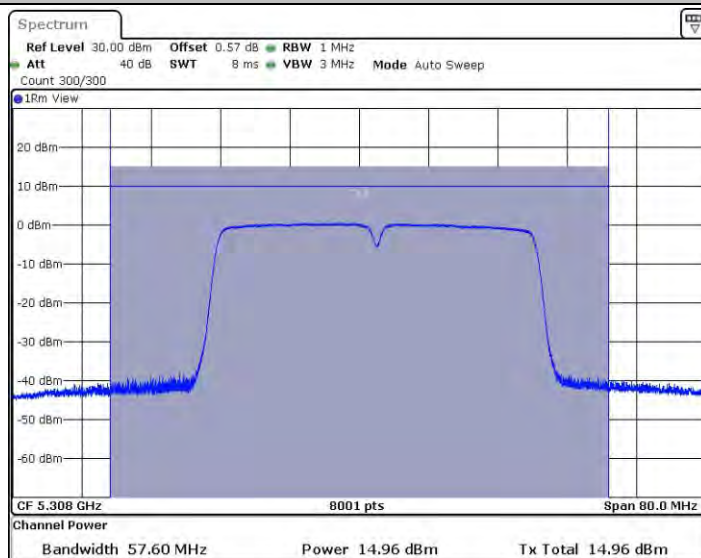
11N40SISO_Ant0_5270



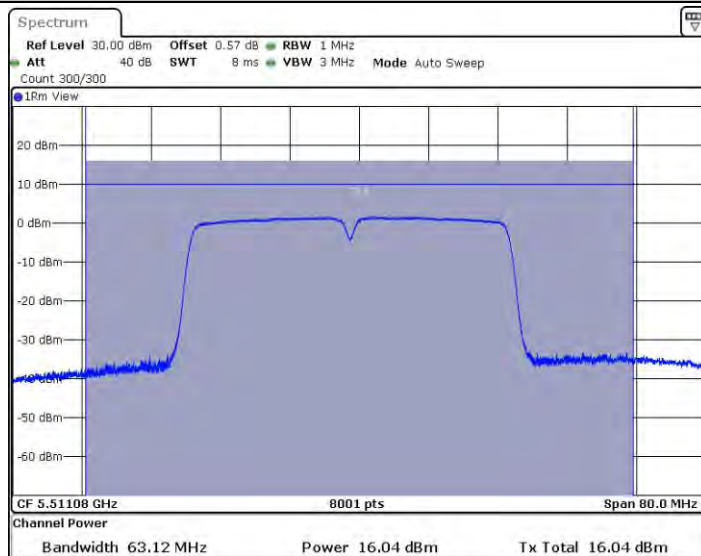
11N40SISO_Ant1_5270



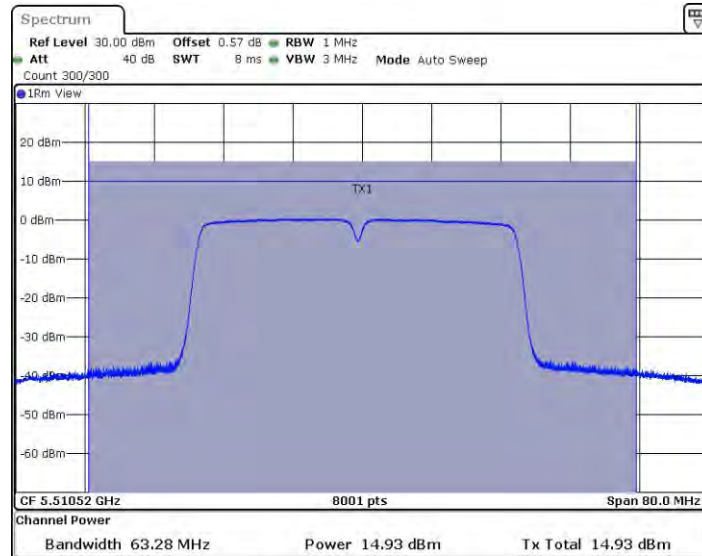
11N40SISO_Ant0_5310



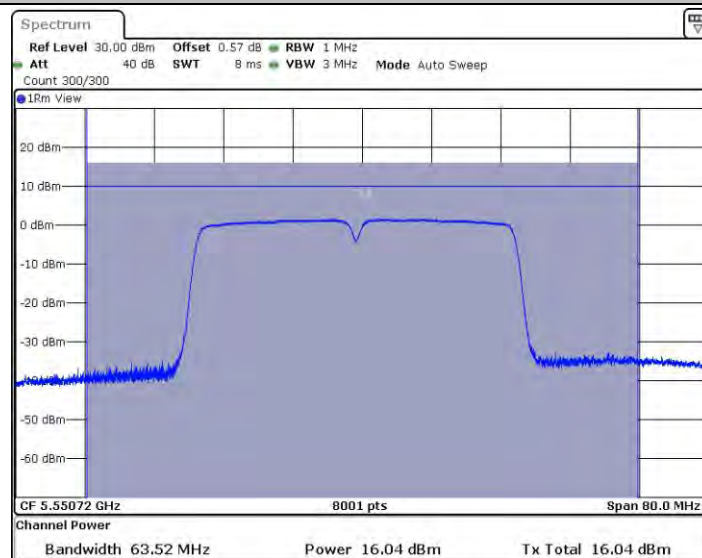
11N40SISO_Ant1_5310



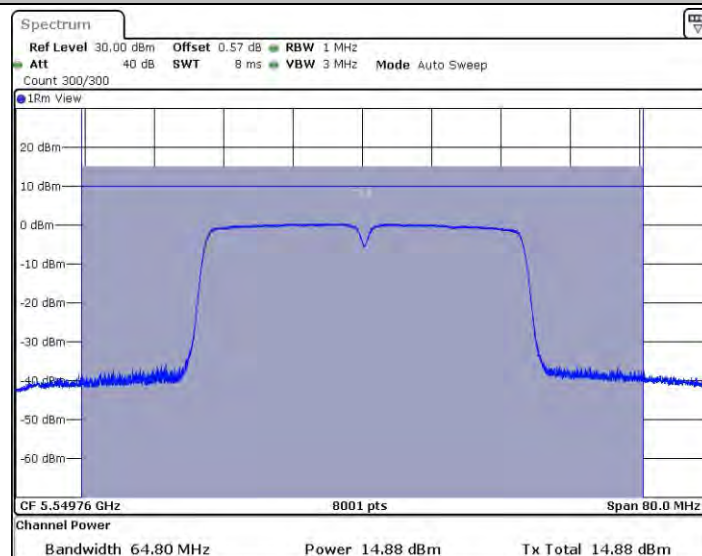
11N40SISO_Ant0_5510



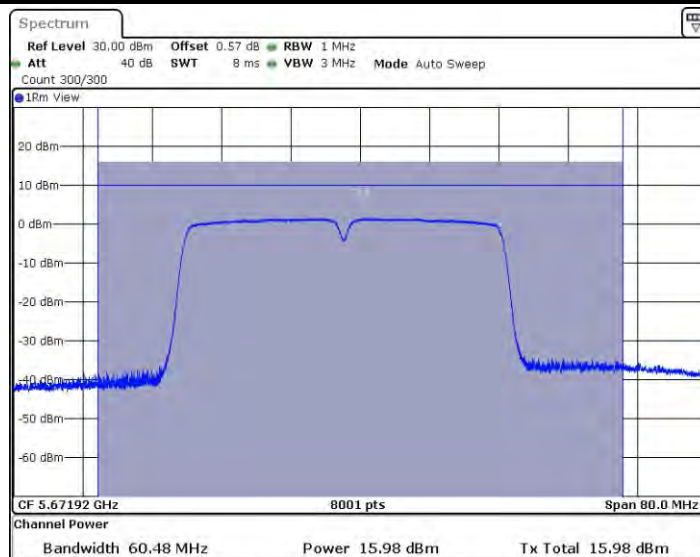
11N40SISO_Ant1_5510



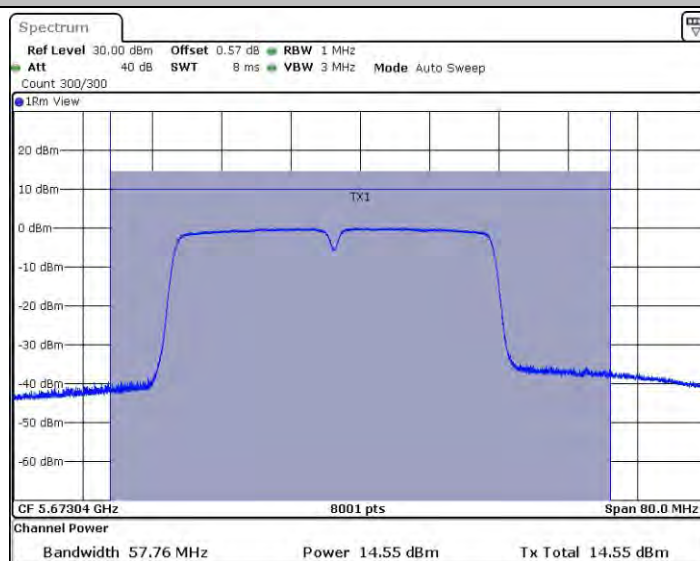
11N40SISO_Ant0_5550



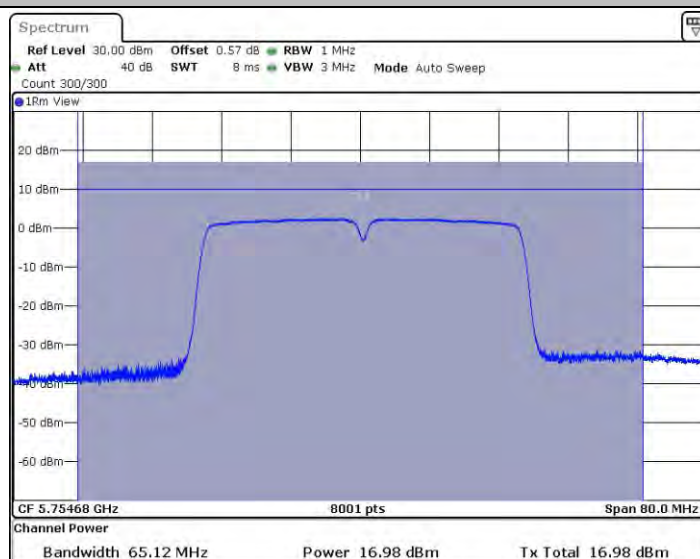
11N40SISO_Ant1_5550



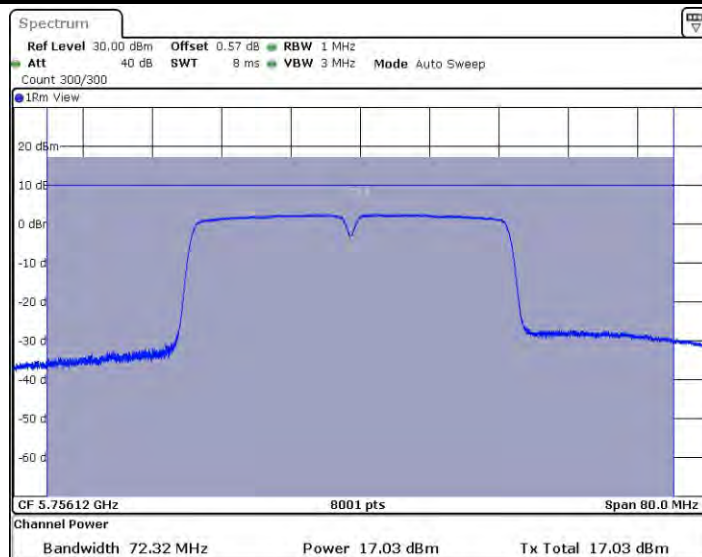
11N40SISO_Ant0_5670



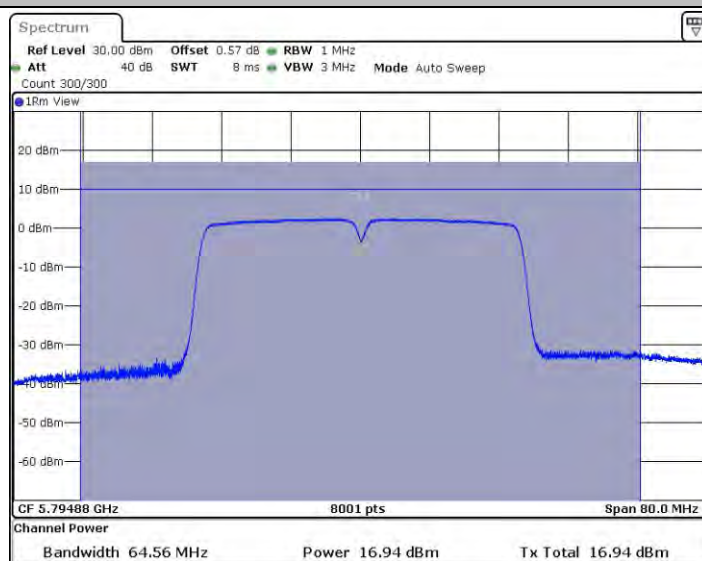
11N40SISO_Ant1_5670



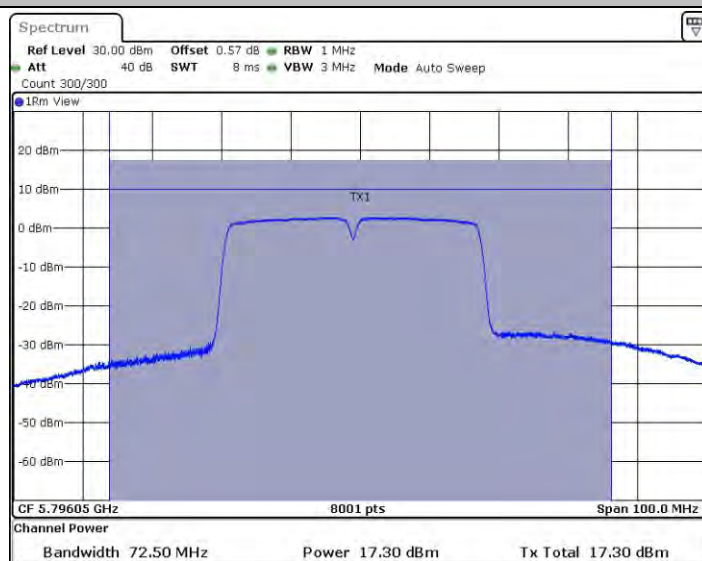
11N40SISO_Ant0_5755



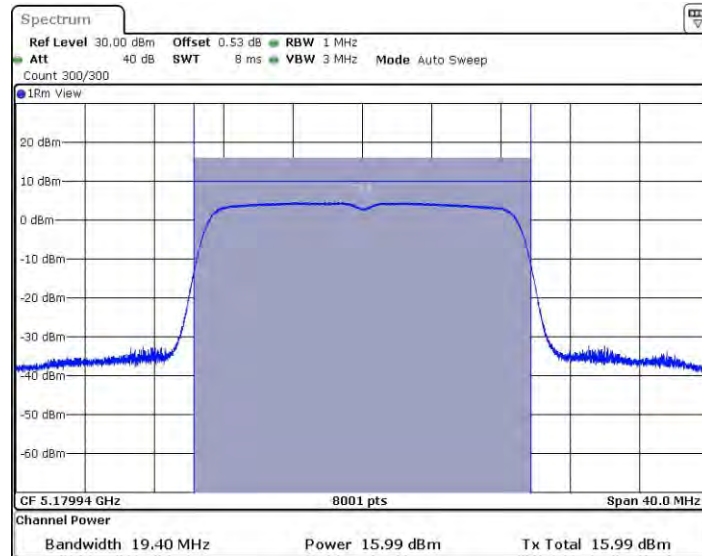
11N40SISO_Ant1_5755



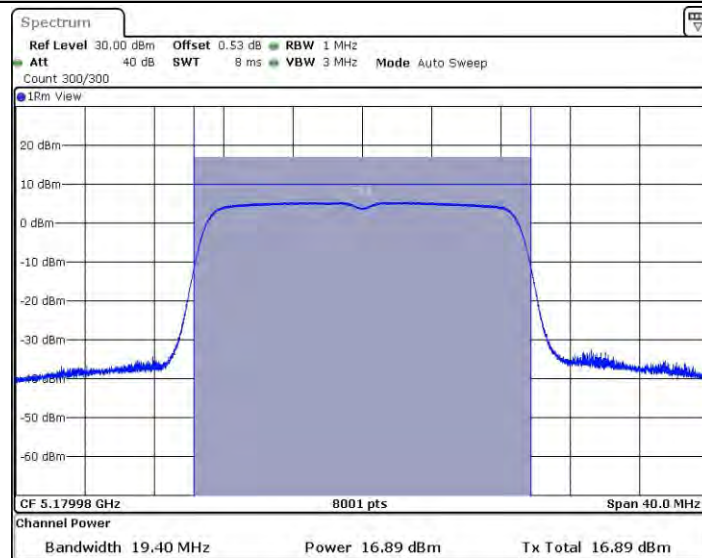
11N40SISO_Ant0_5795



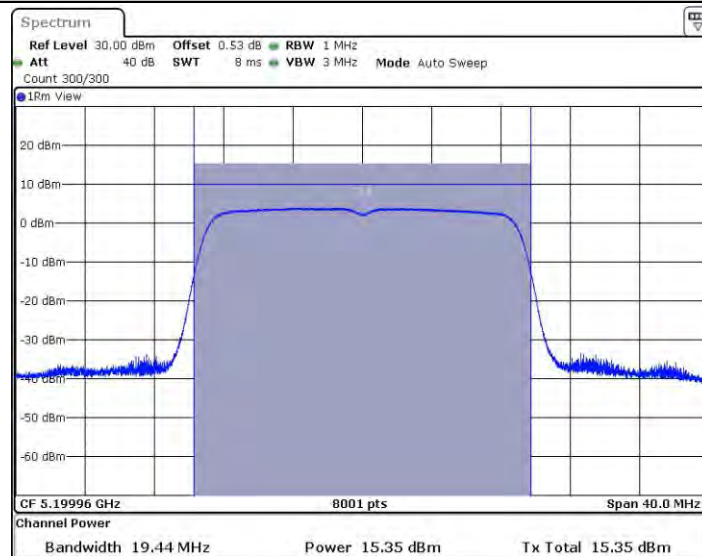
11N40SISO_Ant1_5795



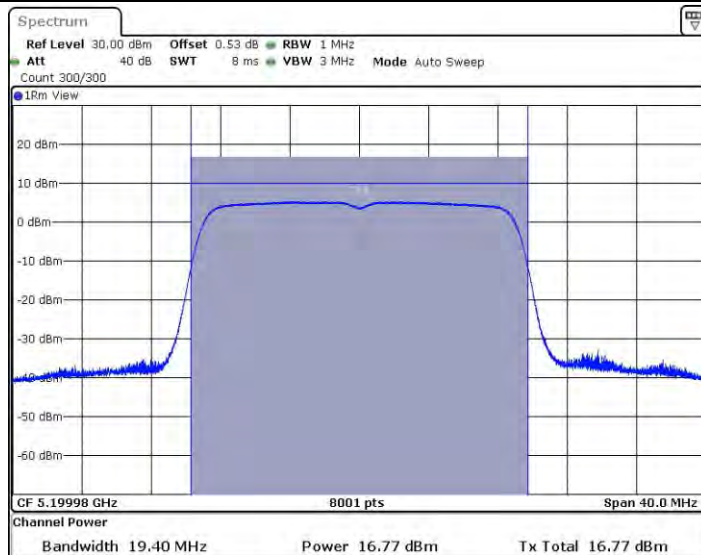
11AC20SISO_Ant0_5180



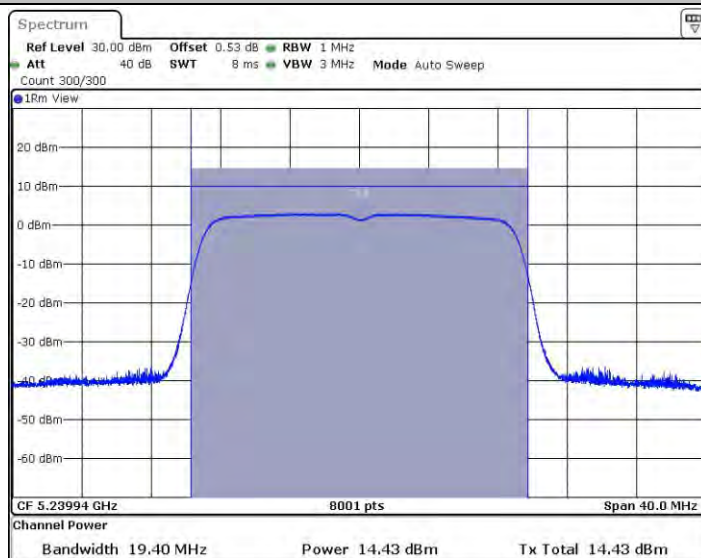
11AC20SISO_Ant1_5180



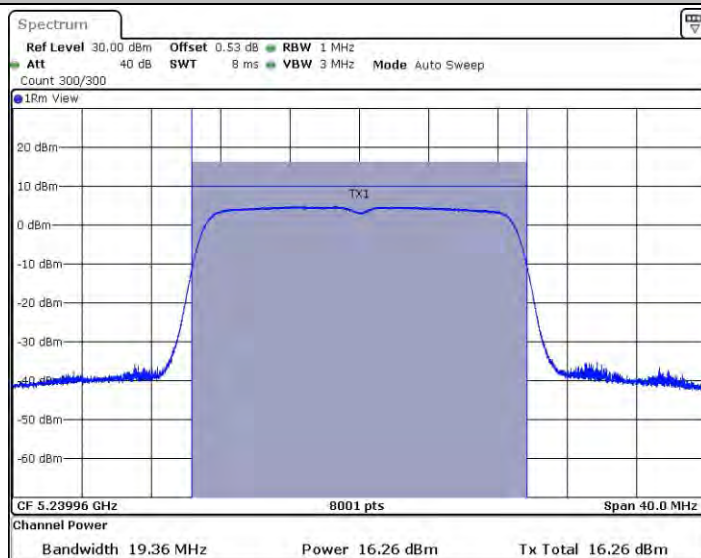
11AC20SISO_Ant0_5200



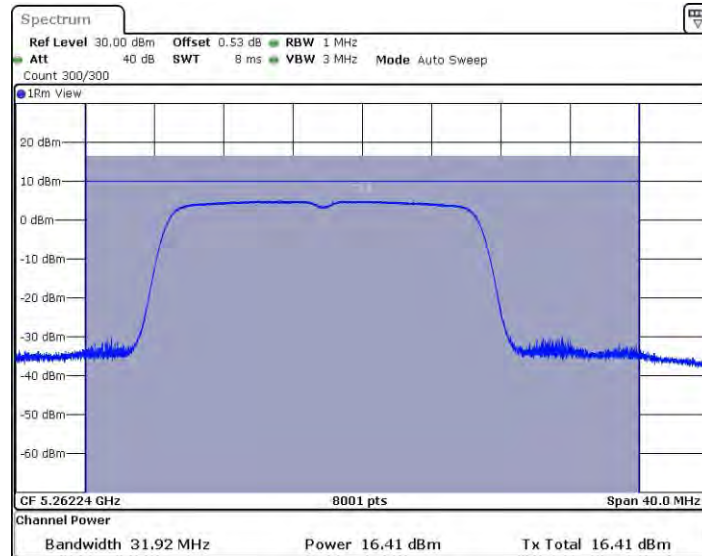
11AC20SISO_Ant1_5200



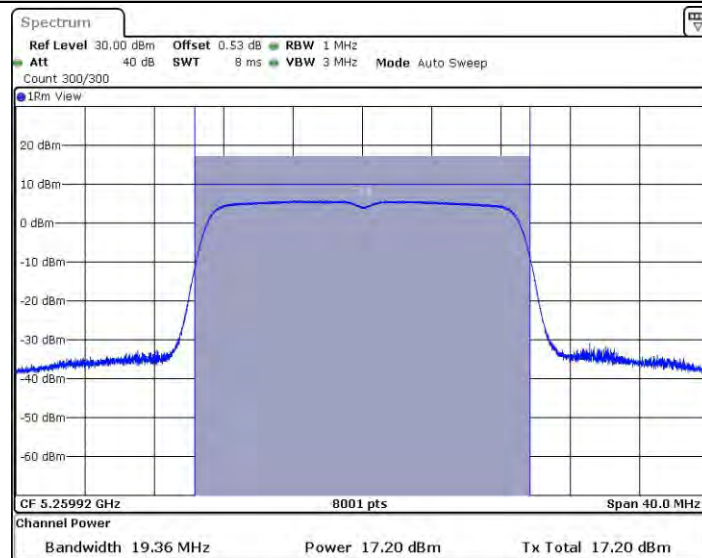
11AC20SISO_Ant0_5240



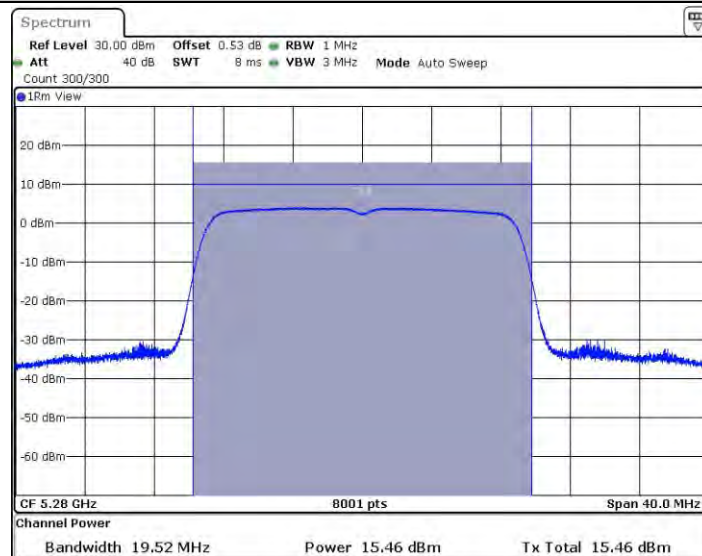
11AC20SISO_Ant1_5240



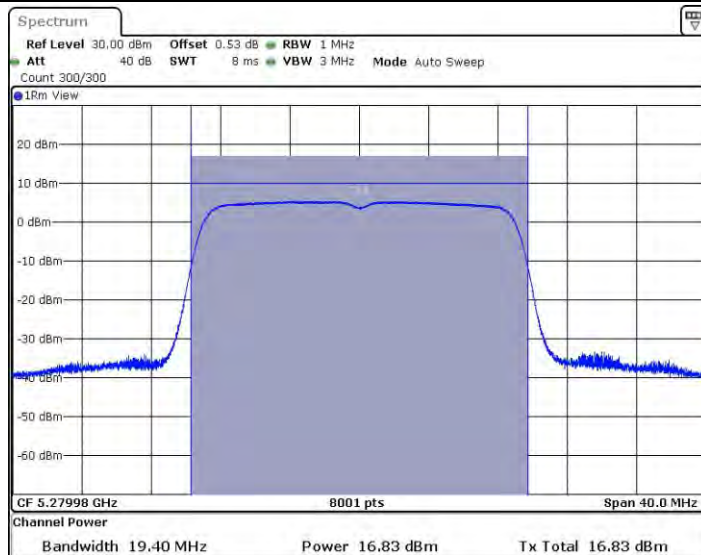
11AC20SISO_Ant0_5260



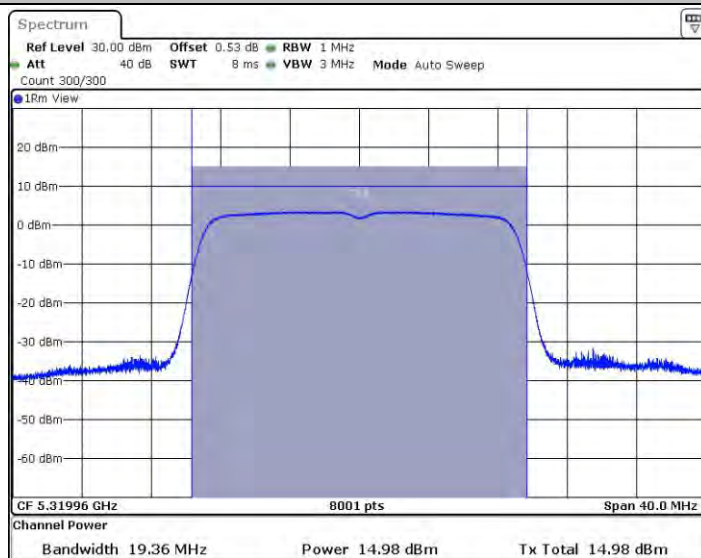
11AC20SISO_Ant1_5260



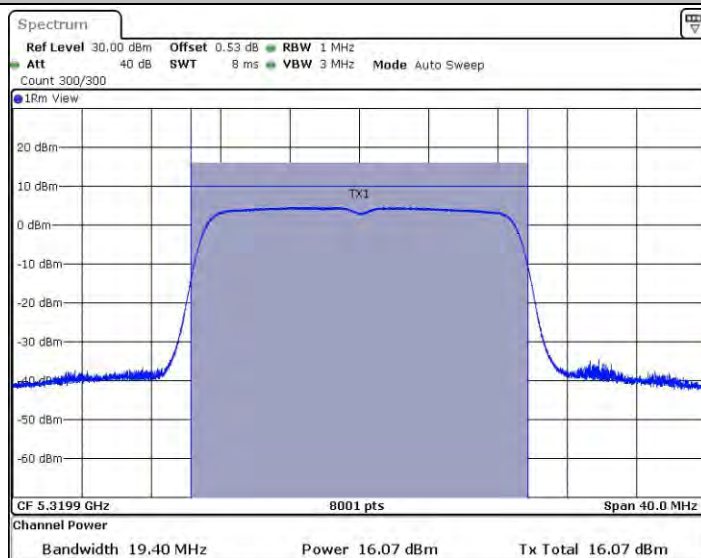
11AC20SISO_Ant0_5280



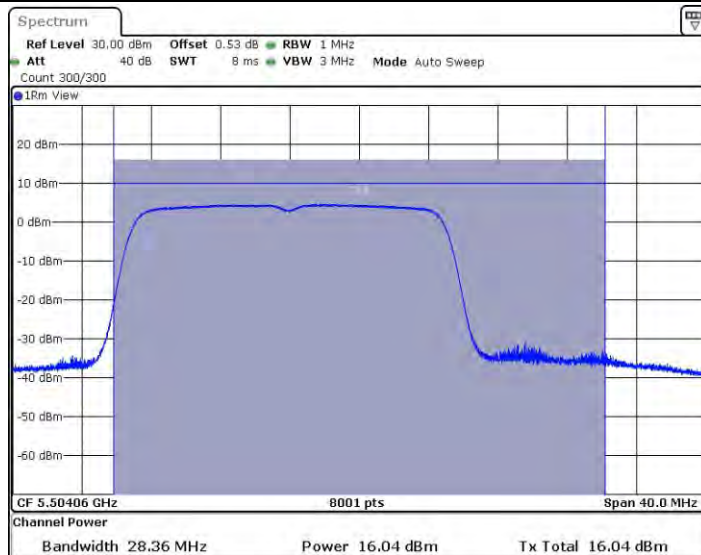
11AC20SISO_Ant1_5280



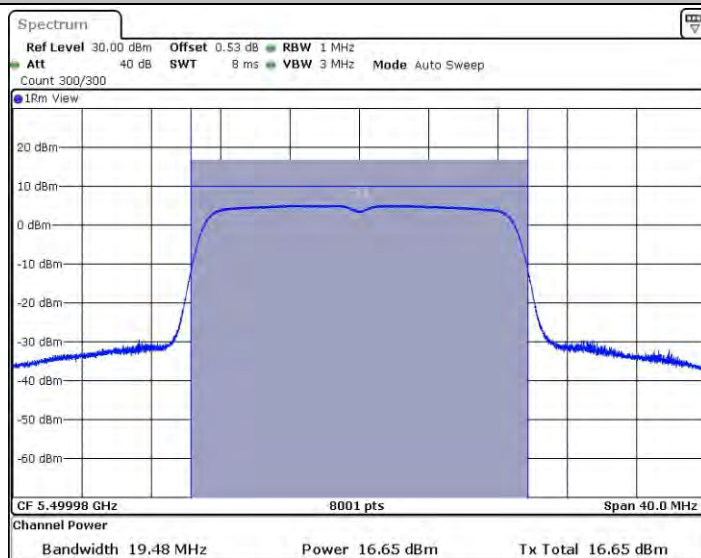
11AC20SISO_Ant0_5320



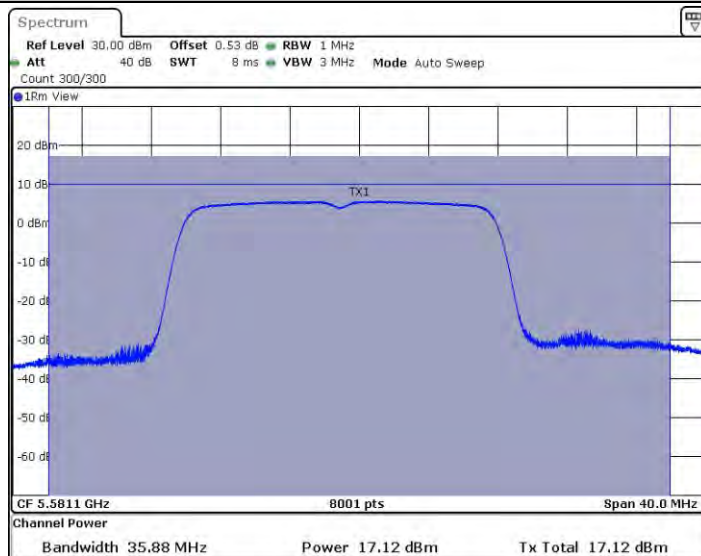
11AC20SISO_Ant1_5320



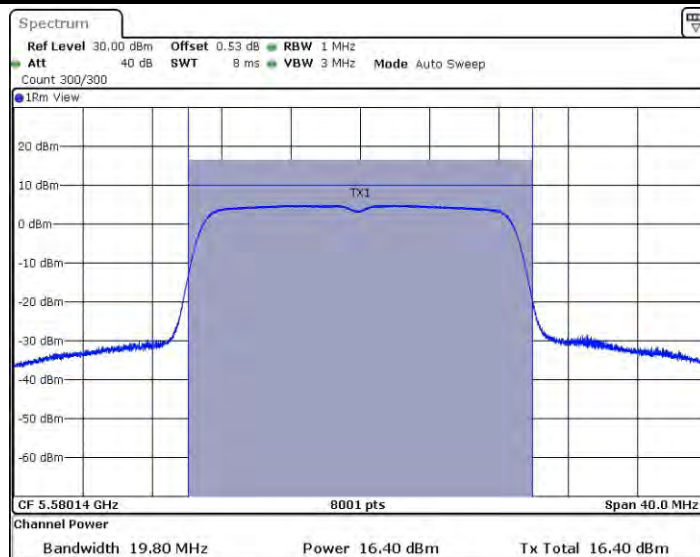
11AC20SISO_Ant0_5500



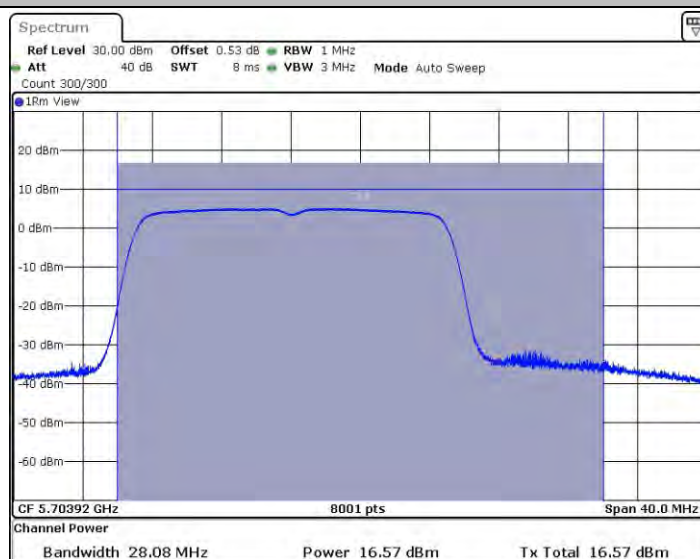
11AC20SISO_Ant1_5500



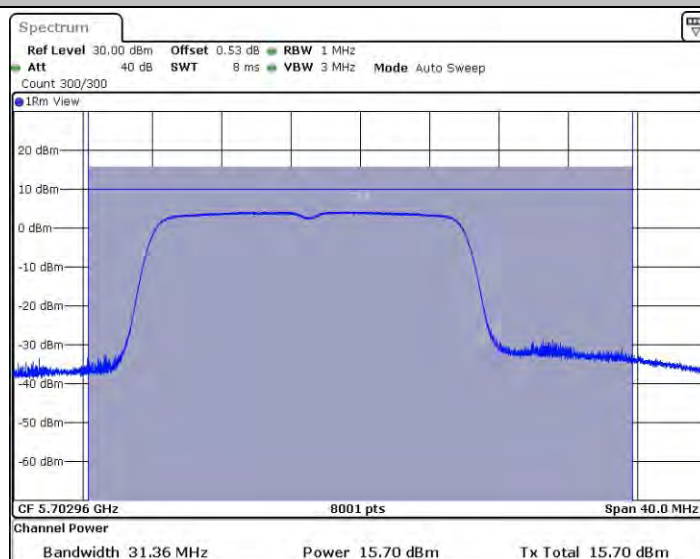
11AC20SISO_Ant0_5580



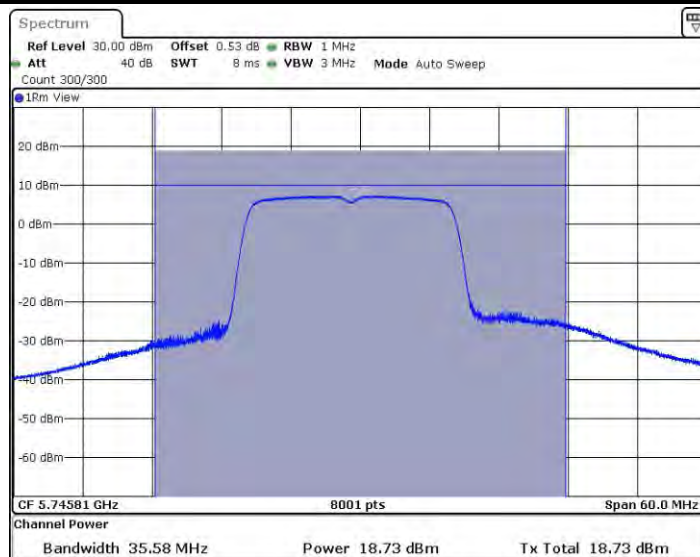
11AC20SISO_Ant1_5580



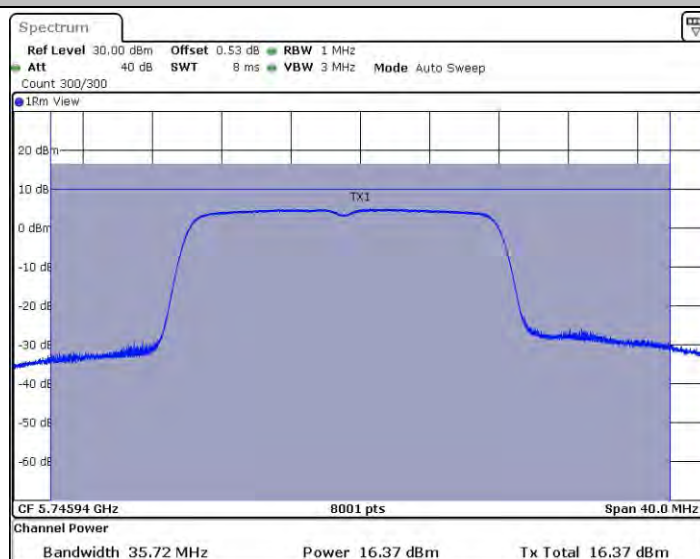
11AC20SISO_Ant0_5700



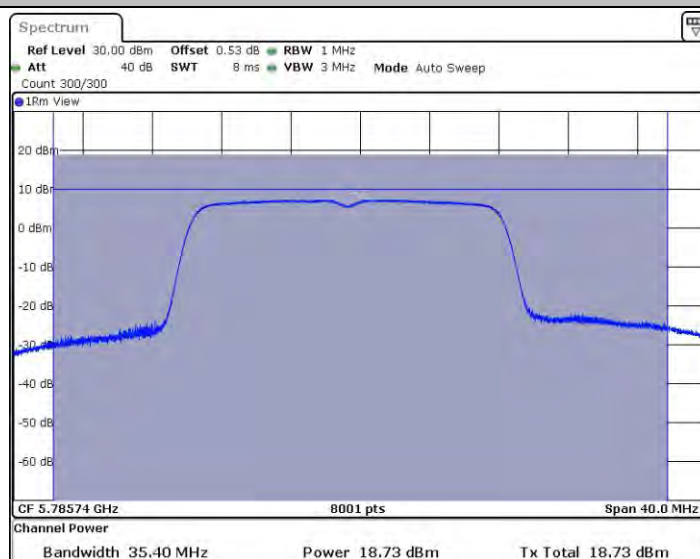
11AC20SISO_Ant1_5700



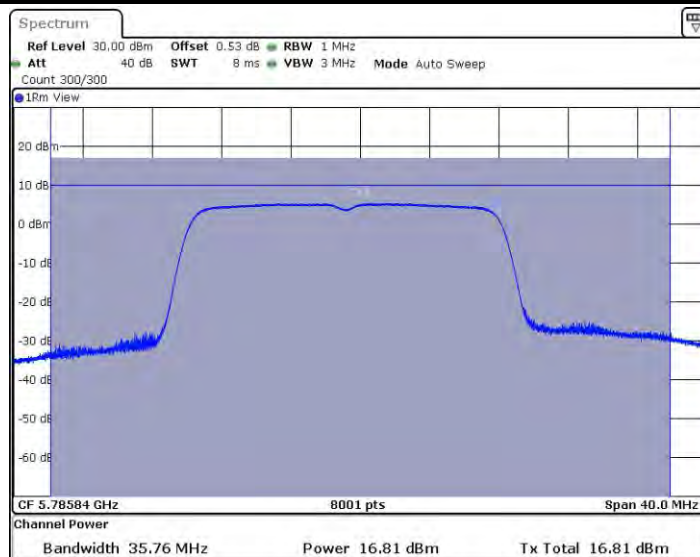
11AC20SISO_Ant0_5745



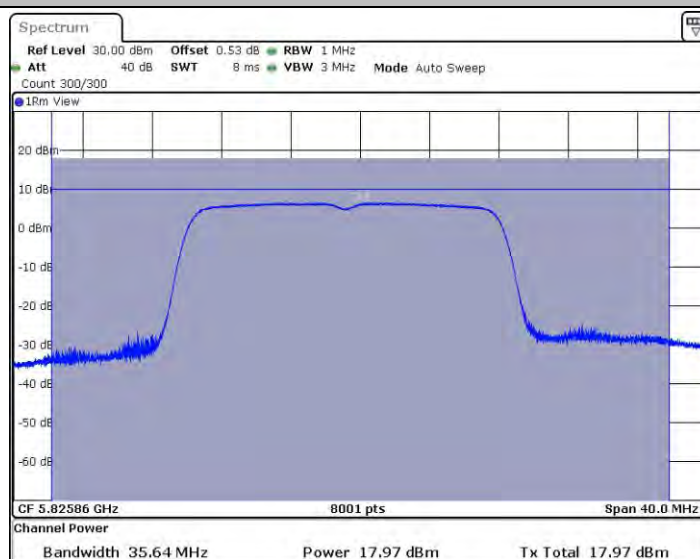
11AC20SISO_Ant1_5745



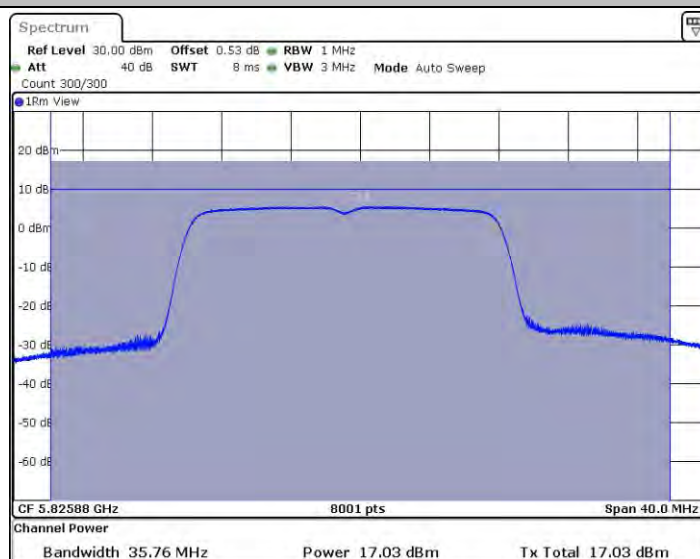
11AC20SISO_Ant0_5785



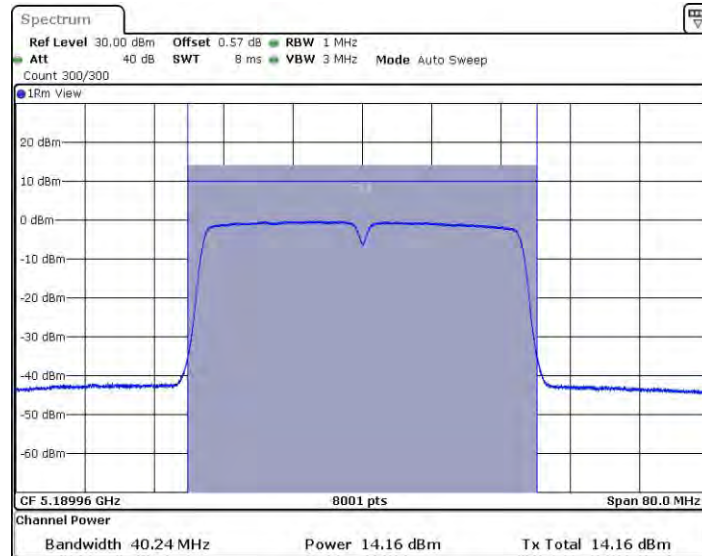
11AC20SISO_Ant1_5785



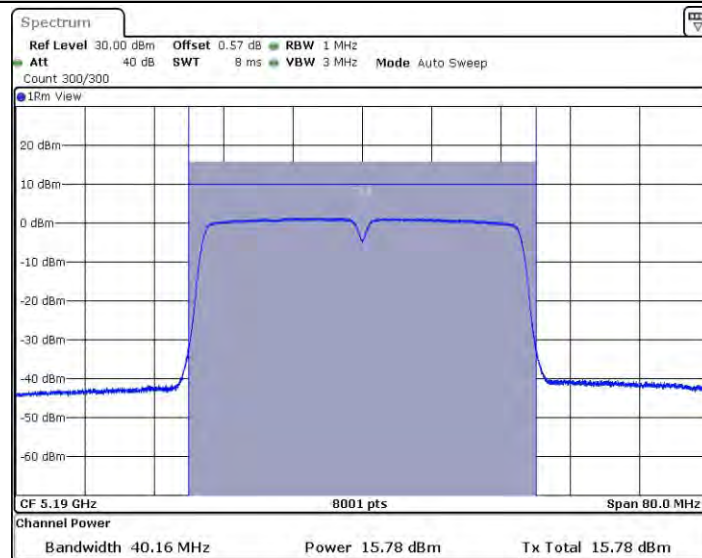
11AC20SISO_Ant0_5825



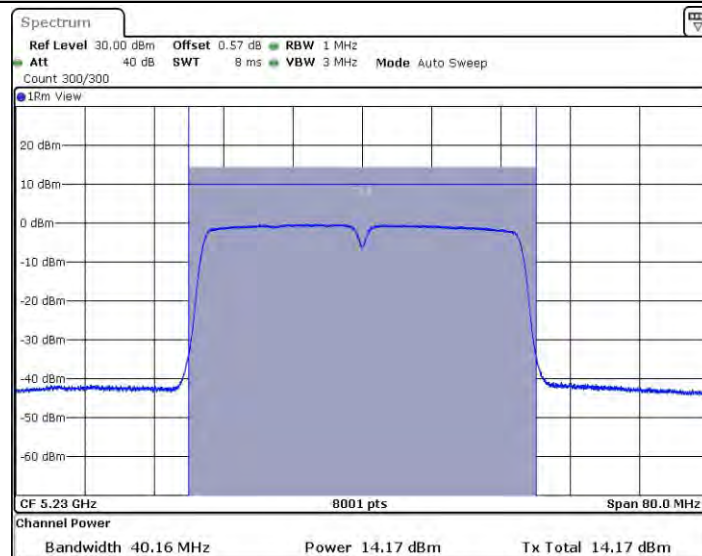
11AC20SISO_Ant1_5825



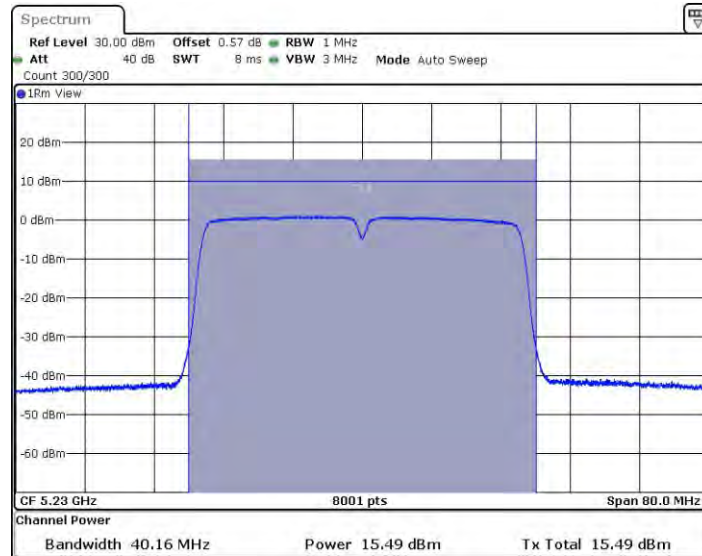
11AC40SISO_Ant0_5190



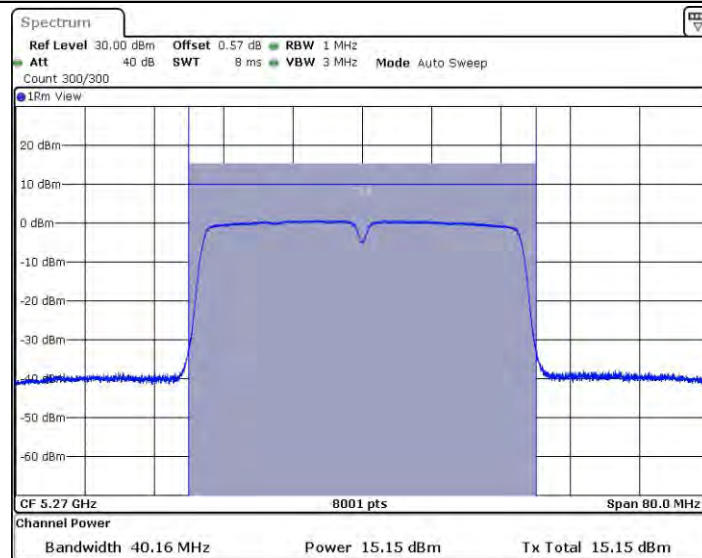
11AC40SISO_Ant1_5190



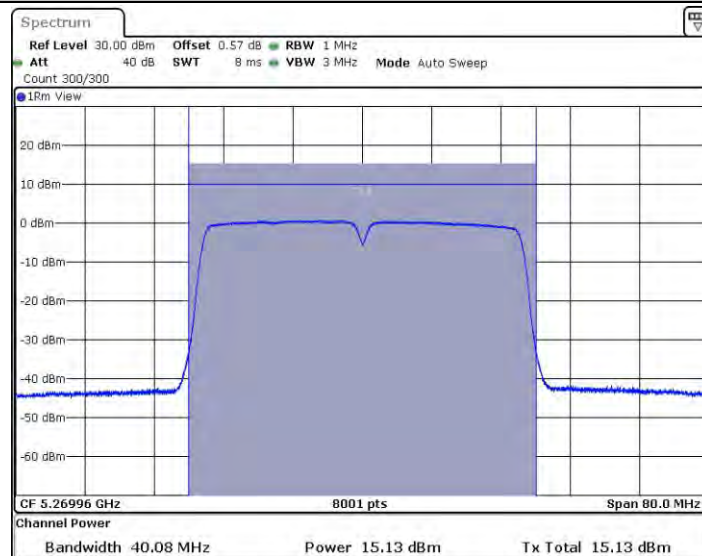
11AC40SISO_Ant0_5230



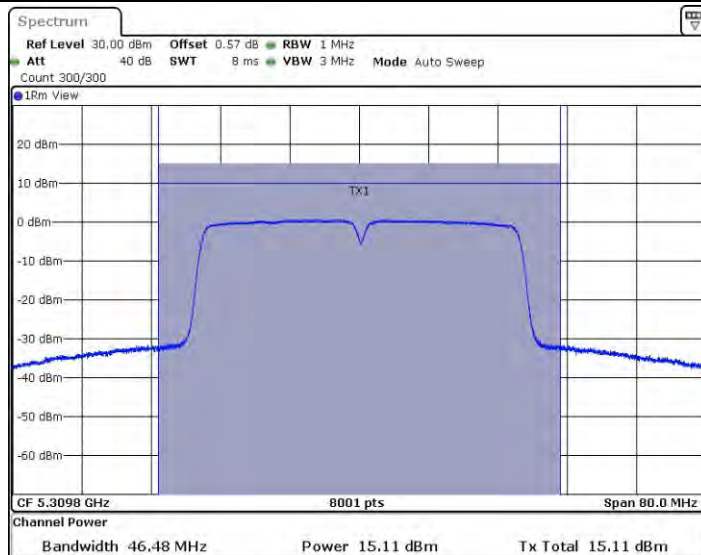
11AC40SISO_Ant1_5230



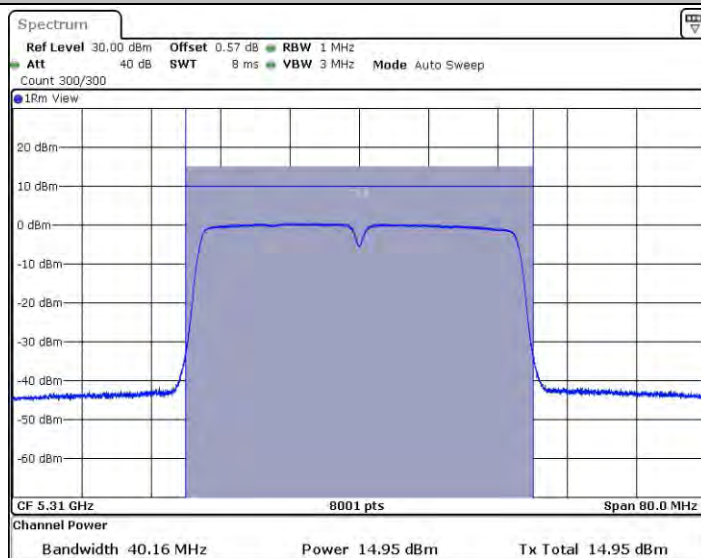
11AC40SISO_Ant0_5270



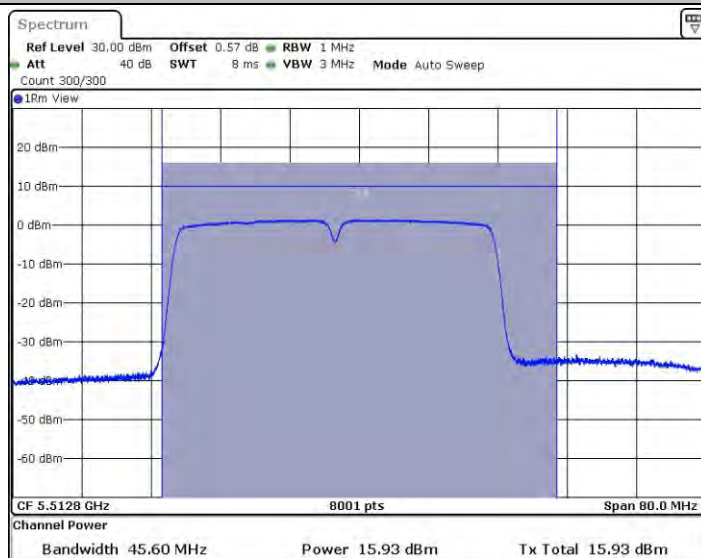
11AC40SISO_Ant1_5270



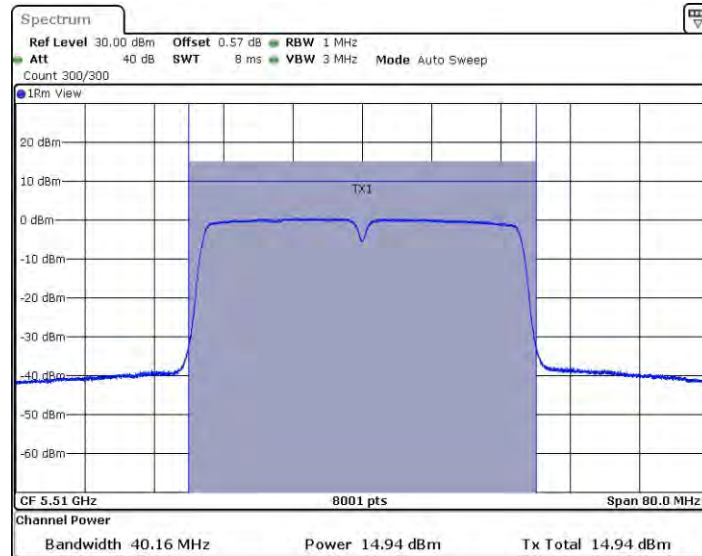
11AC40SISO_Ant0_5310



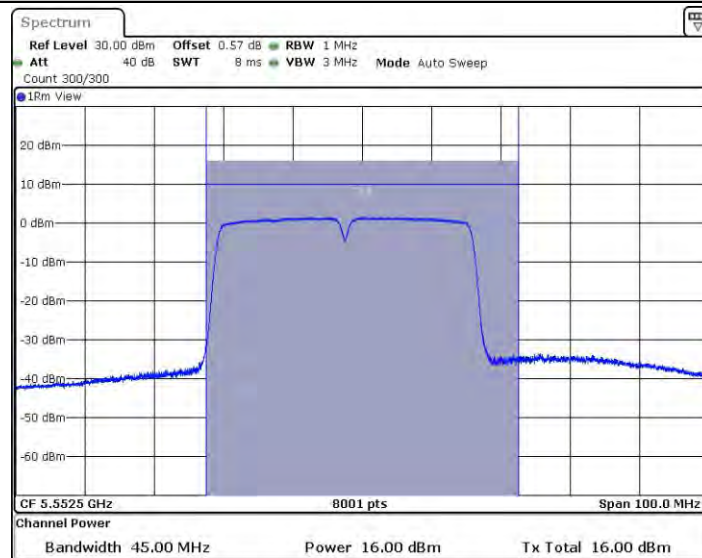
11AC40SISO_Ant1_5310



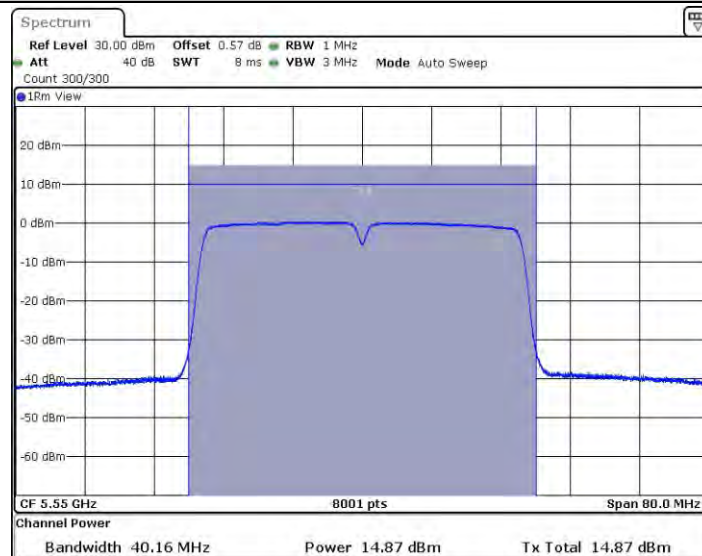
11AC40SISO_Ant0_5510



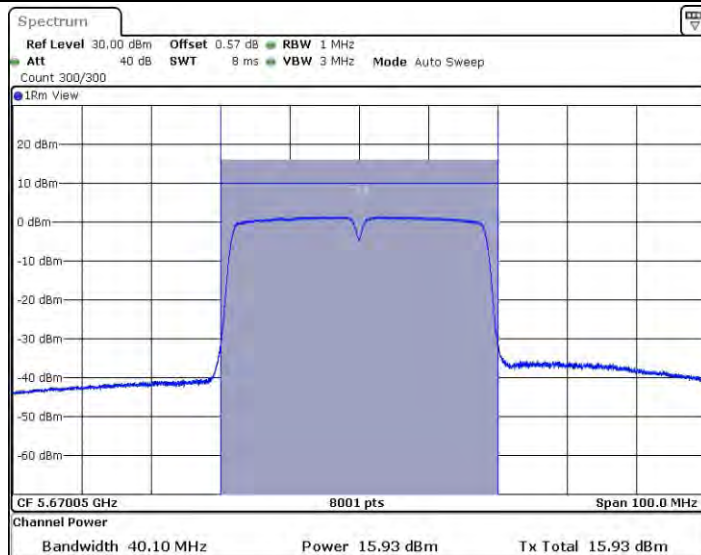
11AC40SISO_Ant1_5510



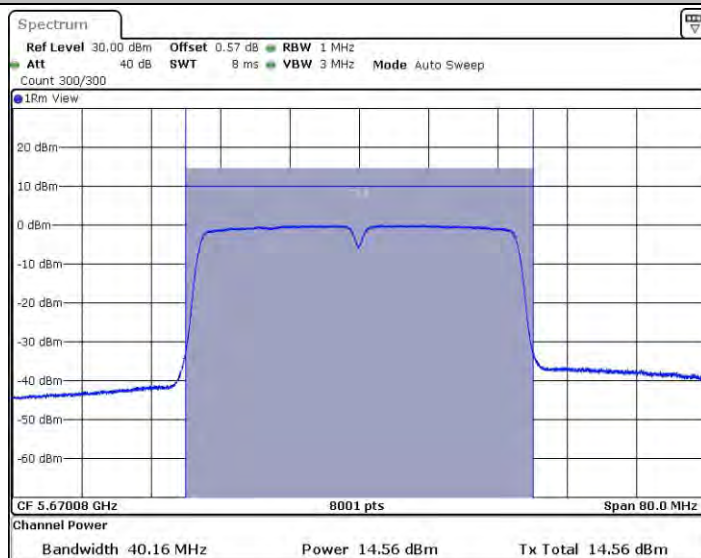
11AC40SISO_Ant0_5550



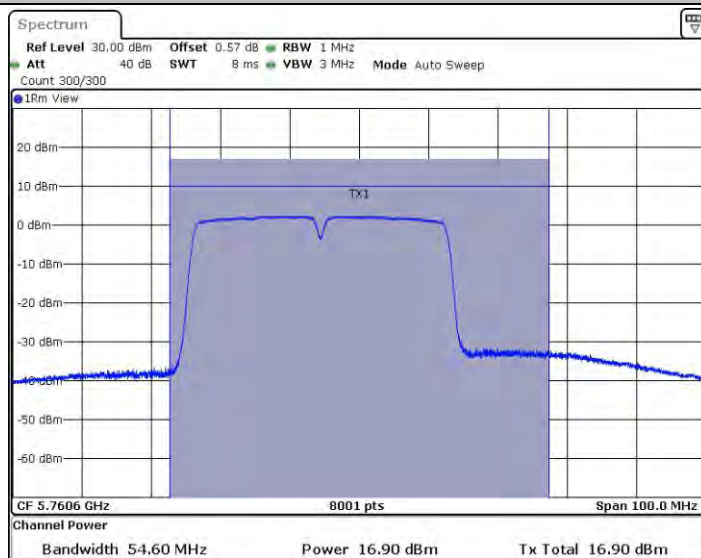
11AC40SISO_Ant1_5550



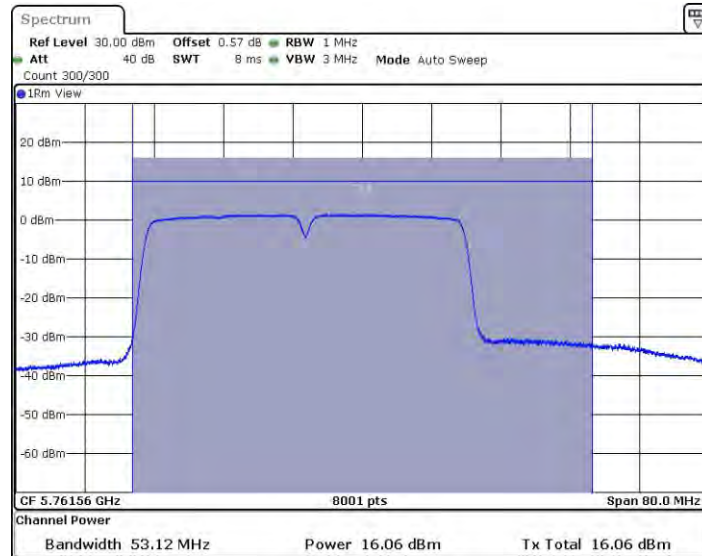
11AC40SISO_Ant0_5670



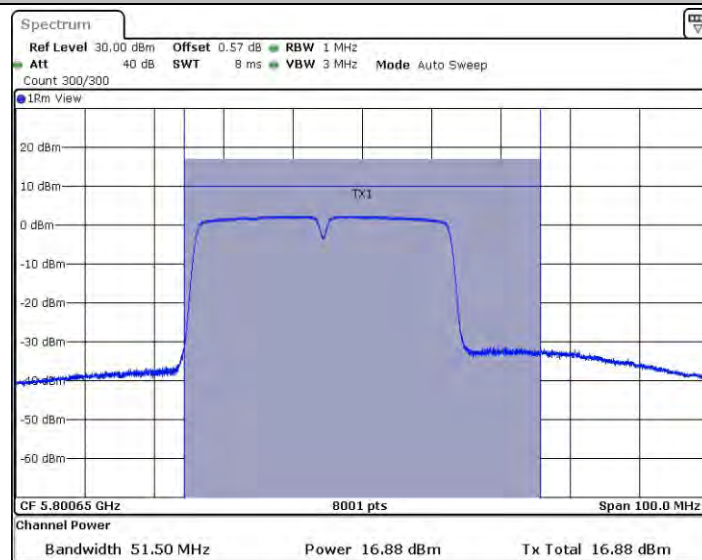
11AC40SISO_Ant1_5670



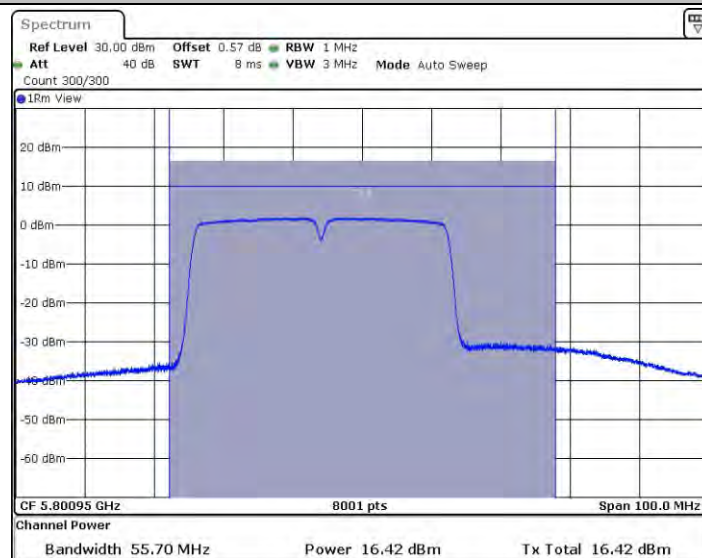
11AC40SISO_Ant0_5755



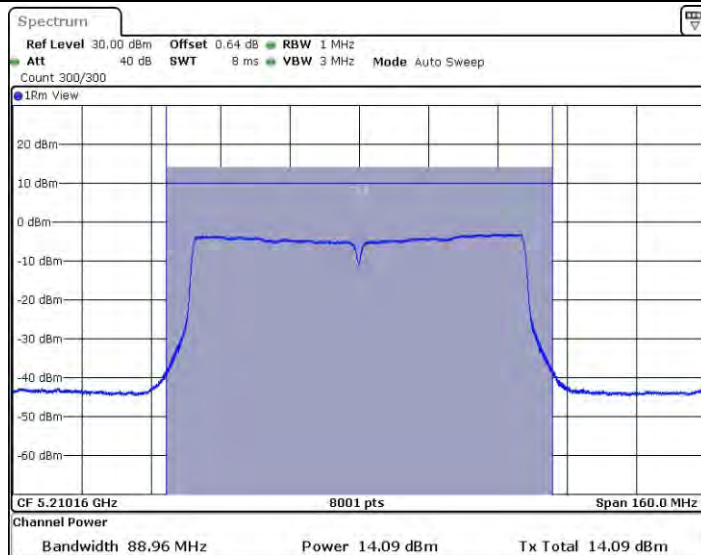
11AC40SISO_Ant1_5755



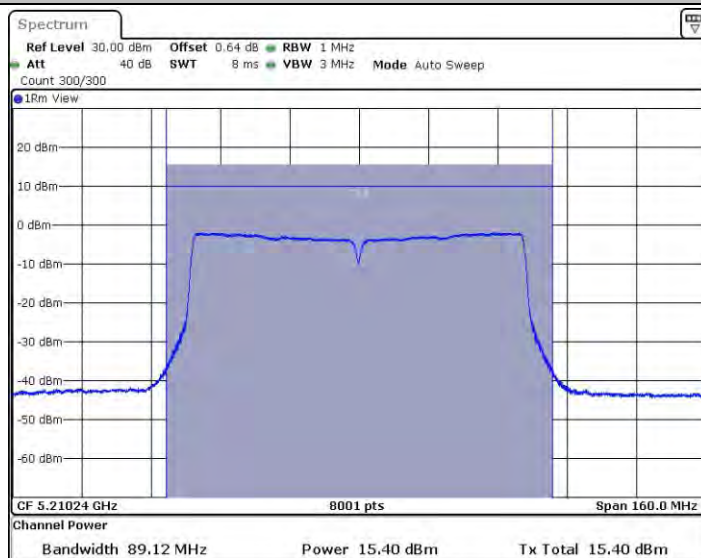
11AC40SISO_Ant0_5795



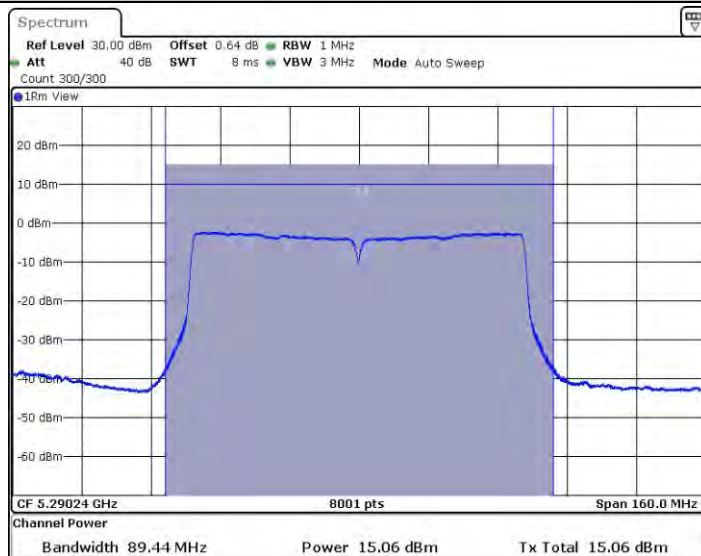
11AC40SISO_Ant1_5795



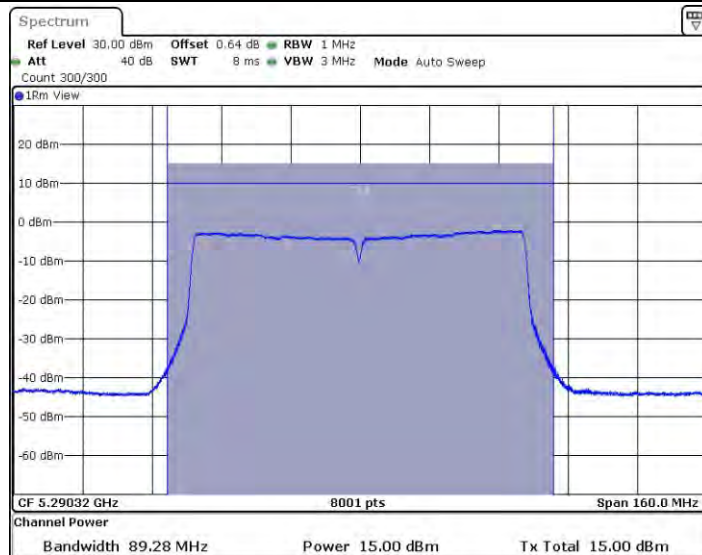
11AC80SISO_Ant0_5210



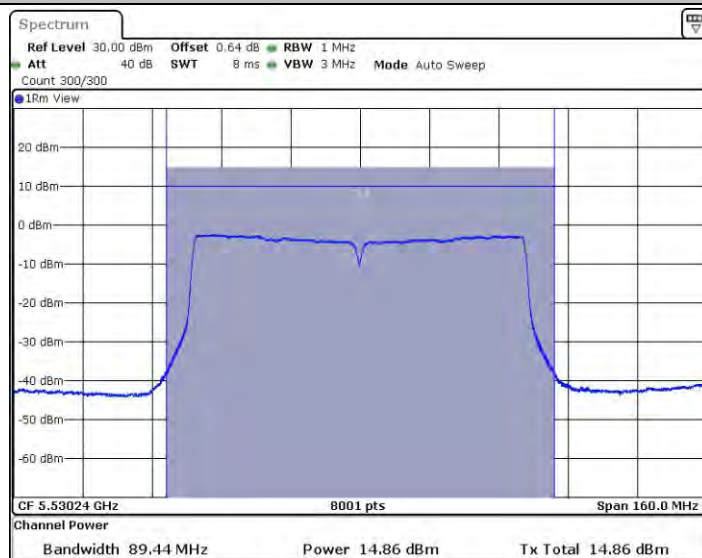
11AC80SISO_Ant1_5210



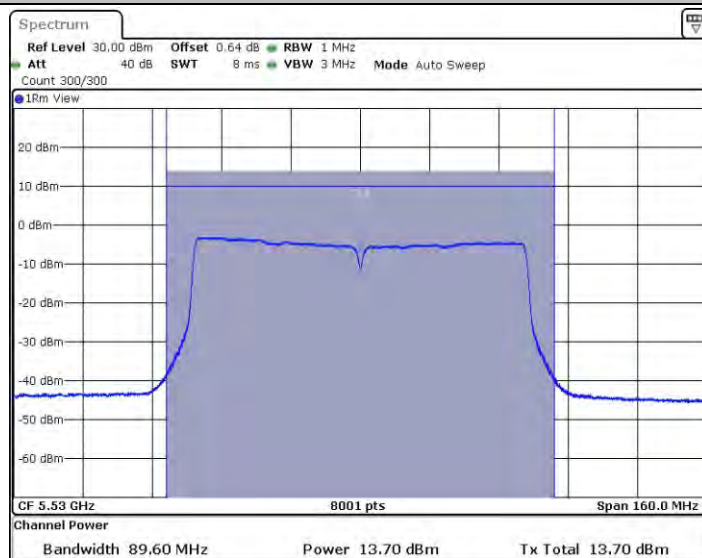
11AC80SISO_Ant0_5290



11AC80SISO_Ant1_5290



11AC80SISO_Ant0_5530



11AC80SISO_Ant1_5530