Appendix for 5A_n2A

Product Name: 5G CPE
Model No: C150

Appendix A: Average Power Output for NSA

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Power (dBm)	Power Class	Verdict
DC_5A_n2A	15	5+5	DFT-QPSK	M+L	Edge_1RB_Left	22.93	PC3	PASS
DC_5A_n2A	15	5+5	DFT-QPSK	M+L	Edge_1RB_Right	22.99	PC3	PASS
DC_5A_n2A	15	5+5	DFT-QPSK	M+L	Outer_Full	22.99	PC3	PASS
DC_5A_n2A	15	5+5	DFT-QPSK	M+L	Inner_Full	22.96	PC3	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+L	Edge_1RB_Left	20.88	PC3	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+L	Edge_1RB_Right	20.62	PC3	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+L	Outer_Full	20.64	PC3	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+L	Inner_Full	22.24	PC3	PASS
DC_5A_n2A	15	5+5	DFT-QPSK	M+M	Edge_1RB_Left	23.65	PC3	PASS
DC_5A_n2A	15	5+5	DFT-QPSK	M+M	Edge_1RB_Right	23.66	PC3	PASS
DC_5A_n2A	15	5+5	DFT-QPSK	M+M	Outer_Full	23.71	PC3	PASS
DC_5A_n2A	15	5+5	DFT-QPSK	M+M	Inner_Full	24.85	PC3	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+M	Edge_1RB_Left	20.87	PC3	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+M	Edge_1RB_Right	20.08	PC3	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+M	Outer_Full	20.96	PC3	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+M	Inner_Full	21.54	PC3	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+L	Outer_Full	20.77	PC3	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+L	Inner_Full	21.72	PC3	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+L	Edge_1RB_Left	20.67	PC3	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+L	Edge_1RB_Right	20.48	PC3	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+M	Outer_Full	20.09	PC3	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+M	Inner_Full	21.07	PC3	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+M	Edge_1RB_Left	20.12	PC3	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+M	Edge_1RB_Right	20.60	PC3	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+H	Outer_Full	20.31	PC3	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+H	Inner_Full	21.67	PC3	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+H	Edge_1RB_Left	20.22	PC3	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+H	Edge_1RB_Right	20.33	PC3	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+H	Outer_Full	20.31	PC3	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+H	Inner_Full	21.19	PC3	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+H	Edge_1RB_Left	20.84	PC3	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+H	Edge_1RB_Right	20.20	PC3	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+L	Outer_Full	21.27	PC3	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+L	Inner_Full	22.73	PC3	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+L	Edge_1RB_Left	21.57	PC3	PASS

DC_5A_n2A	15	5+10	CP-QPSK	M+L	Edge_1RB_Right	21.03	PC3	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+L	Outer_Full	21.36	PC3	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+L	Inner_Full	22.18	PC3	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+L	Edge_1RB_Left	21.54	PC3	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+L	Edge_1RB_Right	20.92	PC3	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+M	Outer_Full	20.97	PC3	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+M	Inner_Full	22.62	PC3	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+M	Edge_1RB_Left	21.05	PC3	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+M	Edge_1RB_Right	20.82	PC3	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+M	Outer_Full	20.96	PC3	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+M	Inner_Full	21.94	PC3	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+M	Edge_1RB_Left	20.69	PC3	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+M	Edge_1RB_Right	21.08	PC3	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+H	Outer_Full	20.26	PC3	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+H	Inner_Full	21.72	PC3	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+H	Edge_1RB_Left	20.00	PC3	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+H	Edge_1RB_Right	20.31	PC3	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+H	Outer_Full	20.17	PC3	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+H	Inner_Full	21.24	PC3	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+H	Edge_1RB_Left	20.62	PC3	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+H	Edge_1RB_Right	20.33	PC3	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+L	Outer_Full	21.07	PC3	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+L	Inner_Full	22.49	PC3	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+L	Edge_1RB_Left	21.27	PC3	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+L	Edge_1RB_Right	20.90	PC3	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+L	Outer_Full	21.07	PC3	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+L	Inner_Full	21.97	PC3	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+L	Edge_1RB_Left	20.95	PC3	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+L	Edge_1RB_Right	20.67	PC3	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+M	Outer_Full	21.08	PC3	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+M	Inner_Full	22.72	PC3	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+M	Edge_1RB_Left	21.19	PC3	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+M	Edge_1RB_Right	20.91	PC3	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+M	Outer_Full	20.97	PC3	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+M	Inner_Full	21.91	PC3	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+M	Edge_1RB_Left	21.05	PC3	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+M	Edge_1RB_Right	20.47	PC3	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+H	Outer_Full	20.13	PC3	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+H	Inner_Full	21.59	PC3	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+H	Edge_1RB_Left	20.13	PC3	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+H	Edge_1RB_Right	20.24	PC3	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+H	Outer_Full	20.12	PC3	PASS

DC_5A_n2A	15	5+15	CP-16QAM	M+H	Inner_Full	21.23	PC3	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+H	Edge_1RB_Left	20.92	PC3	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+H	Edge_1RB_Right	20.92	PC3	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+L	Outer_Full	21.05	PC3	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+L	Inner_Full	23.01	PC3	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+L	Edge_1RB_Left	21.53	PC3	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+L	Edge_1RB_Right	20.87	PC3	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+L	Outer_Full	21.02	PC3	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+L	Inner_Full	22.04	PC3	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+L	Edge_1RB_Left	21.25	PC3	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+L	Edge_1RB_Right	20.43	PC3	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+M	Outer_Full	20.82	PC3	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+M	Inner_Full	22.54	PC3	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+M	Edge_1RB_Left	20.93	PC3	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+M	Edge_1RB_Right	20.58	PC3	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+M	Outer_Full	20.91	PC3	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+M	Inner_Full	22.14	PC3	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+M	Edge_1RB_Left	20.82	PC3	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+M	Edge_1RB_Right	20.41	PC3	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+H	Outer_Full	20.25	PC3	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+H	Inner_Full	21.68	PC3	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+H	Edge_1RB_Left	20.50	PC3	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+H	Edge_1RB_Right	20.16	PC3	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+H	Outer_Full	20.21	PC3	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+H	Inner_Full	21.26	PC3	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+H	Edge_1RB_Left	20.15	PC3	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+H	Edge_1RB_Right	20.36	PC3	PASS

Appendix B: Peak-to-Average Ratio for NSA

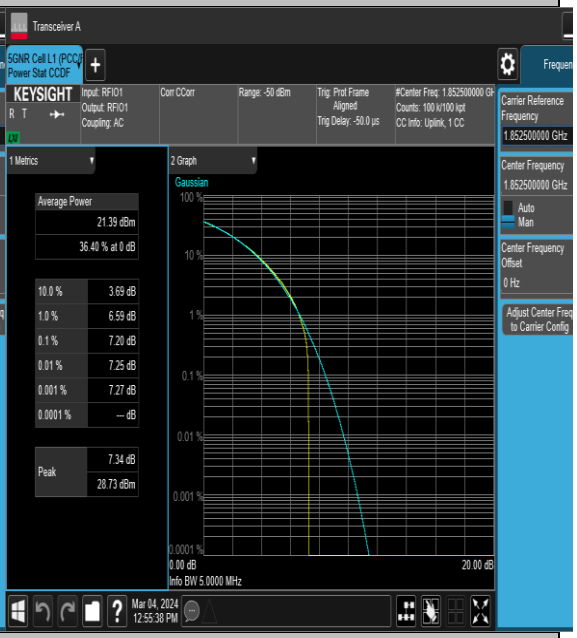
Peak-to-Average Ratio(CCDF)

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result	Limit	Verdict
DC_5A_n2A	15	5+5	CP-QPSK	M+L	Outer_Full	7.14	≤13	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+L	Outer_Full	7.20	≤13	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+M	Outer_Full	7.42	≤13	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+M	Outer_Full	7.53	≤13	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+H	Outer_Full	7.53	≤13	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+H	Outer_Full	7.61	≤13	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+L	Outer_Full	7.87	≤13	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+L	Outer_Full	7.81	≤13	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+M	Outer_Full	7.38	≤13	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+M	Outer_Full	7.40	≤13	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+H	Outer_Full	7.85	≤13	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+H	Outer_Full	7.90	≤13	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+L	Outer_Full	7.69	≤13	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+L	Outer_Full	7.68	≤13	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+M	Outer_Full	7.12	≤13	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+M	Outer_Full	7.12	≤13	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+H	Outer_Full	7.84	≤13	PASS

DC_5A_n2A	15	5+15	CP-16QAM	M+H	Outer_Full	7.83	≤13	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+L	Outer_Full	7.36	≤13	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+L	Outer_Full	7.40	≤13	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+M	Outer_Full	7.75	≤13	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+M	Outer_Full	7.73	≤13	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+H	Outer_Full	7.73	≤13	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+H	Outer_Full	7.68	≤13	PASS

Test Graphs

NTNV_DC_5A_n2A_PC3_15_5+5_M+L_TID6_#1	NTNV_DC_5A_n2A_PC3_15_5+5_M+L_TID7_#1
	
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	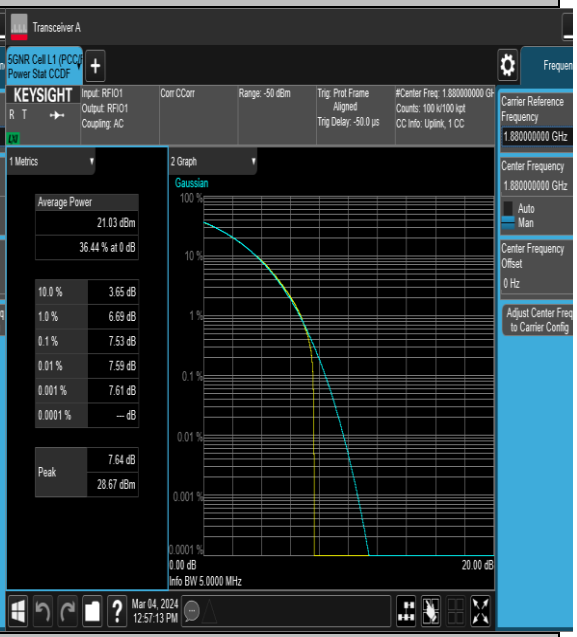
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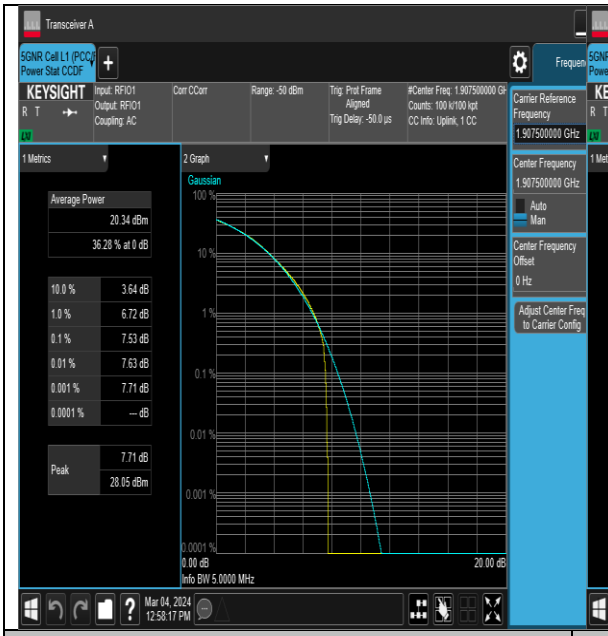

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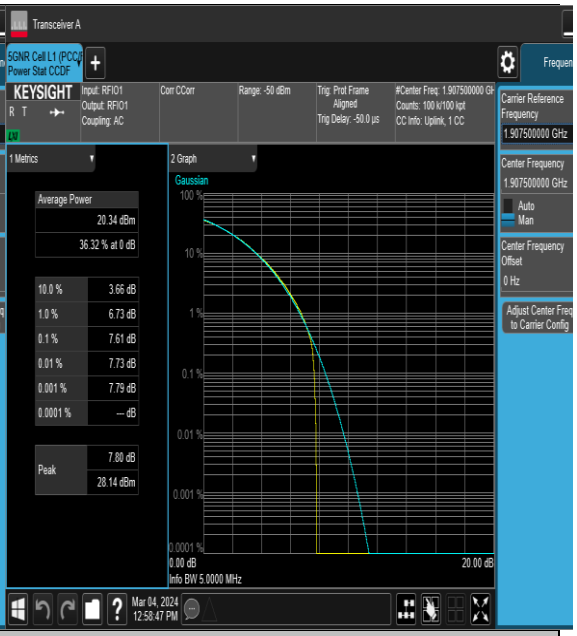

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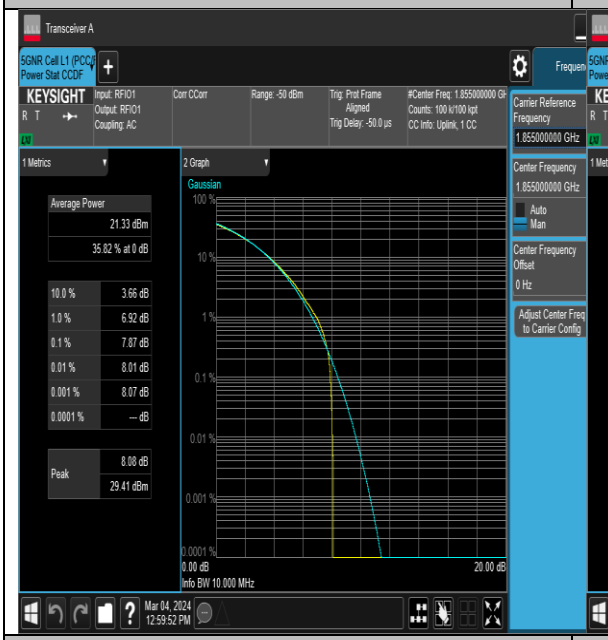

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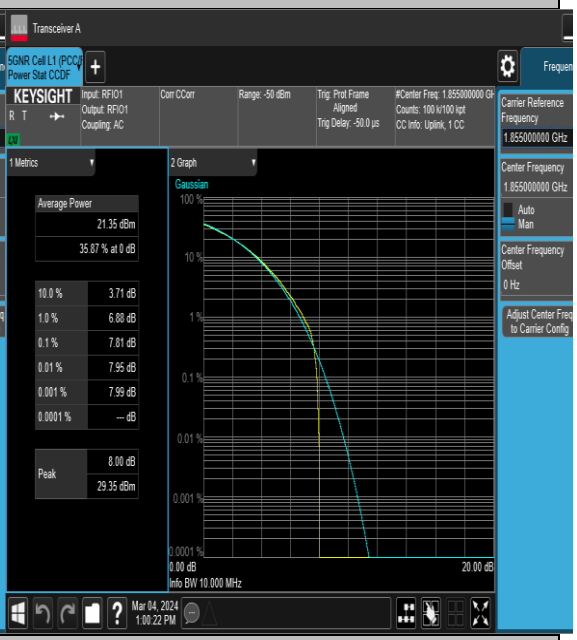

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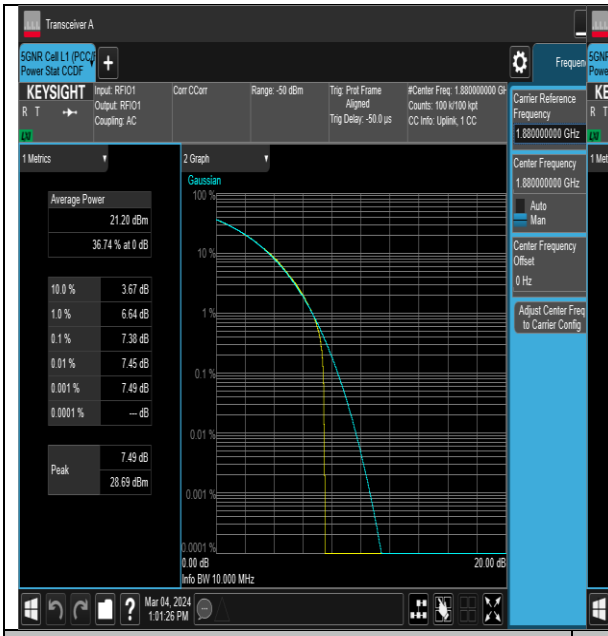

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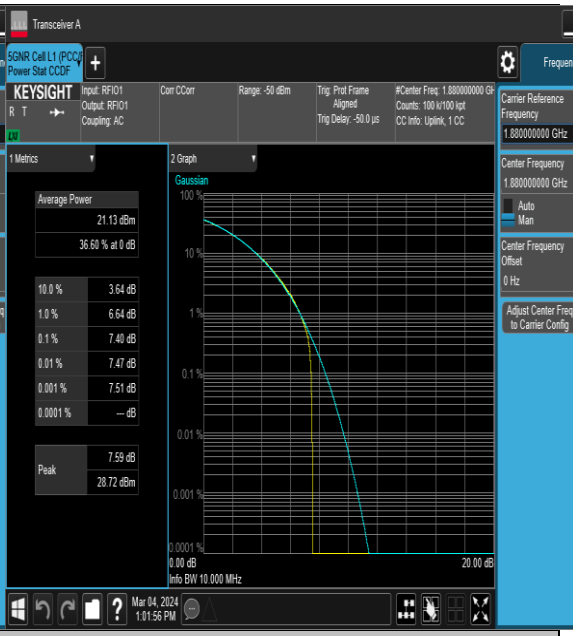

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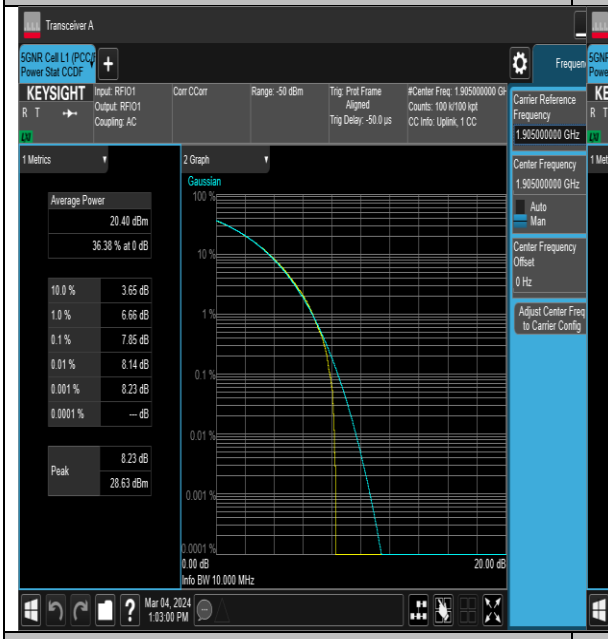

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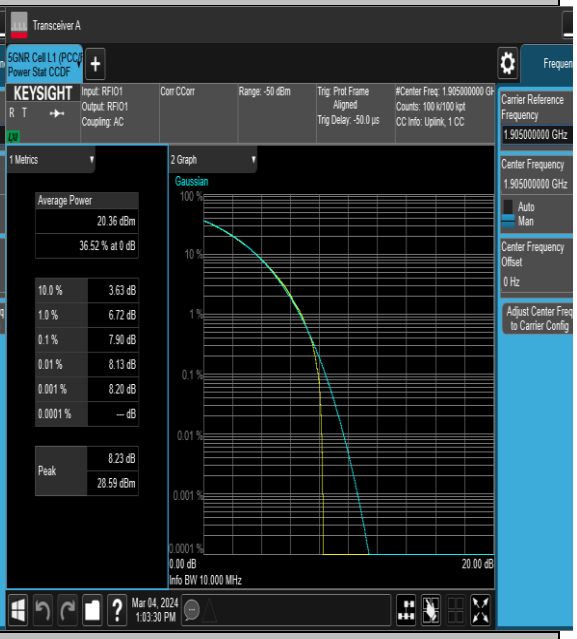

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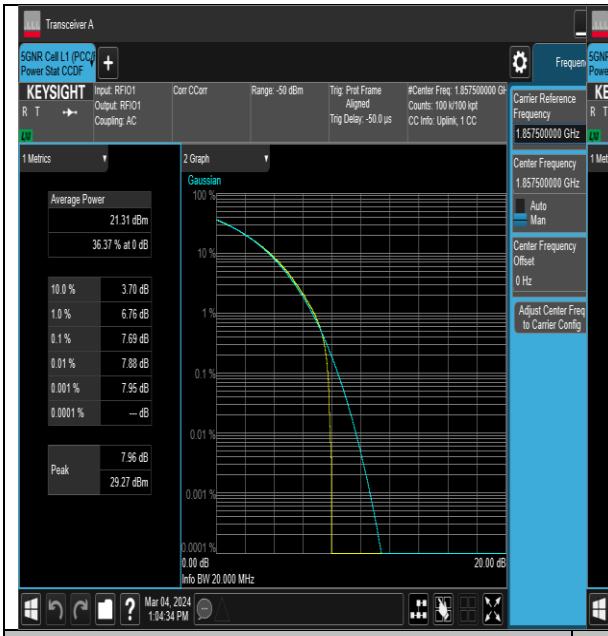

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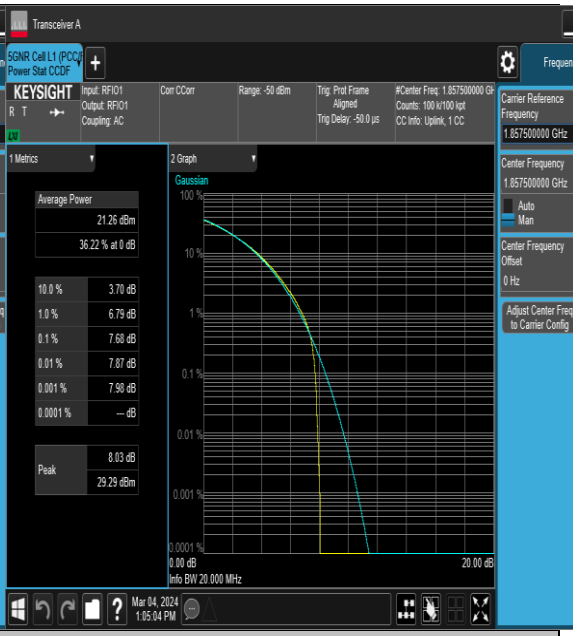

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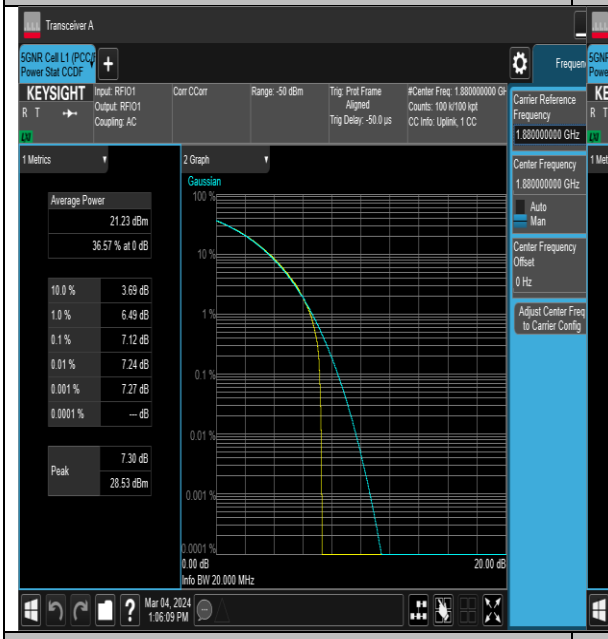

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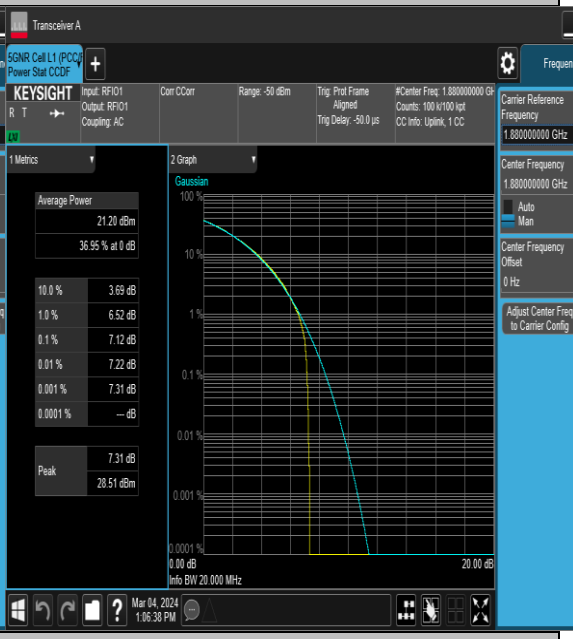

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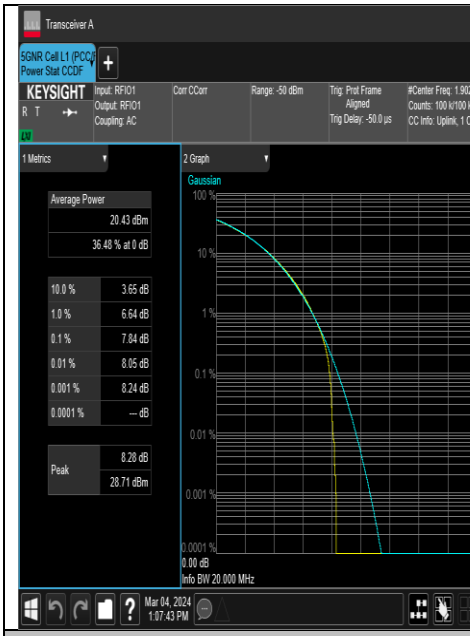

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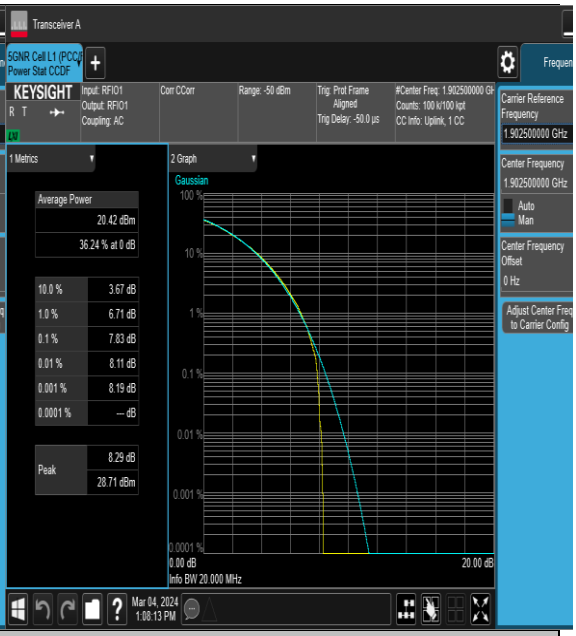
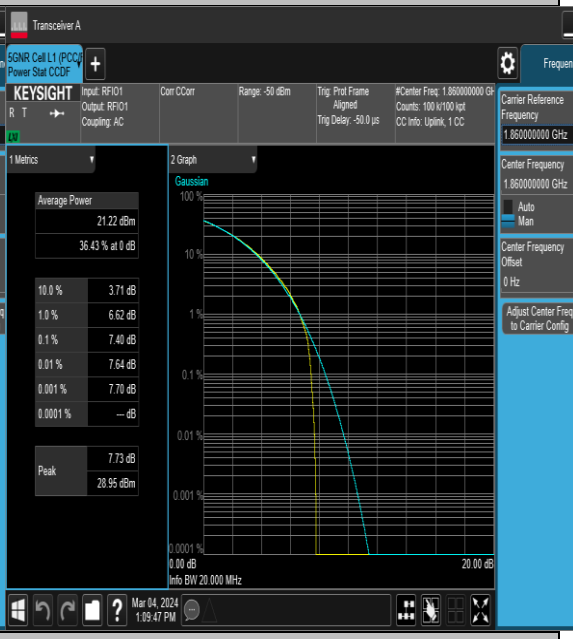
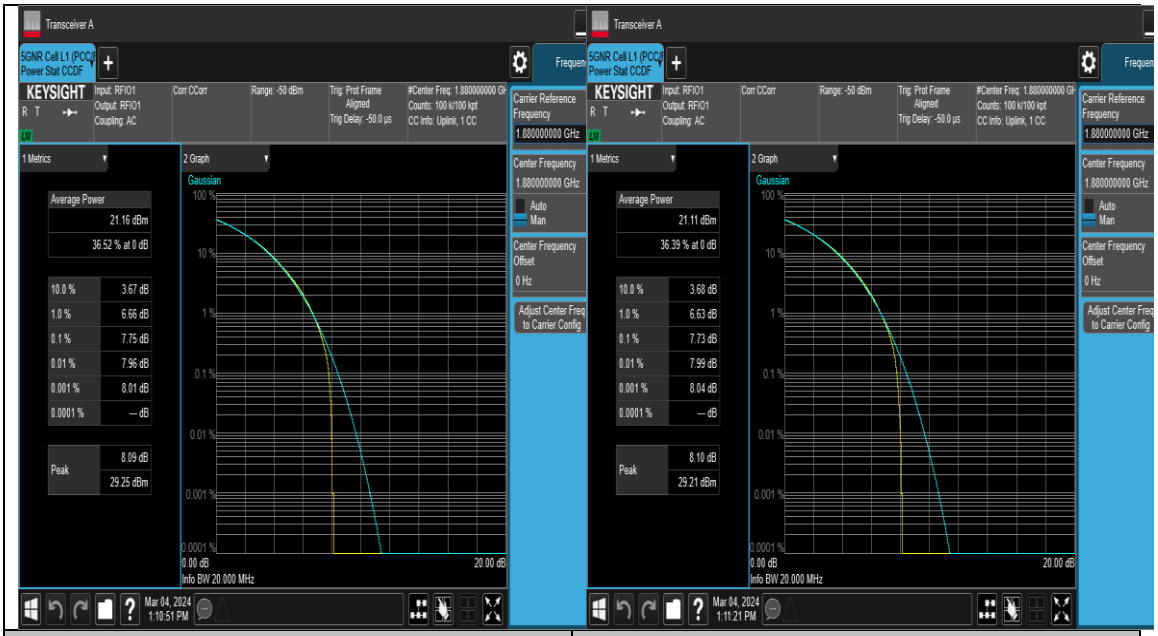


NTNV_DC_5A_n2A_PC3_15_5+20_M+L_TID7_#1

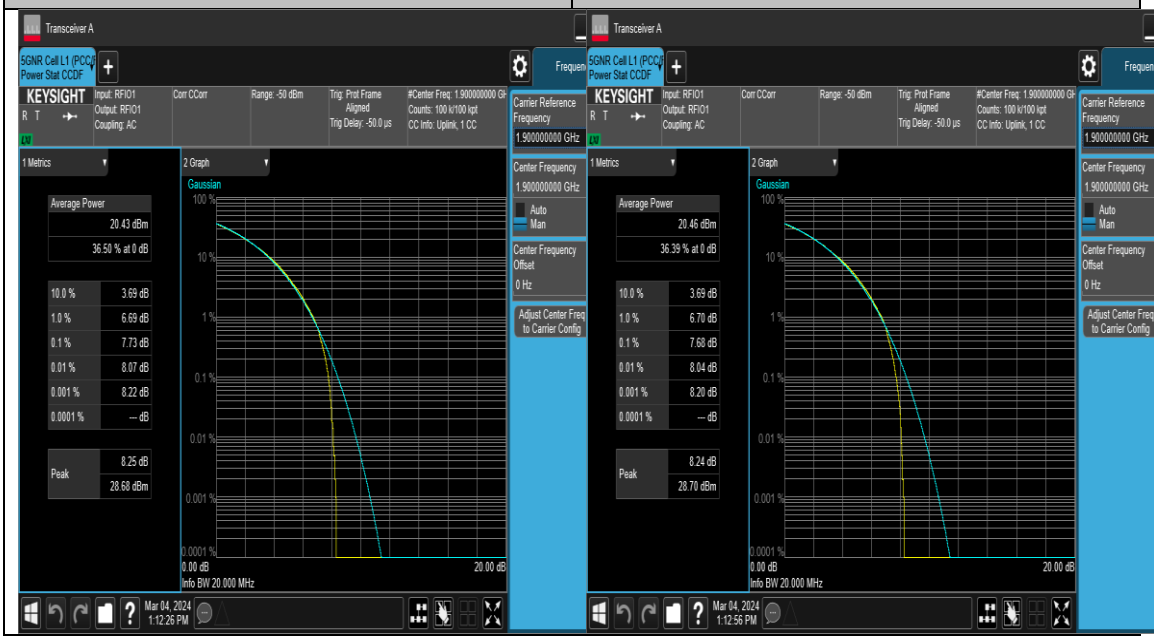
NTNV_DC_5A_n2A_PC3_15_5+20_M+M_TID6_#1



NTNV_DC_5A_n2A_PC3_15_5+20_M+M_TID7_#1



NTNV_DC_5A_n2A_PC3_15_5+20_M+H_TID6_#1	NTNV_DC_5A_n2A_PC3_15_5+20_M+H_TID7_#1

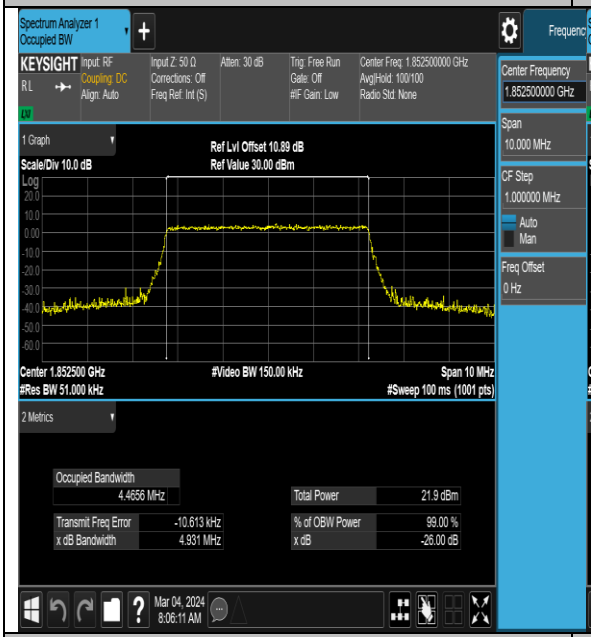
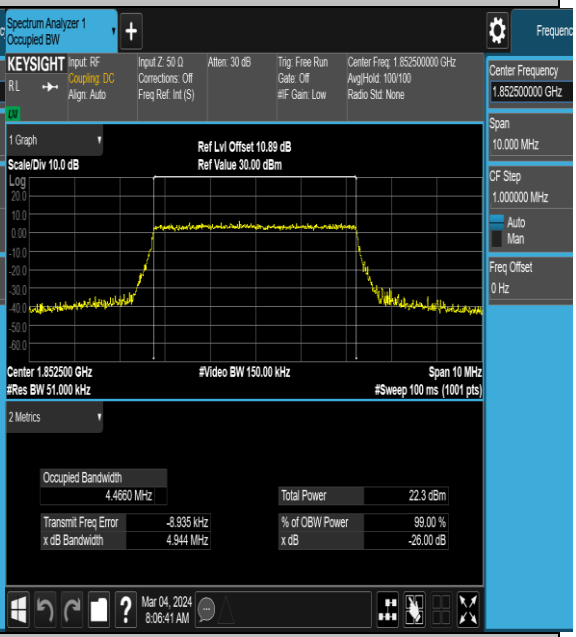
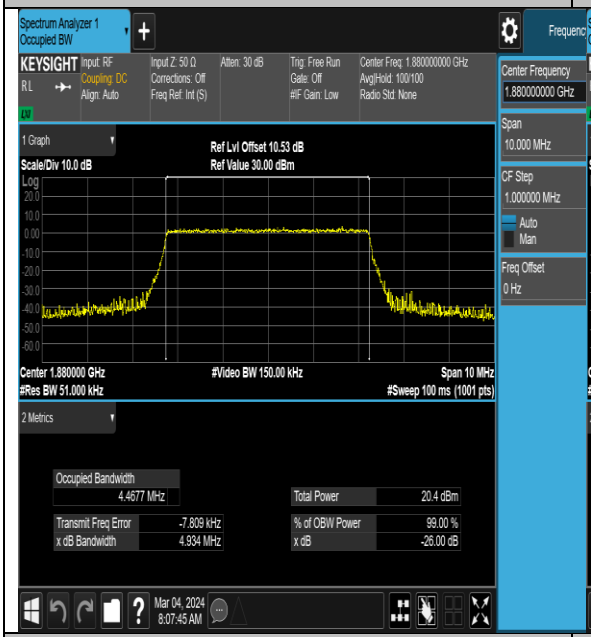
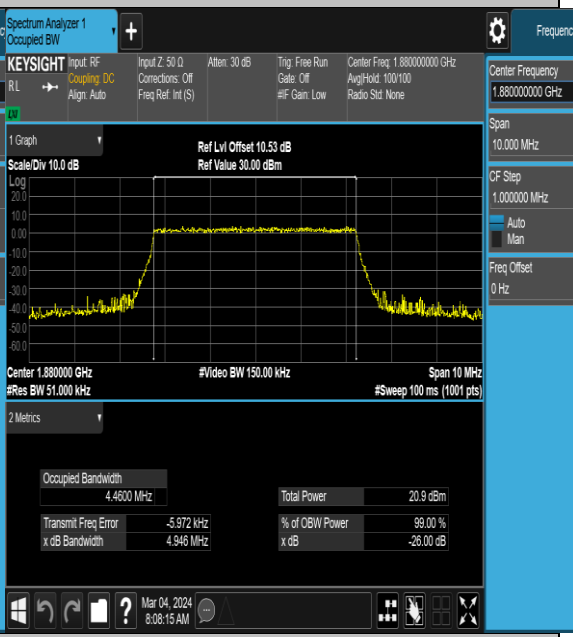


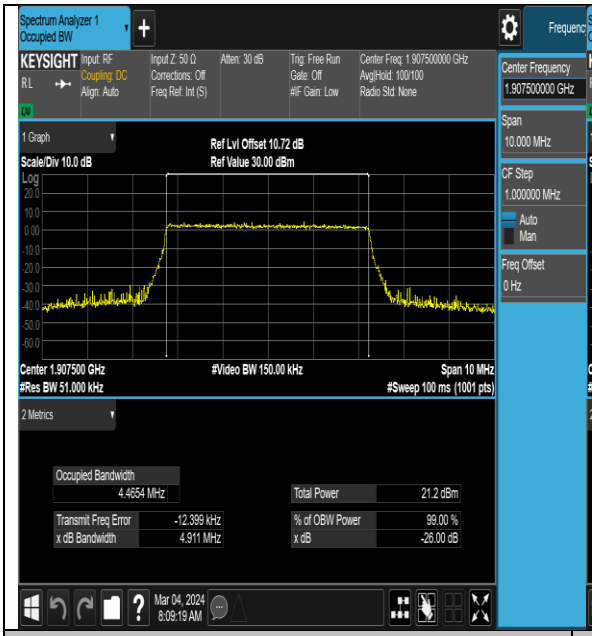
Appendix C: 26dB Bandwidth and Occupied Bandwidth for NSA

Test Result

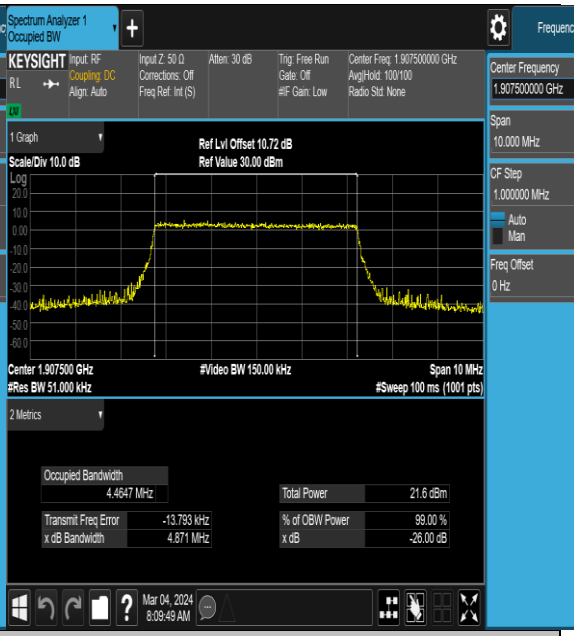
Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result (99%)	Result (26dB)	Verdict
DC_5A_n2A	15	5+5	CP-QPSK	M+L	Outer_Full	4.4656	4.931	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+L	Outer_Full	4.4660	4.944	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+M	Outer_Full	4.4677	4.934	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+M	Outer_Full	4.4600	4.946	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+H	Outer_Full	4.4654	4.911	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+H	Outer_Full	4.4647	4.871	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+L	Outer_Full	9.2737	9.969	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+L	Outer_Full	9.2576	9.900	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+M	Outer_Full	9.2665	10.23	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+M	Outer_Full	9.2478	9.897	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+H	Outer_Full	9.2587	9.855	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+H	Outer_Full	9.2570	9.841	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+L	Outer_Full	14.095	14.86	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+L	Outer_Full	14.093	14.87	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+M	Outer_Full	14.060	14.94	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+M	Outer_Full	14.043	14.84	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+H	Outer_Full	14.047	14.84	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+H	Outer_Full	14.059	14.81	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+L	Outer_Full	18.861	19.74	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+L	Outer_Full	18.901	19.75	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+M	Outer_Full	18.851	19.77	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+M	Outer_Full	18.858	19.79	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+H	Outer_Full	18.840	19.75	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+H	Outer_Full	18.849	19.76	PASS

Test Graphs

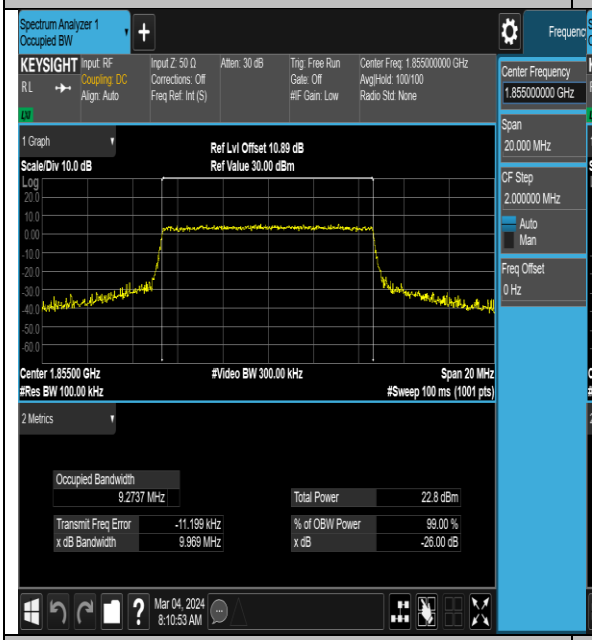
NTNV_DC_5A_n2A_PC3_15_5+5_M+L_TID5_Ant1#1	NTNV_DC_5A_n2A_PC3_15_5+5_M+L_TID6_Ant1#1
	
NTNV_DC_5A_n2A_PC3_15_5+5_M+M_TID5_Ant1#1	NTNV_DC_5A_n2A_PC3_15_5+5_M+M_TID6_Ant1#1
	
NTNV_DC_5A_n2A_PC3_15_5+5_M+H_TID5_Ant1#1	NTNV_DC_5A_n2A_PC3_15_5+5_M+H_TID6_Ant1#1



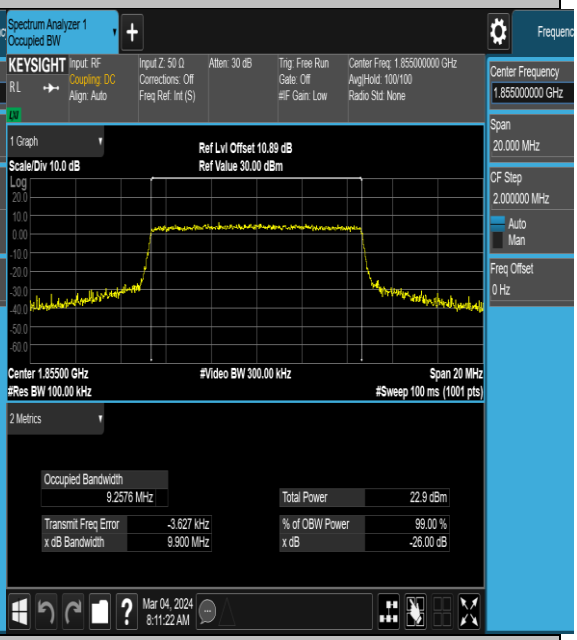
NTNV_DC_5A_n2A_PC3_15_5+10_M+L_TID5_Ant1#1



NTNV_DC_5A_n2A_PC3_15_5+10_M+L_TID6_Ant1#1



NTNV_DC_5A_n2A_PC3_15_5+10_M+M_TID5_Ant1#1



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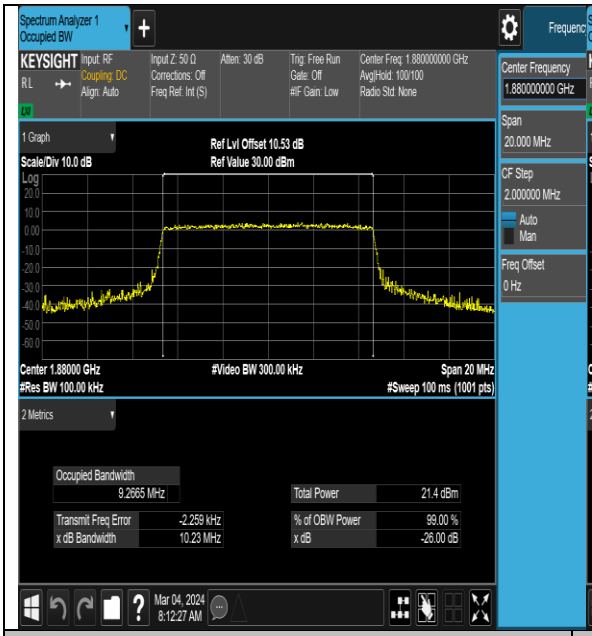

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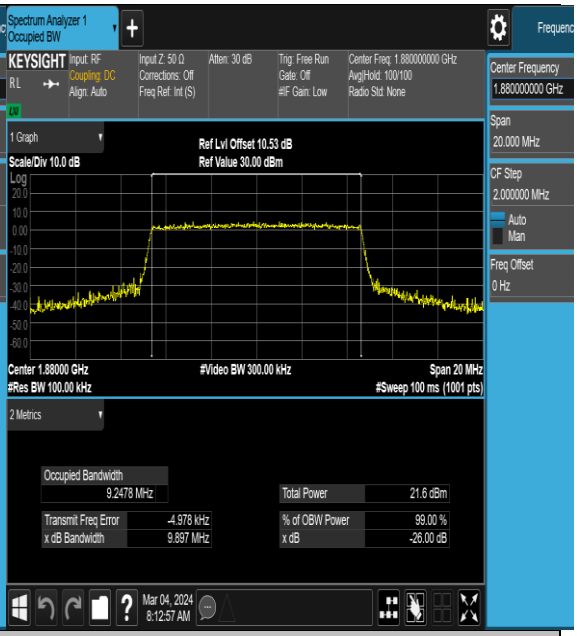
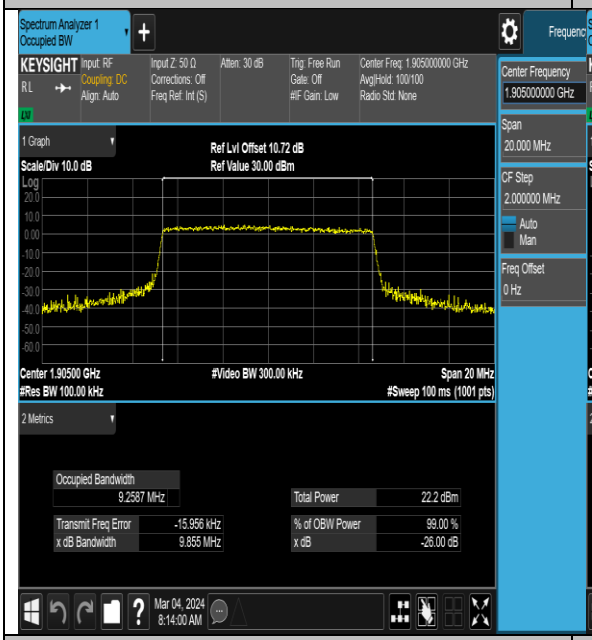
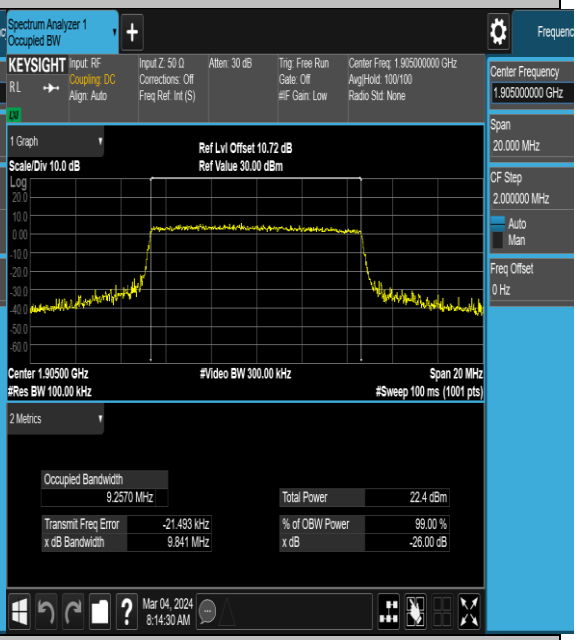


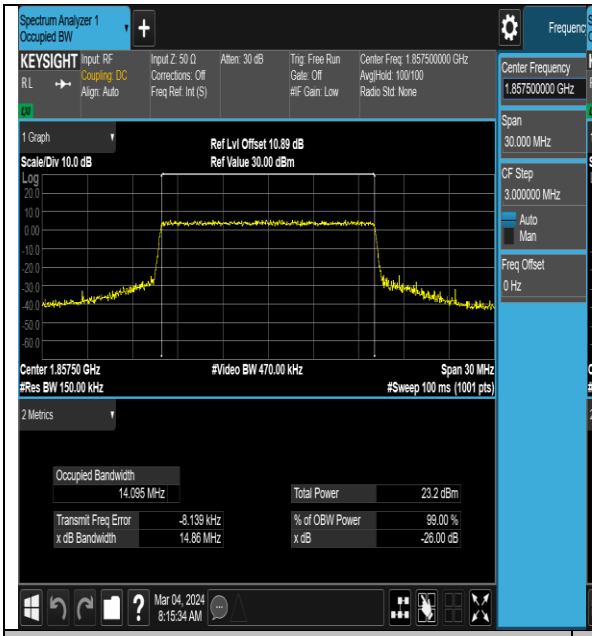
NTNV_DC_5A_n2A_PC3_15_5+10_M+H_TID6_Ant1#1



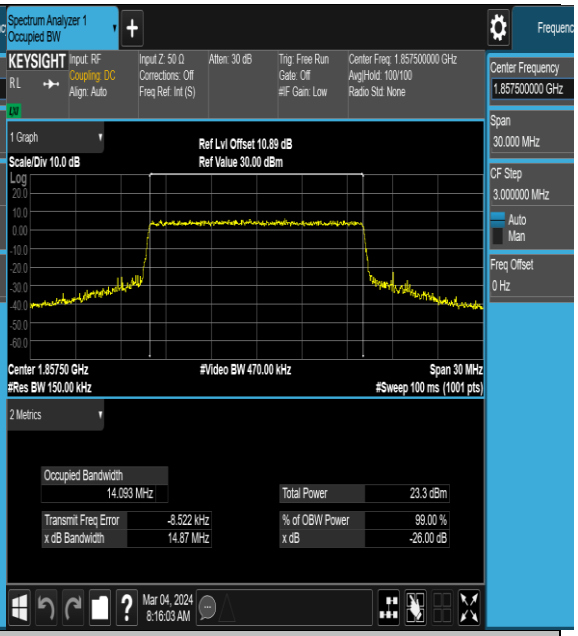
NTNV_DC_5A_n2A_PC3_15_5+15_M+L_TID5_Ant1#1



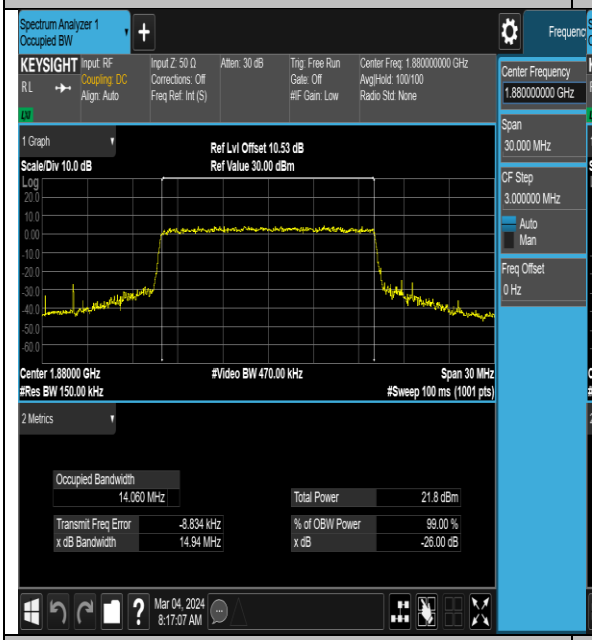
NTNV_DC_5A_n2A_PC3_15_5+15_M+L_TID6_Ant1#1



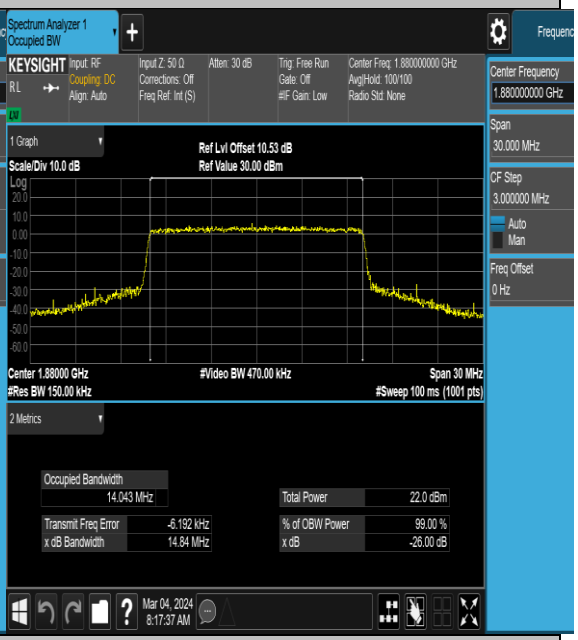
NTNV_DC_5A_n2A_PC3_15_5+15_M+M_TID5_Ant1#1



NTNV_DC_5A_n2A_PC3_15_5+15_M+M_TID6_Ant1#1



NTNV_DC_5A_n2A_PC3_15_5+15_M+H_TID5_Ant1#1



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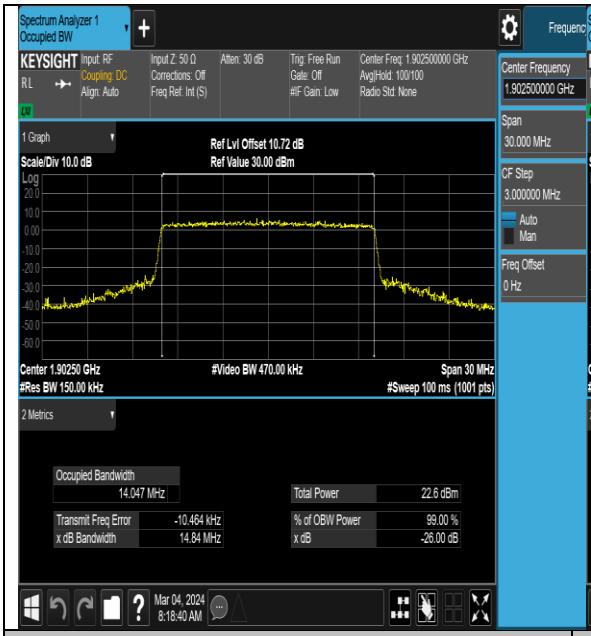

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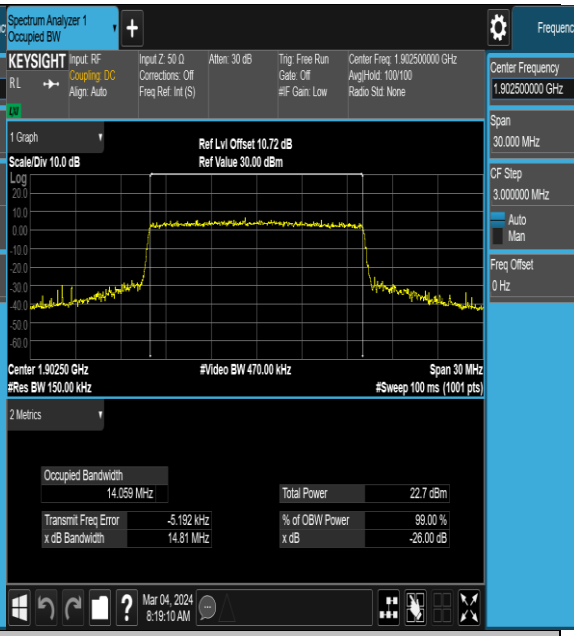

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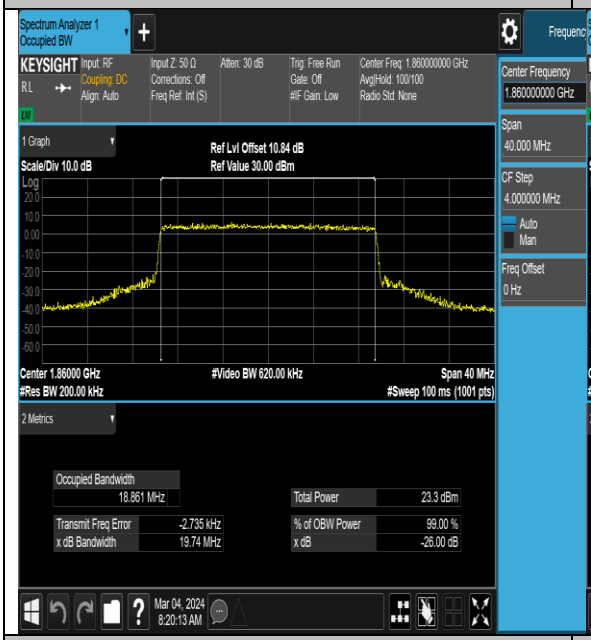

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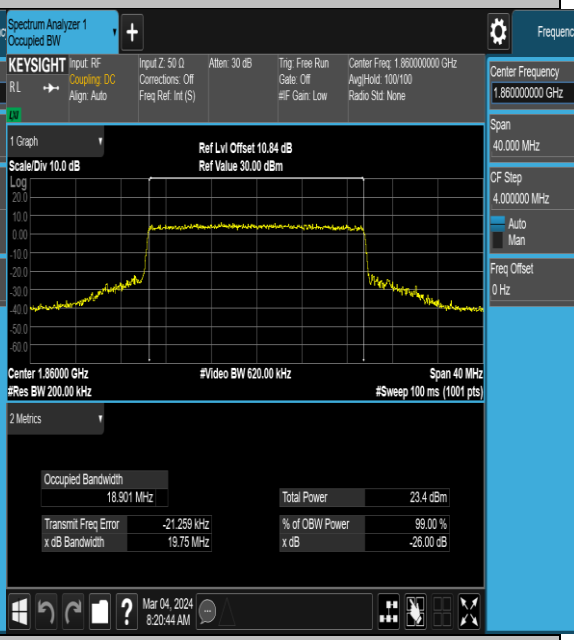
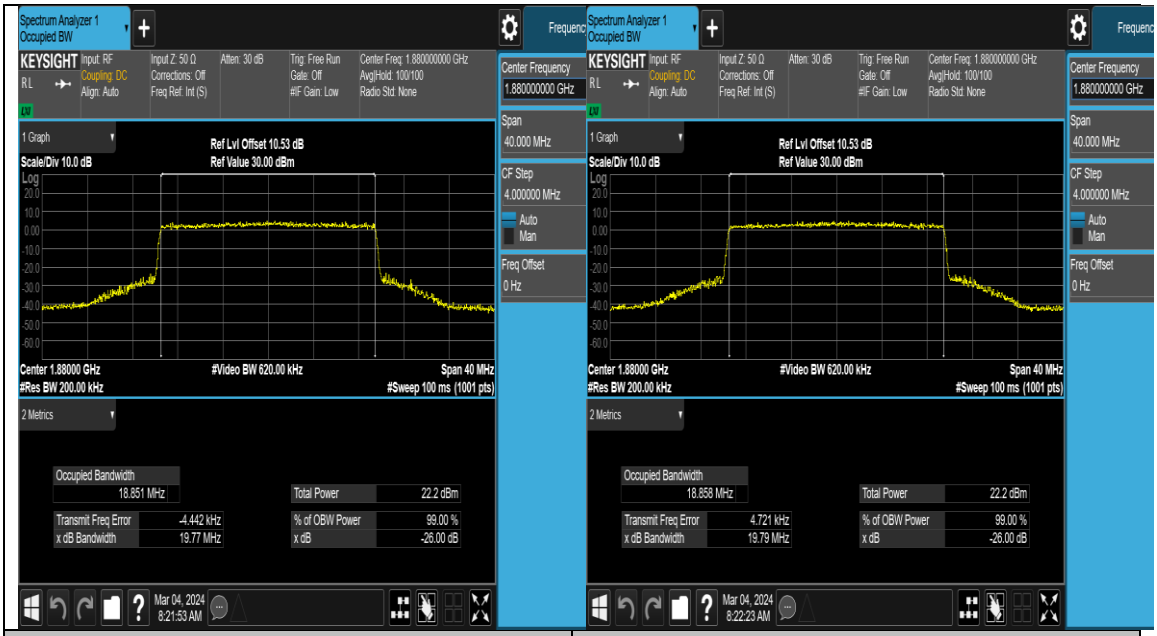
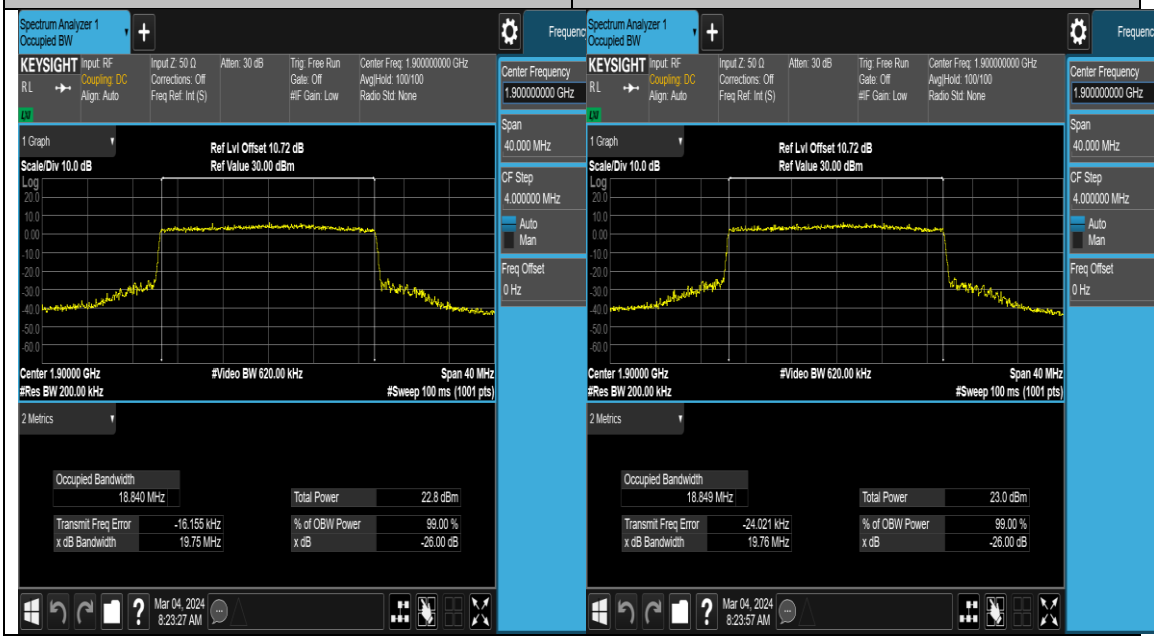


NTNV_DC_5A_n2A_PC3_15_5+20_M+M_TID6_Ant1#1



NTNV_DC_5A_n2A_PC3_15_5+20_M+H_TID5_Ant1#1	NTNV_DC_5A_n2A_PC3_15_5+20_M+H_TID6_Ant1#1

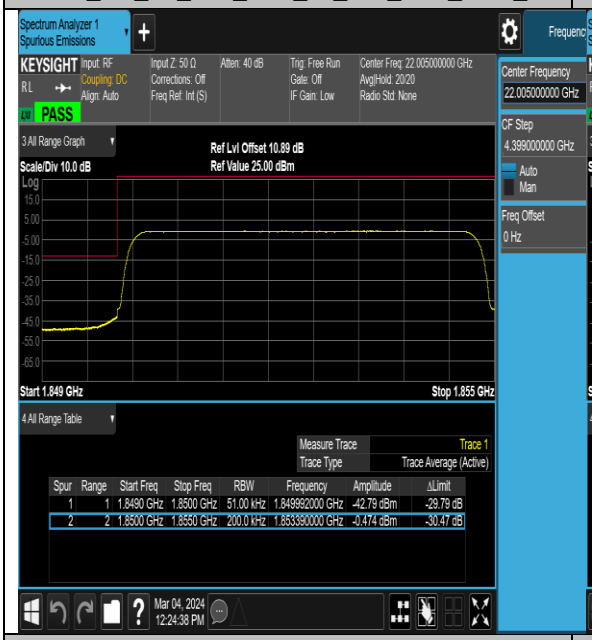
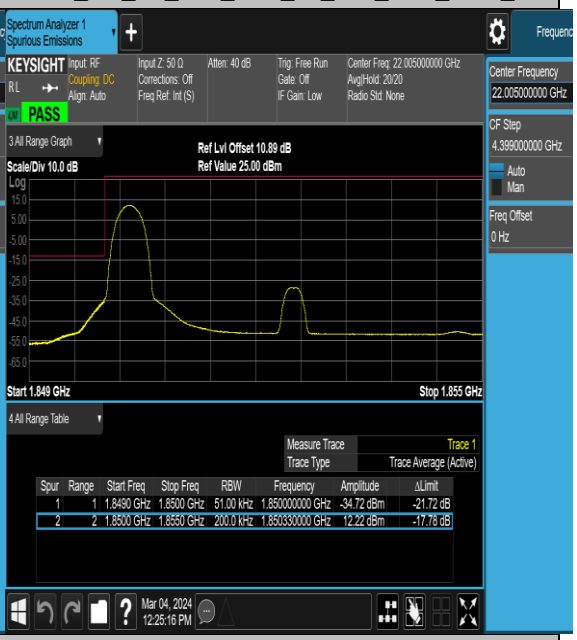
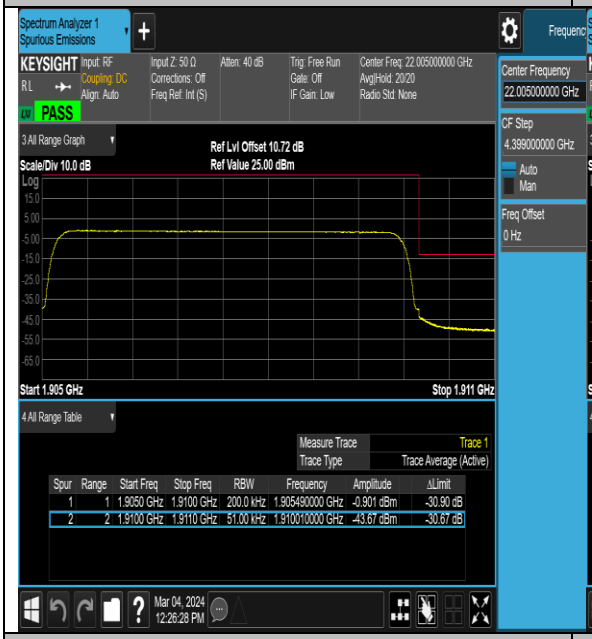
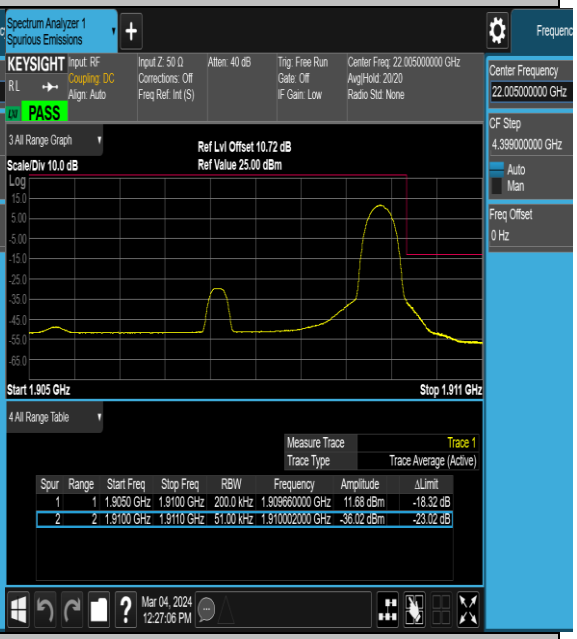


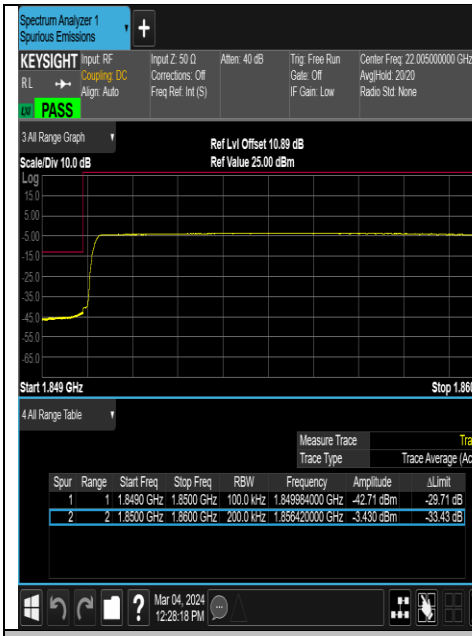
Appendix D: Band Edge for NSA

Test Result

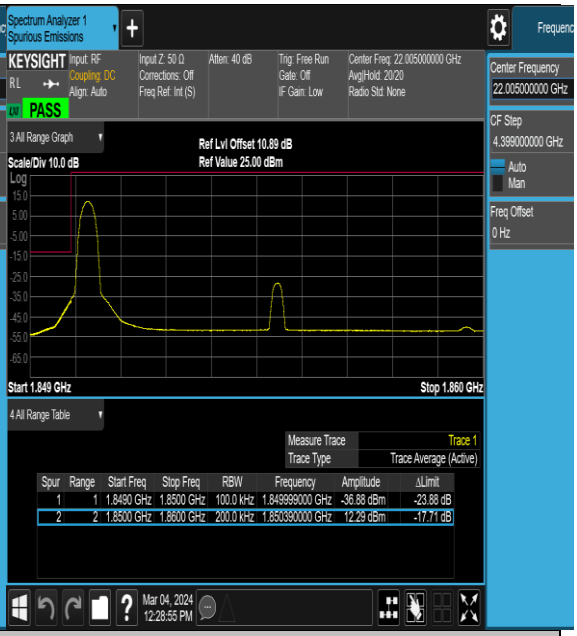
Band	SCS	Bandwidth	Modulation	Channel	RB Config	Verdict
DC_5A_n2A	15	5+5	CP-QPSK	M+L	Outer_Full	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+L	Edge_1RB_Left	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+H	Outer_Full	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+H	Edge_1RB_Right	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+L	Outer_Full	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+L	Edge_1RB_Left	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+H	Outer_Full	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+H	Edge_1RB_Right	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+L	Outer_Full	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+L	Edge_1RB_Left	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+H	Outer_Full	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+H	Edge_1RB_Right	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+L	Outer_Full	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+L	Edge_1RB_Left	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+H	Outer_Full	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+H	Edge_1RB_Right	PASS

Test Graphs

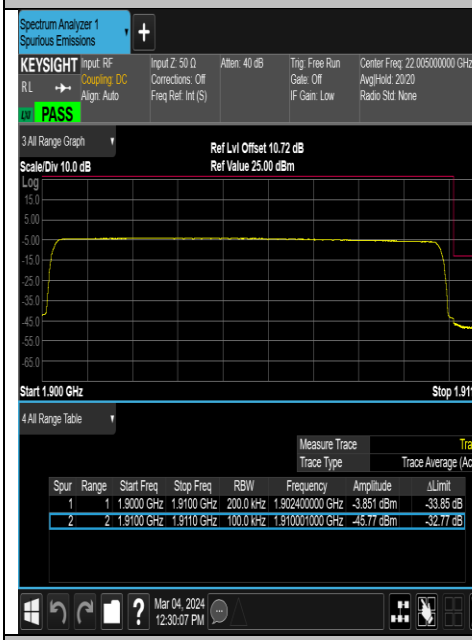
NTNV_DC_5A_n2A_PC3_15_5+5_M+L_TID6_#1	NTNV_DC_5A_n2A_PC3_15_5+5_M+L_TID4_#1
	
NTNV_DC_5A_n2A_PC3_15_5+5_M+H_TID6_#1	NTNV_DC_5A_n2A_PC3_15_5+5_M+H_TID5_#1
	
NTNV_DC_5A_n2A_PC3_15_5+10_M+L_TID6_#1	NTNV_DC_5A_n2A_PC3_15_5+10_M+L_TID4_#1



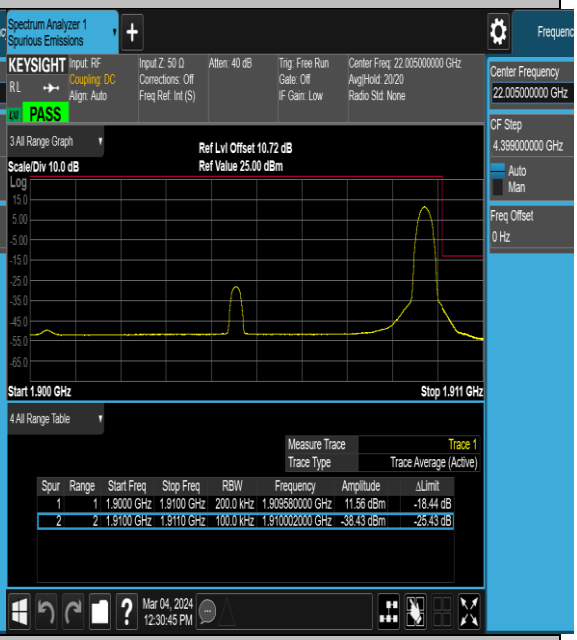
NTNV_DC_5A_n2A_PC3_15_5+10_M+H_TID6_#1



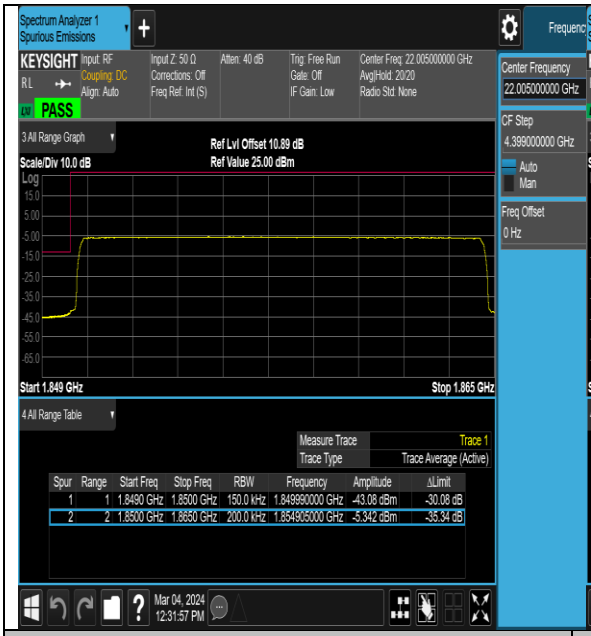
NTNV_DC_5A_n2A_PC3_15_5+10_M+H_TID5_#1



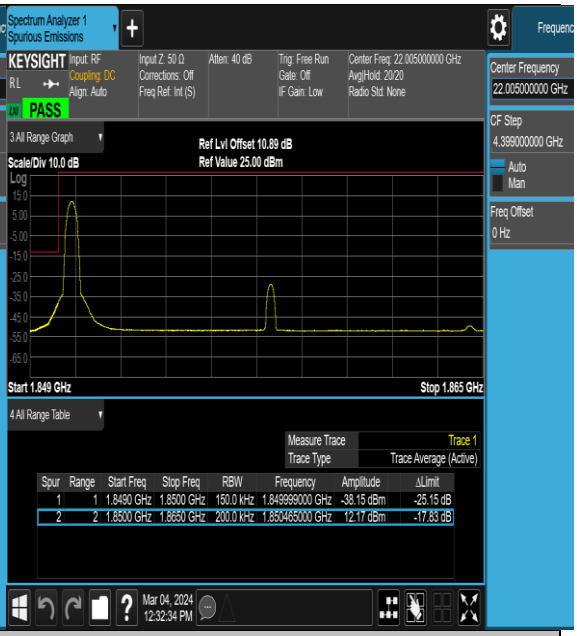
NTNV_DC_5A_n2A_PC3_15_5+15_M+L_TID6_#1



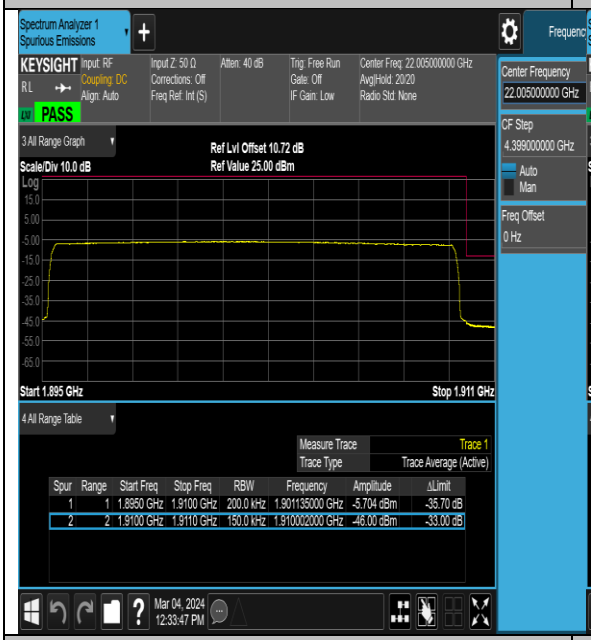
NTNV_DC_5A_n2A_PC3_15_5+15_M+L_TID4_#1



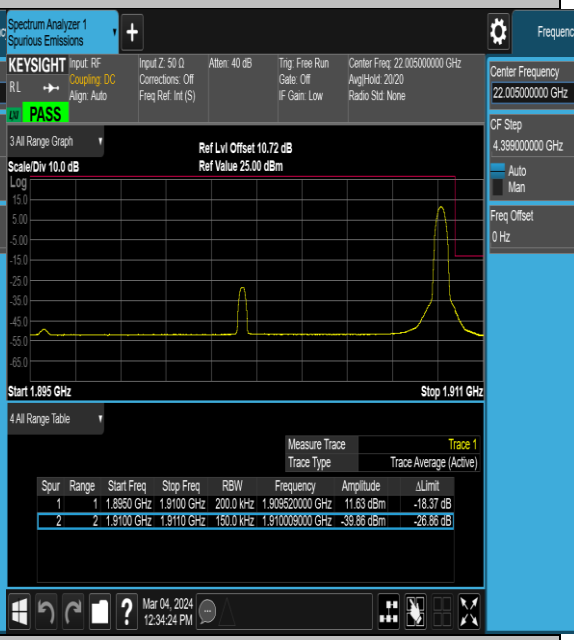
NTNV_DC_5A_n2A_PC3_15_5+15_M+H_TID6_#1



NTNV_DC_5A_n2A_PC3_15_5+15_M+H_TID5_#1



NTNV_DC_5A_n2A_PC3_15_5+20_M+L_TID6_#1



NTNV_DC_5A_n2A_PC3_15_5+20_M+L_TID4_#1

NTNV_DC_5A_n2A_PC3_15_5+20_M+H_TID6_#1	NTNV_DC_5A_n2A_PC3_15_5+20_M+H_TID5_#1

Appendix E: Conducted Spurious Emission for NSA

Test Result

Band	SCS	Band width	Modulation	Channel	RB Config	StartFreq	StopFreq	Limit	Verdict
DC_5A_n2A	15	5+5	CP-QPSK	M+L	Outer_Full	0.009	0.15	-43	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+L	Outer_Full	0.15	30	-33	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+L	Outer_Full	30	1000	-23	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+L	Outer_Full	1000	3000	-13	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+L	Outer_Full	3000	12000	-13	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+L	Outer_Full	12000	20000	-13	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+L	Edge_1RB_Left	0.15	30	-33	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+L	Edge_1RB_Left	30	1000	-23	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+L	Edge_1RB_Left	1000	3000	-13	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+L	Edge_1RB_Left	3000	12000	-13	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+L	Edge_1RB_Left	12000	20000	-13	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+L	Edge_1RB_Right	0.15	30	-33	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+L	Edge_1RB_Right	30	1000	-23	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+L	Edge_1RB_Right	1000	3000	-13	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+L	Edge_1RB_Right	3000	12000	-13	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+L	Edge_1RB_Right	12000	20000	-13	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+L	Outer_Full	0.009	0.15	-43	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+L	Outer_Full	0.15	30	-33	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+L	Outer_Full	30	1000	-23	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+L	Outer_Full	1000	3000	-13	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+L	Outer_Full	3000	12000	-13	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+L	Outer_Full	12000	20000	-13	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+L	Edge_1RB_Left	0.15	30	-33	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+L	Edge_1RB_Left	30	1000	-23	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+L	Edge_1RB_Left	1000	3000	-13	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+L	Edge_1RB_Left	3000	12000	-13	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+L	Edge_1RB_Left	12000	20000	-13	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+L	Edge_1RB_Right	0.15	30	-33	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+L	Edge_1RB_Right	30	1000	-23	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+L	Edge_1RB_Right	1000	3000	-13	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+L	Edge_1RB_Right	3000	12000	-13	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+L	Edge_1RB_Right	12000	20000	-13	PASS

DC_5A_n2A	15	5+5	CP-QPSK	M+M	Outer_Full	0.009	0.15	-43	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+M	Outer_Full	0.15	30	-33	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+M	Outer_Full	30	1000	-23	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+M	Outer_Full	1000	3000	-13	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+M	Outer_Full	3000	12000	-13	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+M	Outer_Full	12000	20000	-13	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+M	Edge_1RB_Left	0.15	30	-33	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+M	Edge_1RB_Left	30	1000	-23	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+M	Edge_1RB_Left	1000	3000	-13	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+M	Edge_1RB_Left	3000	12000	-13	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+M	Edge_1RB_Left	12000	20000	-13	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+M	Edge_1RB_Right	0.15	30	-33	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+M	Edge_1RB_Right	30	1000	-23	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+M	Edge_1RB_Right	1000	3000	-13	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+M	Edge_1RB_Right	3000	12000	-13	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+M	Edge_1RB_Right	12000	20000	-13	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+M	Outer_Full	0.009	0.15	-43	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+M	Outer_Full	0.15	30	-33	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+M	Outer_Full	30	1000	-23	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+M	Outer_Full	1000	3000	-13	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+M	Outer_Full	3000	12000	-13	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+M	Outer_Full	12000	20000	-13	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+M	Edge_1RB_Left	0.15	30	-33	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+M	Edge_1RB_Left	30	1000	-23	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+M	Edge_1RB_Left	1000	3000	-13	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+M	Edge_1RB_Left	3000	12000	-13	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+M	Edge_1RB_Left	12000	20000	-13	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+M	Edge_1RB_Right	0.15	30	-33	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+M	Edge_1RB_Right	30	1000	-23	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+M	Edge_1RB_Right	1000	3000	-13	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+M	Edge_1RB_Right	3000	12000	-13	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+M	Edge_1RB_Right	12000	20000	-13	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+H	Outer_Full	0.009	0.15	-43	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+H	Outer_Full	0.15	30	-33	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+H	Outer_Full	30	1000	-23	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+H	Outer_Full	1000	3000	-13	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+H	Outer_Full	3000	12000	-13	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+H	Outer_Full	12000	20000	-13	PASS

DC_5A_n2A	15	5+5	CP-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+H	Edge_1RB_Left	0.15	30	-33	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+H	Edge_1RB_Left	30	1000	-23	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+H	Edge_1RB_Left	1000	3000	-13	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+H	Edge_1RB_Left	3000	12000	-13	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+H	Edge_1RB_Left	12000	20000	-13	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+H	Edge_1RB_Right	0.15	30	-33	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+H	Edge_1RB_Right	30	1000	-23	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+H	Edge_1RB_Right	1000	3000	-13	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+H	Edge_1RB_Right	3000	12000	-13	PASS
DC_5A_n2A	15	5+5	CP-QPSK	M+H	Edge_1RB_Right	12000	20000	-13	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+H	Outer_Full	0.009	0.15	-43	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+H	Outer_Full	0.15	30	-33	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+H	Outer_Full	30	1000	-23	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+H	Outer_Full	1000	3000	-13	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+H	Outer_Full	3000	12000	-13	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+H	Outer_Full	12000	20000	-13	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+H	Edge_1RB_Left	0.15	30	-33	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+H	Edge_1RB_Left	30	1000	-23	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+H	Edge_1RB_Left	1000	3000	-13	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+H	Edge_1RB_Left	3000	12000	-13	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+H	Edge_1RB_Left	12000	20000	-13	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+H	Edge_1RB_Right	0.15	30	-33	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+H	Edge_1RB_Right	30	1000	-23	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+H	Edge_1RB_Right	1000	3000	-13	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+H	Edge_1RB_Right	3000	12000	-13	PASS
DC_5A_n2A	15	5+5	CP-16QAM	M+H	Edge_1RB_Right	12000	20000	-13	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+L	Outer_Full	0.009	0.15	-43	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+L	Outer_Full	0.15	30	-33	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+L	Outer_Full	30	1000	-23	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+L	Outer_Full	1000	3000	-13	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+L	Outer_Full	3000	12000	-13	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+L	Outer_Full	12000	20000	-13	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+L	Edge_1RB_Left	0.15	30	-33	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+L	Edge_1RB_Left	30	1000	-23	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+L	Edge_1RB_Left	1000	3000	-13	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+L	Edge_1RB_Left	3000	12000	-13	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+L	Edge_1RB_Left	12000	20000	-13	PASS

DC_5A_n2A	15	5+10	CP-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+L	Edge_1RB_Right	0.15	30	-33	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+L	Edge_1RB_Right	30	1000	-23	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+L	Edge_1RB_Right	1000	3000	-13	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+L	Edge_1RB_Right	3000	12000	-13	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+L	Edge_1RB_Right	12000	20000	-13	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+L	Outer_Full	0.009	0.15	-43	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+L	Outer_Full	0.15	30	-33	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+L	Outer_Full	30	1000	-23	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+L	Outer_Full	1000	3000	-13	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+L	Outer_Full	3000	12000	-13	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+L	Outer_Full	12000	20000	-13	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+L	Edge_1RB_Left	0.15	30	-33	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+L	Edge_1RB_Left	30	1000	-23	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+L	Edge_1RB_Left	1000	3000	-13	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+L	Edge_1RB_Left	3000	12000	-13	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+L	Edge_1RB_Left	12000	20000	-13	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+L	Edge_1RB_Right	0.15	30	-33	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+L	Edge_1RB_Right	30	1000	-23	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+L	Edge_1RB_Right	1000	3000	-13	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+L	Edge_1RB_Right	3000	12000	-13	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+L	Edge_1RB_Right	12000	20000	-13	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+M	Outer_Full	0.009	0.15	-43	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+M	Outer_Full	0.15	30	-33	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+M	Outer_Full	30	1000	-23	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+M	Outer_Full	1000	3000	-13	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+M	Outer_Full	3000	12000	-13	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+M	Outer_Full	12000	20000	-13	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+M	Edge_1RB_Left	0.15	30	-33	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+M	Edge_1RB_Left	30	1000	-23	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+M	Edge_1RB_Left	1000	3000	-13	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+M	Edge_1RB_Left	3000	12000	-13	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+M	Edge_1RB_Left	12000	20000	-13	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+M	Edge_1RB_Right	0.15	30	-33	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+M	Edge_1RB_Right	30	1000	-23	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+M	Edge_1RB_Right	1000	3000	-13	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+M	Edge_1RB_Right	3000	12000	-13	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+M	Edge_1RB_Right	12000	20000	-13	PASS

DC_5A_n2A	15	5+10	CP-16QAM	M+M	Outer_Full	0.009	0.15	-43	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+M	Outer_Full	0.15	30	-33	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+M	Outer_Full	30	1000	-23	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+M	Outer_Full	1000	3000	-13	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+M	Outer_Full	3000	12000	-13	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+M	Outer_Full	12000	20000	-13	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+M	Edge_1RB_Left	0.15	30	-33	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+M	Edge_1RB_Left	30	1000	-23	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+M	Edge_1RB_Left	1000	3000	-13	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+M	Edge_1RB_Left	3000	12000	-13	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+M	Edge_1RB_Left	12000	20000	-13	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+M	Edge_1RB_Right	0.15	30	-33	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+M	Edge_1RB_Right	30	1000	-23	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+M	Edge_1RB_Right	1000	3000	-13	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+M	Edge_1RB_Right	3000	12000	-13	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+M	Edge_1RB_Right	12000	20000	-13	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+H	Outer_Full	0.009	0.15	-43	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+H	Outer_Full	0.15	30	-33	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+H	Outer_Full	30	1000	-23	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+H	Outer_Full	1000	3000	-13	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+H	Outer_Full	3000	12000	-13	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+H	Outer_Full	12000	20000	-13	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+H	Edge_1RB_Left	0.15	30	-33	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+H	Edge_1RB_Left	30	1000	-23	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+H	Edge_1RB_Left	1000	3000	-13	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+H	Edge_1RB_Left	3000	12000	-13	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+H	Edge_1RB_Left	12000	20000	-13	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+H	Edge_1RB_Right	0.15	30	-33	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+H	Edge_1RB_Right	30	1000	-23	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+H	Edge_1RB_Right	1000	3000	-13	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+H	Edge_1RB_Right	3000	12000	-13	PASS
DC_5A_n2A	15	5+10	CP-QPSK	M+H	Edge_1RB_Right	12000	20000	-13	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+H	Outer_Full	0.009	0.15	-43	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+H	Outer_Full	0.15	30	-33	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+H	Outer_Full	30	1000	-23	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+H	Outer_Full	1000	3000	-13	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+H	Outer_Full	3000	12000	-13	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+H	Outer_Full	12000	20000	-13	PASS

DC_5A_n2A	15	5+10	CP-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+H	Edge_1RB_Left	0.15	30	-33	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+H	Edge_1RB_Left	30	1000	-23	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+H	Edge_1RB_Left	1000	3000	-13	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+H	Edge_1RB_Left	3000	12000	-13	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+H	Edge_1RB_Left	12000	20000	-13	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+H	Edge_1RB_Right	0.15	30	-33	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+H	Edge_1RB_Right	30	1000	-23	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+H	Edge_1RB_Right	1000	3000	-13	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+H	Edge_1RB_Right	3000	12000	-13	PASS
DC_5A_n2A	15	5+10	CP-16QAM	M+H	Edge_1RB_Right	12000	20000	-13	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+L	Outer_Full	0.009	0.15	-43	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+L	Outer_Full	0.15	30	-33	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+L	Outer_Full	30	1000	-23	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+L	Outer_Full	1000	3000	-13	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+L	Outer_Full	3000	12000	-13	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+L	Outer_Full	12000	20000	-13	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+L	Edge_1RB_Left	0.15	30	-33	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+L	Edge_1RB_Left	30	1000	-23	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+L	Edge_1RB_Left	1000	3000	-13	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+L	Edge_1RB_Left	3000	12000	-13	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+L	Edge_1RB_Left	12000	20000	-13	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+L	Edge_1RB_Right	0.15	30	-33	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+L	Edge_1RB_Right	30	1000	-23	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+L	Edge_1RB_Right	1000	3000	-13	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+L	Edge_1RB_Right	3000	12000	-13	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+L	Edge_1RB_Right	12000	20000	-13	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+L	Outer_Full	0.009	0.15	-43	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+L	Outer_Full	0.15	30	-33	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+L	Outer_Full	30	1000	-23	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+L	Outer_Full	1000	3000	-13	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+L	Outer_Full	3000	12000	-13	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+L	Outer_Full	12000	20000	-13	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+L	Edge_1RB_Left	0.15	30	-33	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+L	Edge_1RB_Left	30	1000	-23	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+L	Edge_1RB_Left	1000	3000	-13	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+L	Edge_1RB_Left	3000	12000	-13	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+L	Edge_1RB_Left	12000	20000	-13	PASS

DC_5A_n2A	15	5+15	CP-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+L	Edge_1RB_Right	0.15	30	-33	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+L	Edge_1RB_Right	30	1000	-23	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+L	Edge_1RB_Right	1000	3000	-13	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+L	Edge_1RB_Right	3000	12000	-13	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+L	Edge_1RB_Right	12000	20000	-13	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+M	Outer_Full	0.009	0.15	-43	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+M	Outer_Full	0.15	30	-33	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+M	Outer_Full	30	1000	-23	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+M	Outer_Full	1000	3000	-13	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+M	Outer_Full	3000	12000	-13	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+M	Outer_Full	12000	20000	-13	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+M	Edge_1RB_Left	0.15	30	-33	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+M	Edge_1RB_Left	30	1000	-23	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+M	Edge_1RB_Left	1000	3000	-13	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+M	Edge_1RB_Left	3000	12000	-13	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+M	Edge_1RB_Left	12000	20000	-13	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+M	Edge_1RB_Right	0.15	30	-33	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+M	Edge_1RB_Right	30	1000	-23	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+M	Edge_1RB_Right	1000	3000	-13	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+M	Edge_1RB_Right	3000	12000	-13	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+M	Edge_1RB_Right	12000	20000	-13	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+M	Outer_Full	0.009	0.15	-43	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+M	Outer_Full	0.15	30	-33	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+M	Outer_Full	30	1000	-23	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+M	Outer_Full	1000	3000	-13	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+M	Outer_Full	3000	12000	-13	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+M	Outer_Full	12000	20000	-13	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+M	Edge_1RB_Left	0.15	30	-33	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+M	Edge_1RB_Left	30	1000	-23	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+M	Edge_1RB_Left	1000	3000	-13	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+M	Edge_1RB_Left	3000	12000	-13	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+M	Edge_1RB_Left	12000	20000	-13	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+M	Edge_1RB_Right	0.15	30	-33	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+M	Edge_1RB_Right	30	1000	-23	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+M	Edge_1RB_Right	1000	3000	-13	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+M	Edge_1RB_Right	3000	12000	-13	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+M	Edge_1RB_Right	12000	20000	-13	PASS

DC_5A_n2A	15	5+15	CP-QPSK	M+H	Outer_Full	0.009	0.15	-43	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+H	Outer_Full	0.15	30	-33	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+H	Outer_Full	30	1000	-23	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+H	Outer_Full	1000	3000	-13	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+H	Outer_Full	3000	12000	-13	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+H	Outer_Full	12000	20000	-13	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+H	Edge_1RB_Left	0.15	30	-33	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+H	Edge_1RB_Left	30	1000	-23	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+H	Edge_1RB_Left	1000	3000	-13	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+H	Edge_1RB_Left	3000	12000	-13	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+H	Edge_1RB_Left	12000	20000	-13	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+H	Edge_1RB_Right	0.15	30	-33	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+H	Edge_1RB_Right	30	1000	-23	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+H	Edge_1RB_Right	1000	3000	-13	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+H	Edge_1RB_Right	3000	12000	-13	PASS
DC_5A_n2A	15	5+15	CP-QPSK	M+H	Edge_1RB_Right	12000	20000	-13	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+H	Outer_Full	0.009	0.15	-43	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+H	Outer_Full	0.15	30	-33	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+H	Outer_Full	30	1000	-23	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+H	Outer_Full	1000	3000	-13	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+H	Outer_Full	3000	12000	-13	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+H	Outer_Full	12000	20000	-13	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+H	Edge_1RB_Left	0.15	30	-33	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+H	Edge_1RB_Left	30	1000	-23	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+H	Edge_1RB_Left	1000	3000	-13	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+H	Edge_1RB_Left	3000	12000	-13	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+H	Edge_1RB_Left	12000	20000	-13	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+H	Edge_1RB_Right	0.15	30	-33	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+H	Edge_1RB_Right	30	1000	-23	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+H	Edge_1RB_Right	1000	3000	-13	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+H	Edge_1RB_Right	3000	12000	-13	PASS
DC_5A_n2A	15	5+15	CP-16QAM	M+H	Edge_1RB_Right	12000	20000	-13	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+L	Outer_Full	0.009	0.15	-43	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+L	Outer_Full	0.15	30	-33	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+L	Outer_Full	30	1000	-23	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+L	Outer_Full	1000	3000	-13	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+L	Outer_Full	3000	12000	-13	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+L	Outer_Full	12000	20000	-13	PASS

DC_5A_n2A	15	5+20	CP-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+L	Edge_1RB_Left	0.15	30	-33	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+L	Edge_1RB_Left	30	1000	-23	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+L	Edge_1RB_Left	1000	3000	-13	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+L	Edge_1RB_Left	3000	12000	-13	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+L	Edge_1RB_Left	12000	20000	-13	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+L	Edge_1RB_Right	0.15	30	-33	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+L	Edge_1RB_Right	30	1000	-23	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+L	Edge_1RB_Right	1000	3000	-13	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+L	Edge_1RB_Right	3000	12000	-13	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+L	Edge_1RB_Right	12000	20000	-13	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+L	Outer_Full	0.009	0.15	-43	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+L	Outer_Full	0.15	30	-33	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+L	Outer_Full	30	1000	-23	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+L	Outer_Full	1000	3000	-13	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+L	Outer_Full	3000	12000	-13	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+L	Outer_Full	12000	20000	-13	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+L	Edge_1RB_Left	0.15	30	-33	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+L	Edge_1RB_Left	30	1000	-23	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+L	Edge_1RB_Left	1000	3000	-13	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+L	Edge_1RB_Left	3000	12000	-13	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+L	Edge_1RB_Left	12000	20000	-13	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+L	Edge_1RB_Right	0.15	30	-33	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+L	Edge_1RB_Right	30	1000	-23	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+L	Edge_1RB_Right	1000	3000	-13	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+L	Edge_1RB_Right	3000	12000	-13	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+L	Edge_1RB_Right	12000	20000	-13	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+M	Outer_Full	0.009	0.15	-43	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+M	Outer_Full	0.15	30	-33	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+M	Outer_Full	30	1000	-23	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+M	Outer_Full	1000	3000	-13	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+M	Outer_Full	3000	12000	-13	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+M	Outer_Full	12000	20000	-13	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+M	Edge_1RB_Left	0.15	30	-33	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+M	Edge_1RB_Left	30	1000	-23	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+M	Edge_1RB_Left	1000	3000	-13	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+M	Edge_1RB_Left	3000	12000	-13	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+M	Edge_1RB_Left	12000	20000	-13	PASS

DC_5A_n2A	15	5+20	CP-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+M	Edge_1RB_Right	0.15	30	-33	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+M	Edge_1RB_Right	30	1000	-23	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+M	Edge_1RB_Right	1000	3000	-13	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+M	Edge_1RB_Right	3000	12000	-13	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+M	Edge_1RB_Right	12000	20000	-13	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+M	Outer_Full	0.009	0.15	-43	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+M	Outer_Full	0.15	30	-33	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+M	Outer_Full	30	1000	-23	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+M	Outer_Full	1000	3000	-13	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+M	Outer_Full	3000	12000	-13	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+M	Outer_Full	12000	20000	-13	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+M	Edge_1RB_Left	0.15	30	-33	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+M	Edge_1RB_Left	30	1000	-23	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+M	Edge_1RB_Left	1000	3000	-13	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+M	Edge_1RB_Left	3000	12000	-13	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+M	Edge_1RB_Left	12000	20000	-13	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+M	Edge_1RB_Right	0.15	30	-33	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+M	Edge_1RB_Right	30	1000	-23	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+M	Edge_1RB_Right	1000	3000	-13	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+M	Edge_1RB_Right	3000	12000	-13	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+M	Edge_1RB_Right	12000	20000	-13	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+H	Outer_Full	0.009	0.15	-43	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+H	Outer_Full	0.15	30	-33	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+H	Outer_Full	30	1000	-23	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+H	Outer_Full	1000	3000	-13	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+H	Outer_Full	3000	12000	-13	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+H	Outer_Full	12000	20000	-13	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+H	Edge_1RB_Left	0.15	30	-33	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+H	Edge_1RB_Left	30	1000	-23	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+H	Edge_1RB_Left	1000	3000	-13	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+H	Edge_1RB_Left	3000	12000	-13	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+H	Edge_1RB_Left	12000	20000	-13	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+H	Edge_1RB_Right	0.15	30	-33	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+H	Edge_1RB_Right	30	1000	-23	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+H	Edge_1RB_Right	1000	3000	-13	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+H	Edge_1RB_Right	3000	12000	-13	PASS
DC_5A_n2A	15	5+20	CP-QPSK	M+H	Edge_1RB_Right	12000	20000	-13	PASS

DC_5A_n2A	15	5+20	CP-16QAM	M+H	Outer_Full	0.009	0.15	-43	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+H	Outer_Full	0.15	30	-33	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+H	Outer_Full	30	1000	-23	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+H	Outer_Full	1000	3000	-13	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+H	Outer_Full	3000	12000	-13	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+H	Outer_Full	12000	20000	-13	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+H	Edge_1RB_Left	0.15	30	-33	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+H	Edge_1RB_Left	30	1000	-23	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+H	Edge_1RB_Left	1000	3000	-13	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+H	Edge_1RB_Left	3000	12000	-13	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+H	Edge_1RB_Left	12000	20000	-13	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+H	Edge_1RB_Right	0.15	30	-33	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+H	Edge_1RB_Right	30	1000	-23	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+H	Edge_1RB_Right	1000	3000	-13	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+H	Edge_1RB_Right	3000	12000	-13	PASS
DC_5A_n2A	15	5+20	CP-16QAM	M+H	Edge_1RB_Right	12000	20000	-13	PASS