## **BLOCK DIAGRAM OF THE DEVICE**

FCC CFR47,SECTION 2.1033(5). For Part 15 A block diagram showing the frequency of all oscillators in the device. IC RSP-100, SECTION 8.3.1 The documents ... shall include: ... block diagrams, schematics and product literature.

	. 01.8												
-	<-SW1_ <-SW1_	_											
	<- SW1_	.c Dip switches											
	<-SW1_ <-SW1_												
	(-2W	E											
	Vswitch1->												
	vswitch1->												
											Matching Network		
											(C58, C59, L10, R7)		
	Vswitch2 ->											<- RF_In	Antenna 1
· 	721152112									Antenna			
00011 55410										Switching Network (U3)	Matching Network (C38, C60, L11, R6)		
SOC Micro + RF (U1)											(C30, C00, E11, 110)	<- RF_In	Antenna 2
<u>:</u>		26 MHz Crystal			0.001								
					3.3V →								
							LNA						
		RF Balum (R2, R3, L2, C3,		LNA Matching Network (L6, R64,		LNA (U3)	Matching Network (L:	Mixed IF Stage: 345MHz Filter					
		L1, C1, C2)	<-> RFMicro	C6)			C9, C7)		<- RX_Path				
_	Desense ->												
	LED_Red->	Gren LED											
	LED_Yellow	> Yellow LED (DNP)											
_		ODEEN LED											
-	LED_Green -	> GREEN LED											