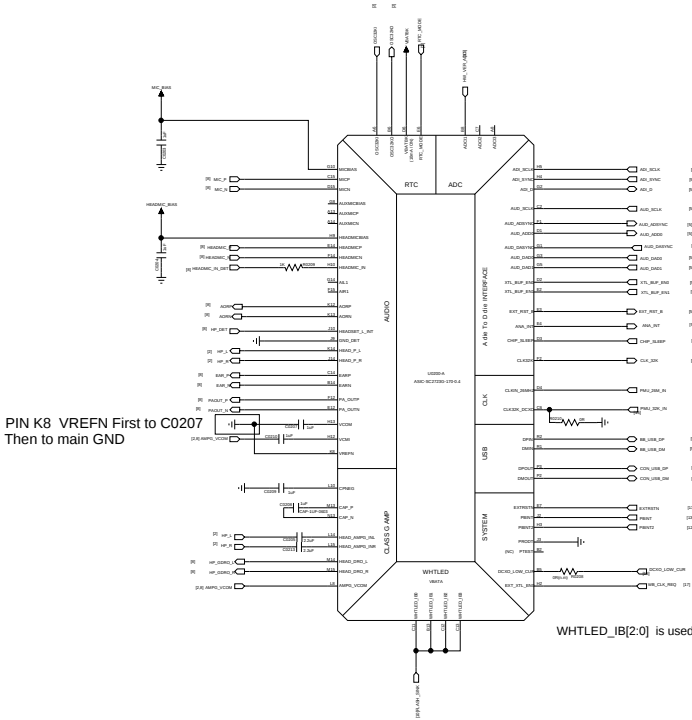


REVISION HISTORY			
VER.	REV. NO.	DESCRIPTION	DATE

PMU_ANALOG



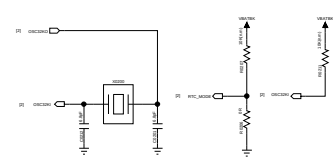
PIN K8 VREFN First to C0207
Then to main GND

WHTLED_IB[2:0] is used for RGB LED if need.

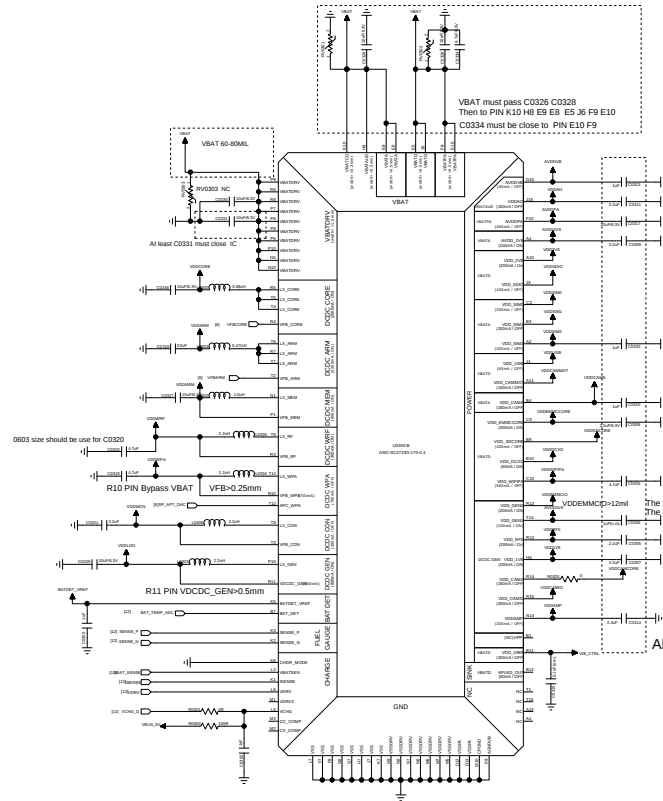
BKBT



RTC



SC2723 LDO output



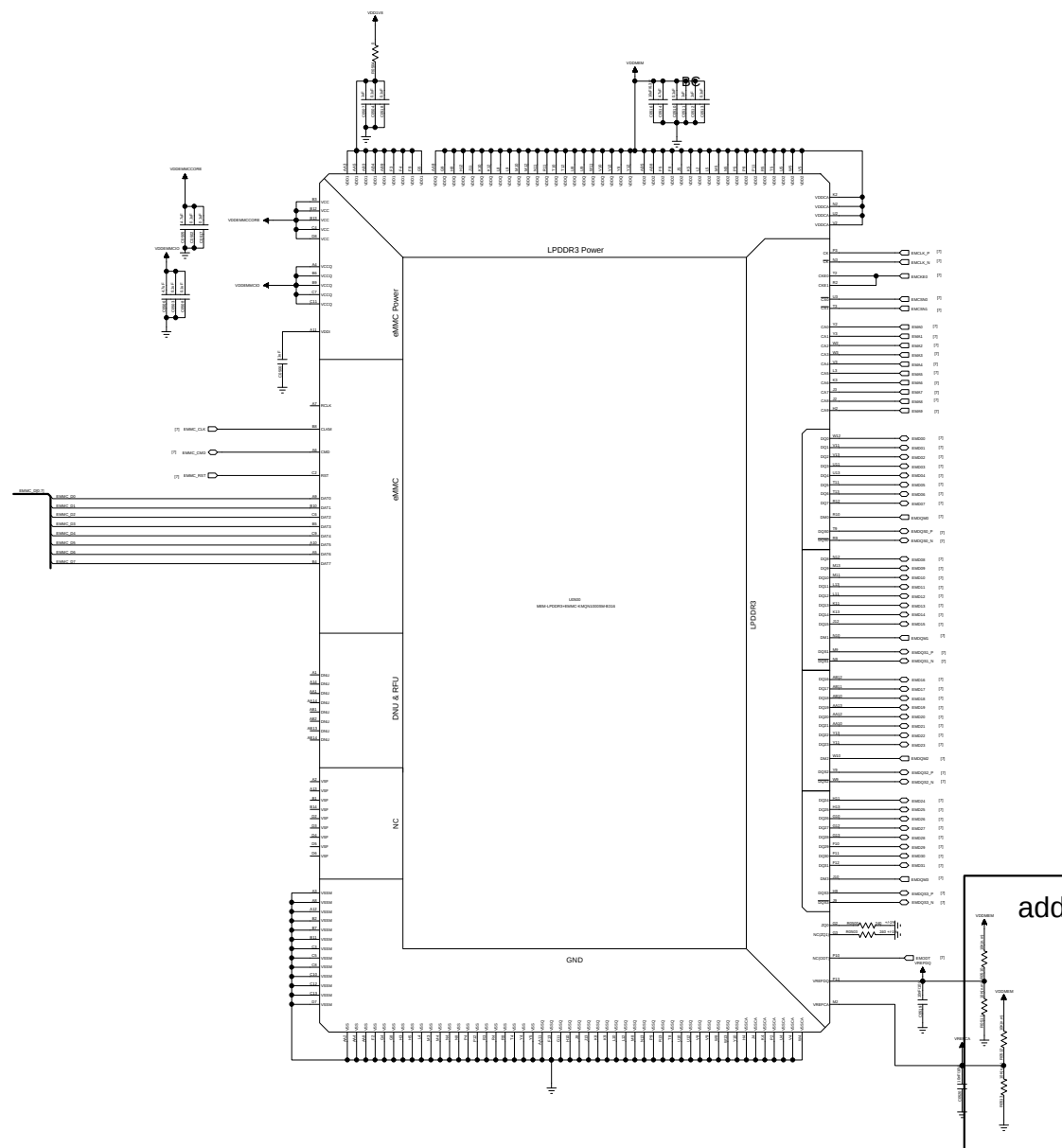
The power of U0200.R12 and U0200.T14 pin is swapped,
The change should be modified by SW

⋮ All capacitors must be close to U0200.

Power	Output Voltage (V)	Output Current	Default Voltage	Default
VDD1V8	1.2~1.8	200mA	1.8V	ON
AVDD28	1.8~3.3	100mA	2.8V	ON
VDD2V8	1.8~3.3	200mA	2.8V	ON
VDDSIM0	1.8~3.3	200mA		OFF
VDDSIM1	1.8~3.3	200mA		OFF
VDDSIM2	1.8~3.3	200mA		OFF
VDDRF0	1.2~1.8	200mA	1.8V	ON
VDDCAMA	1.8~3.3	150mA		OFF
VDDCAMMOT	1.8~3.3	100mA		OFF
VDDEMMCCORE	1.8~3.3	300mA	3.0V	ON
VDDSDCORE	1.8~3.3	300mA		OFF
VDDSDIO	1.8~3.3	100mA		OFF
VBATBK	2.6/2.8/3.0/3.2	10mA	2.8V	ON
VDDVIB	1.8~3.3	200mA		OFF
KPLED_OUT	1.8~3.3	50mA		OFF
VDD_WIFI0	1.8~3.3	500mA		OFF
VDD_DCXO	1.8~3.3	50mA	1.8V	ON
VDDCAMD	1.2~1.8	150mA		OFF
VDDUSB	1.8~3.3	60mA	3.3V	OFF
VDDAO	2.9/3.1/3.2/3.3	150mA		OFF
AVDDVB	2.9/3.1/3.2/3.3	30mA		OFF
AVDDPA	2.9/3.1/3.2/3.3	400mA		OFF
VDDCAMIO	1.2~1.8	200mA		OFF
AVDDAMP	1.2~1.8	200mA		OFF
VDDGEN0	1.2~1.8	200mA	1.8V	ON
VDDGEN1	1.2~1.8	100mA	1.8V	ON
DCDCARM	0.6~3.7	3000mA	0.9V	ON
DCDCCORE	0.6~3.7	2000mA	0.9V	ON
DCDCMEM	0.6~3.7	800mA	1.2V	ON
DCDC_GEN	0.6~3.7	800mA	2.2V	ON
DCDCRF	0.6~3.7	300mA	1.5V	ON
DCDCWPA	0.6~3.7	700mA		OFF
DCDCCON	0.6~3.7	300mA		OFF

REVISION RECORD			
LTN	BID NO	APPROVED	SA

eMMC+LPDDR3



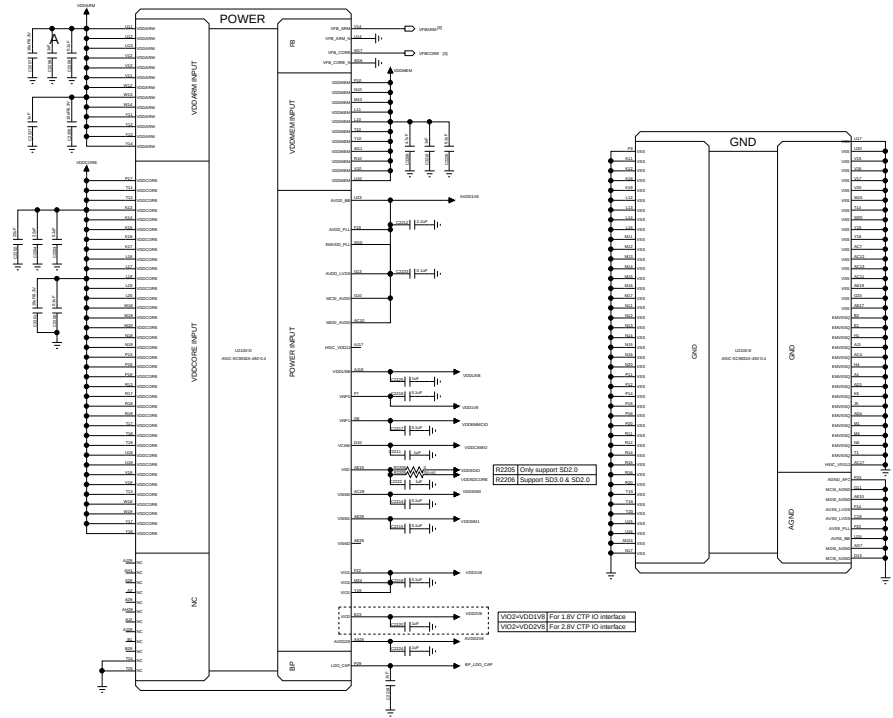
DR SOURCE: Ryeir Du		DATE: 2015-10-12	TOTAL & REV: SP9832A-2 CS SCH V0.1.0	
RQA: Mulder Han		DATE: 2015-10-12	DOCUMENT NO.:	
APPROVED: Gao Xu		DATE: 2015-10-13	EXP-DEPT: Hardware DEPT. COMPANY: Spreadtrum communications, Inc. CONFIDENTIAL AND PROPRIETARY	
			SHEET: 5 OF 23	

D



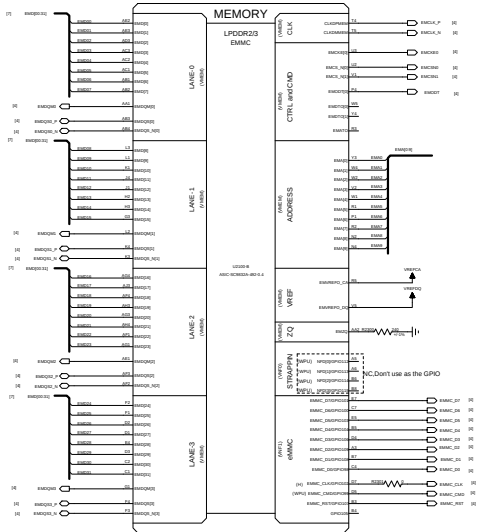
REVISION HISTORY			
VER.	REV. NO.	DESCRIPTION	DATE

POWER

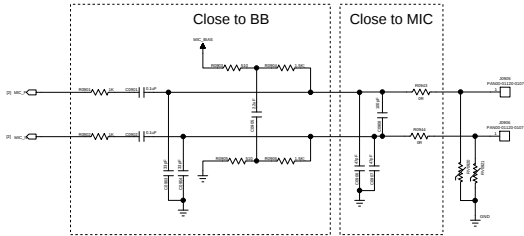


REVISION HISTORY			
VER.	REV. NO.	DESCRIPTION	DATE

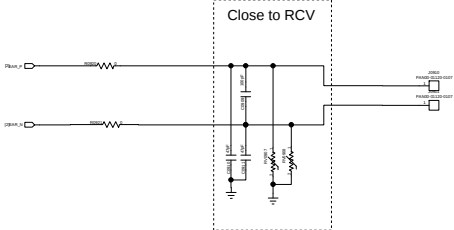
MEMORY



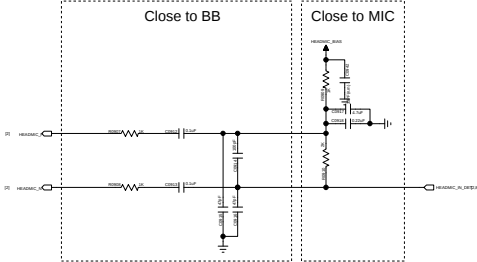
MAIN MIC



RECEIVER

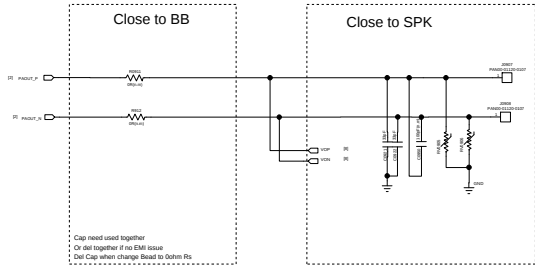


HEAD MIC

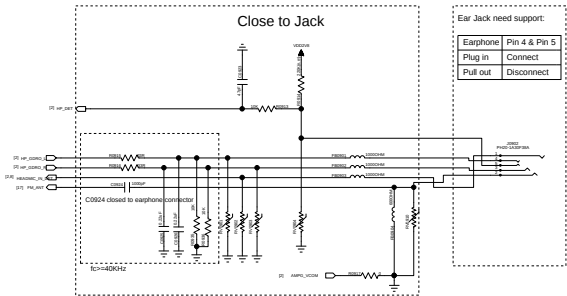


Note: C0912, C0913 use 1uF Cap

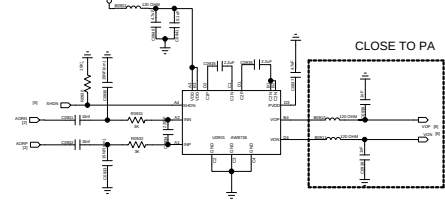
SPEAKER



3.5mm Headphone Jack

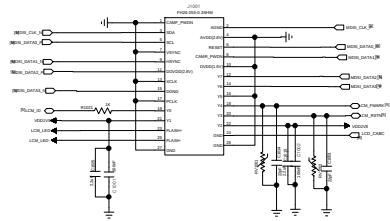


Audio PA

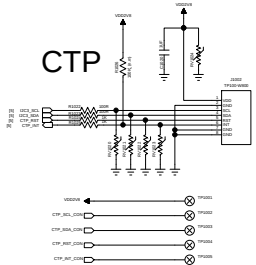


REVISION HISTORY			
VER.	REV. NO.	DESCRIPTION	DATE

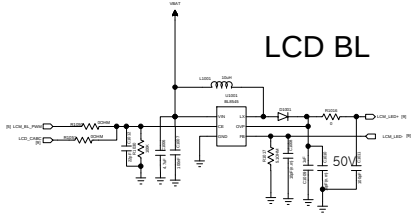
4.5" HVGA LCM



CTP

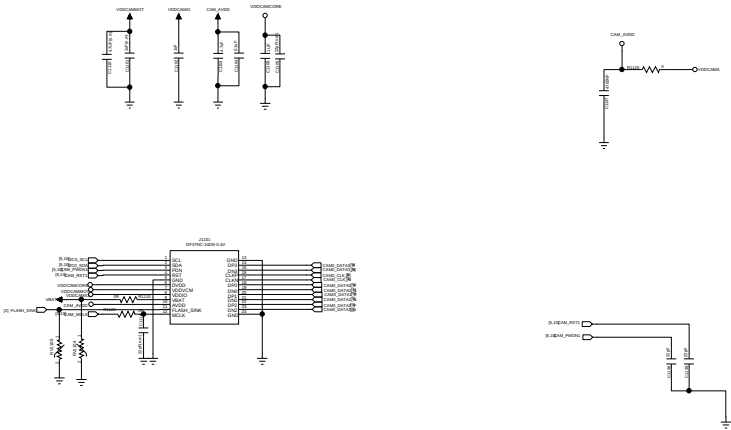


LCD BL

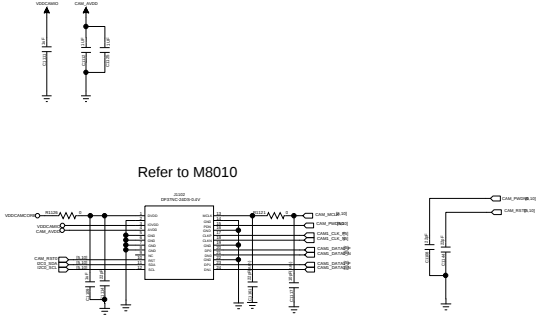


REVISION HISTORY			
VER.	REV. NO.	DESCRIPTION	DATE

8M Sensor



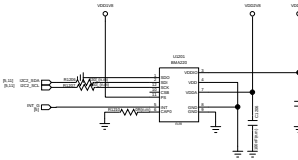
5M Sensor



Refer to M8010

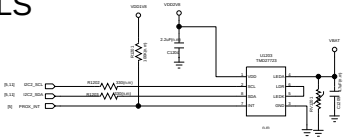
REVISION RECORD			
VER.	REVISED	REVISIONS	DATE

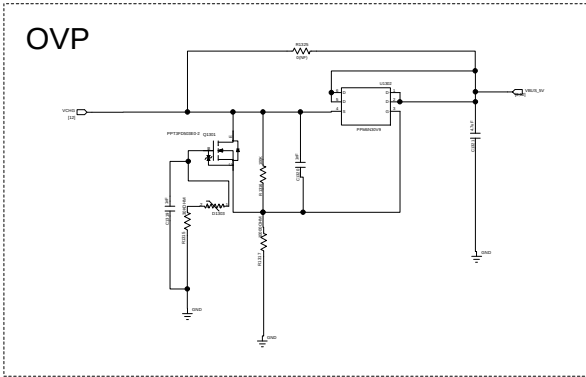
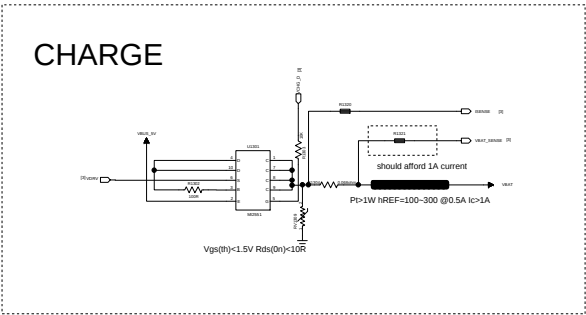
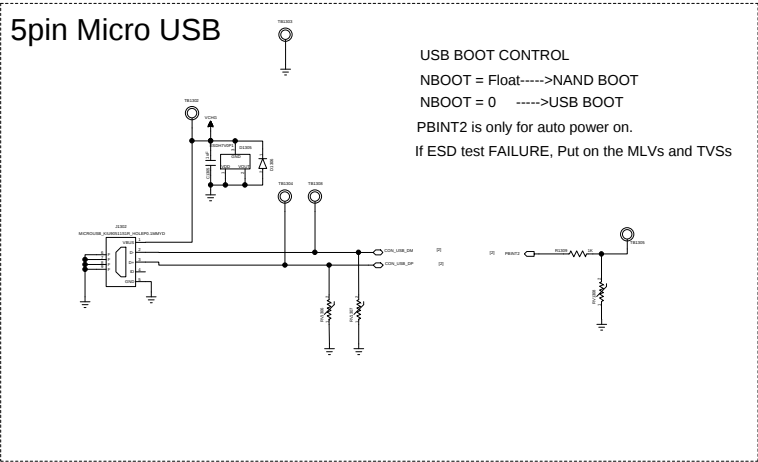
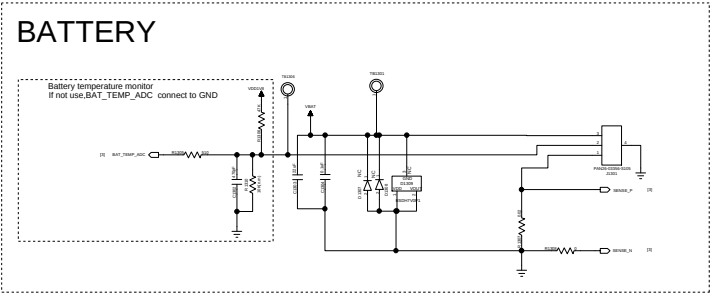
G Sensor



Default 3D BMA223

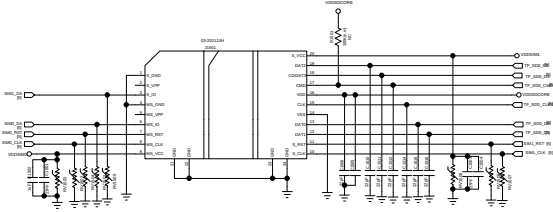
ALS





REVISION RECORD			
VER.	REVISED	REVISIONS	DATE

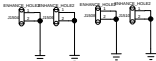
SIM0 & SIM1&T-Flash 3in1



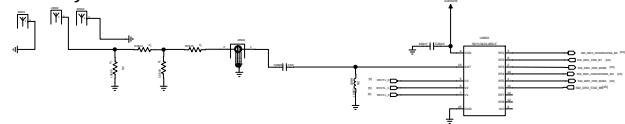
Rear cam enhance_element



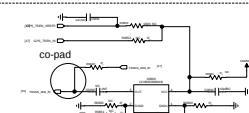
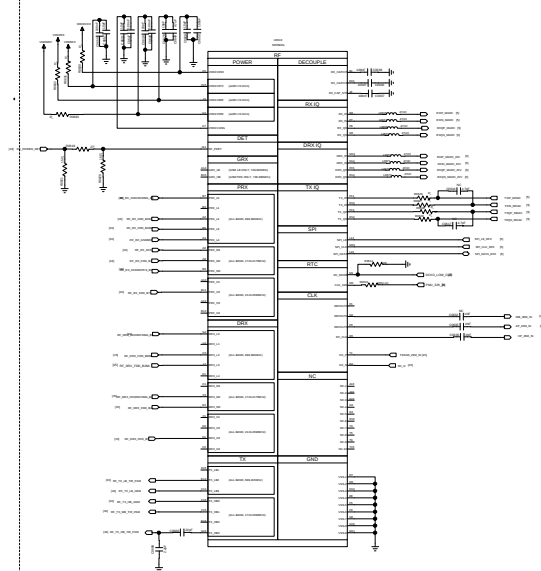
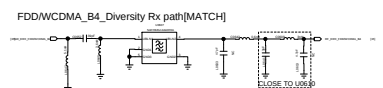
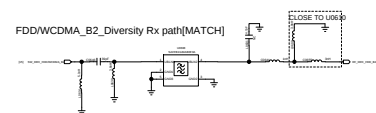
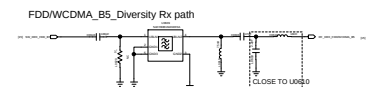
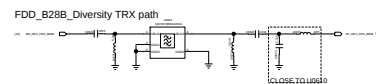
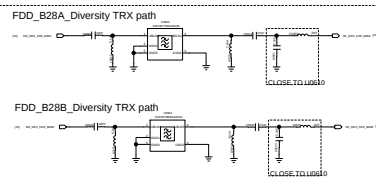
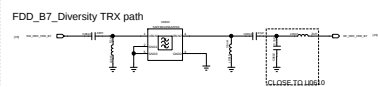
PCB enhance_hole



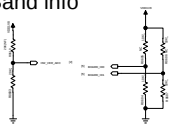
TOTAL SHEET			
SP9832A-2_CS_SCH_V0.1.0			
REVISION RECORD			
VERSION	DATE	REVISION	REVISIONS
River Zu	2015-10-12	Hardware DEPT.	
NAME	DATE	COMPANY	
Mukder Han	2015-10-12	Spreadtrum communications, Inc.	
APPROVED	DATE	CONFIDENTIAL AND PROPRIETARY	
Gang Xu	2015-10-12		



DIV ANT SAME WITH LA



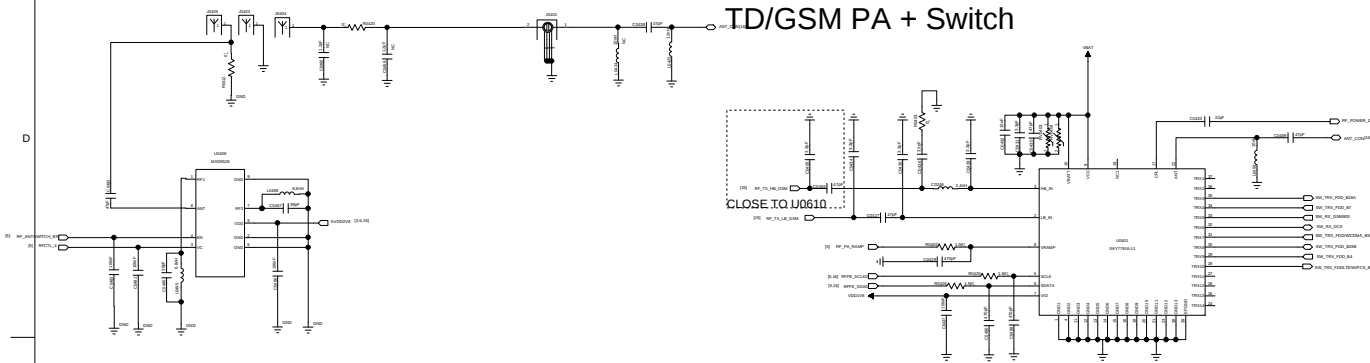
Dual TCXO/VCTCXO+TCXO/Share TCXO																
24M Configuration	X060	Y1801	C0600	C0636	C0635	C0632	C1808	C1817	C1809	C1834	R0640	R0650	R0602	C0601	R1812	R1811
Dual TCXO	TCXO	TCXO	1000pF	1000pF	1000pF	0ohms	NF	1000pF	NF	1000pF	NF	NF	0 ohms	10pF	NF	0 ohms
VCTCXO+TCXO	VCTCXO	TCXO	1000pF	1000pF	1000pF	0ohms	NF	1000pF	NF	1000pF	NF	0 ohms	NF	10pF	NF	0 ohms
Share TCXO	NF	TCXO	NF	1000pF	1000pF	1000pF	1000pF	NF	1000pF	1000pF	0 ohms	NF	0 ohms	NF	0 ohms	NF



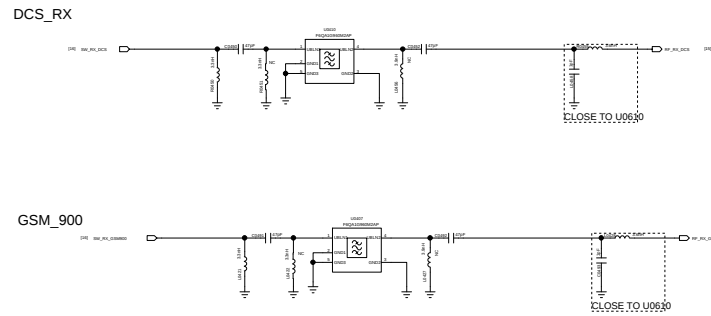
ADC/GPIO	00	01	10	11
0	reserved	reserved	reserved	reserved
1	reserved	reserved	reserved	reserved
2	reserved	reserved	reserved	reserved
3	reserved	reserved	reserved	reserved

1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31 32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47 48 49 50 51 52 53 54 55 56 57 58 59 60 61 62 63 64 65 66 67 68 69 70 71 72 73 74 75 76 77 78 79 80 81 82 83 84 85 86 87 88 89 90 91 92 93 94 95 96 97 98 99 100 101 102 103 104 105 106 107 108 109 110 111 112 113 114 115 116 117 118 119 120 121 122 123 124 125 126 127 128 129 130 131 132 133 134 135 136 137 138 139 140 141 142 143 144 145 146 147 148 149 150 151 152 153 154 155 156 157 158 159 160 161 162 163 164 165 166 167 168 169 170 171 172 173 174 175 176 177 178 179 180 181 182 183 184 185 186 187 188 189 190 191 192 193 194 195 196 197 198 199 200 201 202 203 204 205 206 207 208 209 210 211 212 213 214 215 216 217 218 219 220 221 222 223 224 225 226 227 228 229 230 231 232 233 234 235 236 237 238 239 240 241 242 243 244 245 246 247 248 249 250 251 252 253 254 255 256 257 258 259 260 261 262 263 264 265 266 267 268 269 270 271 272 273 274 275 276 277 278 279 280 281 282 283 284 285 286 287 288 289 290 291 292 293 294 295 296 297 298 299 300 301 302 303 304 305 306 307 308 309 310 311 312 313 314 315 316 317 318 319 320 321 322 323 324 325 326 327 328 329 330 331 332 333 334 335 336 337 338 339 340 341 342 343 344 345 346 347 348 349 350 351 352 353 354 355 356 357 358 359 360 361 362 363 364 365 366 367 368 369 370 371 372 373 374 375 376 377 378 379 380 381 382 383 384 385 386 387 388 389 390 391 392 393 394 395 396 397 398 399 400 401 402 403 404 405 406 407 408 409 410 411 412 413 414 415 416 417 418 419 420 421 422 423 424 425 426 427 428 429 430 431 432 433 434 435 436 437 438 439 440 441 442 443 444 445 446 447 448 449 450 451 452 453 454 455 456 457 458 459 460 461 462 463 464 465 466 467 468 469 470 471 472 473 474 475 476 477 478 479 480 481 482 483 484 485 486 487 488 489 490 491 492 493 494 495 496 497 498 499 500 501 502 503 504 505 506 507 508 509 510 511 512 513 514 515 516 517 518 519 520 521 522 523 524 525 526 527 528 529 530 531 532 533 534 535 536 537 538 539 540 541 542 543 544 545 546 547 548 549 550 551 552 553 554 555 556 557 558 559 560 561 562 563 564 565 566 567 568 569 570 571 572 573 574 575 576 577 578 579 580 581 582 583 584 585 586 587 588 589 590 591 592 593 594 595 596 597 598 599 600 601 602 603 604 605 606 607 608 609 610 611 612 613 614 615 616 617 618 619 620 621 622 623 624 625 626 627 628 629 630 631 632 633 634 635 636 637 638 639 640 641 642 643 644 645 646 647 648 649 650 651 652 653 654 655 656 657 658 659 660 661 662 663 664 665 666 667 668 669 670 671 672 673 674 675 676 677 678 679 680 681 682 683 684 685 686 687 688 689 690 691 692 693 694 695 696 697 698 699 700 701 702 703 704 705 706 707 708 709 710 711 712 713 714 715 716 717 718 719 720 721 722 723 724 725 726 727 728 729 730 731 732 733 734 735 736 737 738 739 740 741 742 743 744 745 746 747 748 749 750 751 752 753 754 755 756 757 758 759 760 761 762 763 764 765 766 767 768 769 770 771 772 773 774 775 776 777 778 779 780 781 782 783 784 785 786 787 788 789 790 791 792 793 794 795 796 797 798 799 800 801 802 803 804 805 806 807 808 809 810 811 812 813 814 815 816 817 818 819 820 821 822 823 824 825 826 827 828 829 830 831 832 833 834 835 836 837 83	
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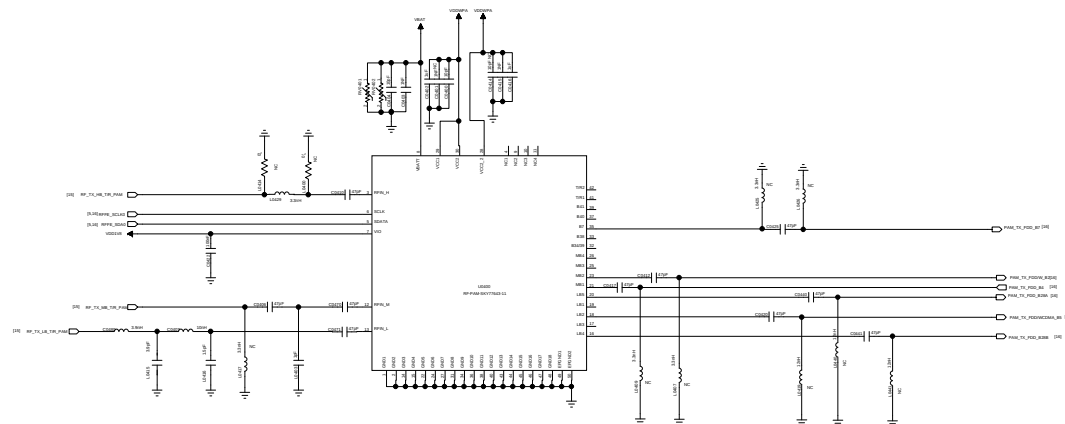
Primary Antenna



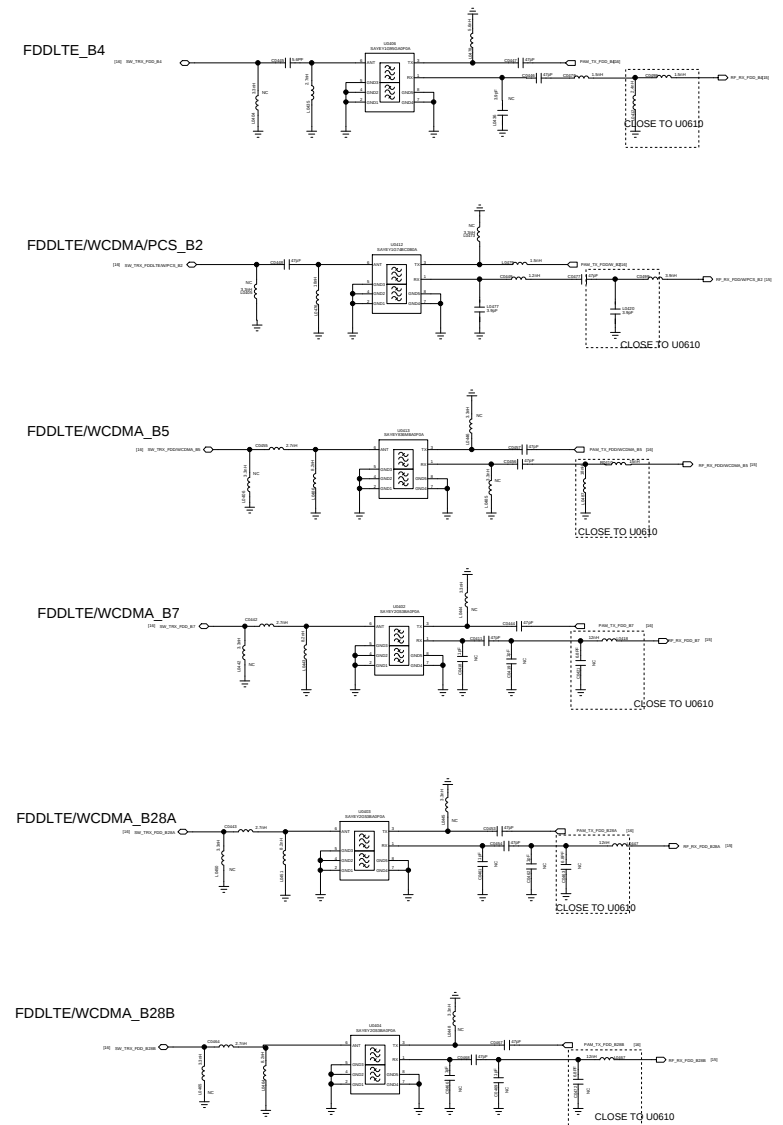
Primary RX Path GSM/TDS



LTE/WCDMA PAM



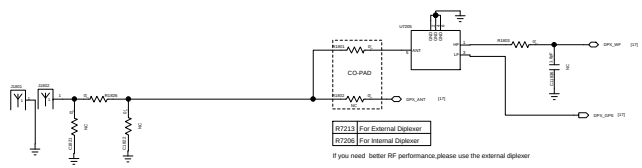
Primary TRX Path FDD/WCDMA



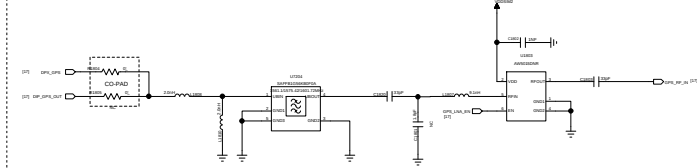
REVISION RECORD			
USE	ECO NO	APPROVED	DATE

		TITLE & REV:	
		SP9832A-2 CS SCH V0.1.0	
		DOCUMENT NO.:	
DESIGNED:	DATED:	APPROVED:	
River Du	2016-06-06	Hardware	
		DEPT.	
CHK:	DATED:	CHECKED:	
Mulder Han	2016-06-06		
APPROVED:	DATED:	Spectrum communications, Inc.	
Gang Xu	2016-06-07	CONFIDENTIAL AND PROPRIETARY	
			INSET: 7 OF 33

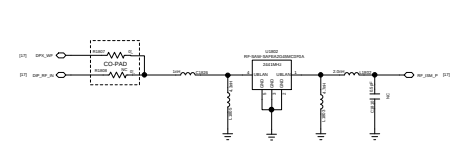
BT&WIFI&GPS ANT



GPS SAW LNA

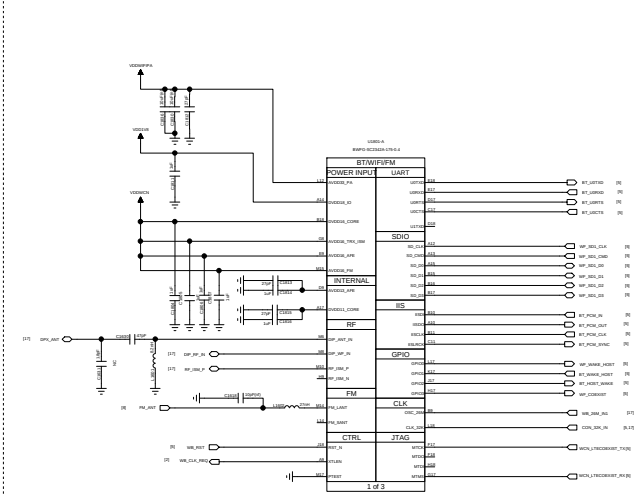


WIFI/BT SAW LNA

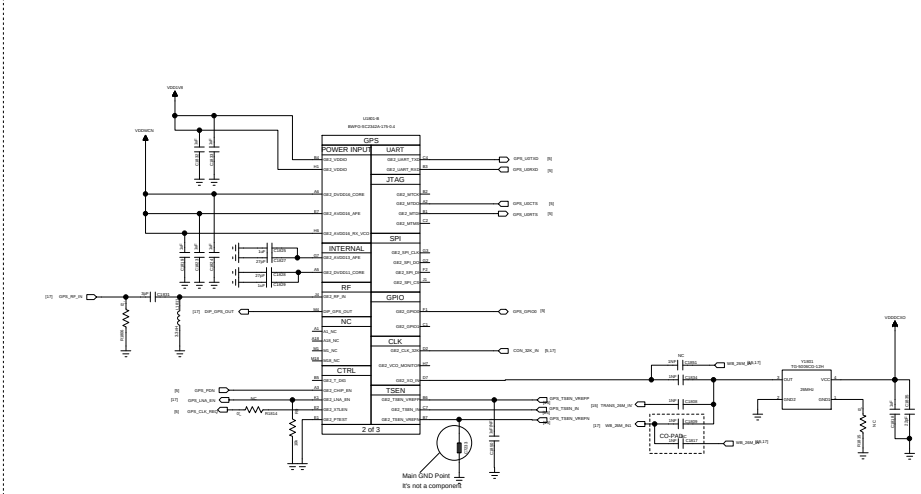


DPX selection	VALUE	R7213	R7206	R7224	R7223	R7253	R7252
External DPX	0ohm	NF	0ohm	NF	0ohm	NF	0ohm
Internal DPX	NF	0ohm	NF	0ohm	NF	0ohm	0ohm

MARLIN



GE2



GND

