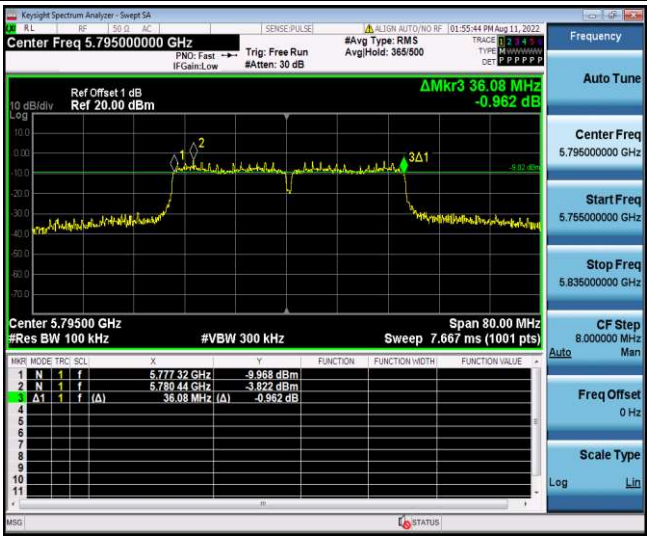
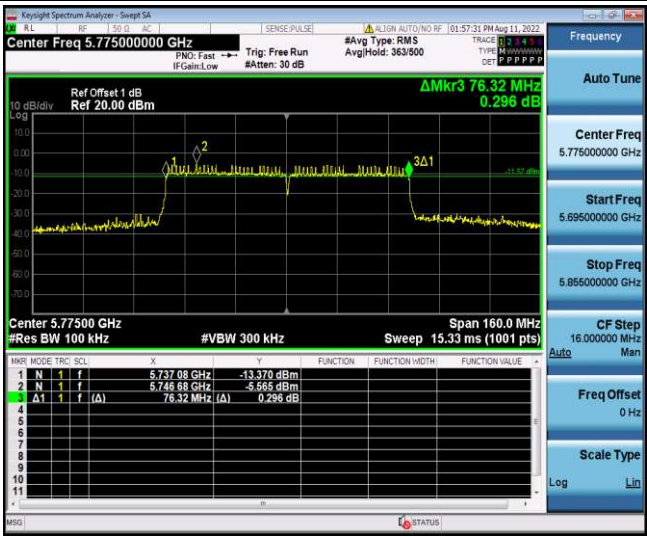




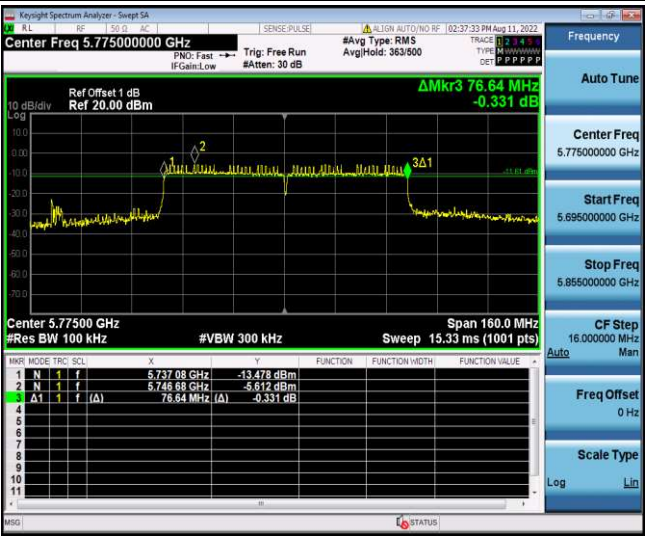
11AC40SISO_Ant1_5795



11AC40SISO_Ant2_5795



11AC80SISO_Ant1_5775



11AC80SISO_Ant2_5775

Appendix C: Maximum conducted output power

Test Result

TestMode	Antenna	Channel	Result[dBm]	Limit[dBm]	Verdict
11A	Ant1	5180	13.37	≤23.98	PASS
	Ant2	5180	13.42	≤23.98	PASS
	Ant1	5200	13.62	≤23.98	PASS
	Ant2	5200	13.68	≤23.98	PASS
	Ant1	5240	13.90	≤23.98	PASS
	Ant2	5240	13.90	≤23.98	PASS
	Ant1	5745	12.33	≤30	PASS
	Ant2	5745	12.15	≤30	PASS
	Ant1	5785	11.47	≤30	PASS
	Ant2	5785	11.52	≤30	PASS
	Ant1	5825	10.95	≤30	PASS
	Ant2	5825	11.17	≤30	PASS
11N20SISO	Ant1	5180	13.46	≤23.98	PASS
	Ant2	5180	12.70	≤23.98	PASS
	Ant1	5200	13.78	≤23.98	PASS
	Ant2	5200	13.01	≤23.98	PASS
	Ant1	5240	13.96	≤23.98	PASS
	Ant2	5240	13.29	≤23.98	PASS
	Ant1	5745	12.44	≤30	PASS
	Ant2	5745	12.35	≤30	PASS
	Ant1	5785	11.71	≤30	PASS
	Ant2	5785	11.67	≤30	PASS
	Ant1	5825	11.26	≤30	PASS
	Ant2	5825	11.39	≤30	PASS
11N40SISO	Ant1	5190	13.55	≤23.98	PASS
	Ant2	5190	12.71	≤23.98	PASS
	Ant1	5230	14.00	≤23.98	PASS
	Ant2	5230	13.31	≤23.98	PASS
	Ant1	5755	12.25	≤30	PASS
	Ant2	5755	12.11	≤30	PASS
	Ant1	5795	11.51	≤30	PASS
	Ant2	5795	11.64	≤30	PASS
11AC20SISO	Ant1	5180	13.51	≤23.98	PASS
	Ant2	5180	12.80	≤23.98	PASS
	Ant1	5200	13.86	≤23.98	PASS
	Ant2	5200	13.06	≤23.98	PASS
	Ant1	5240	14.05	≤23.98	PASS
	Ant2	5240	13.36	≤23.98	PASS

	Ant1	5745	11.47	≤30	PASS
	Ant2	5745	11.27	≤30	PASS
	Ant1	5785	10.57	≤30	PASS
	Ant2	5785	10.52	≤30	PASS
	Ant1	5825	10.05	≤30	PASS
	Ant2	5825	10.32	≤30	PASS
11AC40SISO	Ant1	5190	13.45	≤23.98	PASS
	Ant2	5190	12.77	≤23.98	PASS
	Ant1	5230	13.85	≤23.98	PASS
	Ant2	5230	13.28	≤23.98	PASS
	Ant1	5755	11.05	≤30	PASS
	Ant2	5755	11.15	≤30	PASS
	Ant1	5795	10.15	≤30	PASS
	Ant2	5795	10.46	≤30	PASS
11AC80SISO	Ant1	5210	12.00	≤23.98	PASS
	Ant2	5210	13.24	≤23.98	PASS
	Ant1	5775	11.42	≤30	PASS
	Ant2	5775	11.52	≤30	PASS

Note: 1. The Duty Cycle Factor was compensated in the spectrum analyzer Ref offset.

2. Spectrum analyzer Ref offset=Cable Loss(1dB) + Duty Cycle Factor.

3. Duty Cycle Factor=10Log(1/X), where X is the Duty Cycle.

Test Graphs



11A_Ant1_5180



11A_Ant2_5180



11A_Ant1_5200



11A_Ant2_5200



11A_Ant1_5240



11A_Ant2_5240



11A_Ant1_5745



11A_Ant2_5745



11A_Ant1_5785



11A_Ant2_5785



11A_Ant1_5825



11A_Ant2_5825



11N20SISO_Ant1_5180



11N20SISO_Ant2_5180



11N20SISO_Ant1_5200



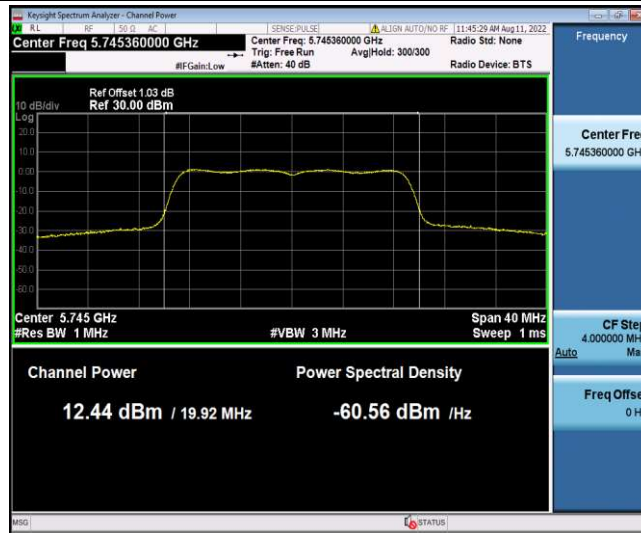
11N20SISO_Ant2_5200



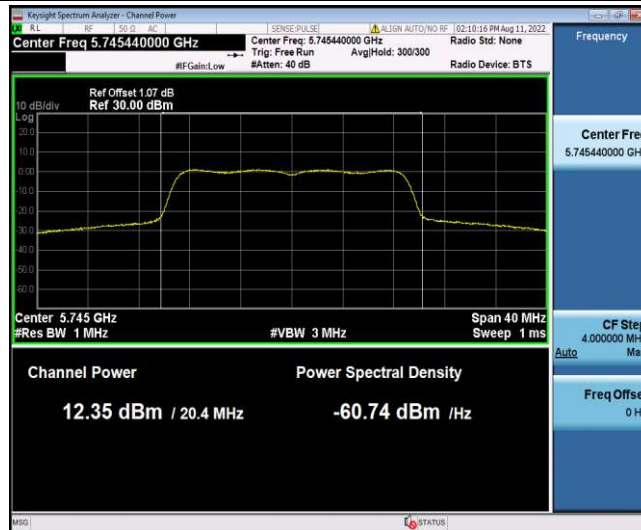
11N20SISO_Ant1_5240



11N20SISO_Ant2_5240



11N20SISO_Ant1_5745



11N20SISO_Ant2_5745



11N20SISO_Ant1_5785



11N20SISO_Ant2_5785



11N20SISO_Ant1_5825



11N20SISO_Ant2_5825



11N40SISO_Ant1_5190



11N40SISO_Ant2_5190



11N40SISO_Ant1_5230



11N40SISO_Ant2_5230



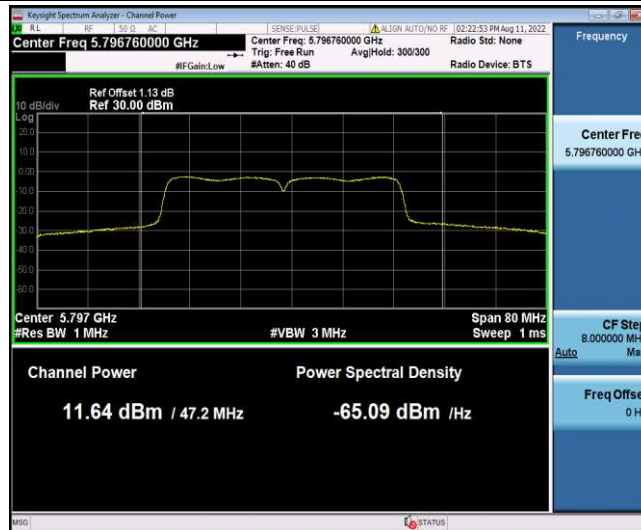
11N40SISO_Ant1_5755



11N40SISO_Ant2_5755



11N40SISO_Ant1_5795



11N40SISO_Ant2_5795



11AC20SISO_Ant1_5180



11AC20SISO_Ant2_5180



11AC20SISO_Ant1_5200



11AC20SISO_Ant2_5200



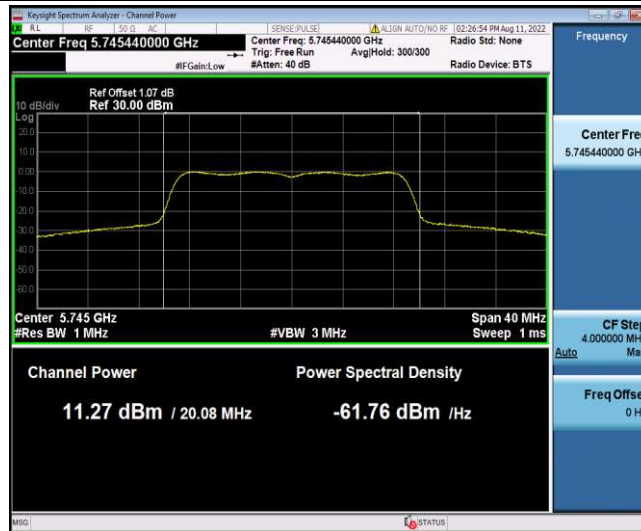
11AC20SISO_Ant1_5240



11AC20SISO_Ant2_5240



11AC20SISO_Ant1_5745



11AC20SISO_Ant2_5745



11AC20SISO_Ant1_5785



11AC20SISO_Ant2_5785



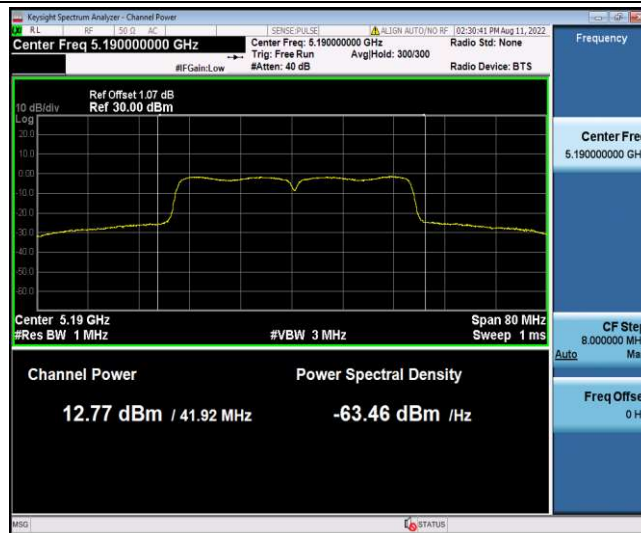
11AC20SISO_Ant1_5825



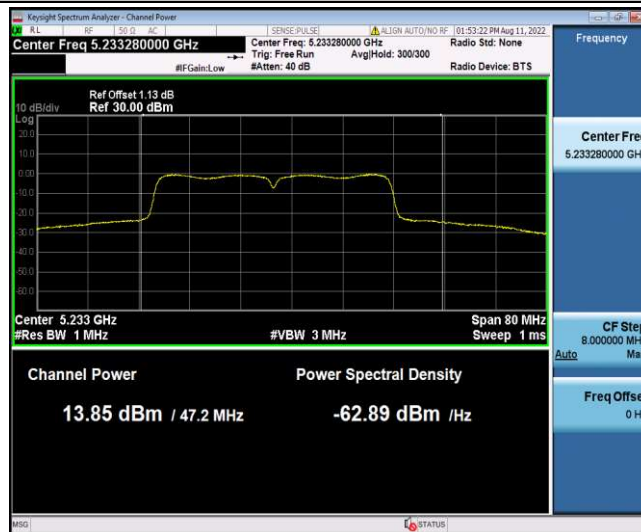
11AC20SISO_Ant2_5825



11AC40SISO_Ant1_5190



11AC40SISO_Ant2_5190



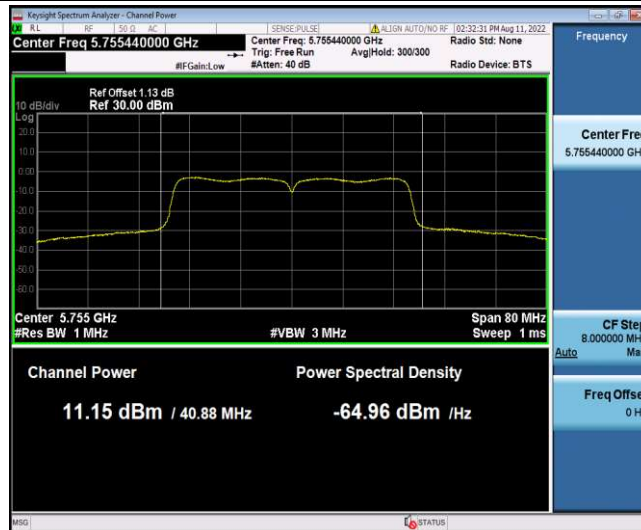
11AC40SISO_Ant1_5230



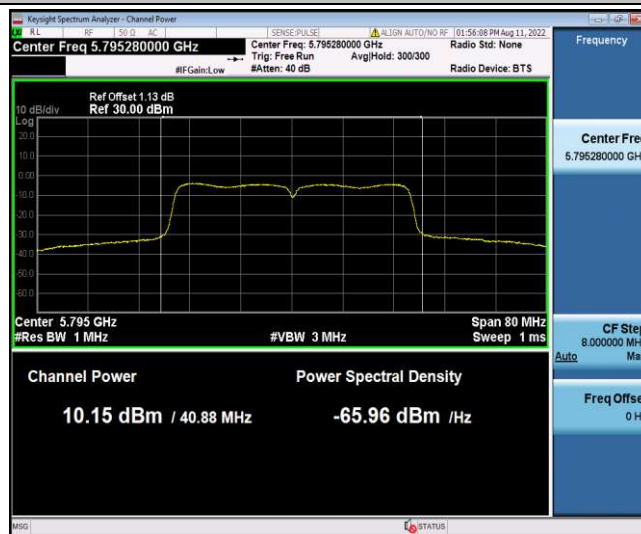
11AC40SISO_Ant2_5230



11AC40SISO_Ant1_5755



11AC40SISO_Ant2_5755



11AC40SISO_Ant1_5795



11AC40SISO_Ant2_5795



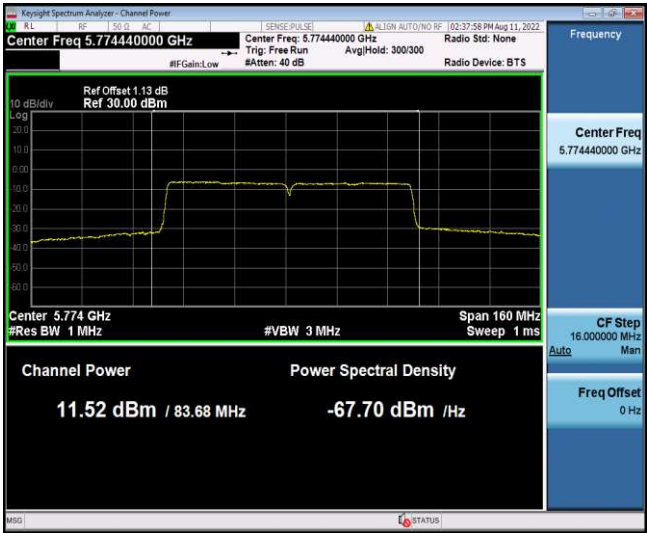
11AC80SISO_Ant1_5210



11AC80SISO_Ant2_5210



11AC80SISO_Ant1_5775



11AC80SISO_Ant2_5775

Appendix D: Maximum power spectral density

Test Result

TestMode	Antenna	Channel	Result [dBm/MHz]	Limit[dBm/MHz]	Verdict
11A	Ant1	5180	2.46	≤11	PASS
	Ant2	5180	2.56	≤11	PASS
	Ant1	5200	2.8	≤11	PASS
	Ant2	5200	2.83	≤11	PASS
	Ant1	5240	2.97	≤11	PASS
	Ant2	5240	3.1	≤11	PASS
	Ant1	5745	-1.12	≤30	PASS
	Ant2	5745	-1.37	≤30	PASS
	Ant1	5785	-2.02	≤30	PASS
	Ant2	5785	-2.05	≤30	PASS
	Ant1	5825	-2.64	≤30	PASS
	Ant2	5825	-2.19	≤30	PASS
11N20SISO	Ant1	5180	2.33	≤11	PASS
	Ant2	5180	1.68	≤11	PASS
	Ant1	5200	2.56	≤11	PASS
	Ant2	5200	1.97	≤11	PASS
	Ant1	5240	2.81	≤11	PASS
	Ant2	5240	2.18	≤11	PASS
	Ant1	5745	-1.42	≤30	PASS
	Ant2	5745	-1.32	≤30	PASS
	Ant1	5785	-2.25	≤30	PASS
	Ant2	5785	-2.08	≤30	PASS
	Ant1	5825	-2.62	≤30	PASS
	Ant2	5825	-2.08	≤30	PASS
11N40SISO	Ant1	5190	-0.41	≤11	PASS
	Ant2	5190	-1.17	≤11	PASS
	Ant1	5230	0.03	≤11	PASS
	Ant2	5230	-0.52	≤11	PASS
	Ant1	5755	-4.19	≤30	PASS
	Ant2	5755	-4.44	≤30	PASS
	Ant1	5795	-5.3	≤30	PASS
	Ant2	5795	-5.09	≤30	PASS
11AC20SISO	Ant1	5180	2.34	≤11	PASS
	Ant2	5180	1.62	≤11	PASS
	Ant1	5200	2.59	≤11	PASS
	Ant2	5200	1.89	≤11	PASS
	Ant1	5240	2.8	≤11	PASS
	Ant2	5240	2.06	≤11	PASS

	Ant1	5745	-2.29	≤30	PASS
	Ant2	5745	-2.3	≤30	PASS
	Ant1	5785	-3.25	≤30	PASS
	Ant2	5785	-3.24	≤30	PASS
	Ant1	5825	-3.94	≤30	PASS
	Ant2	5825	-3.37	≤30	PASS
11AC40SISO	Ant1	5190	-0.68	≤11	PASS
	Ant2	5190	-1.34	≤11	PASS
	Ant1	5230	-0.34	≤11	PASS
	Ant2	5230	-0.62	≤11	PASS
	Ant1	5755	-5.67	≤30	PASS
	Ant2	5755	-5.37	≤30	PASS
	Ant1	5795	-6.4	≤30	PASS
	Ant2	5795	-6.1	≤30	PASS
11AC80SISO	Ant1	5210	-3.78	≤11	PASS
	Ant2	5210	-4.32	≤11	PASS
	Ant1	5775	-8.39	≤30	PASS
	Ant2	5775	-8.45	≤30	PASS

Note: 1. The Duty Cycle Factor was compensated in the spectrum analyzer Ref offset.

2. Spectrum analyzer Ref offset=Cable Loss(1dB) + Duty Cycle Factor.

3. Duty Cycle Factor=10Log(1/X), where X is the Duty Cycle.

Test Graphs



11A_Ant1_5180



11A_Ant2_5180



11A_Ant1_5200



11A_Ant2_5200



11A_Ant1_5240



11A_Ant2_5240



11A_Ant1_5745



11A_Ant2_5745



11A_Ant1_5785



11A_Ant2_5785



11A_Ant1_5825



11A_Ant2_5825



11N20SISO_Ant1_5180



11N20SISO_Ant2_5180



11N20SISO_Ant1_5200



11N20SISO_Ant2_5200



11N20SISO_Ant1_5240



11N20SISO_Ant2_5240



11N20SISO_Ant1_5745



11N20SISO_Ant2_5745



11N20SISO_Ant1_5785



11N20SISO_Ant2_5785



11N20SISO_Ant1_5825



11N20SISO_Ant2_5825



11N40SISO_Ant1_5190



11N40SISO_Ant2_5190



11N40SISO_Ant1_5230



11N40SISO_Ant2_5230



11N40SISO_Ant1_5755



11N40SISO_Ant2_5755



11N40SISO_Ant1_5795



11N40SISO_Ant2_5795



11AC20SISO_Ant1_5180



11AC20SISO_Ant2_5180



11AC20SISO_Ant1_5200



11AC20SISO_Ant2_5200



11AC20SISO_Ant1_5240



11AC20SISO_Ant2_5240



11AC20SISO_Ant1_5745



11AC20SISO_Ant2_5745



11AC20SISO_Ant1_5785



11AC20SISO_Ant2_5785