CIRCUIT DESCRIPTION

ANT1 & ANT2 provide diversity operation for both transmitter and receiver. They are switched by CR1, CR4, Q3, Q22 etc., all under control of the microprocessor (U5). Transmit and receive signals are multiplexed by C3, L5, FL1,L6, & C6, so no TR switch is needed. The receiver consists of a single chip receiver (U1) with a internal PLL'ed LO who's FDE is YL3 (20.89375 X 16 = 334.30 MHz. for low side injection.). FL2 (10.7 MHz) gives IF selectivity, and op-amp (U4) etc., processes the base-band information before passing it to the microprocessor (U5) The transmitter is a single chip, PLL'ed (U2), etc., whose FDE is YL5 (10.78125 X 32 = 345 MHz.) operation is under control of the microprocessor (U5).