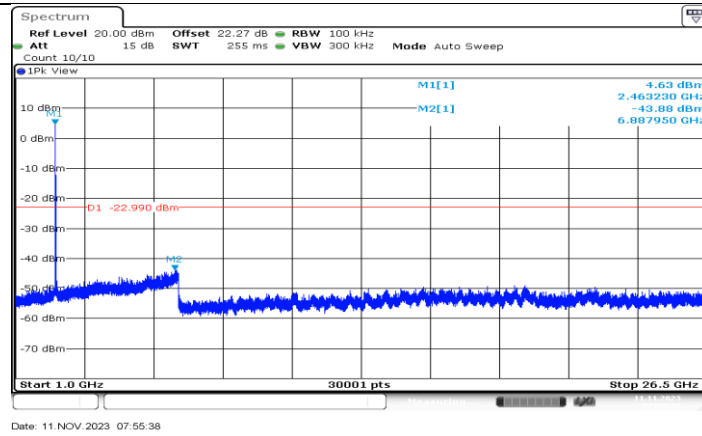
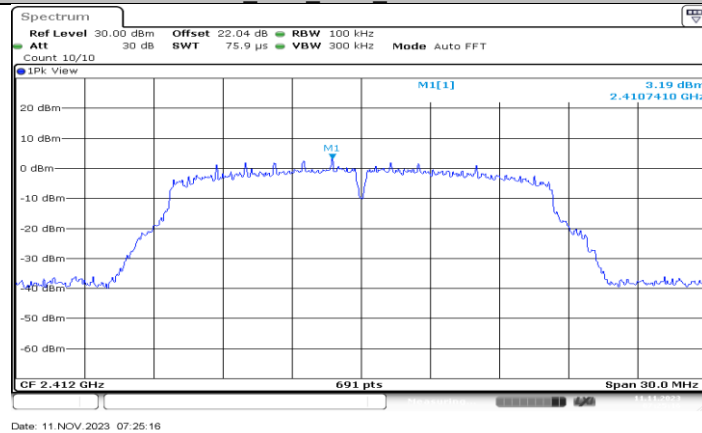
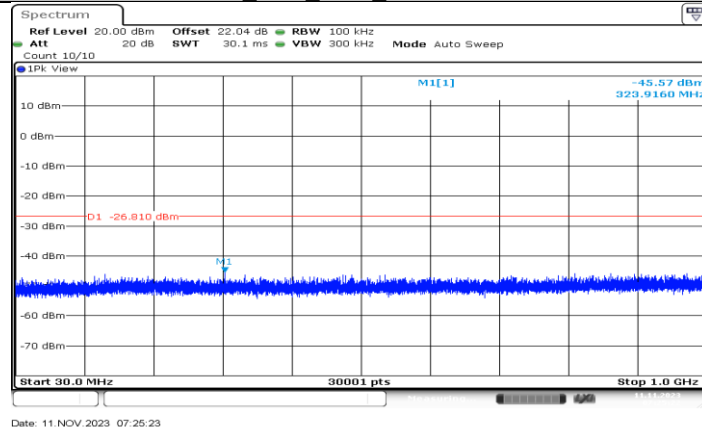




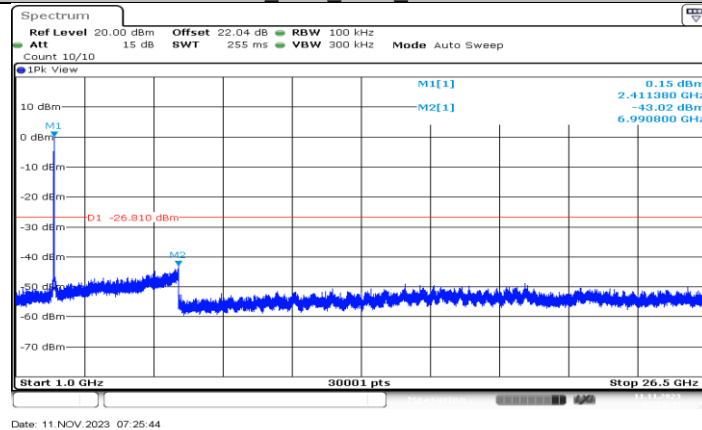
11B Ant1 2462 1000~26500



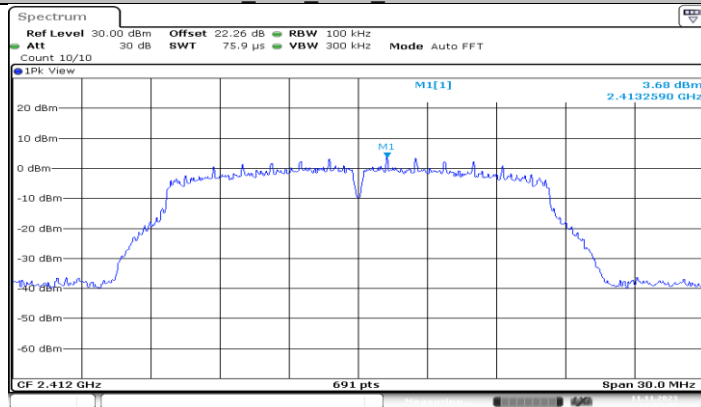
11G Ant0 2412 0-Reference



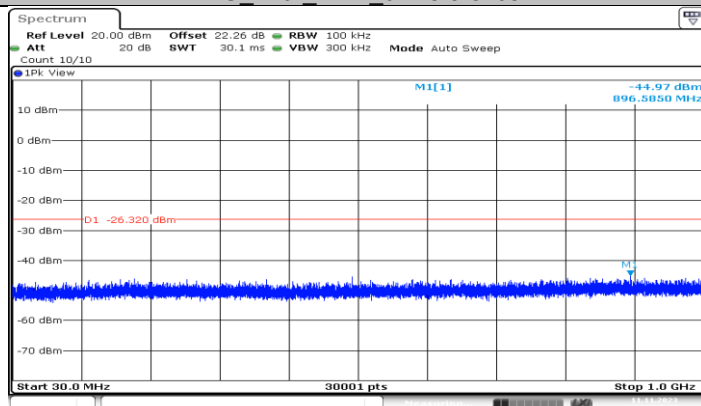
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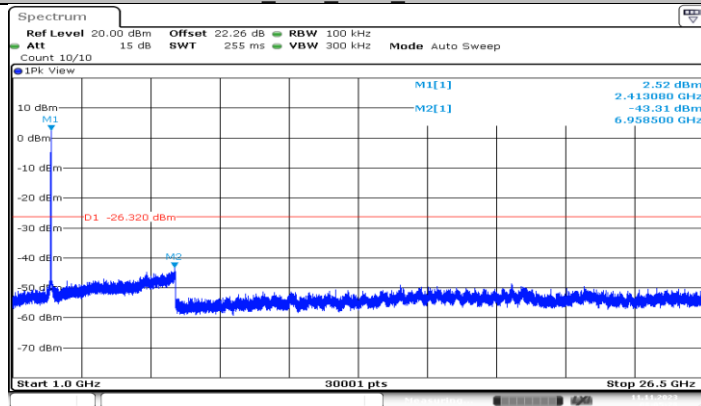
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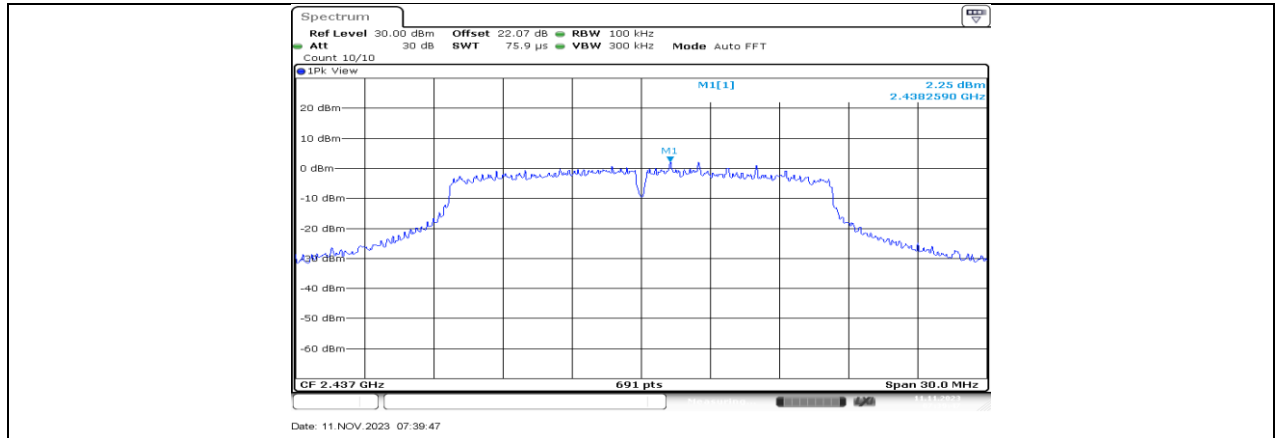
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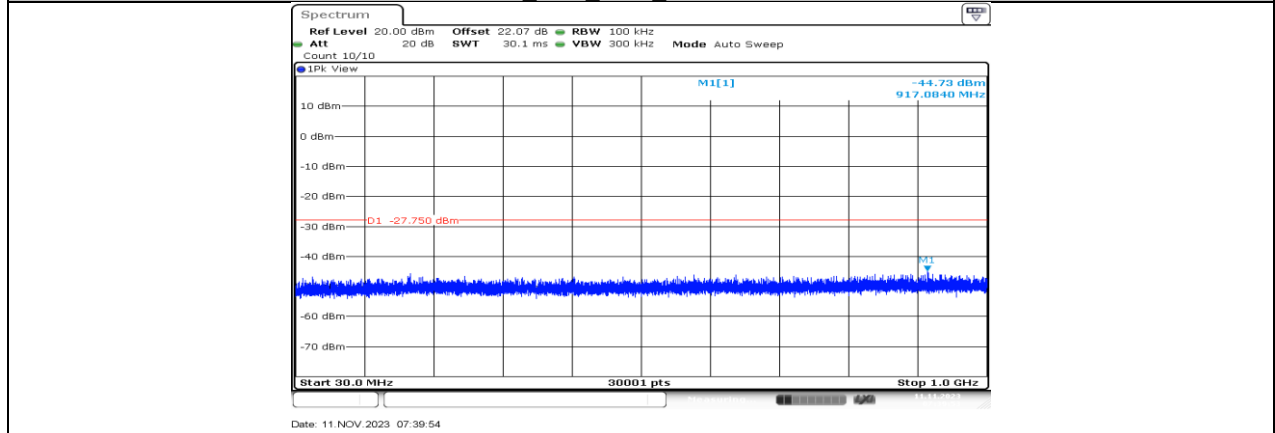
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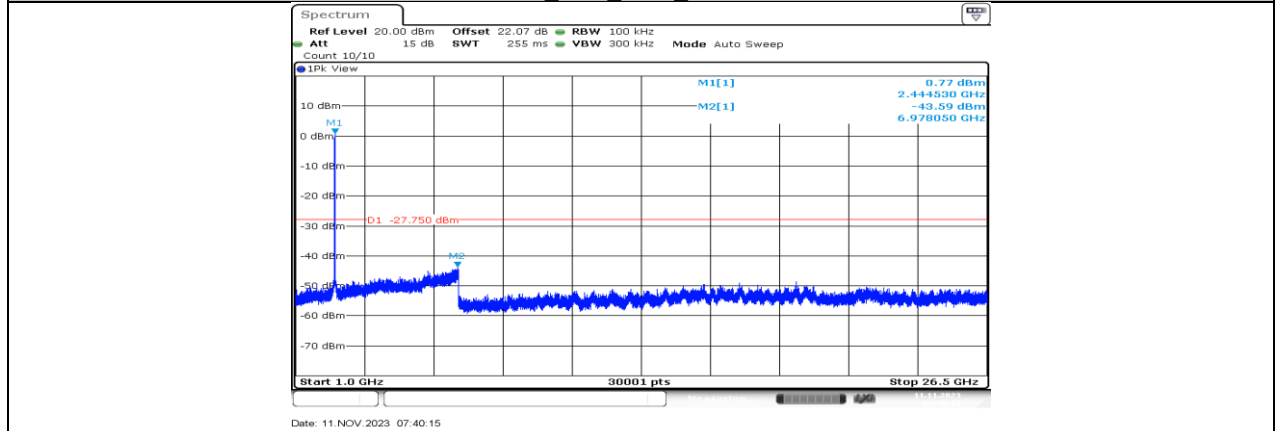
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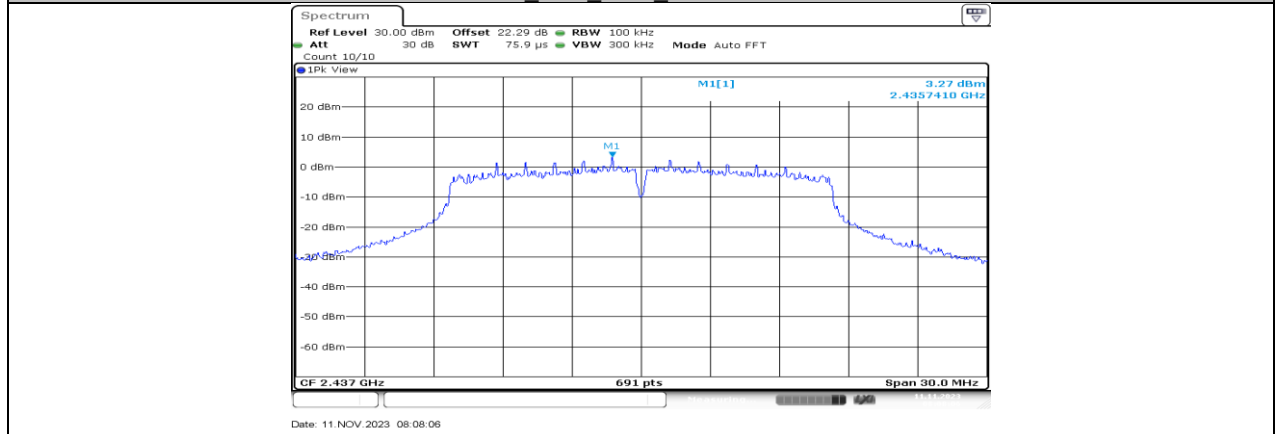
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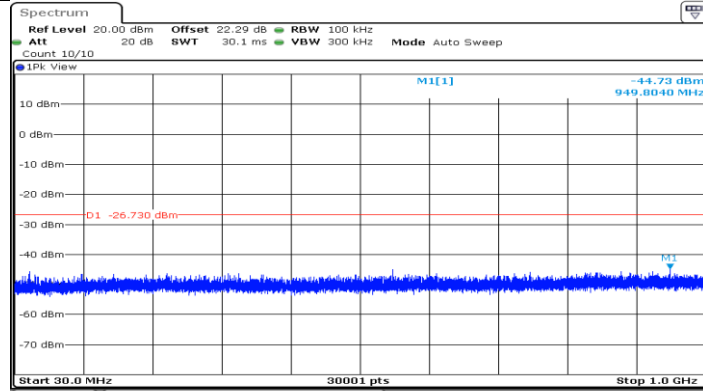
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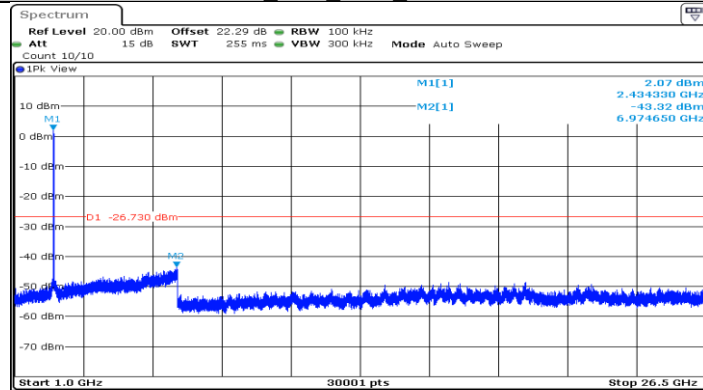


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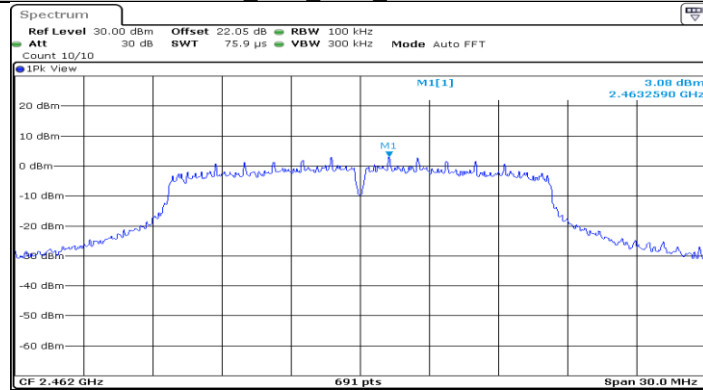
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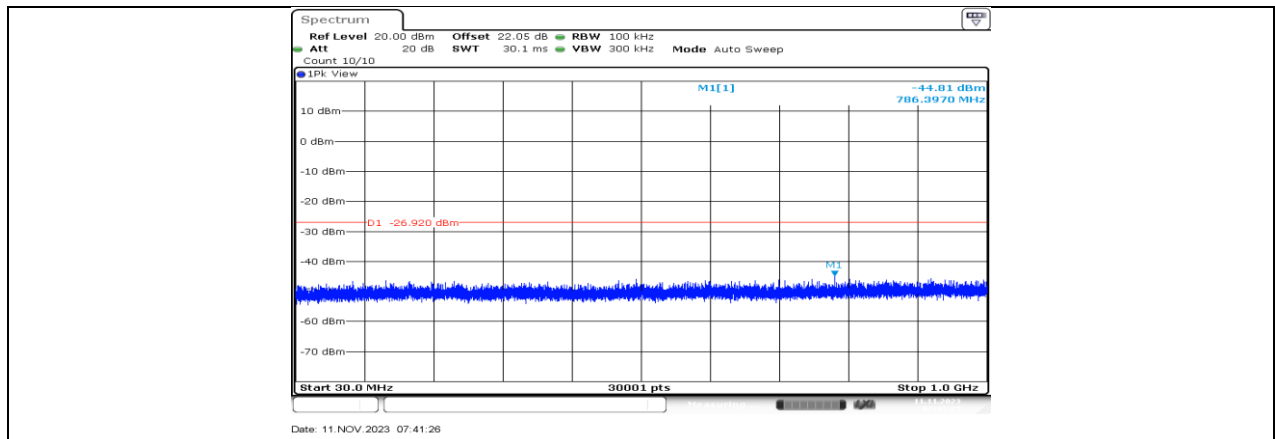
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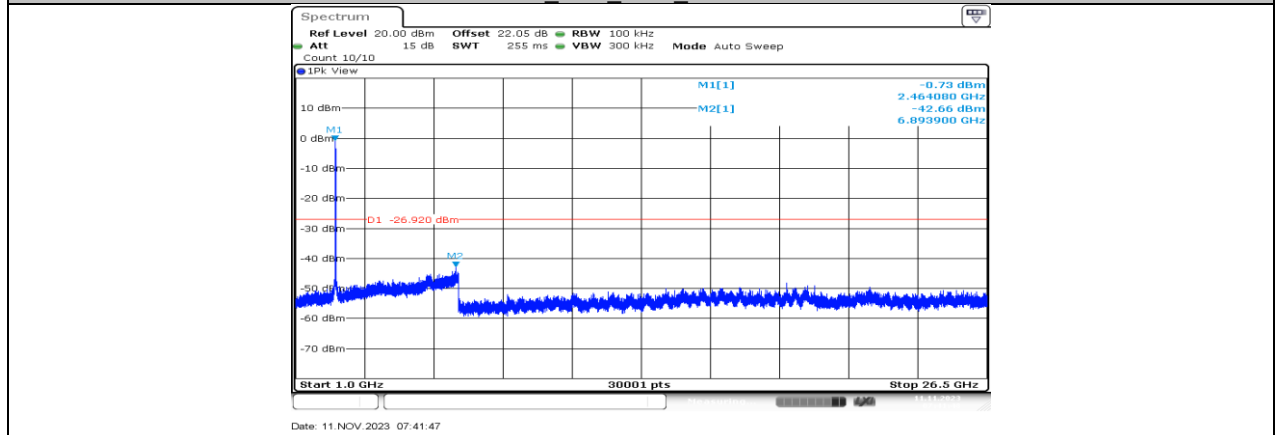


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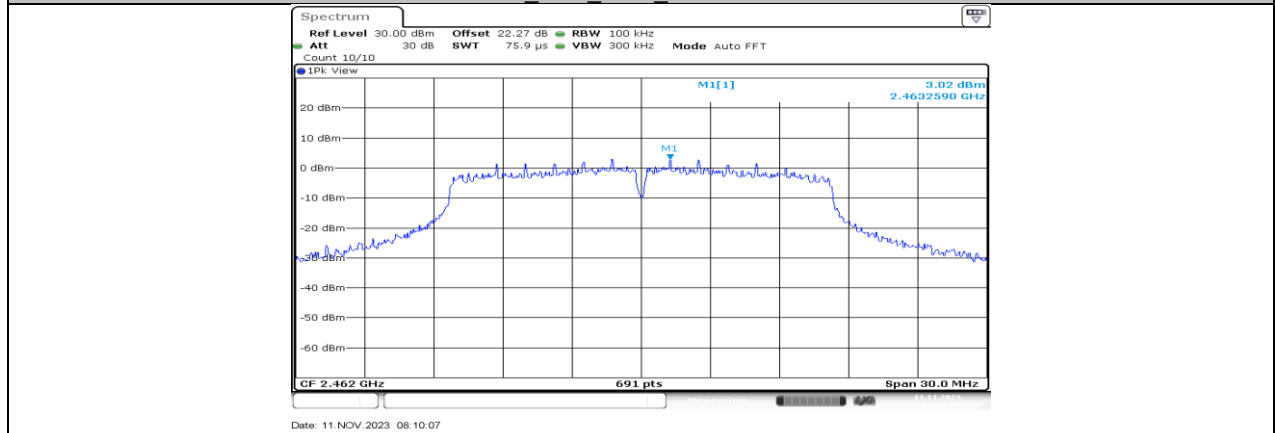
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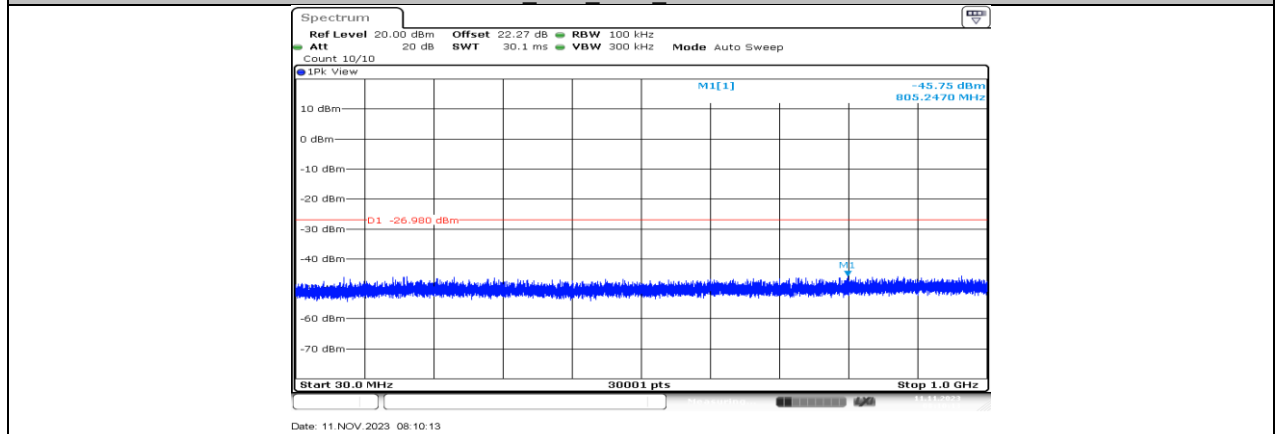
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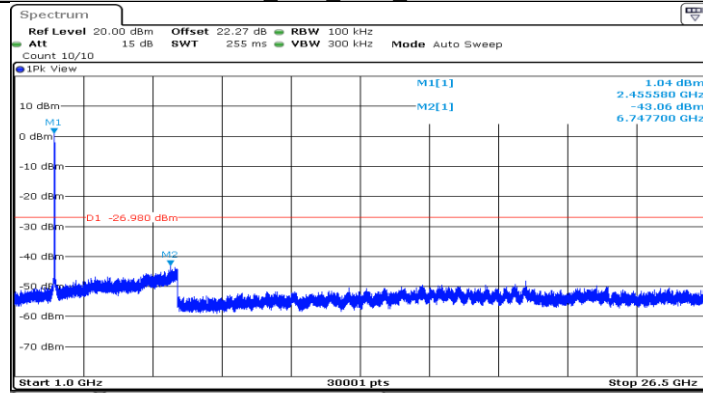
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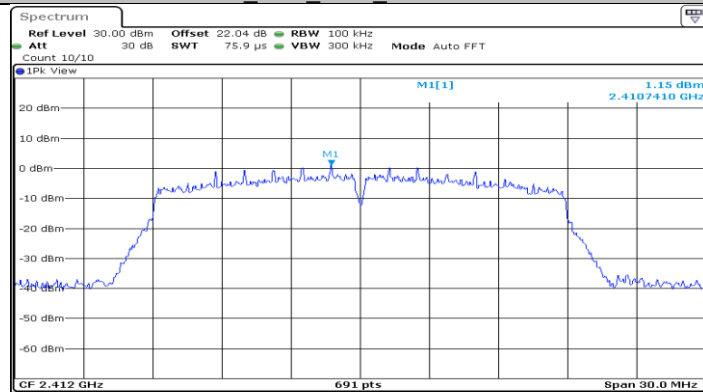


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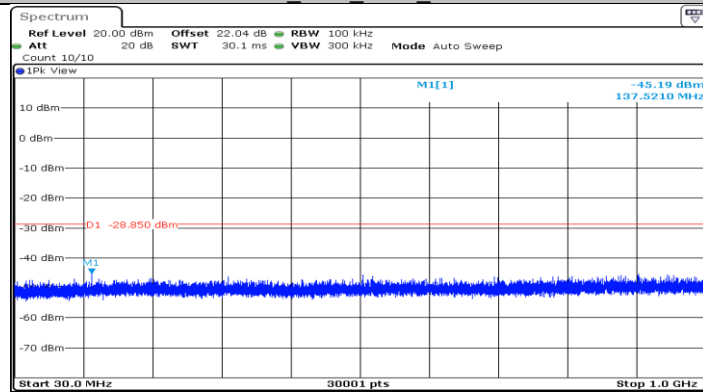
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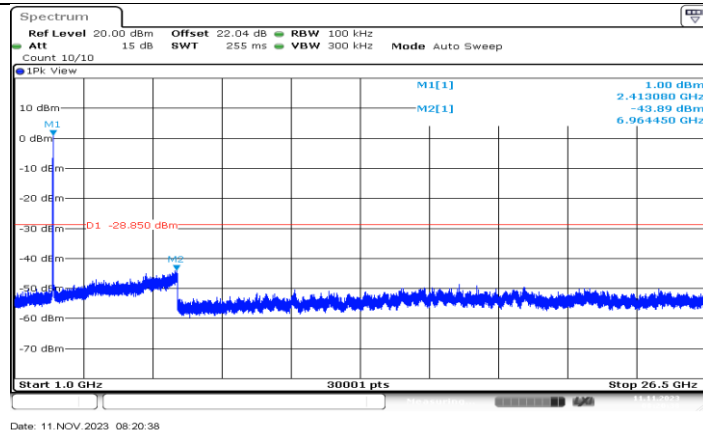
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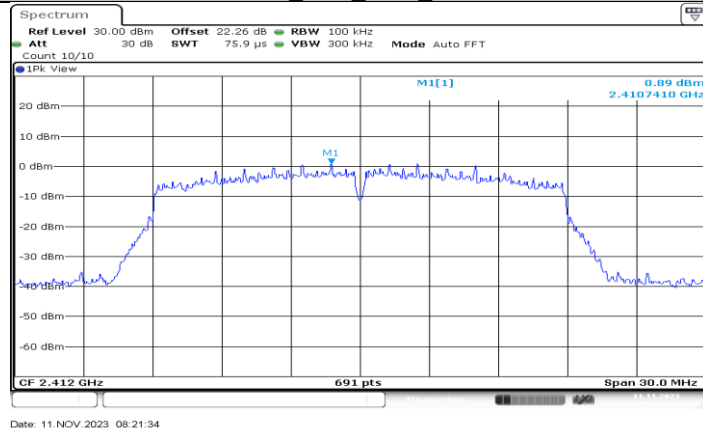


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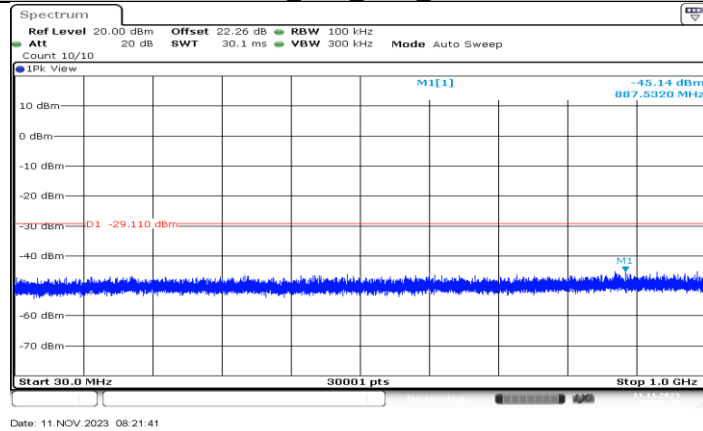
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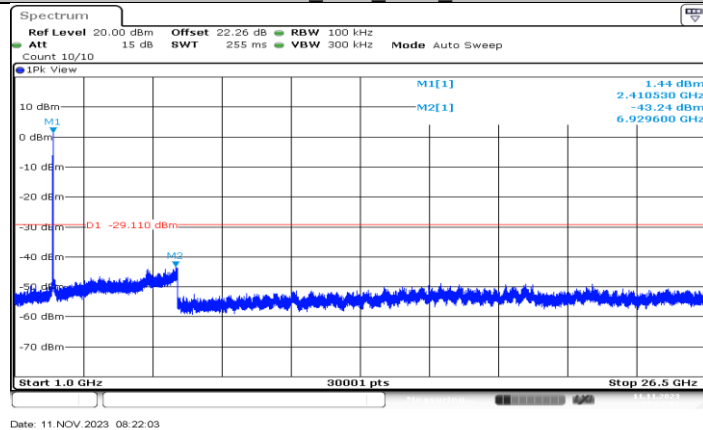
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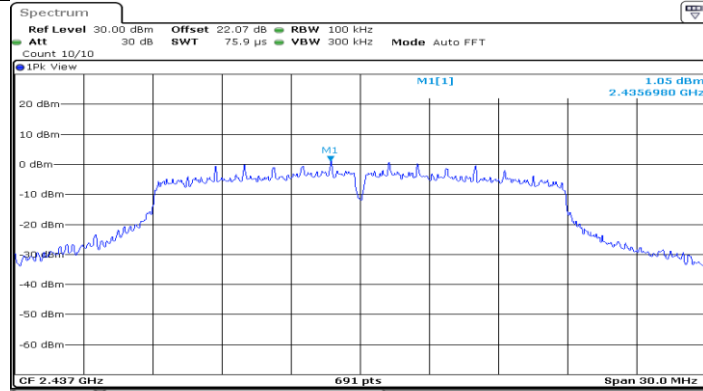
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11N20MIMO_Ant1_2412_30~1000

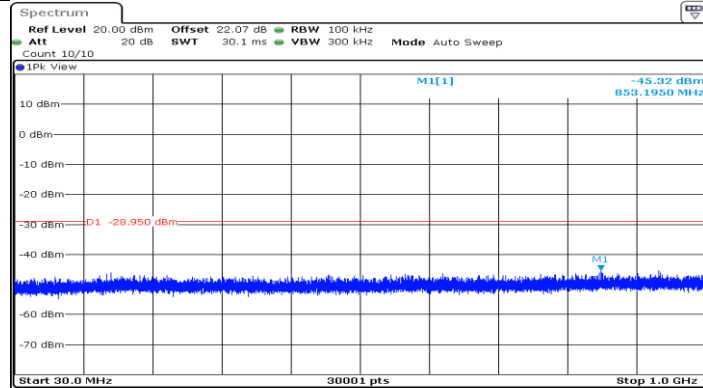


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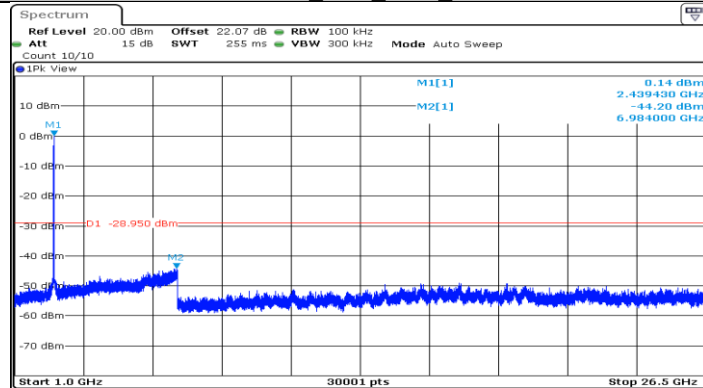
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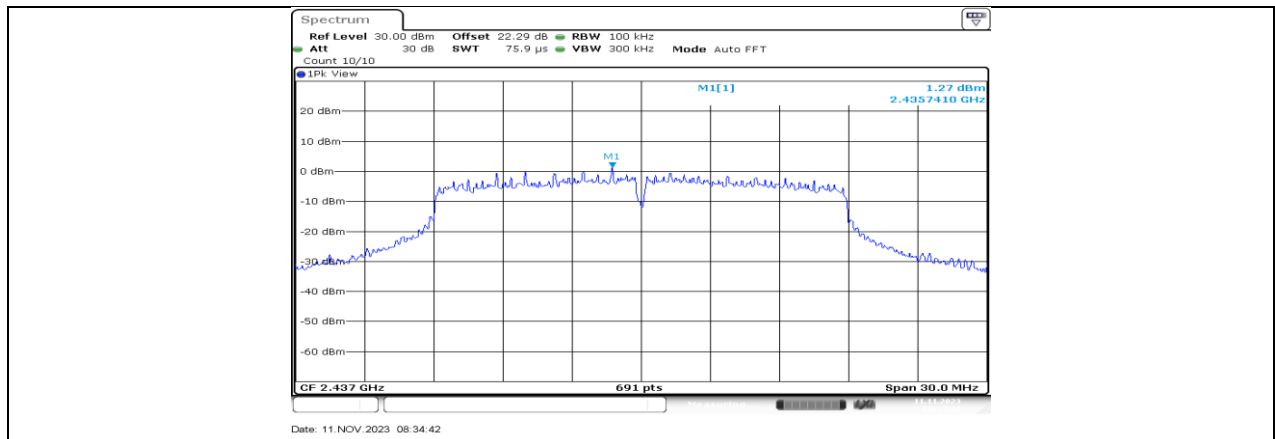
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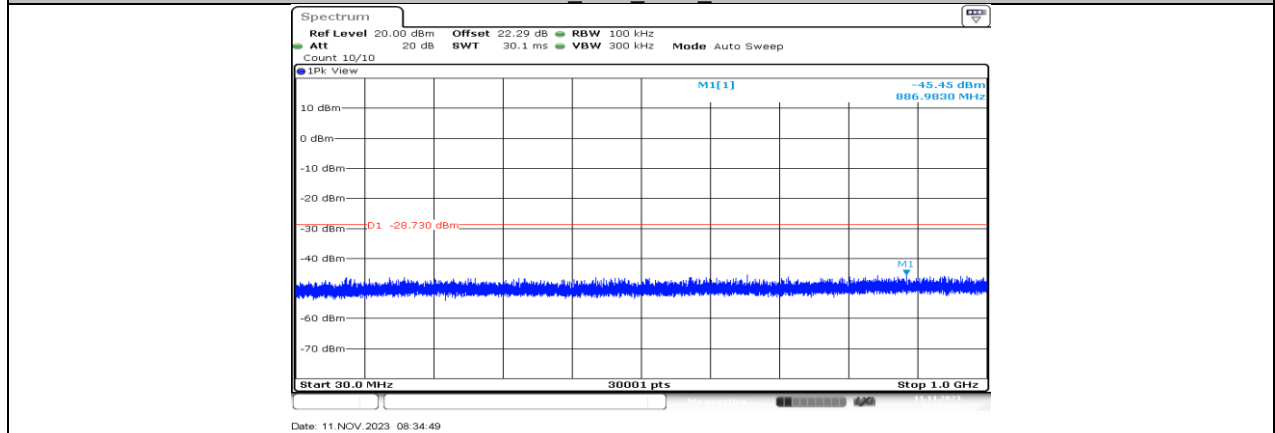


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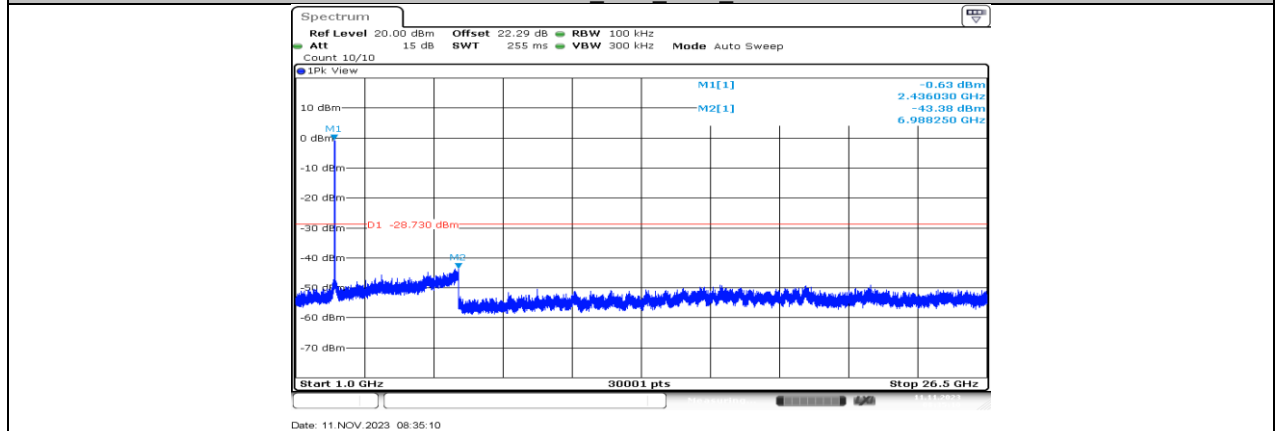
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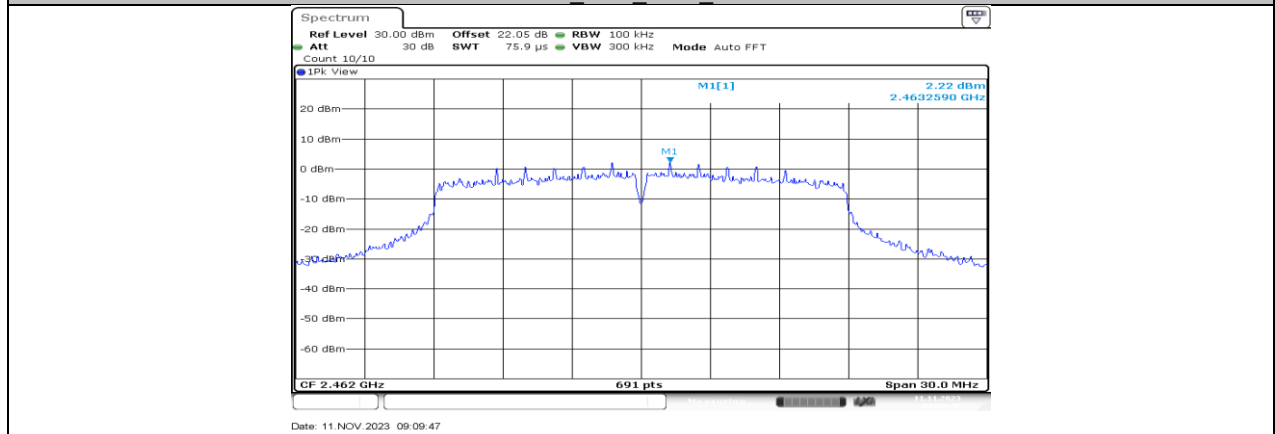
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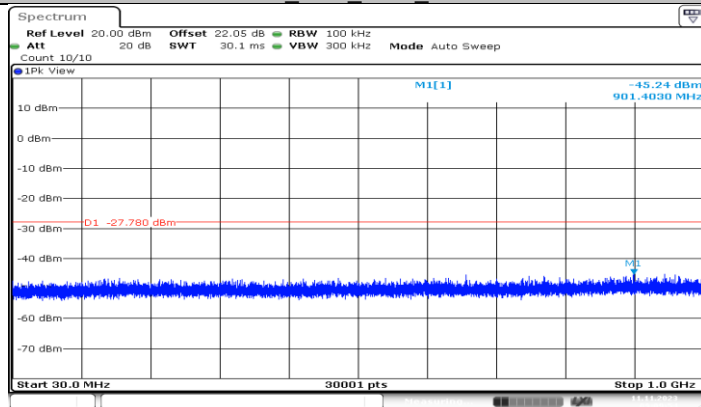
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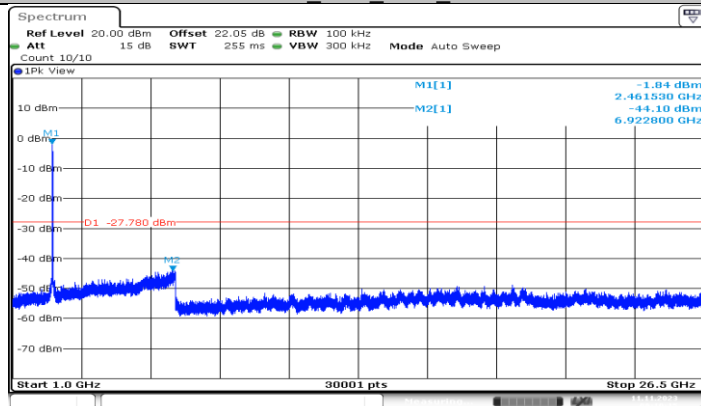


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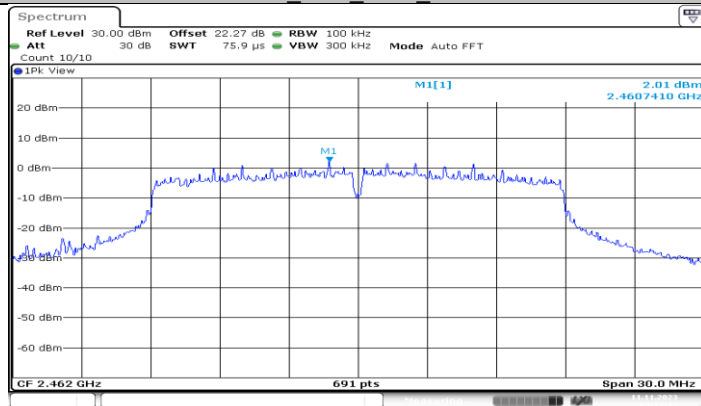
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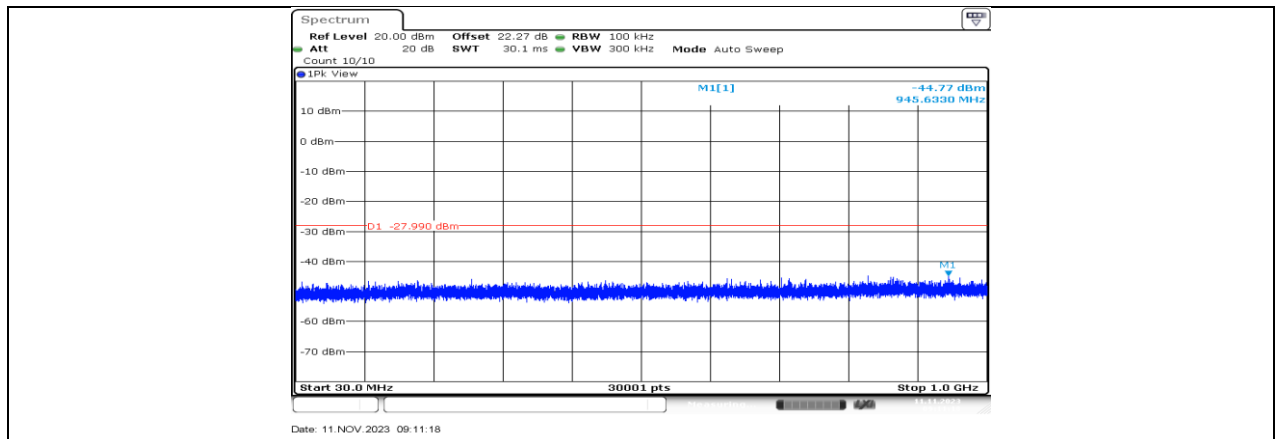
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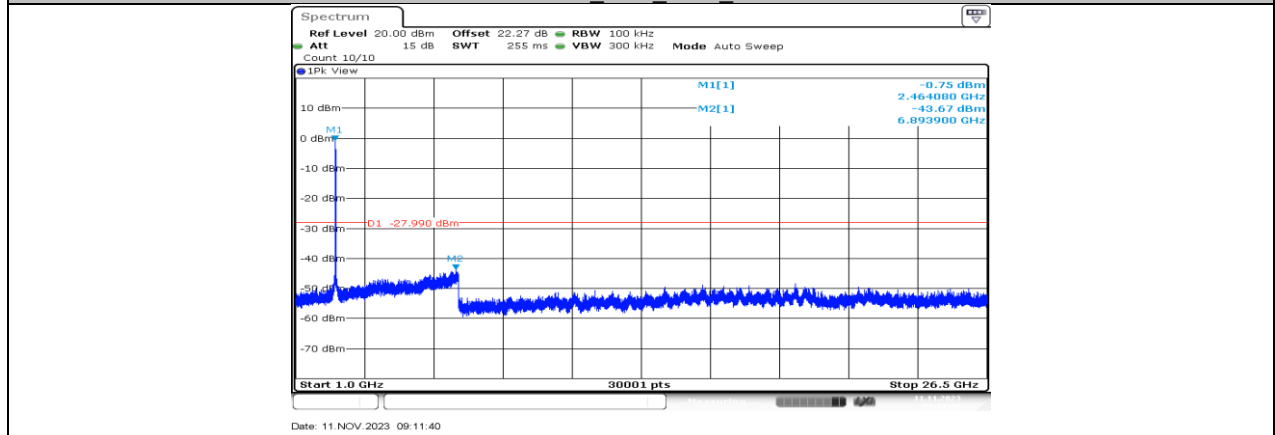


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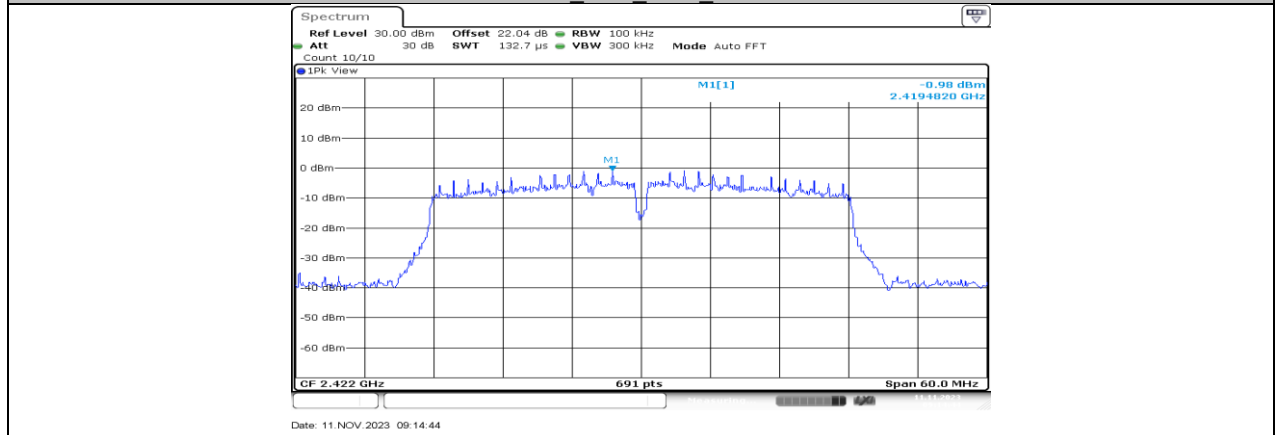
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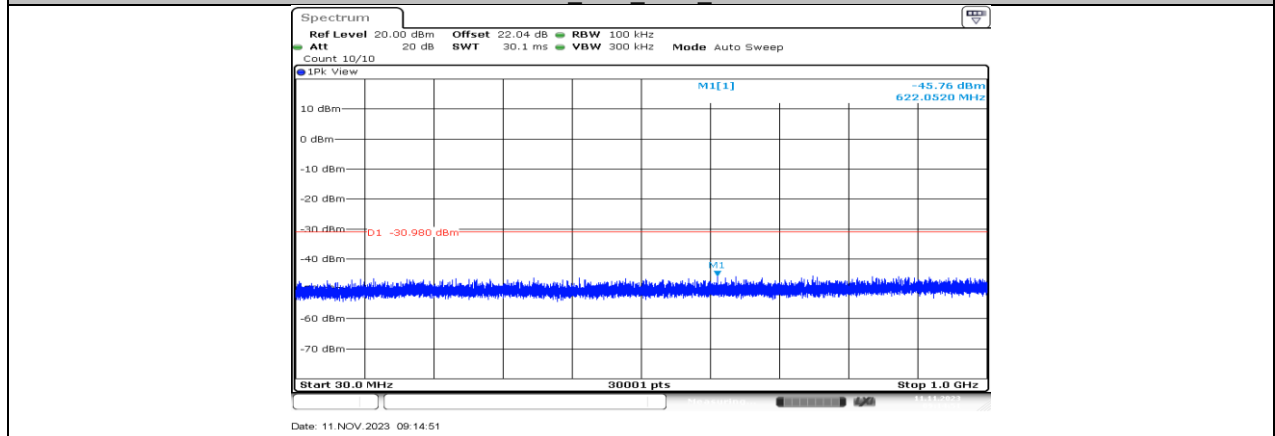
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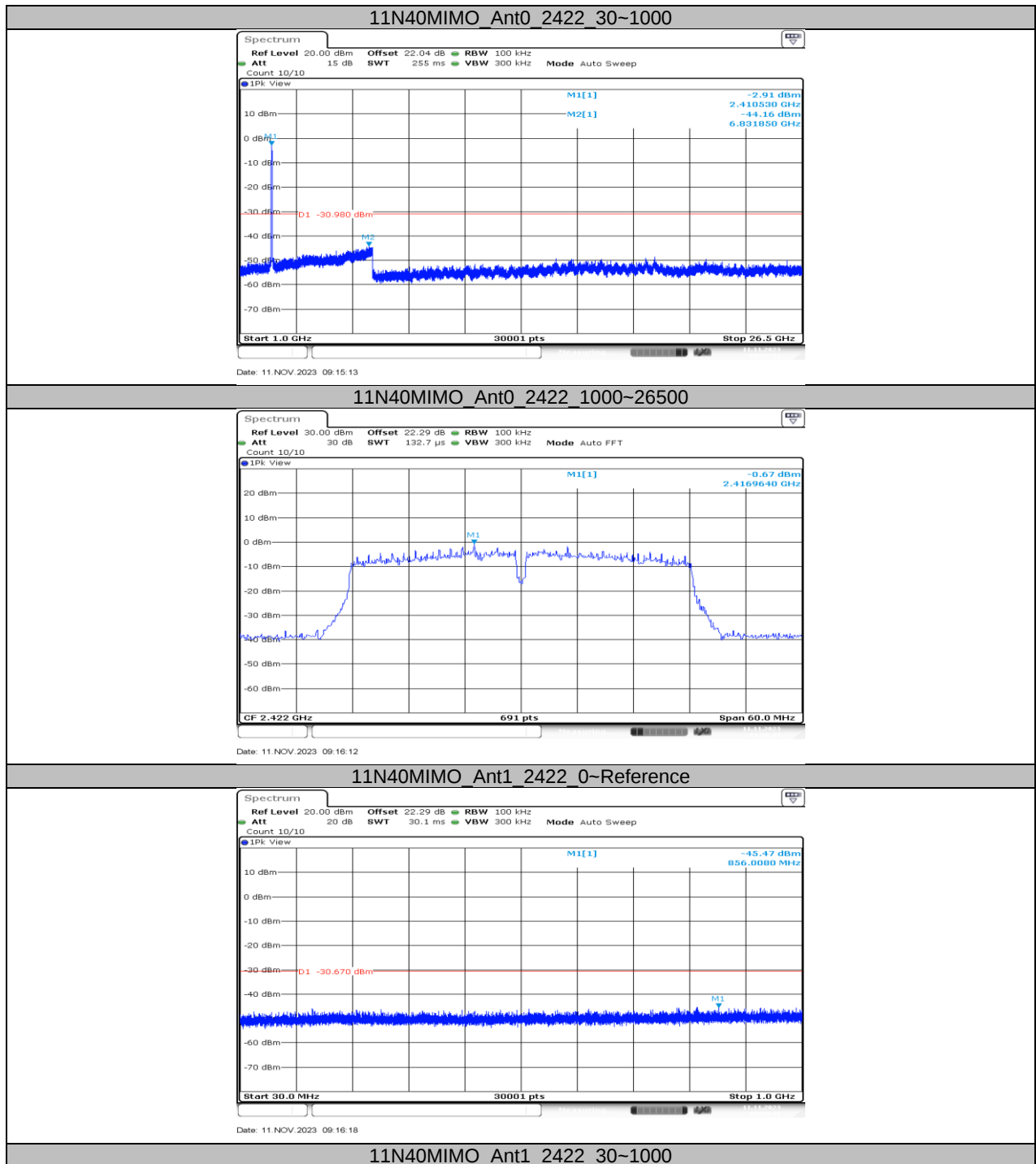


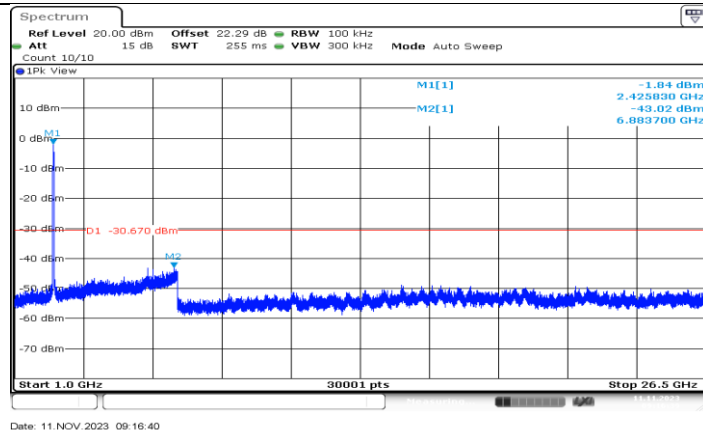
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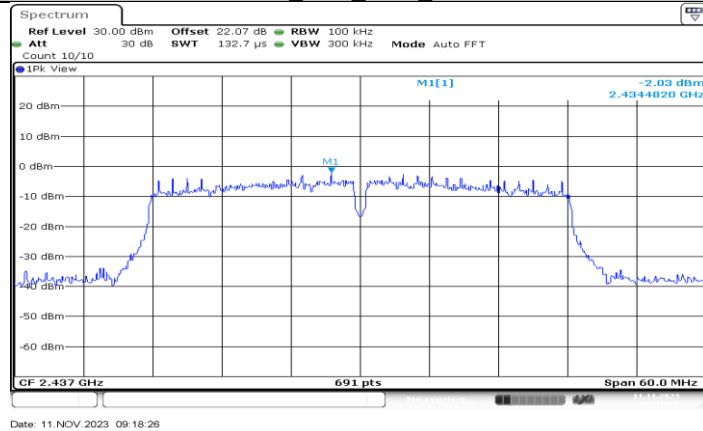
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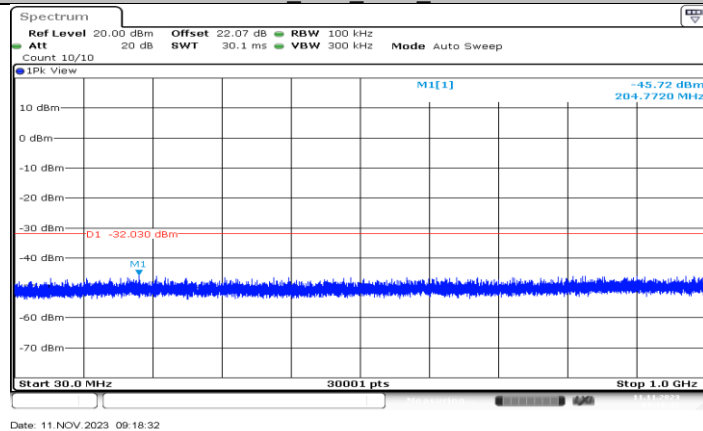




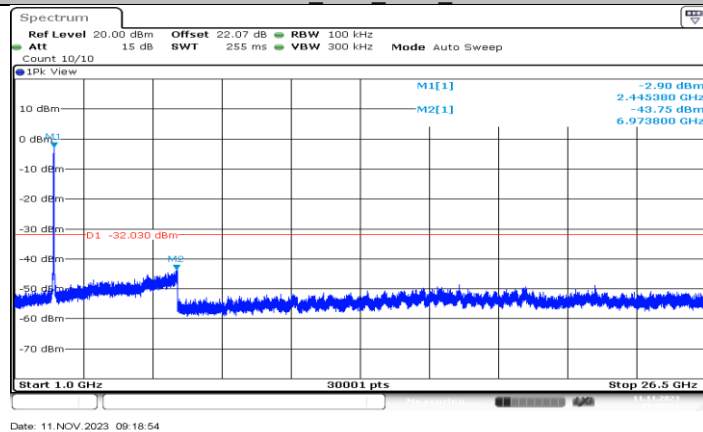
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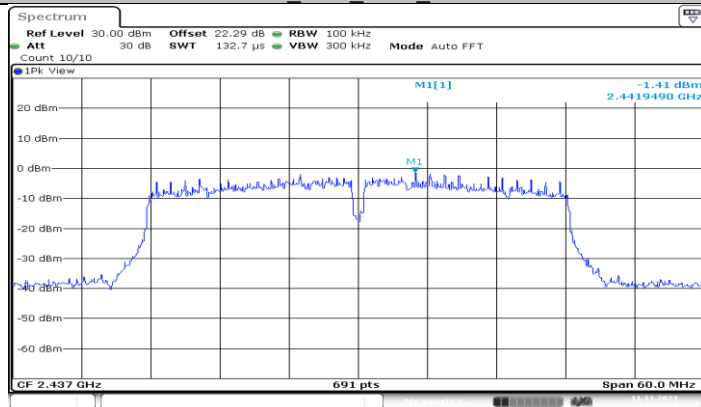
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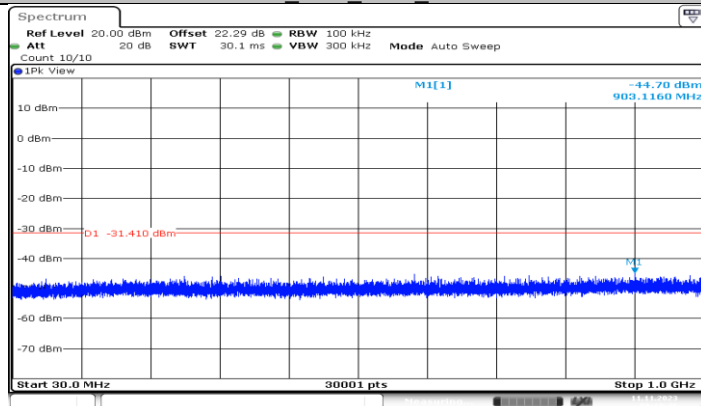
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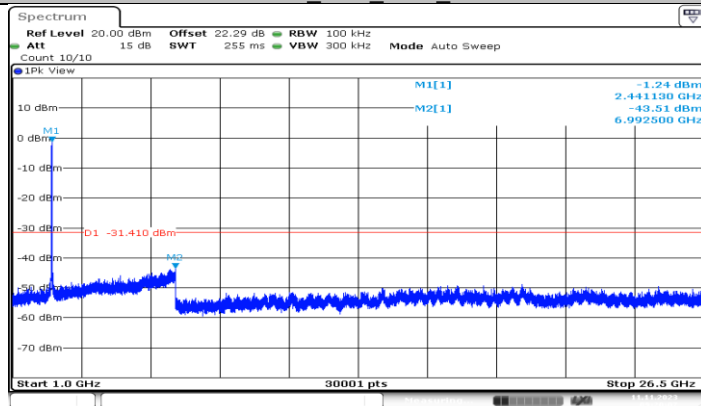
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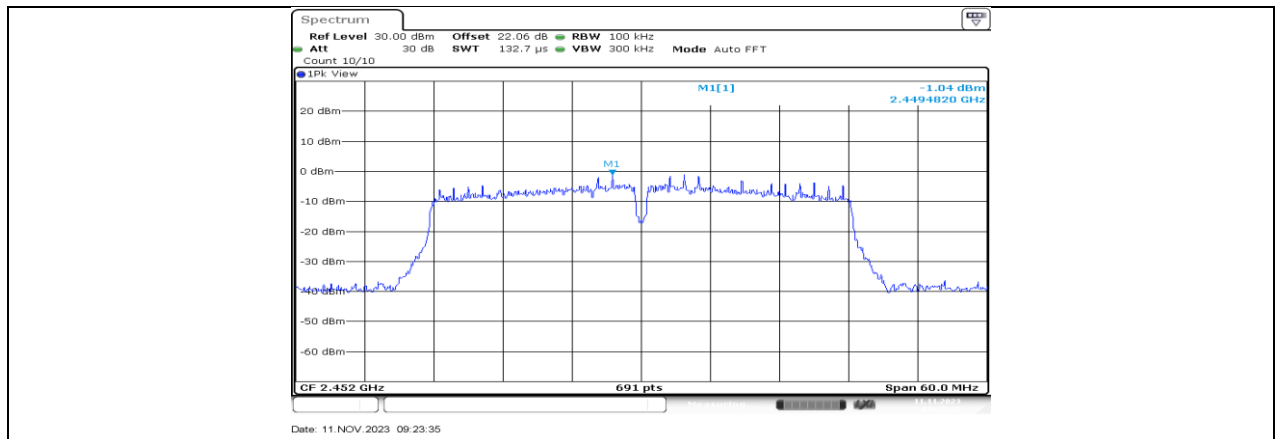
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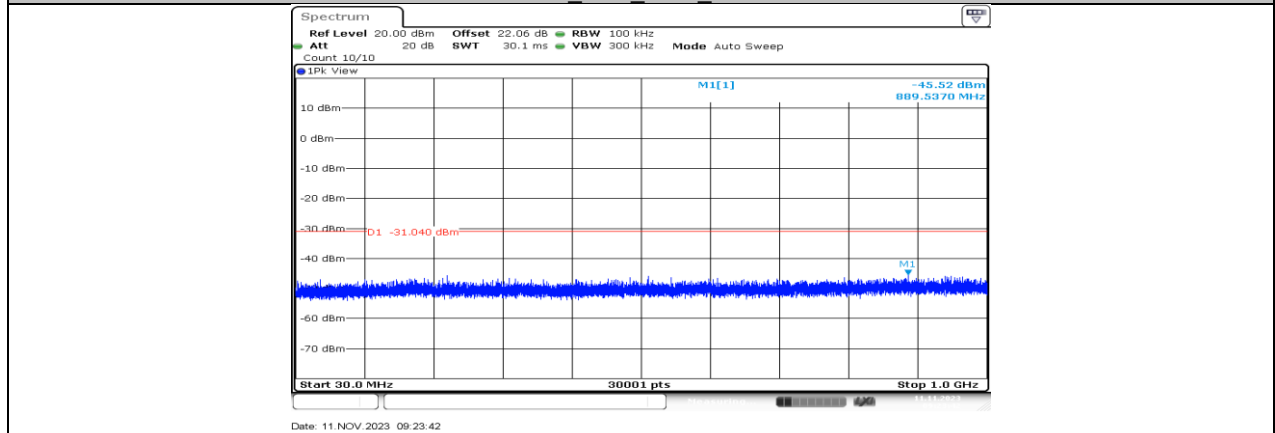
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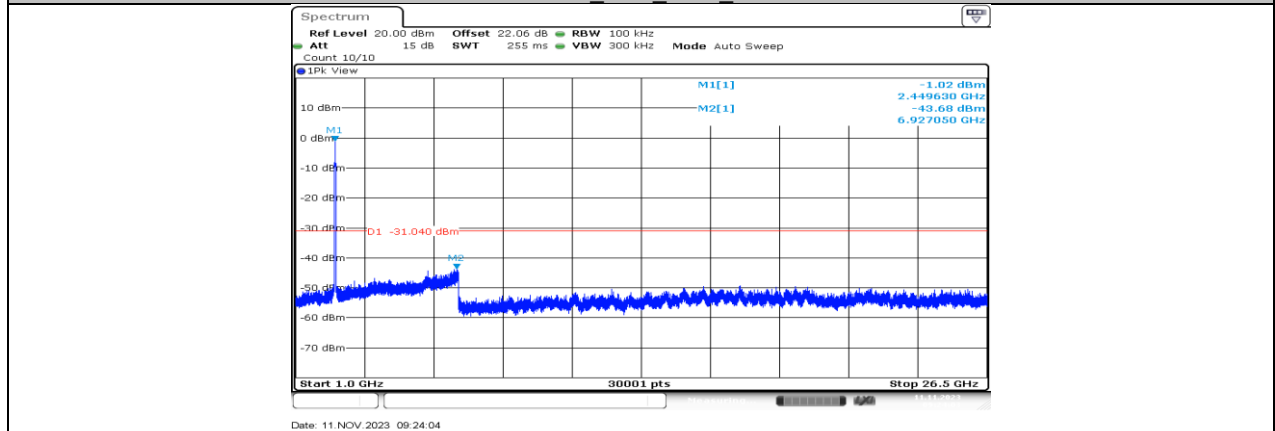
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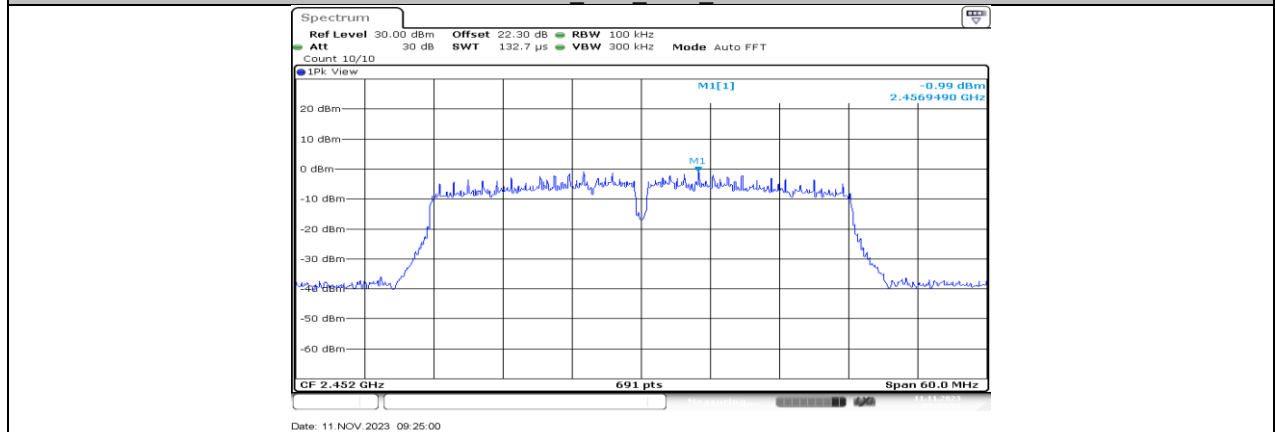
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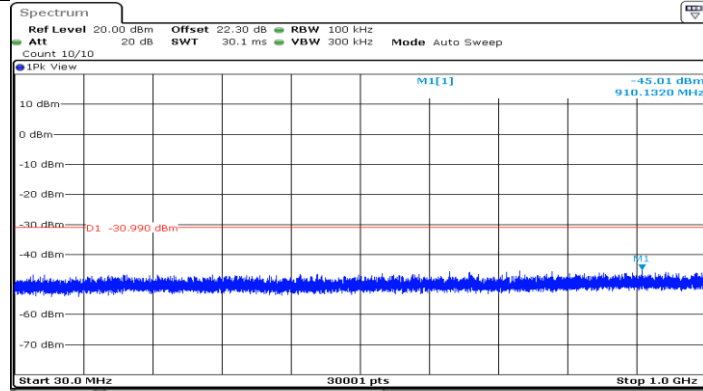
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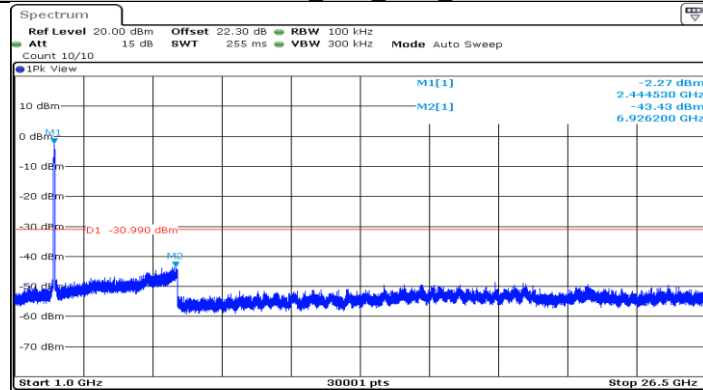
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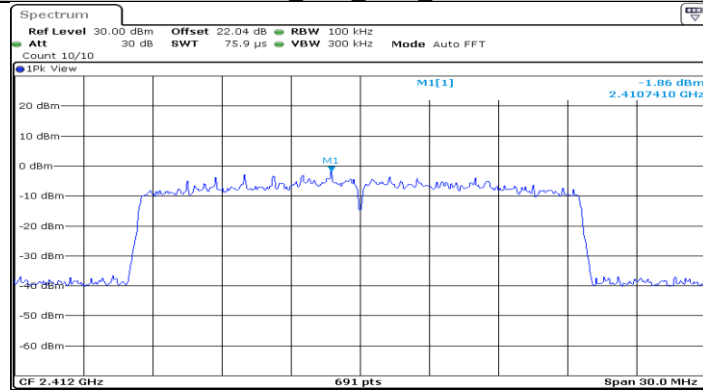
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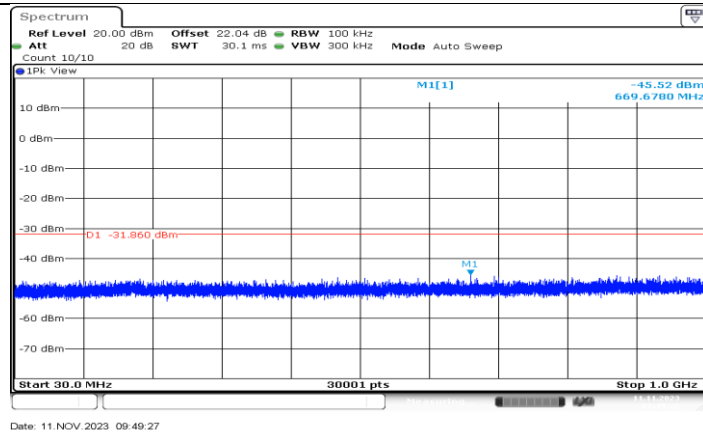
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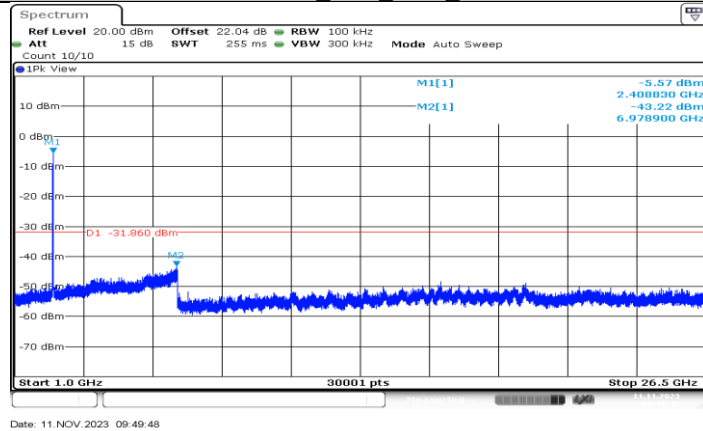
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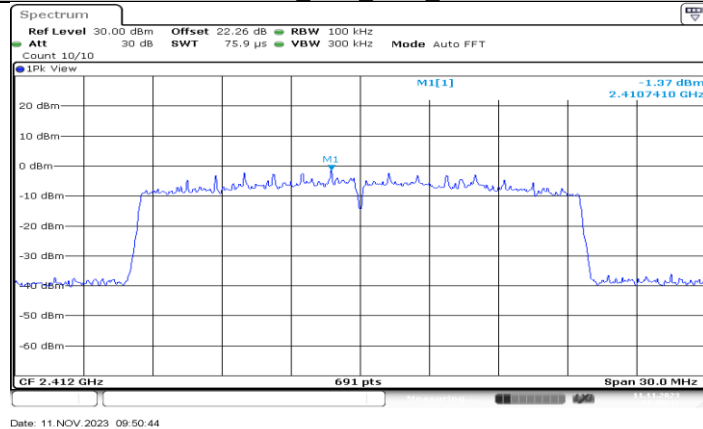
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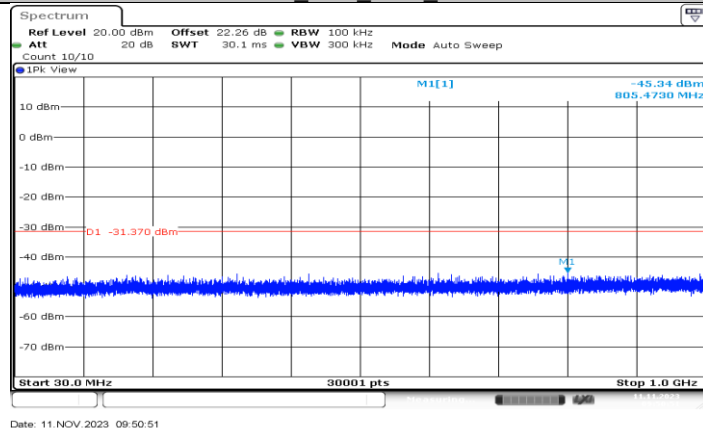
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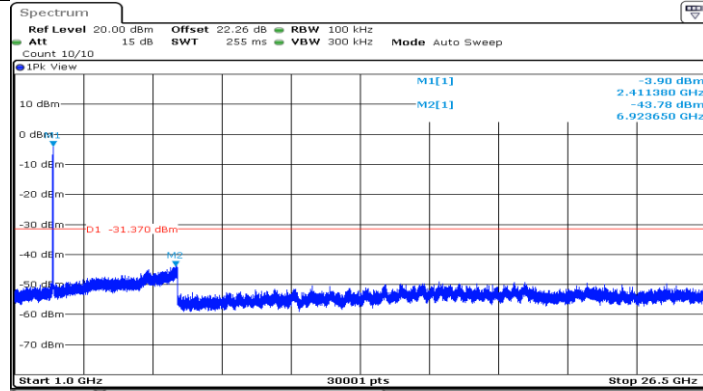
11AX20MIMO Ant0 2412 1000~26500



11AX20MIMO Ant1 2412 0~Reference

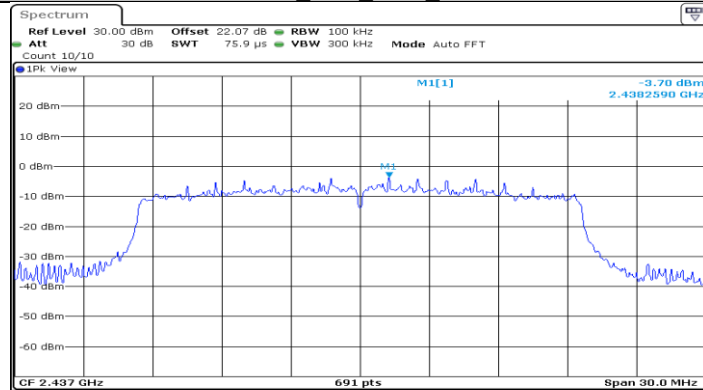


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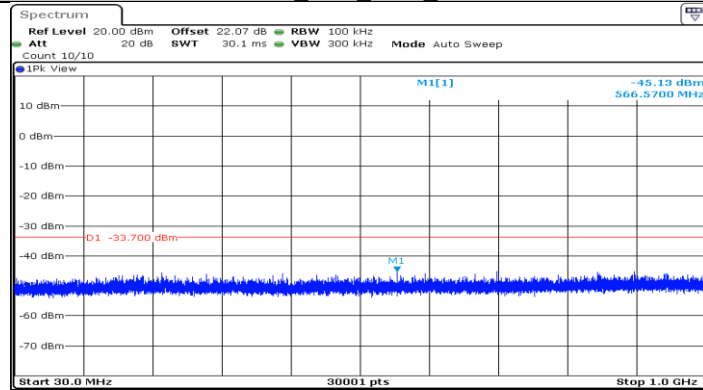
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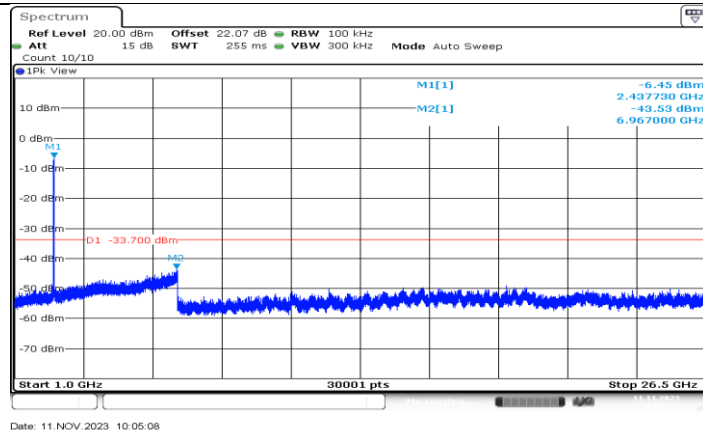
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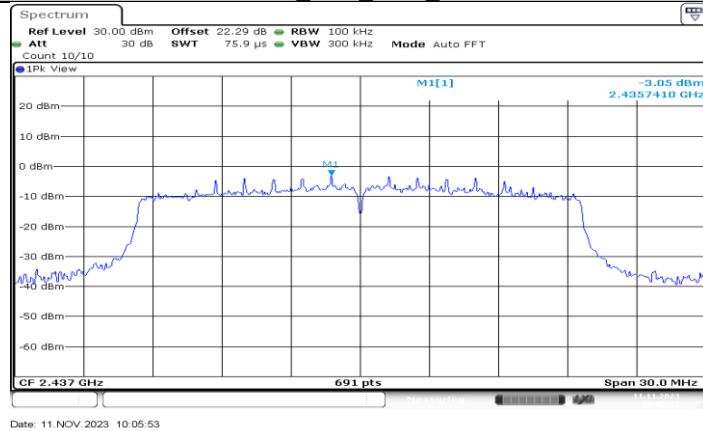


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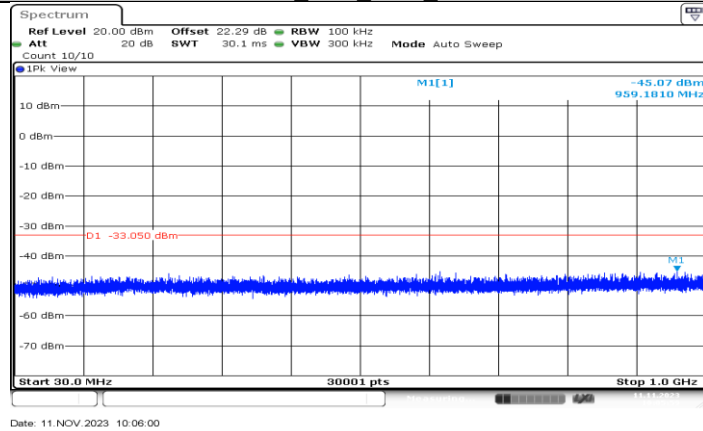
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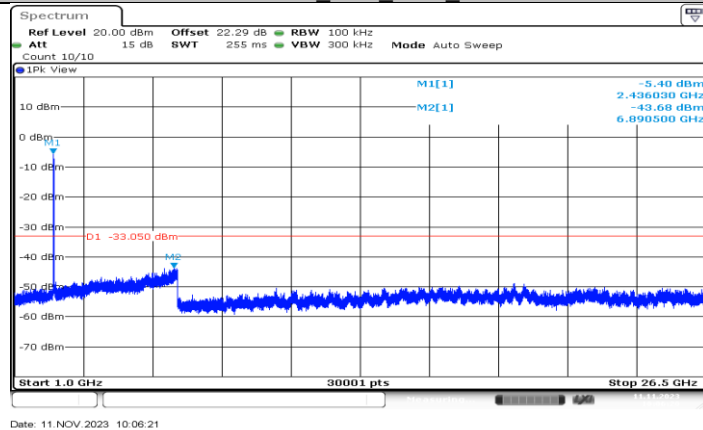
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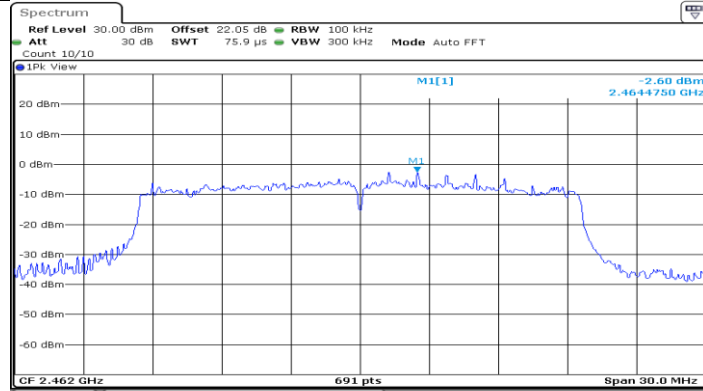
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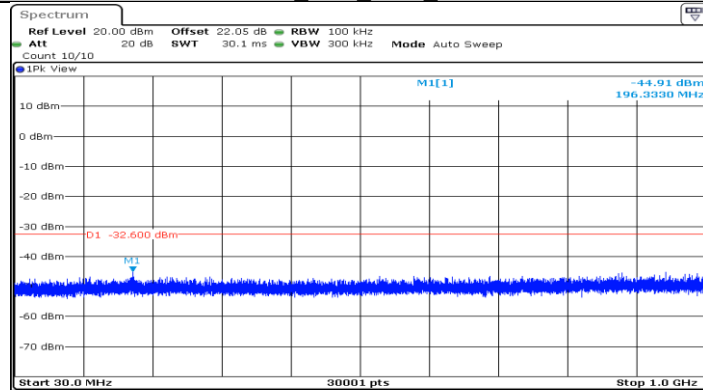


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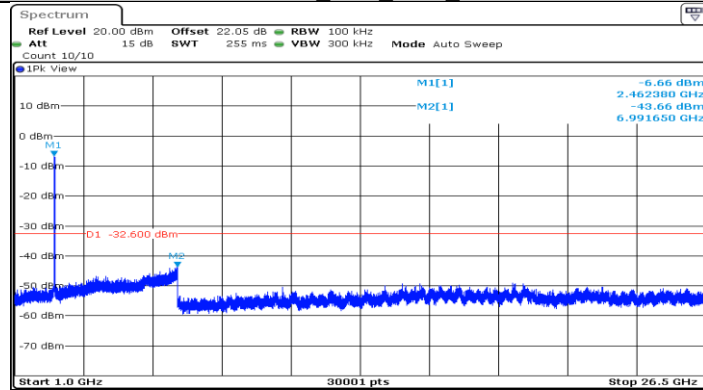
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11AX20MIMO_Ant0_2462_0~Reference



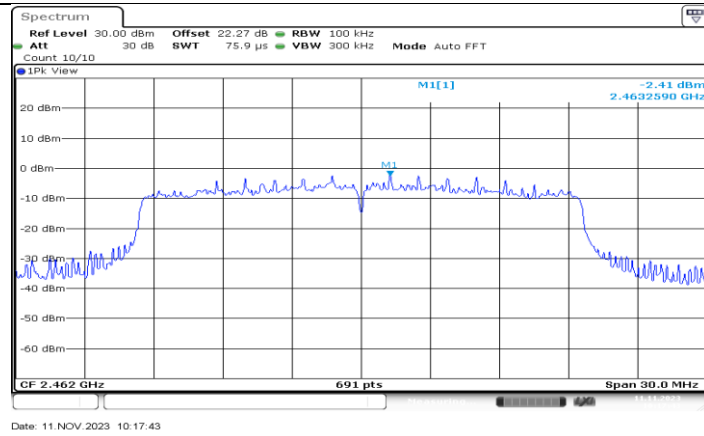
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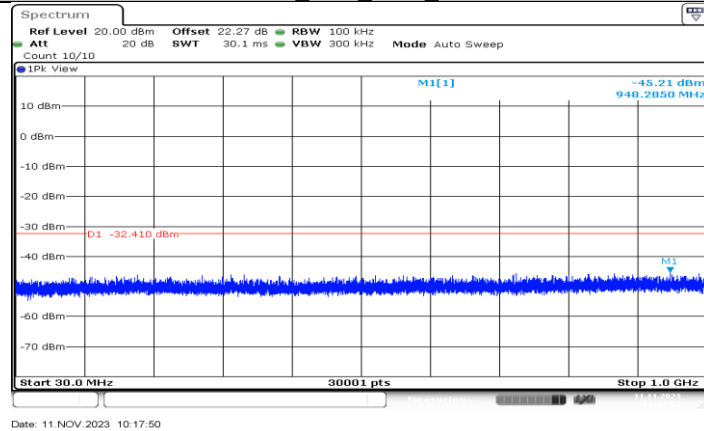


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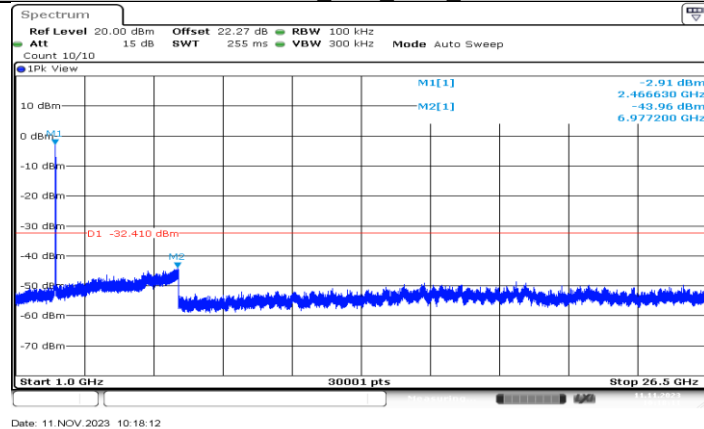
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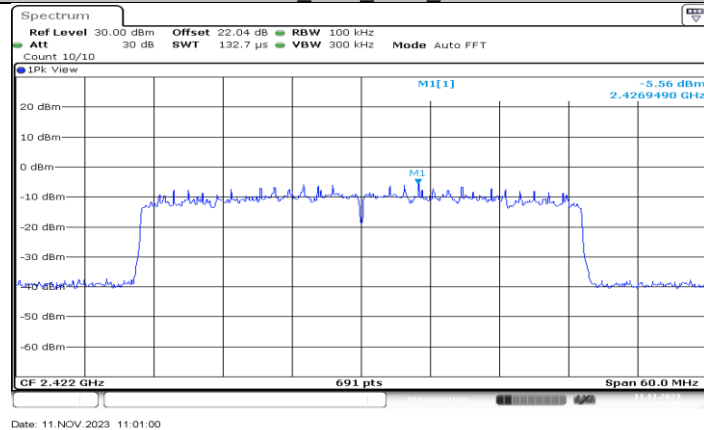
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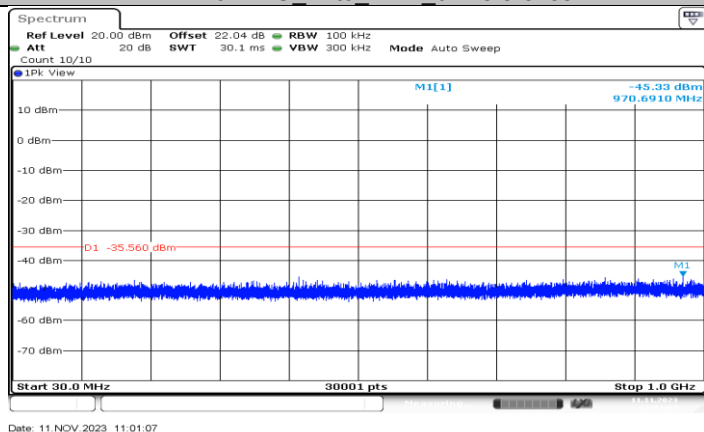
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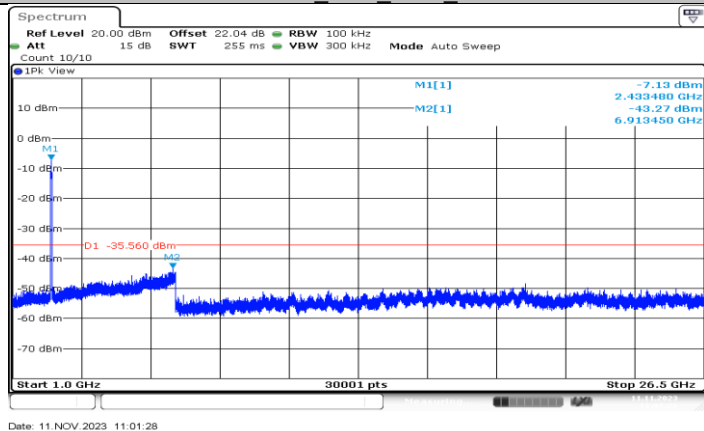
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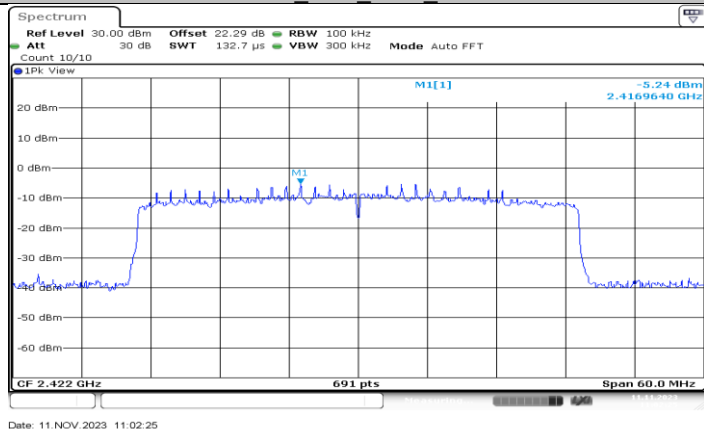
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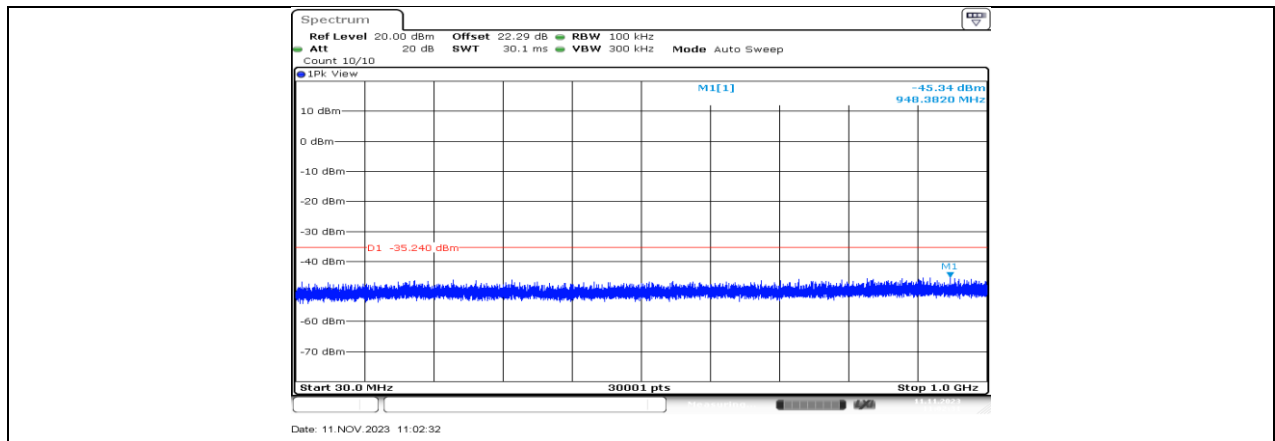
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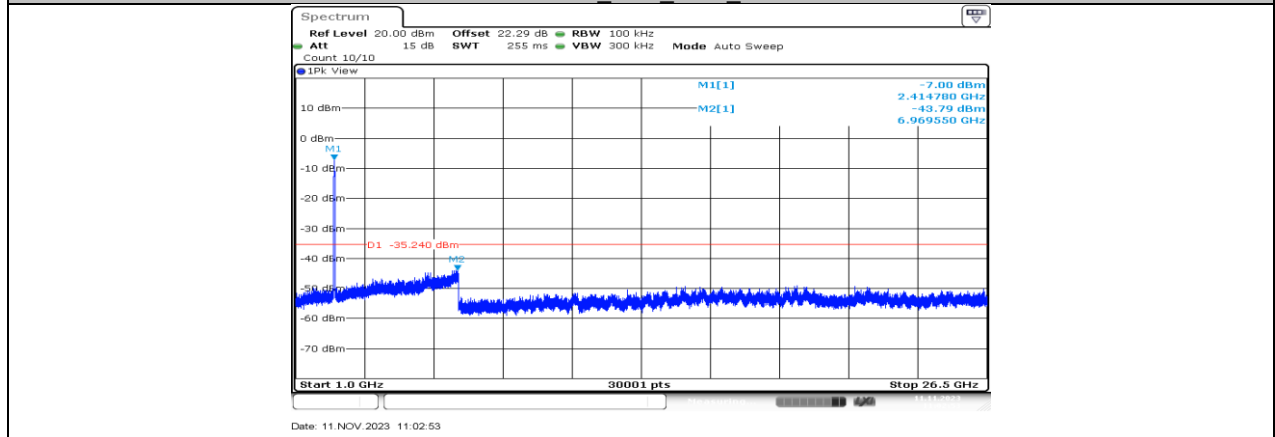
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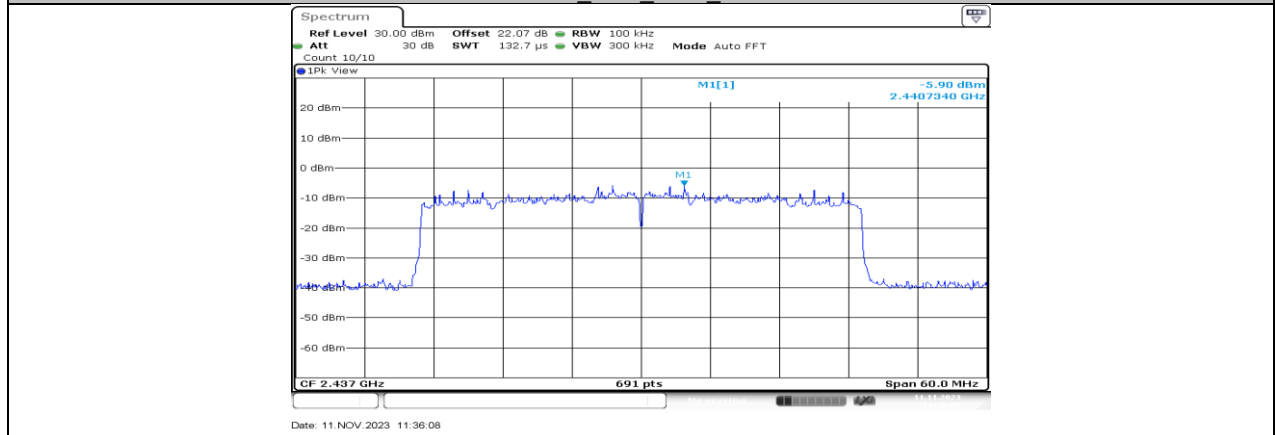
11AX40MIMO_Ant1_2422_0~Reference



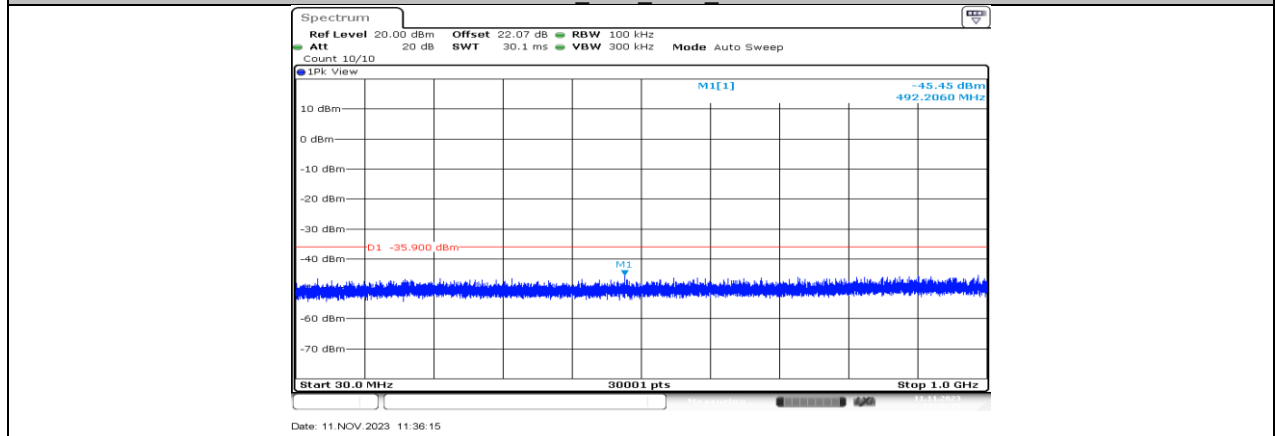
11AX40MIMO Ant1 2422 30~1000

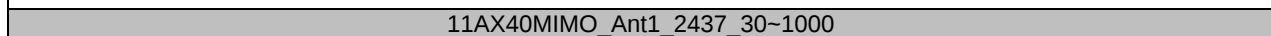


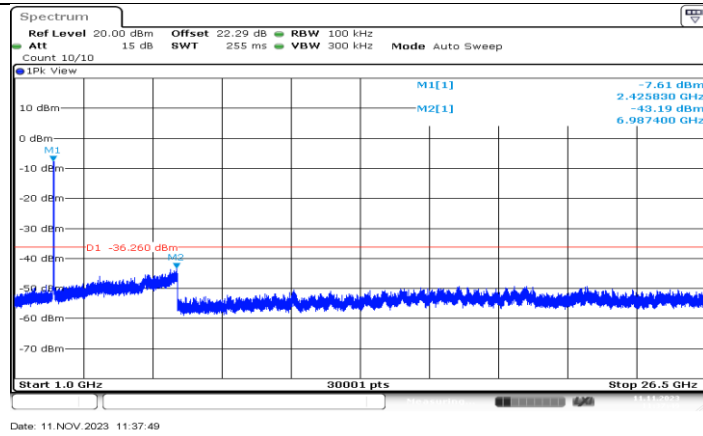
11AX40MIMO Ant1 2422 1000~26500



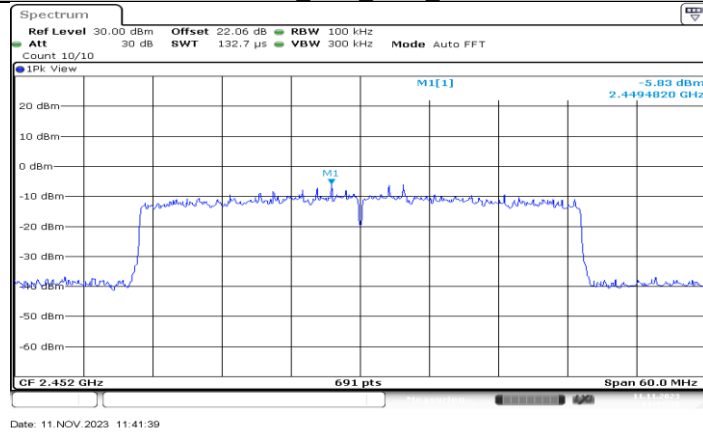
11AX40MIMO Ant0 2437 0~Reference



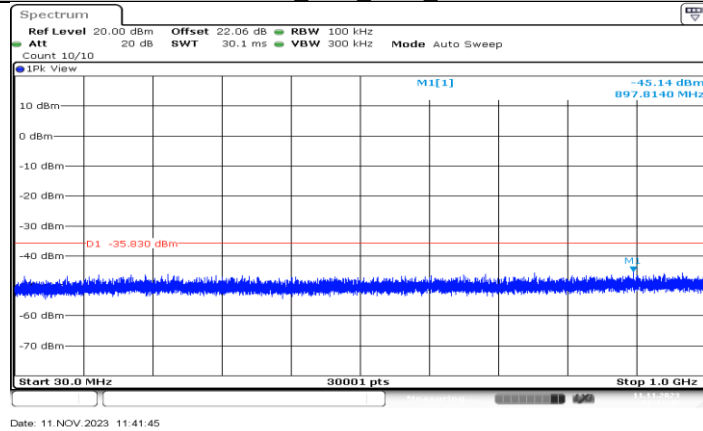




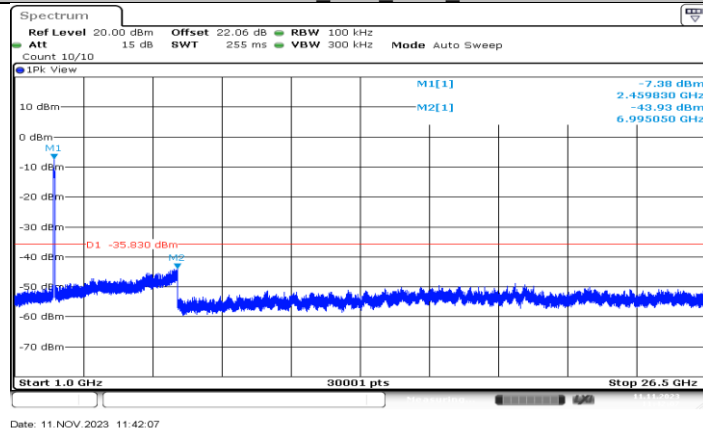
11AX40MIMO_Ant1_2437_1000-26500



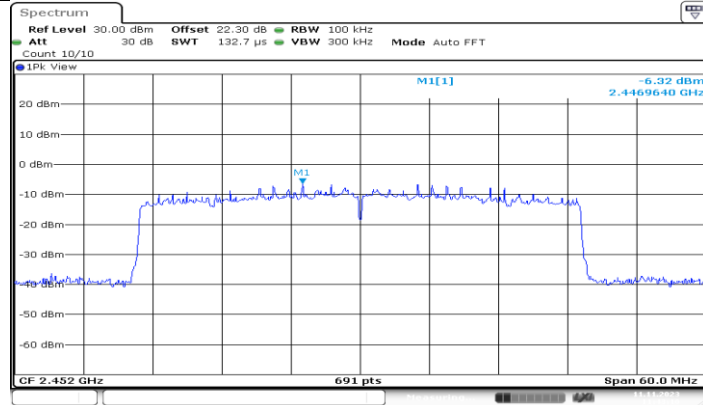
11AX40MIMO_Ant0_2452_0-Reference



11AX40MIMO_Ant0_2452_30-1000

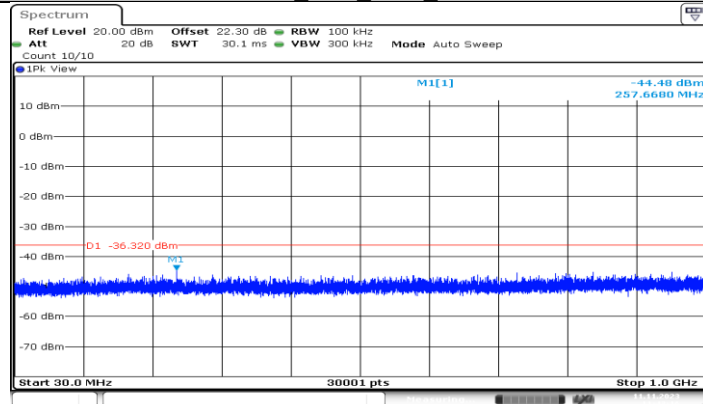


11AX40MIMO_Ant0_2452_1000~26500



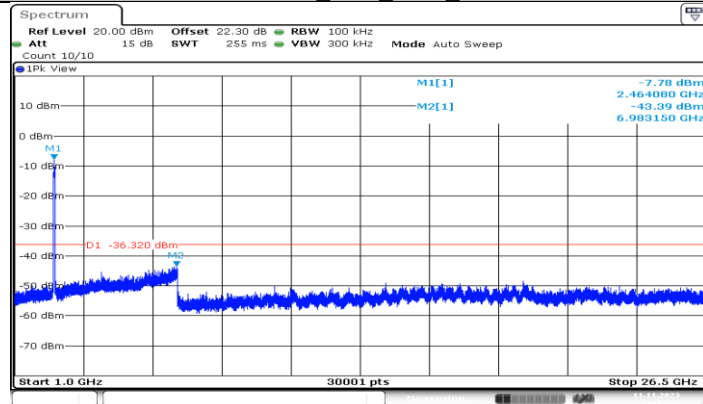
Date: 11.NOV.2023 11:43:10

11AX40MIMO_Ant1_2452_0-Reference



Date: 11.NOV.2023 11:43:17

11AX40MIMO_Ant1_2452_30~1000



Date: 11.NOV.2023 11:43:38

11AX40MIMO_Ant1_2452_1000~26500

11.7. APPENDIX G: DUTY CYCLE**11.7.1. Test Result**

Test Mode	On Time (msec)	Period (msec)	Duty Cycle x (Linear)	Duty Cycle (%)	Duty Cycle Correction Factor (dB)	1/T Minimum VBW (kHz)	Final setting For VBW (kHz)
11B	8.36	8.97	0.9320	93.20	0.31	0.12	1
11G	1.38	2.00	0.6900	69.00	1.61	0.72	1
11N20MIMO	1.29	1.91	0.6754	67.54	1.70	0.78	1
11N40MIMO	0.64	1.34	0.4776	47.76	3.21	1.56	2
11AX20MIMO	1.02	1.65	0.6182	61.82	2.09	0.98	1
11AX40MIMO	1.96	2.67	0.7341	73.41	1.34	0.51	1

Note:

Duty Cycle Correction Factor= $10\log(1/x)$.

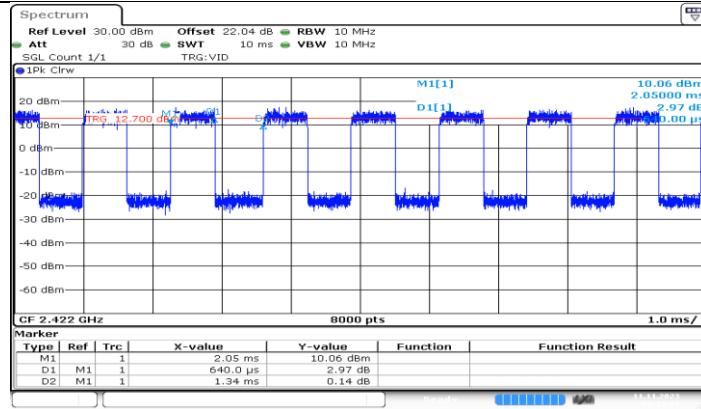
Where: x is Duty Cycle (Linear)

Where: T is On Time

If that calculated VBW is not available on the analyzer then the next higher value should be used.

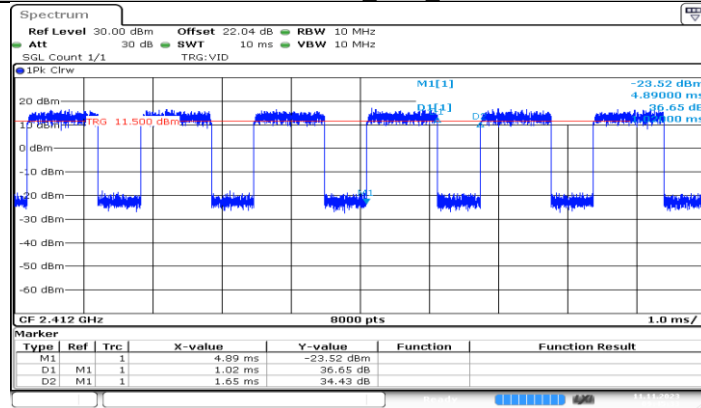
11.7.2. Test Graphs





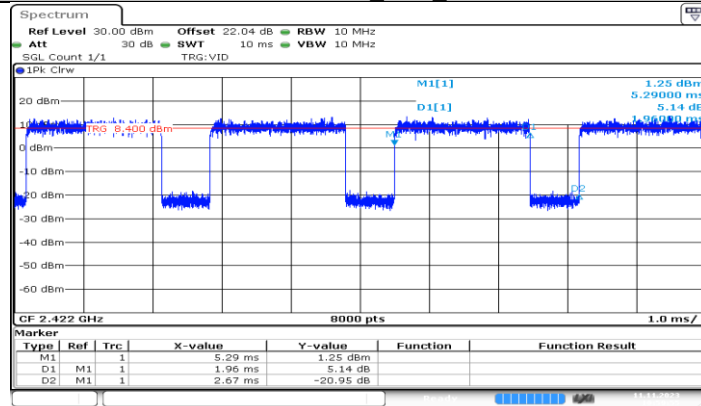
Date: 11.NOV.2023 09:13:59

11N40MIMO_Ant0_2422



Date: 11.NOV.2023 09:45:33

11AX20MIMO_Ant0_2412



Date: 11.NOV.2023 10:59:55

11AX40MIMO_Ant0_2422

END OF REPORT