

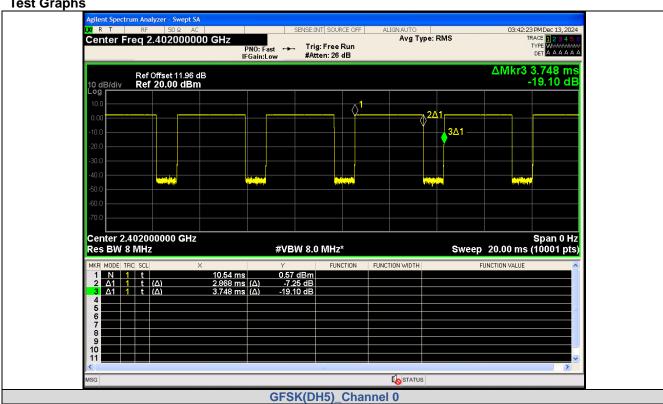
Page 45 of 54 Report No.: MTEB24120216-R

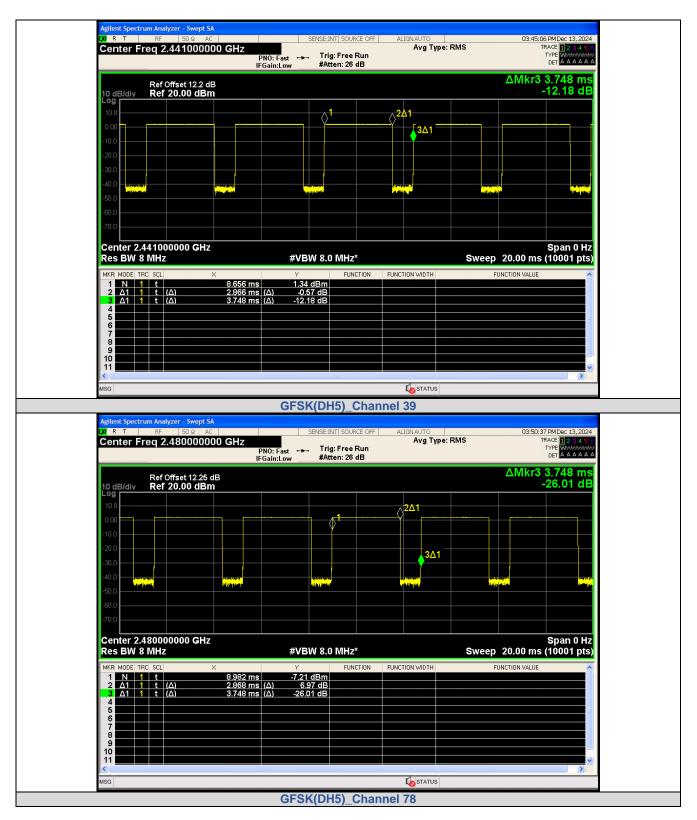
APPENDIX VI. Duty Cycle

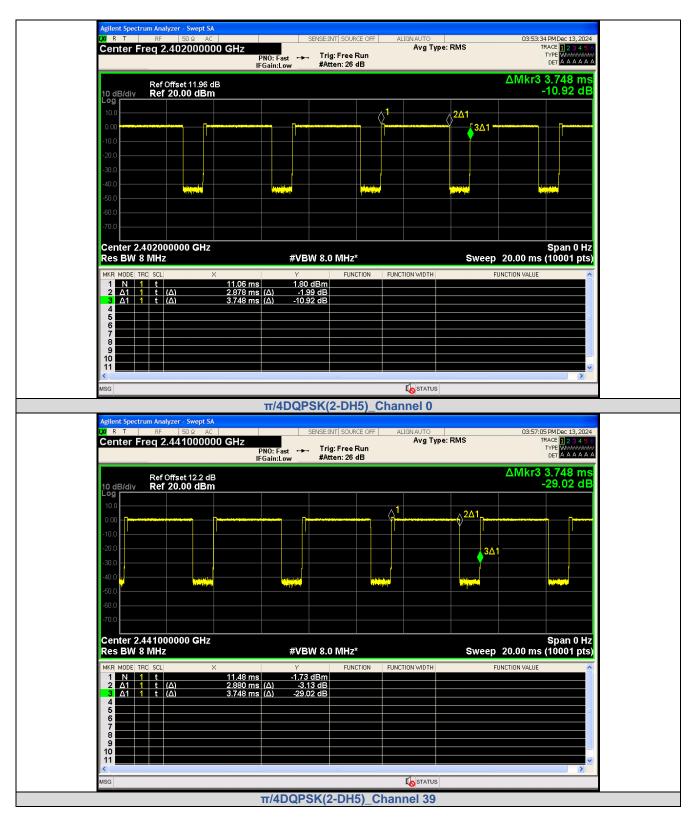
Test Result

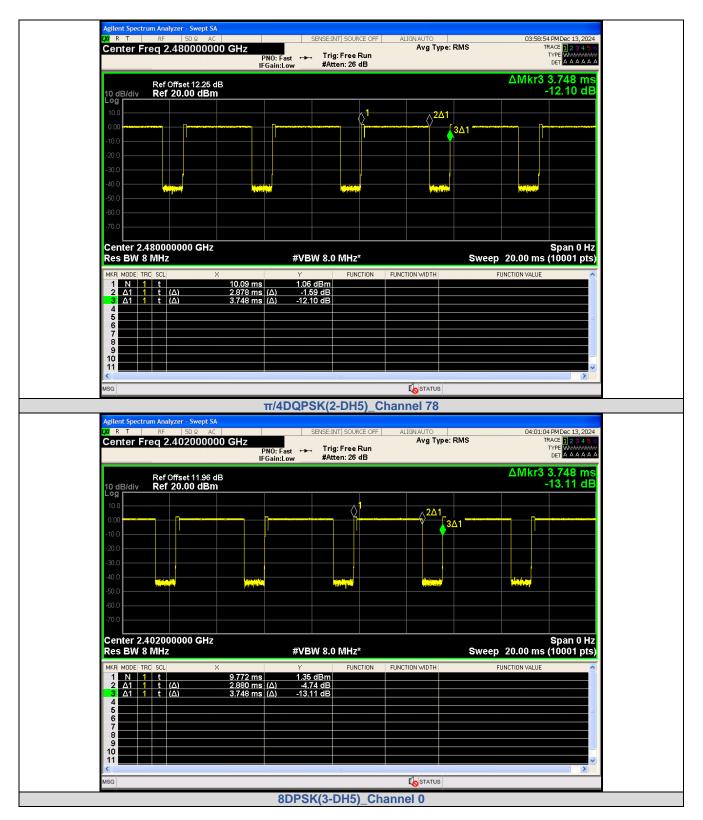
Modulation	Packets	Channel	On Time (ms)	Period (ms)	Duty Cycle (%)	Duty Cycle (linear)	Duty Cycle Factor (dB)
GFSK	DH5	0	2.868	3.748	76.52	0.7652	1.1623
		39	2.866	3.748	76.47	0.7647	1.1651
		78	2.868	3.748	76.52	0.7652	1.1623
π/4DQPSK	2-DH5	0	2.878	3.748	76.79	0.7679	1.147
		39	2.880	3.748	76.84	0.7684	1.1441
		78	2.878	3.748	76.79	0.7679	1.147
8DPSK	3-DH5	0	2.880	3.748	76.84	0.7684	1.1441
		39	2.880	3.748	76.84	0.7684	1.1441
		78	2.880	3.748	76.84	0.7684	1.1441

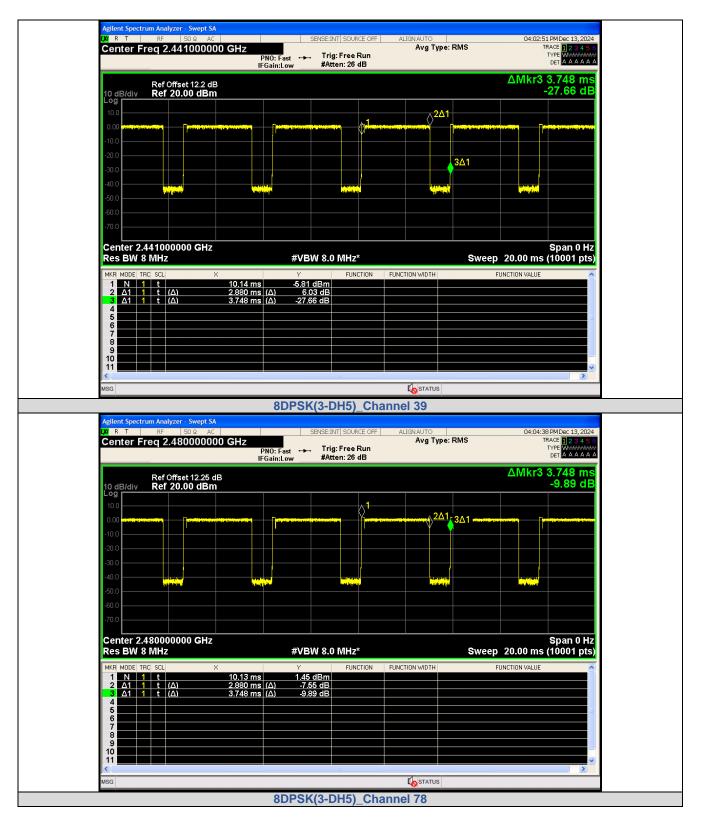
Test Graphs









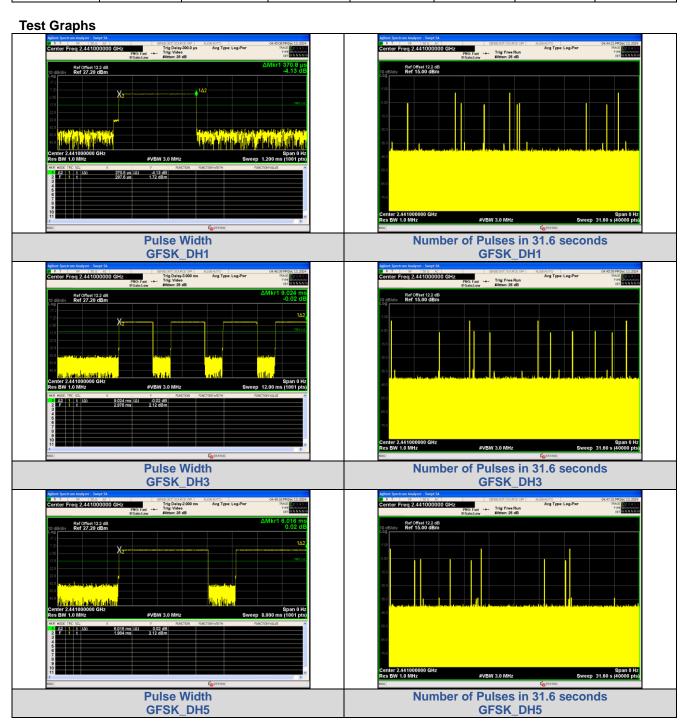


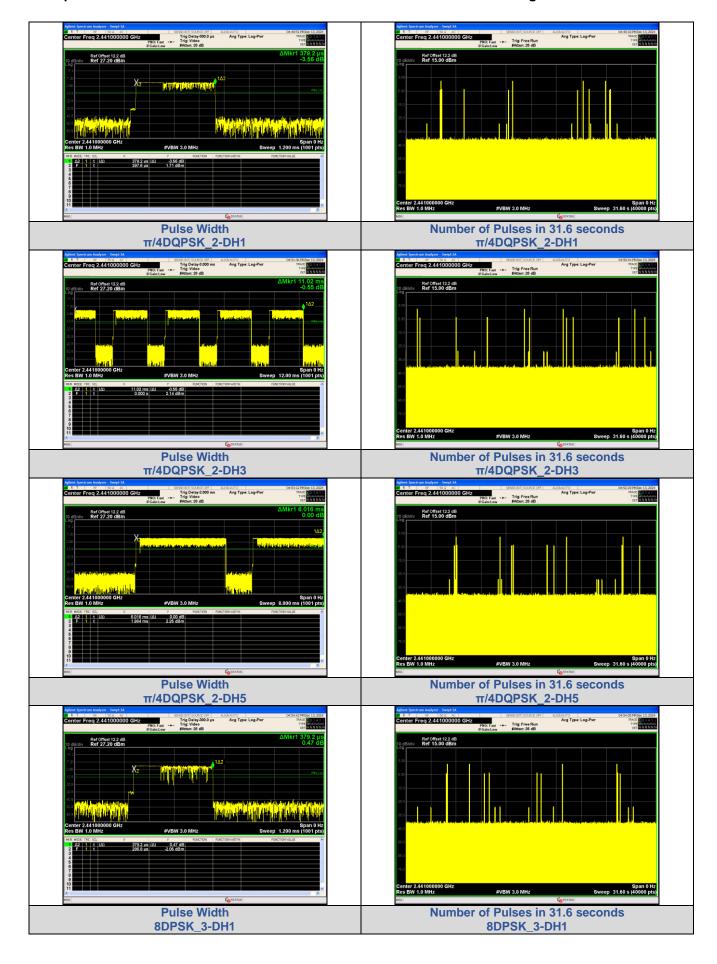
Report No.: MTEB24120216-R Page 50 of 54

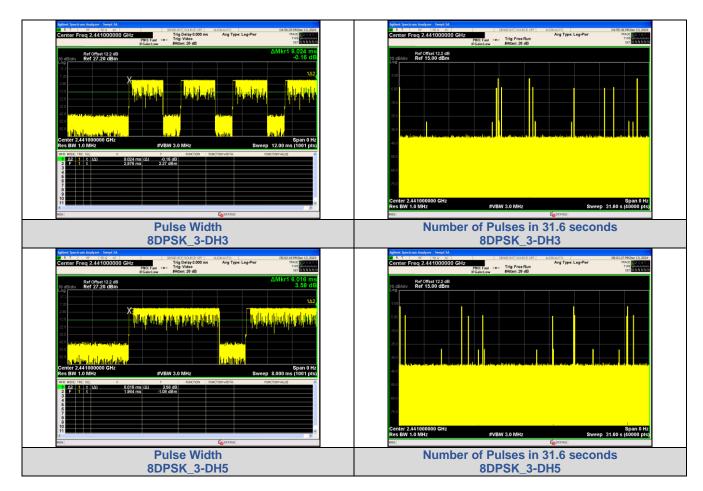
APPENDIX VII. Dwell Time

Test Result

Modulation	Packet	Channel	Pulse Width (ms)	Number of Pulses in 31.6 seconds	Dwell Time (ms)	Limit (ms)	Result
GFSK	DH1	CH39 - (2441MHz)	0.3708	3	1.11	< 400	PASS
	DH3		9.024	4	36.1		PASS
	DH5		6.016	3	18.05		PASS
π/4DQPSK	2-DH1		0.3792	4	1.52		PASS
	2-DH3		11.016	4	44.06		PASS
	2-DH5		6.016	4	24.06		PASS
8DPSK	3-DH1		0.3792	4	1.52		PASS
	3-DH3		9.024	3	27.07		PASS
	3-DH5		6.016	4	24.06		PASS





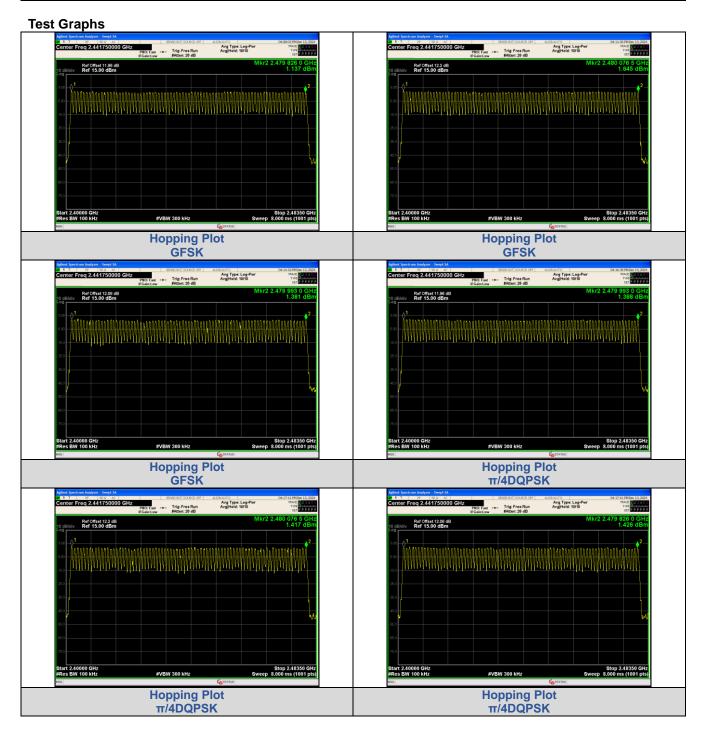


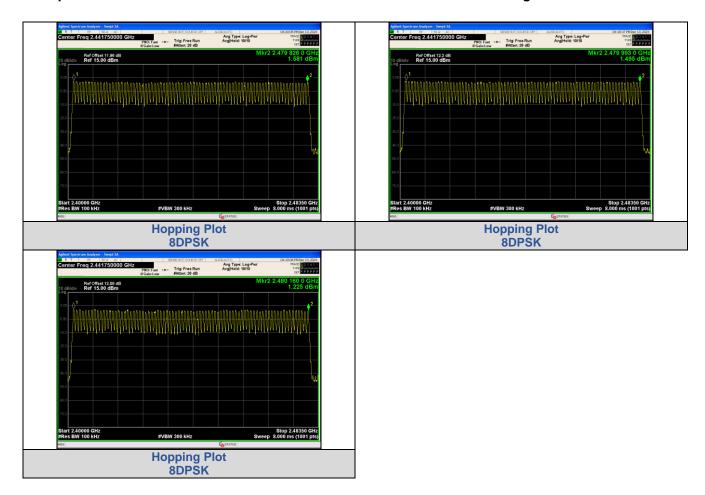
Report No.: MTEB24120216-R Page 53 of 54

APPENDIX VIII. Number Of Hopping Channel

Test Result

Modulation	Packet	Number of Hopping Channel	Limit	Result
GFSK	DH5	79	15	PASS
GFSK	DH5	79	15	PASS
GFSK	DH5	79	15	PASS
π/4DQPSK	2-DH5	79	15	PASS
π/4DQPSK	2-DH5	79	15	PASS
π/4DQPSK	2-DH5	79	15	PASS
8DPSK	3-DH5	79	15	PASS
8DPSK	3-DH5	79	15	PASS
8DPSK	3-DH5	79	15	PASS





******************** End of Report ***************