

Appendix E.4: Maximum conducted output power

Test Result

Test Mode	Antenna	Frequency [MHz]	Channel Power [dBm]	Duty Cycle [%]	DC Factor [dBm]	Result [dBm]	Limit [dBm]	Verdict
11A	Ant0	5260	5.75	20.73	6.83	12.58	≤23.98	PASS
		5280	8.03	40.70	3.90	11.93	≤23.98	PASS
		5320	10.76	71.54	1.45	12.21	≤23.98	PASS
11N20SI SO	Ant0	5260	14.72	94.59	0.24	14.96	≤23.98	PASS
		5280	14.84	95.95	0.18	15.02	≤23.98	PASS
		5320	15.35	94.67	0.24	15.59	≤23.98	PASS
11N40SI SO	Ant0	5270	8.97	65.35	1.85	10.82	≤23.98	PASS
		5310	9.49	66.81	1.75	11.24	≤23.98	PASS
11AC20 SISO	Ant0	5260	11.60	80.00	0.97	12.57	≤23.98	PASS
		5280	11.37	85.00	0.71	12.08	≤23.98	PASS
		5320	11.57	80.95	0.92	12.49	≤23.98	PASS
11AC40 SISO	Ant0	5270	9.18	69.84	1.56	10.74	≤23.98	PASS
		5310	9.85	69.66	1.57	11.42	≤23.98	PASS
11AC80 SISO	Ant0	5290	7.03	48.32	3.16	10.19	≤23.98	PASS
11AX20 SISO	Ant0	5260	11.61	78.95	1.03	12.64	≤23.98	PASS
		5280	11.39	84.21	0.75	12.14	≤23.98	PASS
		5320	11.55	84.21	0.75	12.30	≤23.98	PASS
11AX40 SISO	Ant0	5270	8.67	57.01	2.44	11.11	≤23.98	PASS
		5310	9.36	56.72	2.46	11.82	≤23.98	PASS
11AX80 SISO	Ant0	5290	7.68	50.49	2.97	10.65	≤23.98	PASS

Note: The Duty Cycle Factor is compensated in the graph.

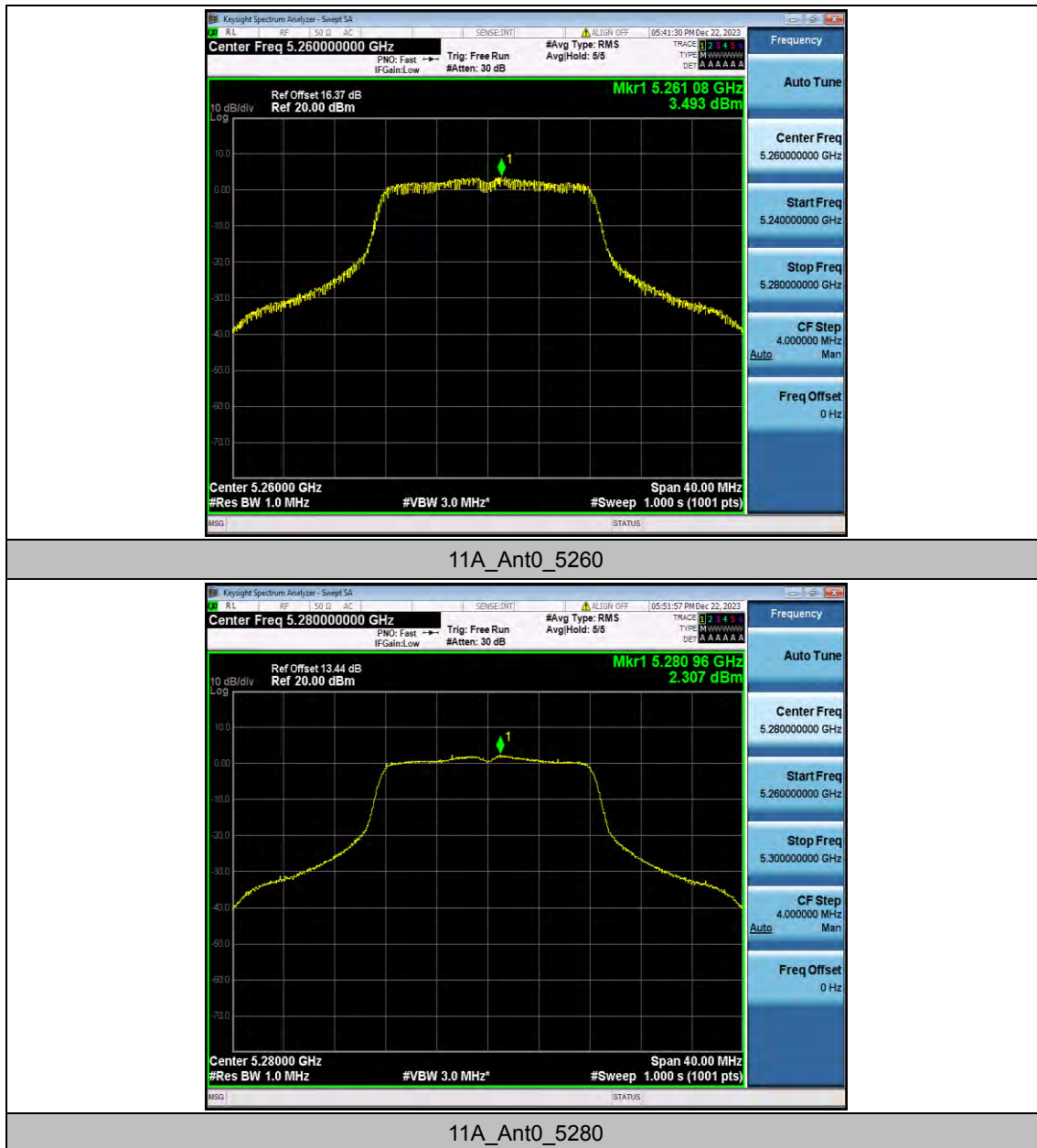
Appendix E.5: Maximum power spectral density

Test Result

Test Mode	Antenna	Frequency[MHz]	Result [dBm/MHz]	Limit[dBm/MHz]	Verdict
11A	Ant0	5260	3.49	≤11.00	PASS
		5280	2.31	≤11.00	PASS
		5320	2.23	≤11.00	PASS
11N20SISO	Ant0	5260	4.03	≤11.00	PASS
		5280	4	≤11.00	PASS
		5320	4.66	≤11.00	PASS
11N40SISO	Ant0	5270	-1.76	≤11.00	PASS
		5310	-1.01	≤11.00	PASS
11AC20SISO	Ant0	5260	1.56	≤11.00	PASS
		5280	1.06	≤11.00	PASS
		5320	1.39	≤11.00	PASS
11AC40SISO	Ant0	5270	-2.05	≤11.00	PASS
		5310	-1.31	≤11.00	PASS
11AC80SISO	Ant0	5290	-5.57	≤11.00	PASS
11AX20SISO	Ant0	5260	1.24	≤11.00	PASS
		5280	0.75	≤11.00	PASS
		5320	0.9	≤11.00	PASS
11AX40SISO	Ant0	5270	-3.41	≤11.00	PASS
		5310	-2.37	≤11.00	PASS
11AX80SISO	Ant0	5290	-6.6	≤11.00	PASS

Note: 1.The Result and Limit Unit is dBm/500 kHz in the band 5.725–5.85 GHz.
 2.The Duty Cycle Factor and RBW Factor is compensated in the graph.

Test Graphs





11A_Ant0_5320



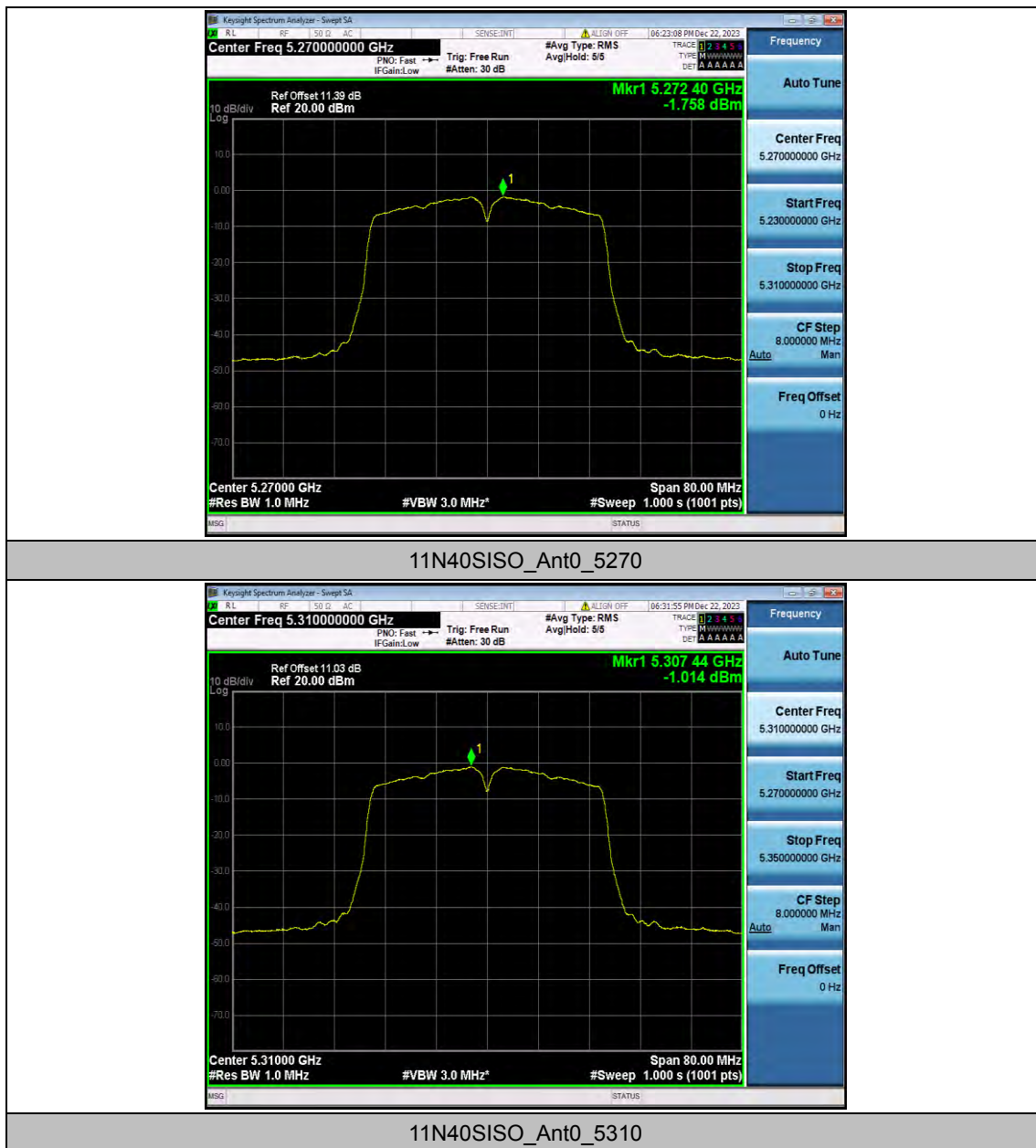
11N20SISO_Ant0_5260

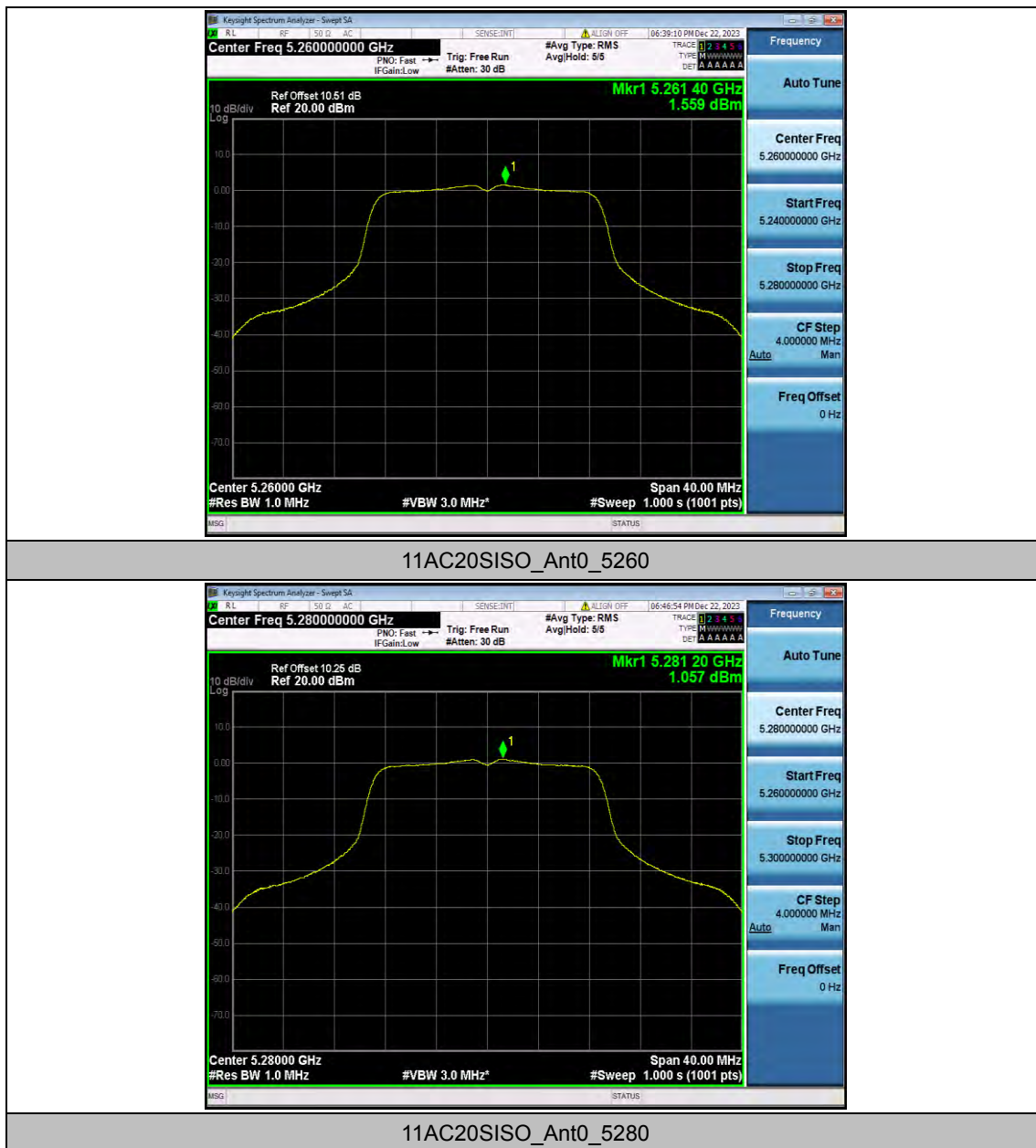


11N20SISO_Ant0_5280



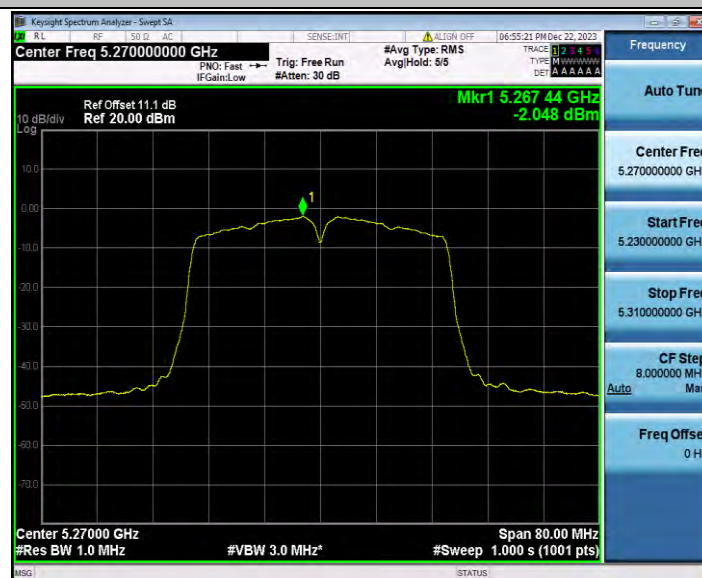
11N20SISO_Ant0_5320



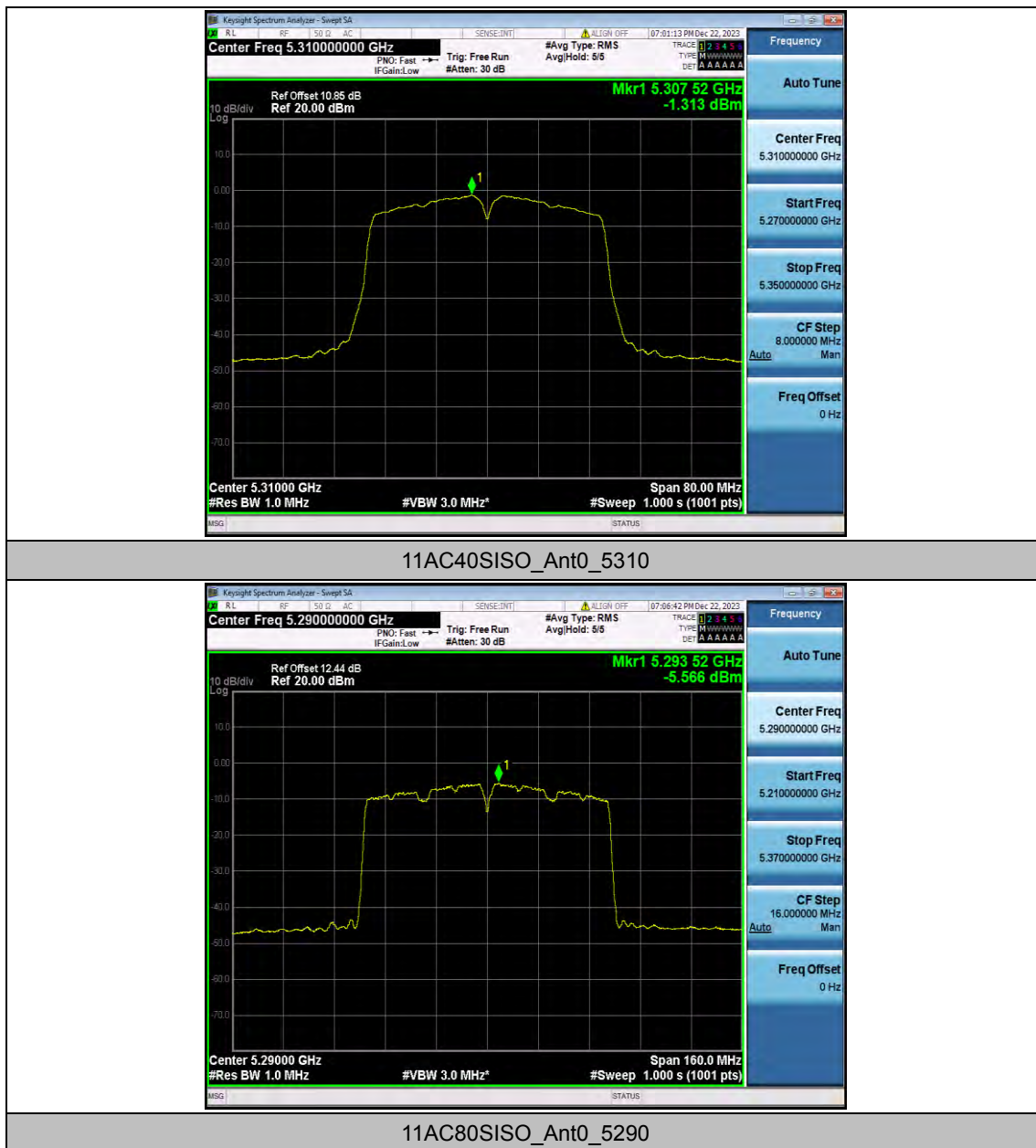


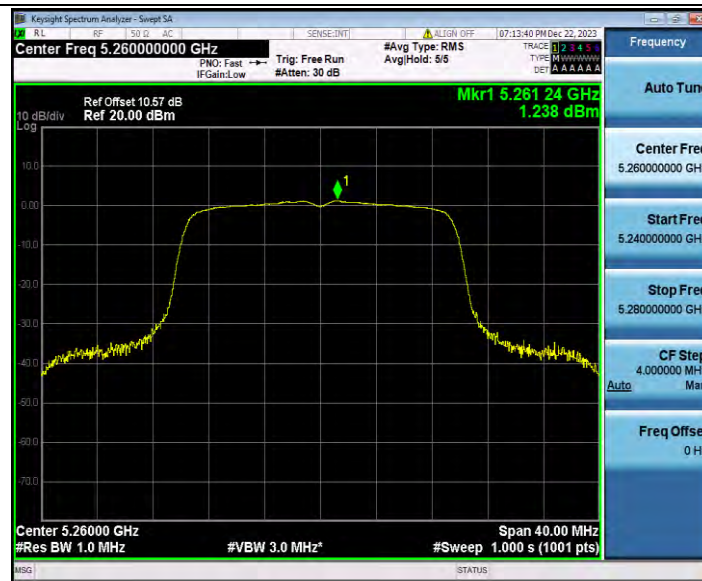


11AC20SISO_Ant0_5320



11AC40SISO_Ant0_5270

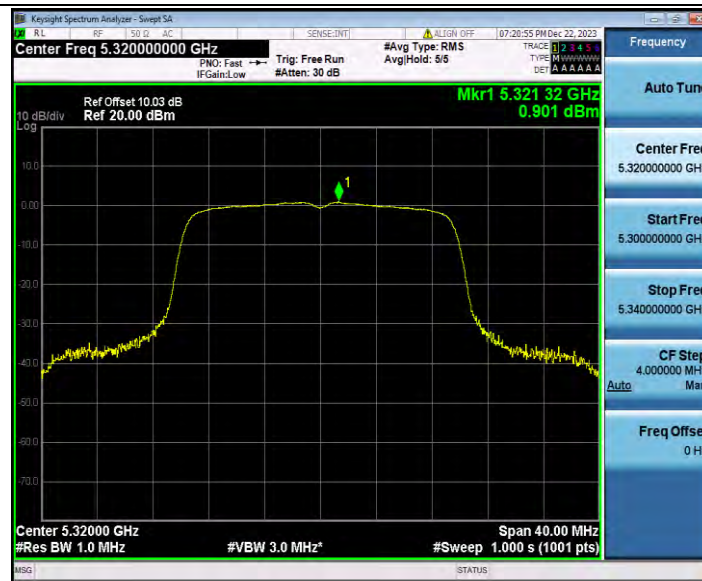




11AX20SISO_Ant0_5260



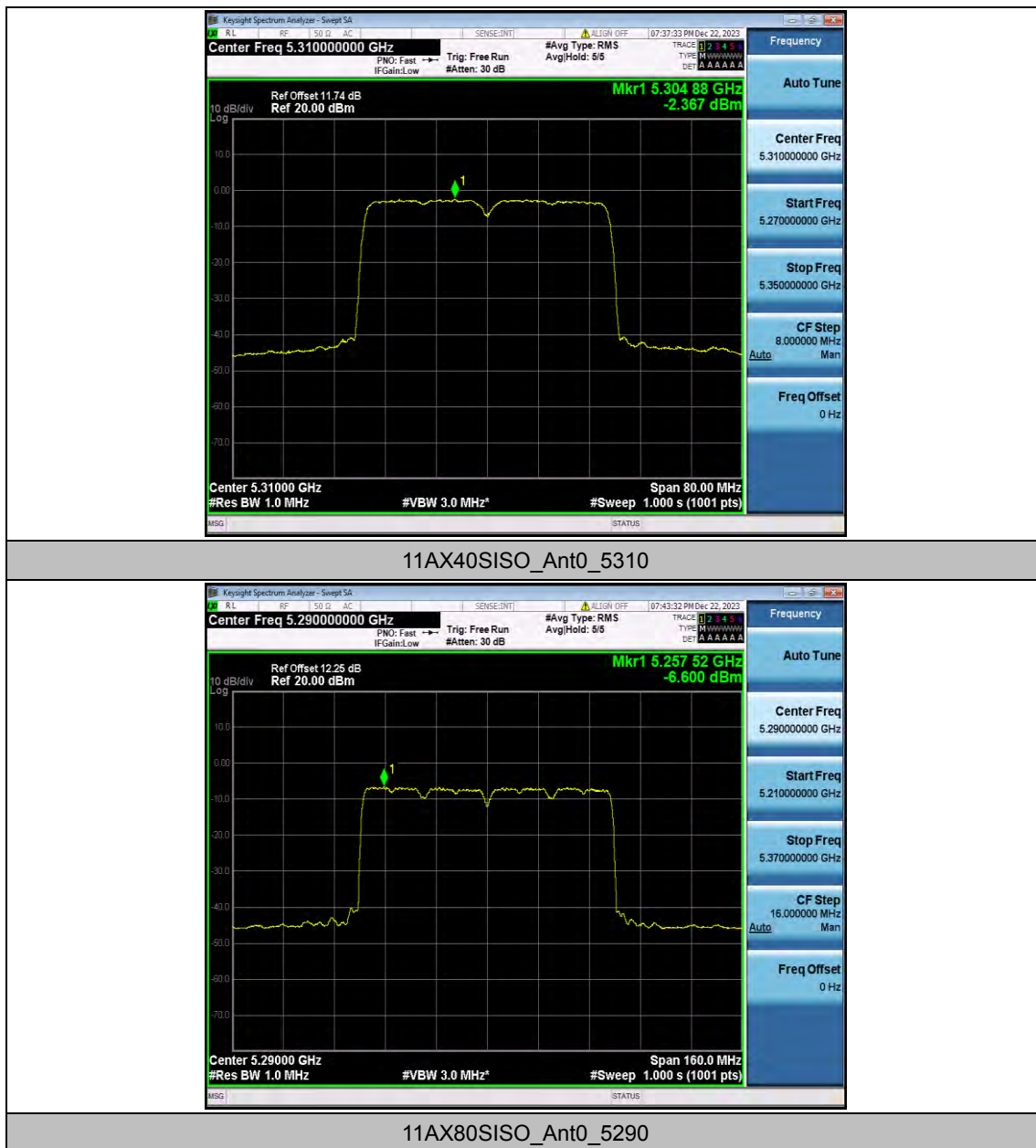
11AX20SISO_Ant0_5280



11AX20SISO_Ant0_5320



11AX40SISO_Ant0_5270

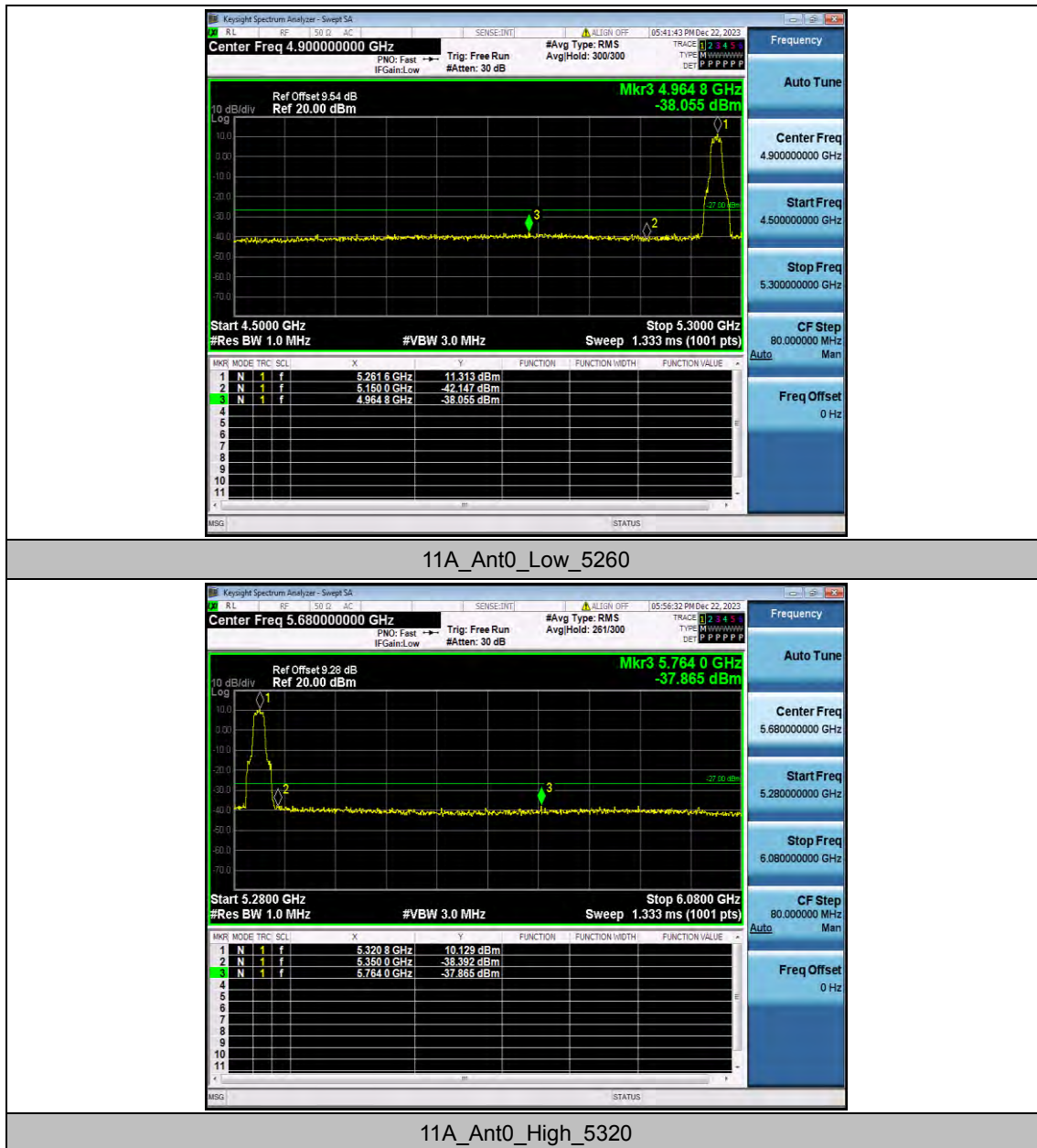


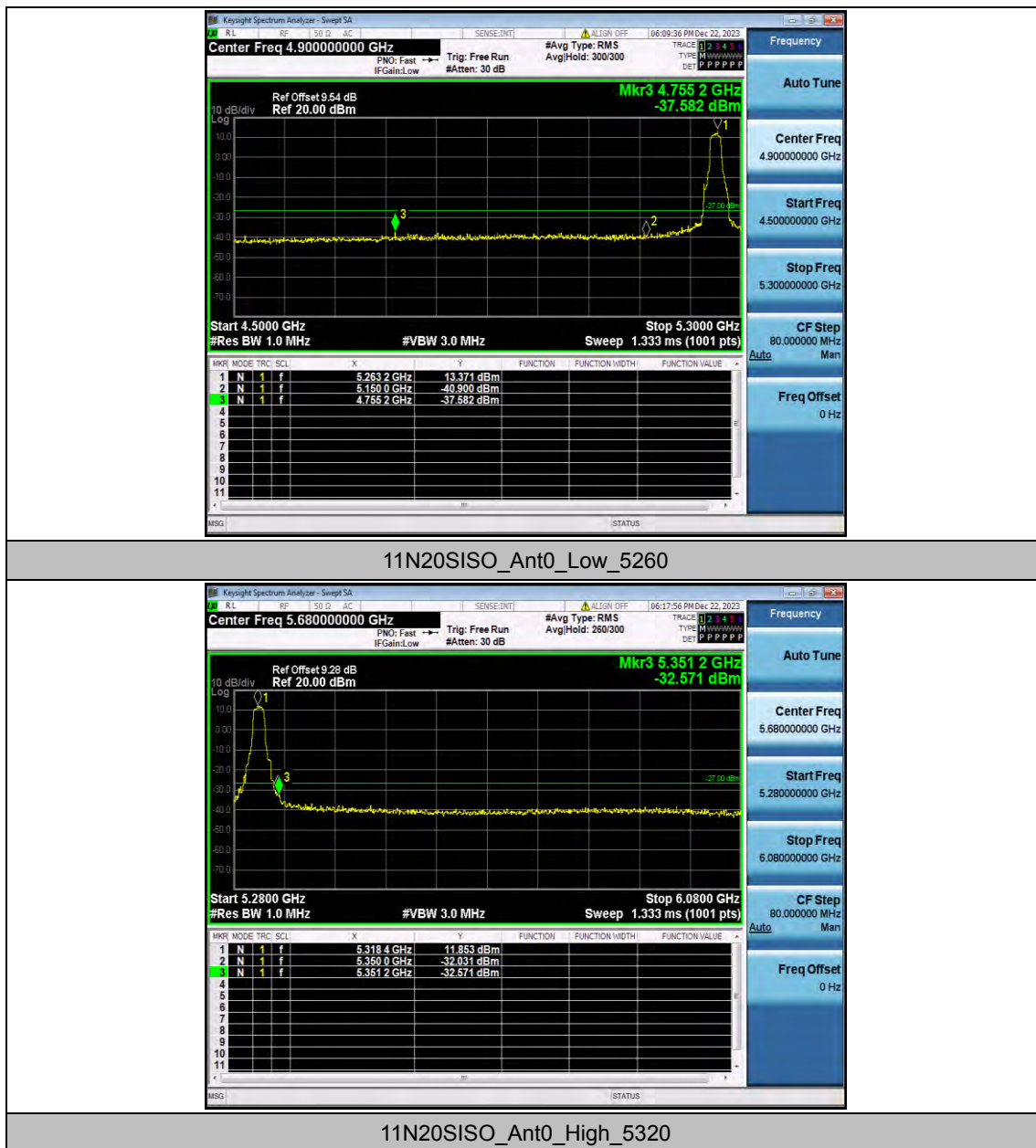
Appendix E.6: Band edge measurements

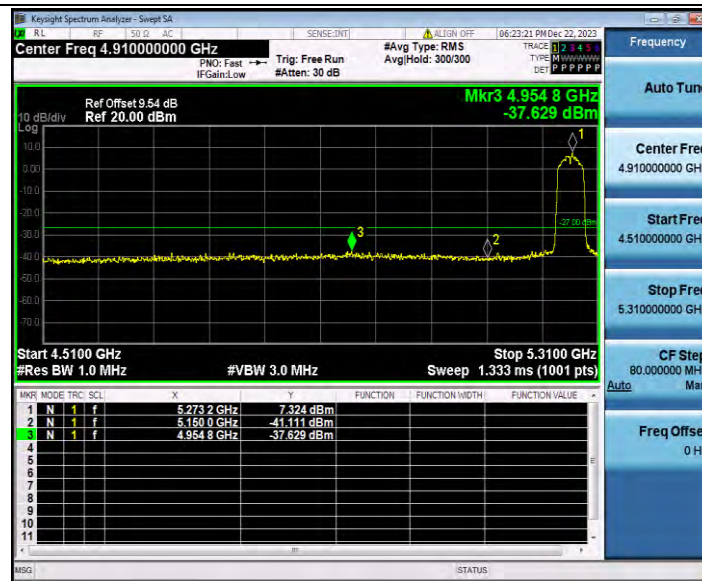
Test Result B2

Test Mode	Antenna	ChName	Frequency[MHz]	Result[dBm]	Limit[dBm]	Verdict
11A	Ant0	Low	5260	-38.06	≤ -27	PASS
		High	5320	-37.87	≤ -27	PASS
11N20SISO	Ant0	Low	5260	-37.58	≤ -27	PASS
		High	5320	-32.57	≤ -27	PASS
11N40SISO	Ant0	Low	5270	-37.63	≤ -27	PASS
		High	5310	-38.59	≤ -27	PASS
11AC20SISO	Ant0	Low	5260	-37.43	≤ -27	PASS
		High	5320	-36.94	≤ -27	PASS
11AC40SISO	Ant0	Low	5270	-37.7	≤ -27	PASS
		High	5310	-38.38	≤ -27	PASS
11AC80SISO	Ant0	Low	5290	-37.75	≤ -27	PASS
		High	5290	-37.57	≤ -27	PASS
11AX20SISO	Ant0	Low	5260	-37.59	≤ -27	PASS
		High	5320	-37.88	≤ -27	PASS
11AX40SISO	Ant0	Low	5270	-37.58	≤ -27	PASS
		High	5310	-36.28	≤ -27	PASS
11AX80SISO	Ant0	Low	5290	-38.15	≤ -27	PASS
		High	5290	-35.39	≤ -27	PASS

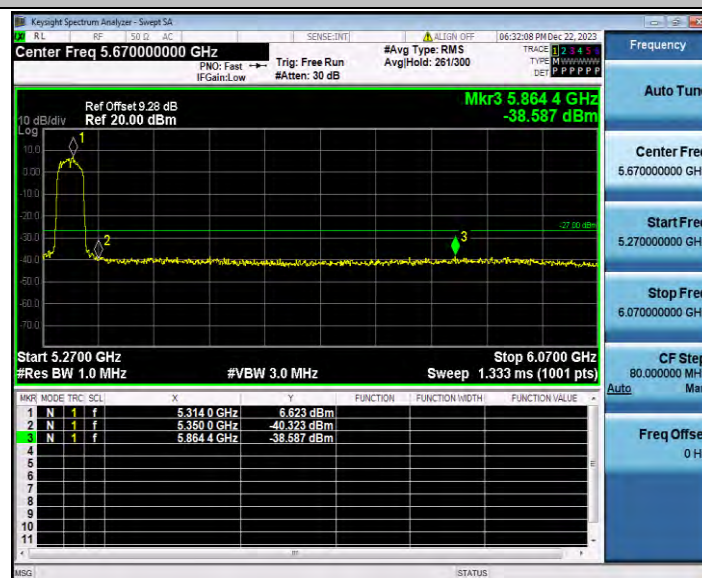
Test Graphs B2







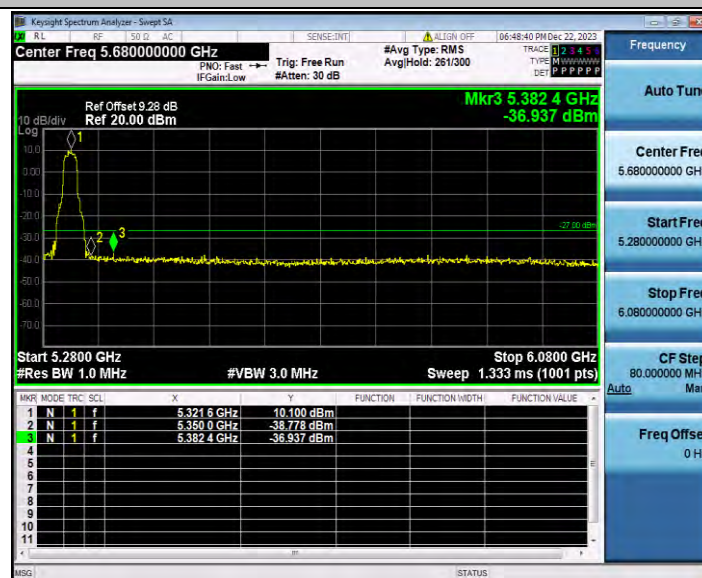
11N40SISO_Ant0_Low_5270



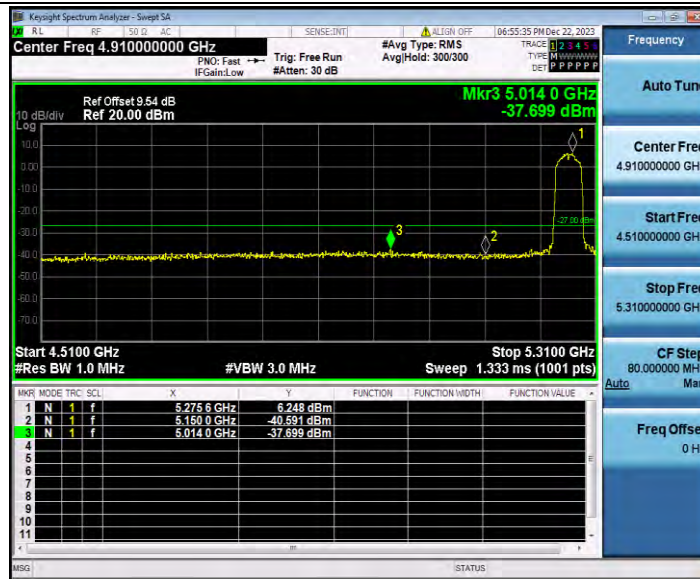
11N40SISO_Ant0_High_5310



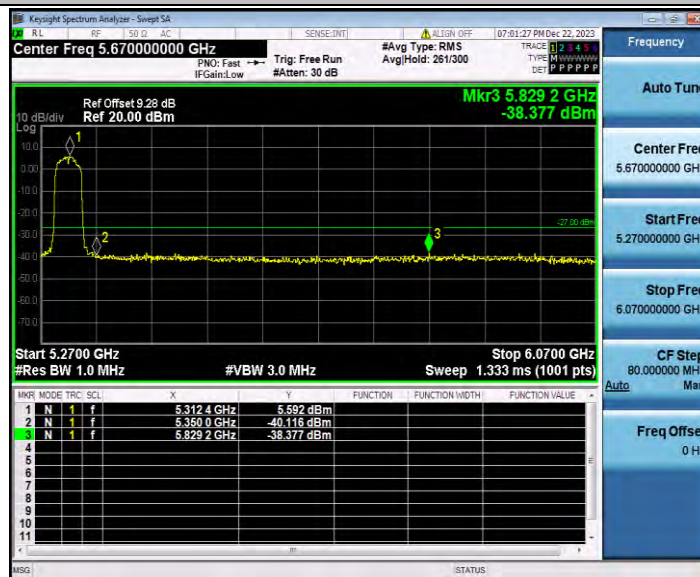
11AC20SISO_Ant0_Low_5260



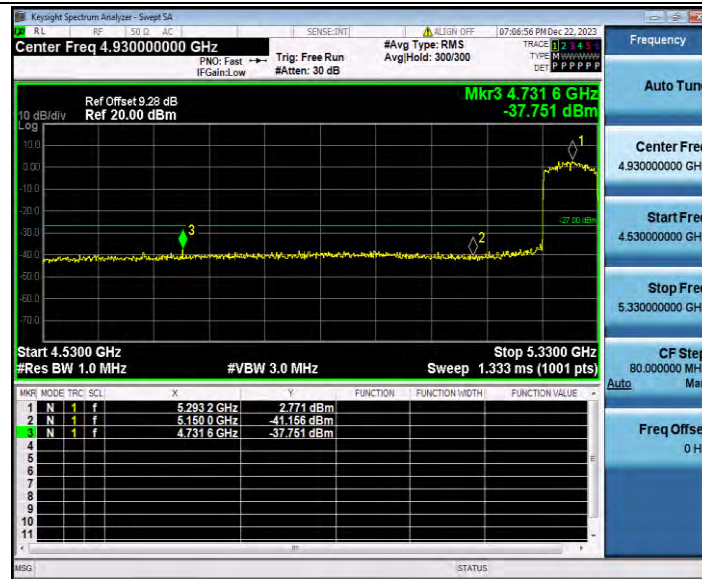
11AC20SISO_Ant0_High_5320



11AC40SISO_Ant0_Low_5270



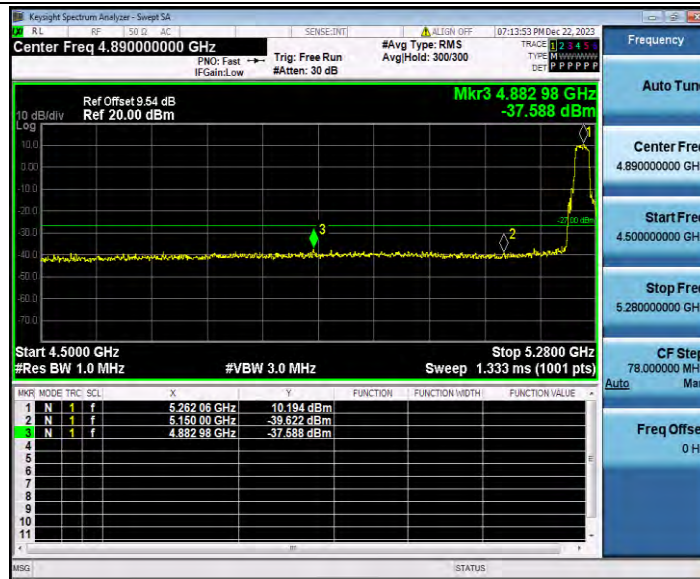
11AC40SISO_Ant0_High_5310



11AC80SISO_Ant0_Low_5290



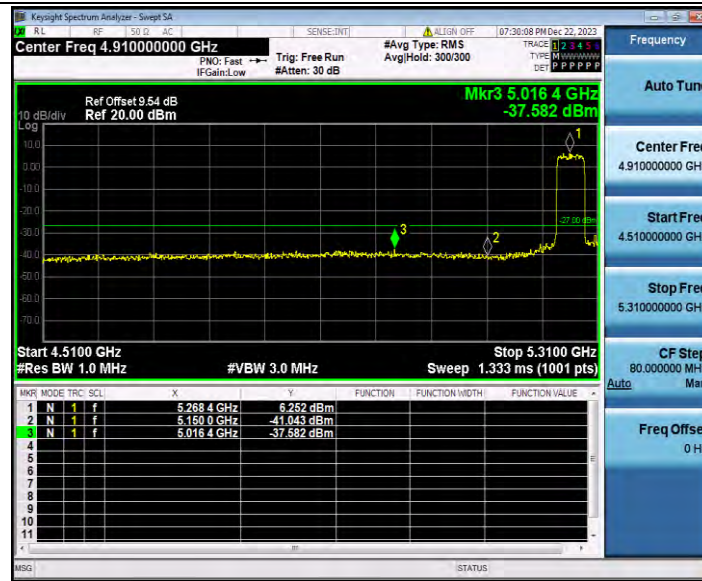
11AC80SISO_Ant0_High_5290



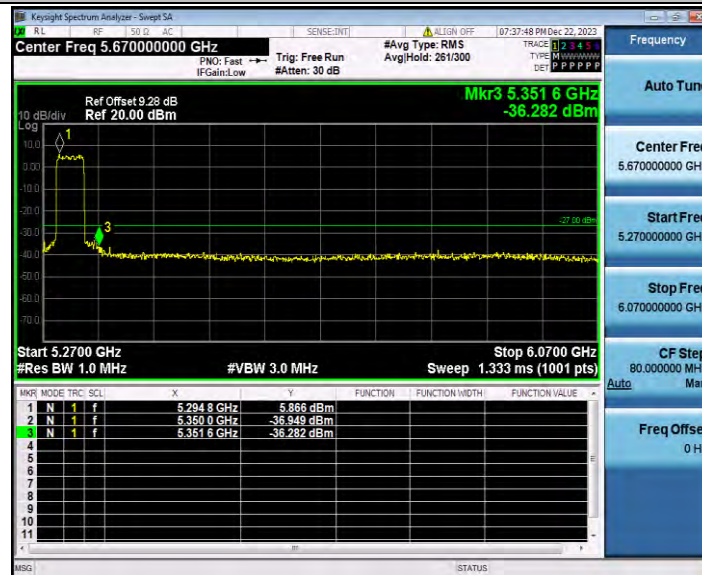
11AX20SISO_Ant0_Low_5260



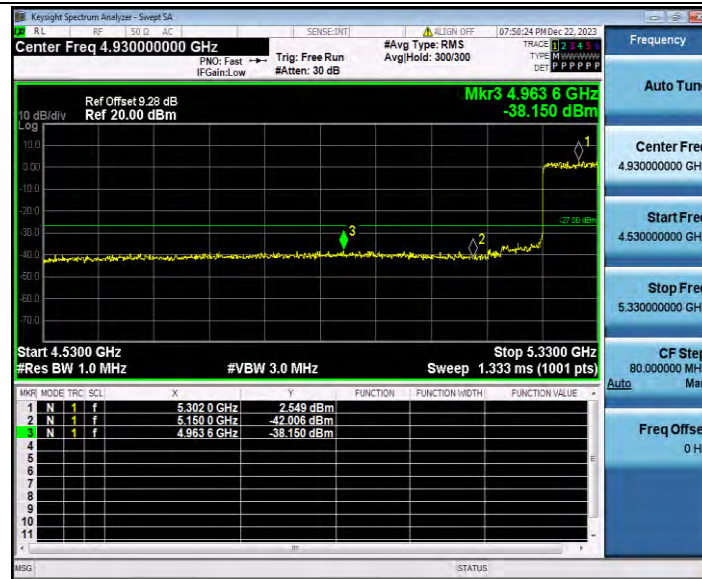
11AX20SISO_Ant0_High_5320



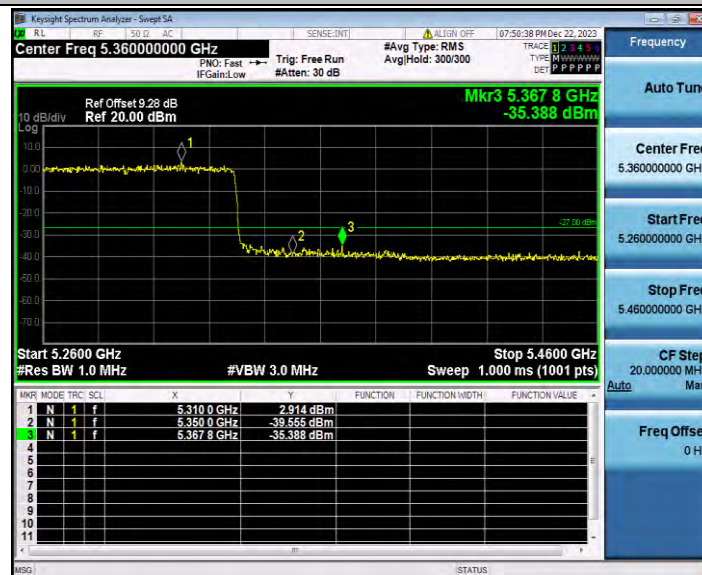
11AX40SISO_Ant0_Low_5270



11AX40SISO_Ant0_High_5310



11AX80SISO_Ant0_Low_5290



11AX80SISO_Ant0_High_5290

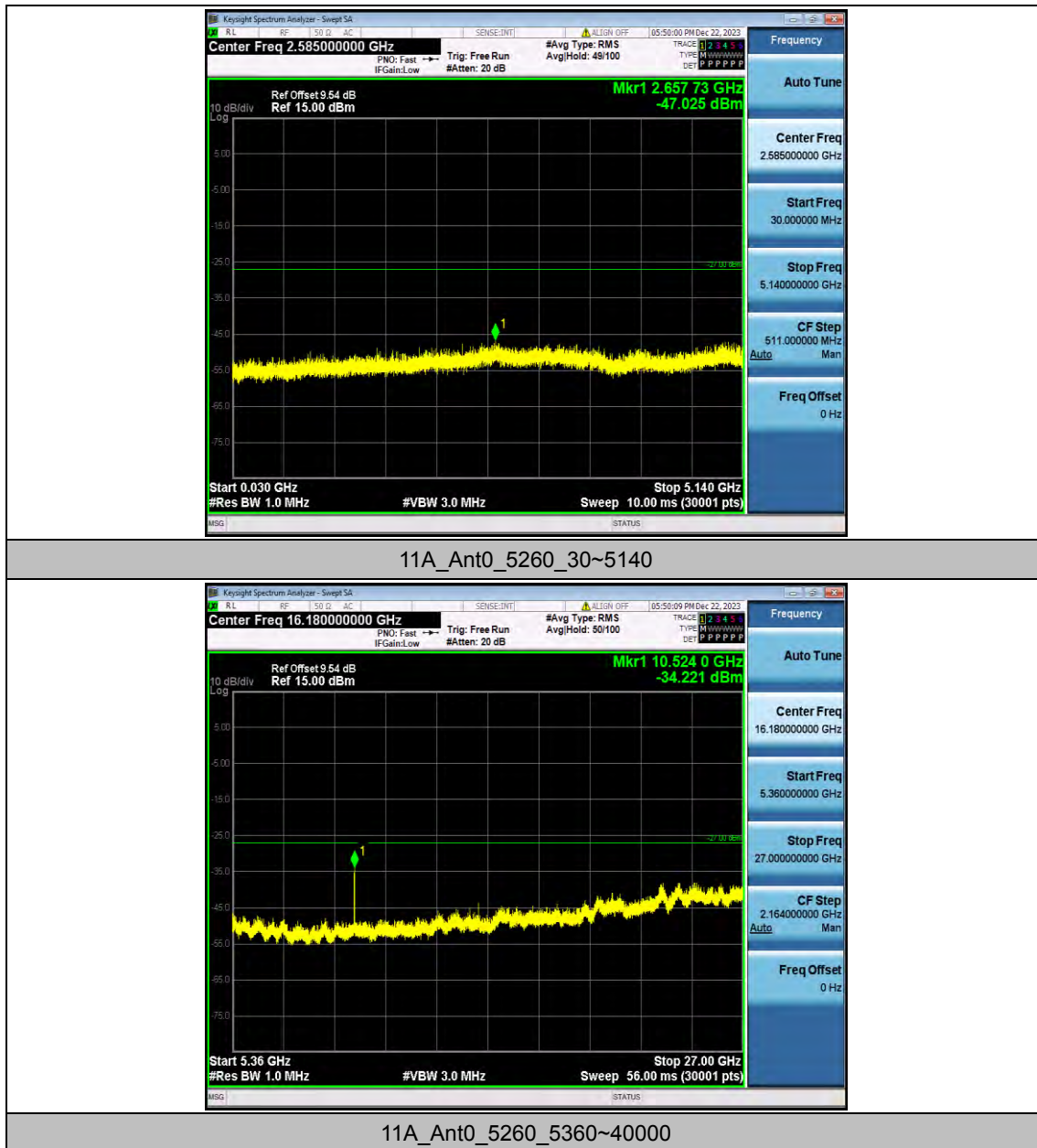
Appendix E.7: Conducted Spurious Emission

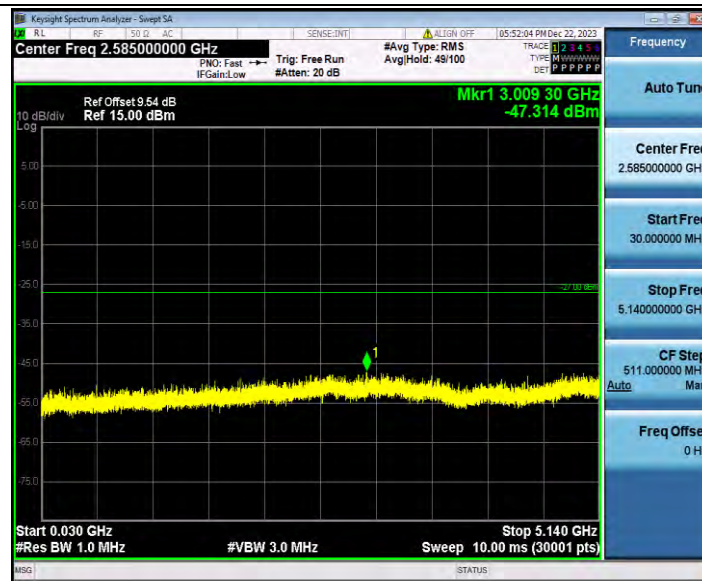
Test Result

Test Mode	Antenna	Frequency[MHz]	FreqRange [MHz]	Max. Fre [MHz]	Max. Level [dBm]	Limit [dBm]	Verdict
11A	Ant0	5260	30~5140	2657.73	-47.03	≤-27	PASS
			5360~40000	10524.03	-34.22	≤-27	PASS
		5280	30~5140	3009.3	-47.31	≤-27	PASS
			5360~40000	10561.53	-35.64	≤-27	PASS
		5320	30~5140	5128.76	-47.2	≤-27	PASS
			5360~40000	10637.27	-34.61	≤-27	PASS
11N20SISO	Ant0	5260	30~5140	5117.86	-45.4	≤-27	PASS
			5360~40000	10524.03	-31.6	≤-27	PASS
		5280	30~5140	5024	-46.53	≤-27	PASS
			5360~40000	10563.7	-31.42	≤-27	PASS
		5320	30~5140	4888.76	-47.18	≤-27	PASS
			5360~40000	15962.88	-30.42	≤-27	PASS
11N40SISO	Ant0	5270	30~5140	3029.74	-47.17	≤-27	PASS
			5360~40000	10538.45	-37.98	≤-27	PASS
		5310	30~5140	2667.61	-46.67	≤-27	PASS
			5360~40000	10616.36	-38	≤-27	PASS
11AC20SISO	Ant0	5260	30~5140	4978.86	-47.19	≤-27	PASS
			5360~40000	10519.7	-34.31	≤-27	PASS
		5280	30~5140	5117.01	-46.64	≤-27	PASS
			5360~40000	10560.09	-34.17	≤-27	PASS
		5320	30~5140	2670.34	-47.48	≤-27	PASS
			5360~40000	10640.16	-34.77	≤-27	PASS
11AC40SISO	Ant0	5270	30~5140	4955.36	-46.01	≤-27	PASS
			5360~40000	10537.01	-37.49	≤-27	PASS
		5310	30~5140	3136.71	-46.73	≤-27	PASS
			5360~40000	10617.08	-37.76	≤-27	PASS
11AC80SISO	Ant0	5290	30~5140	2580.06	-47.17	≤-27	PASS
			5360~40000	24919.67	-37.87	≤-27	PASS
11AX20SISO	Ant0	5260	30~5140	2414.33	-47.02	≤-27	PASS
			5360~40000	10518.98	-32.56	≤-27	PASS
		5280	30~5140	5126.71	-46.82	≤-27	PASS
			5360~40000	10559.37	-34.82	≤-27	PASS
		5320	30~5140	5042.91	-46.51	≤-27	PASS
			5360~40000	10639.44	-34.03	≤-27	PASS
11AX40SISO	Ant0	5270	30~5140	2413.82	-44.02	≤-27	PASS
			5360~40000	24263.26	-37.74	≤-27	PASS

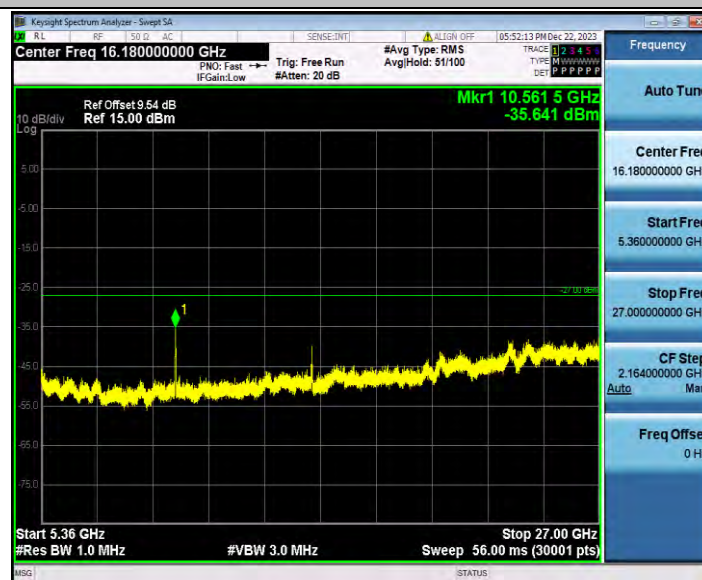
		5310	30~5140	2619.92	-46.26	≤-27	PASS
			5360~40000	15935.47	-37.22	≤-27	PASS
11AX80SISO	Ant0	5290	30~5140	2621.96	-46.86	≤-27	PASS
			5360~40000	24173.82	-38.57	≤-27	PASS

Test Graphs





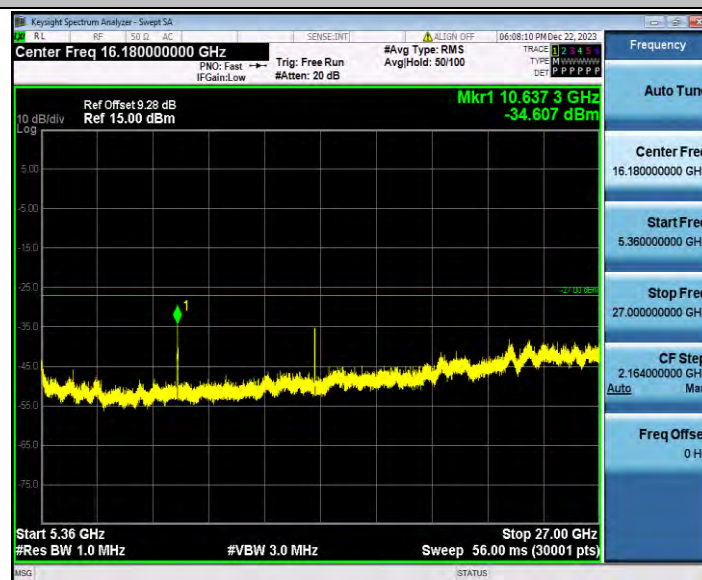
11A_Ant0_5280_30~5140



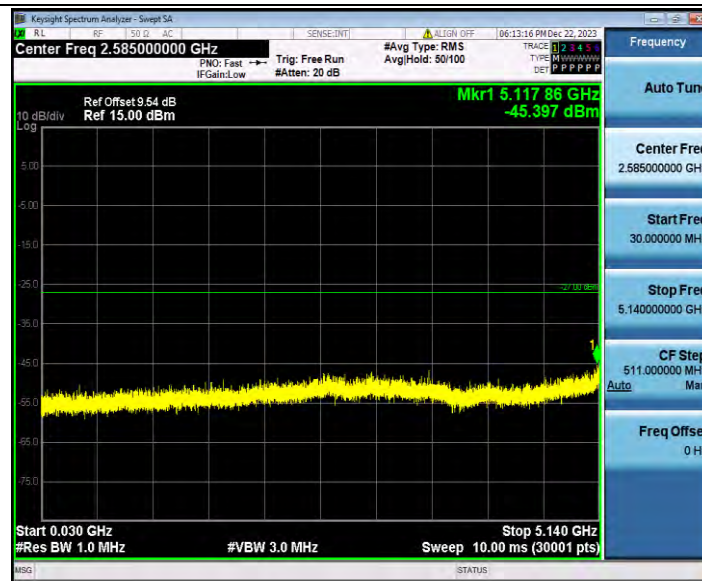
11A_Ant0_5280_5360~40000



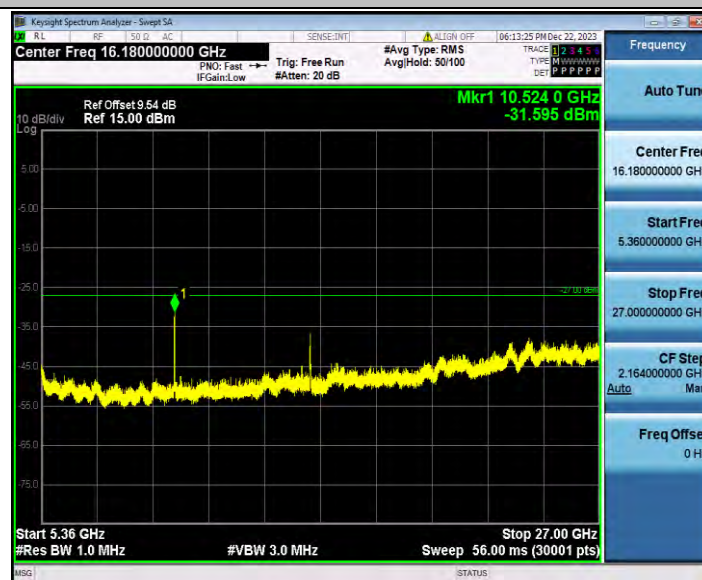
11A_Ant0_5320_30~5140



11A_Ant0_5320_5360~40000



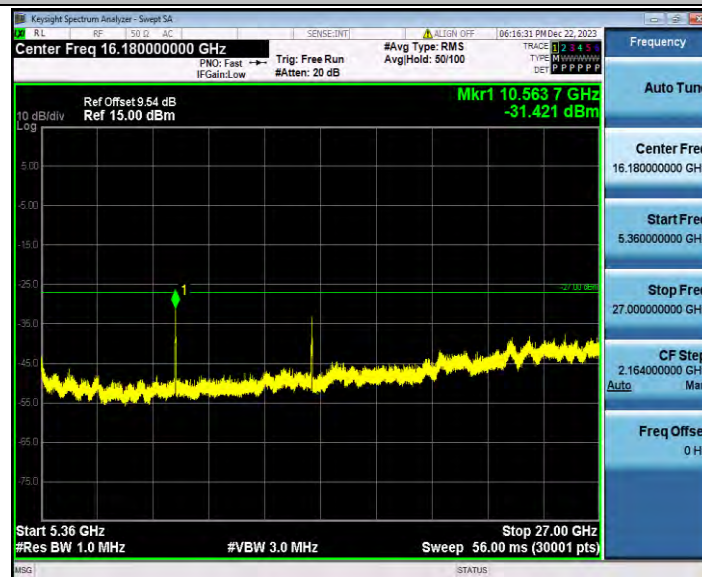
11N20SISO_Ant0_5260_30~5140



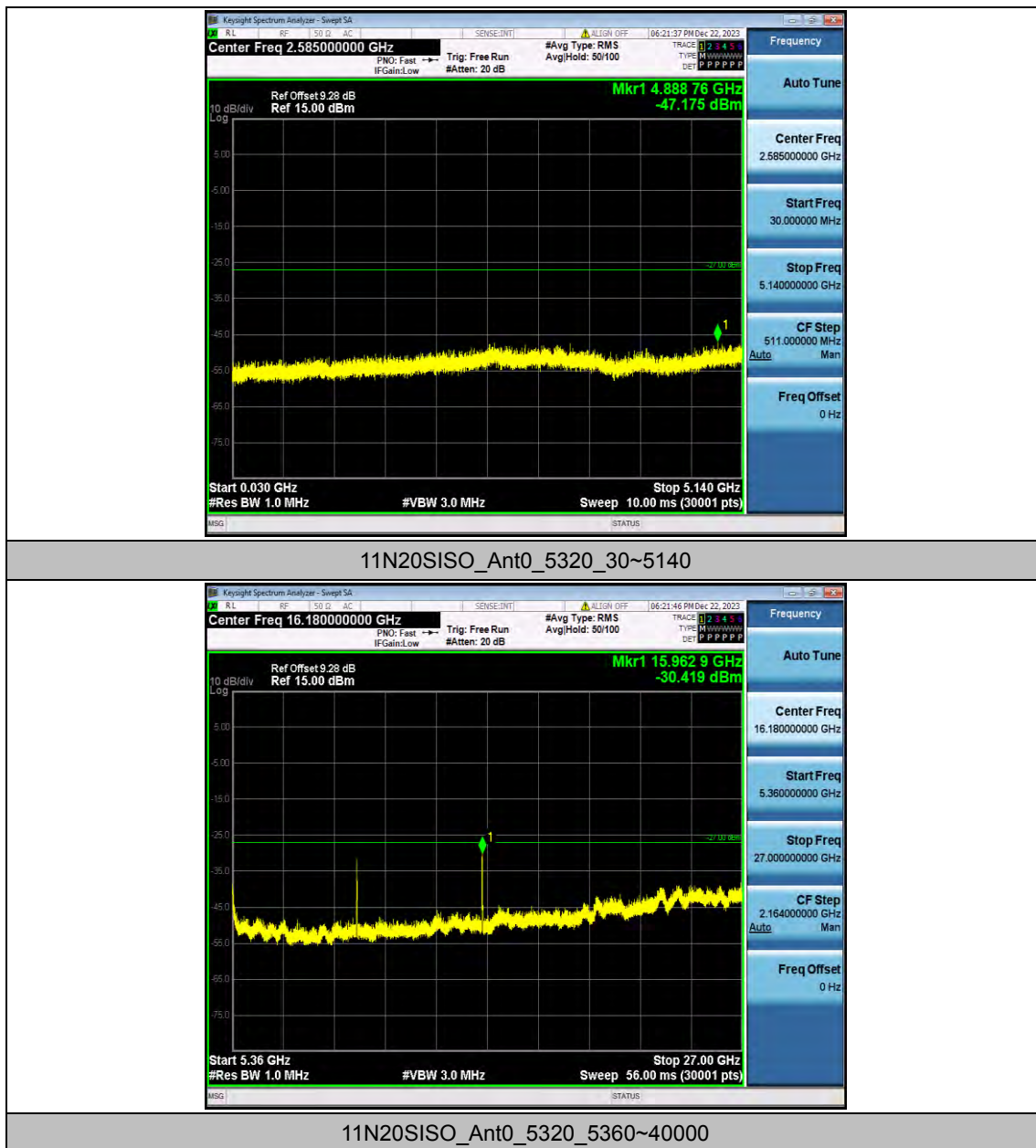
11N20SISO_Ant0_5260_5360~40000



11N20SISO_Ant0_5280_30~5140

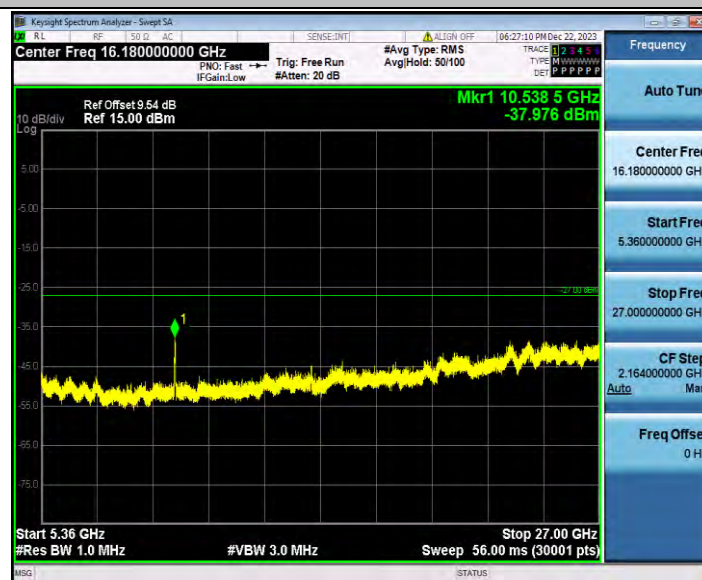


11N20SISO_Ant0_5280_5360~40000

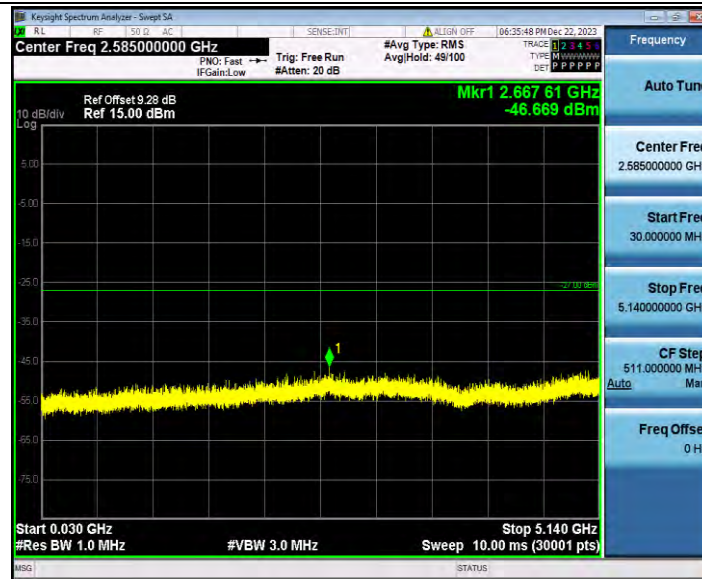




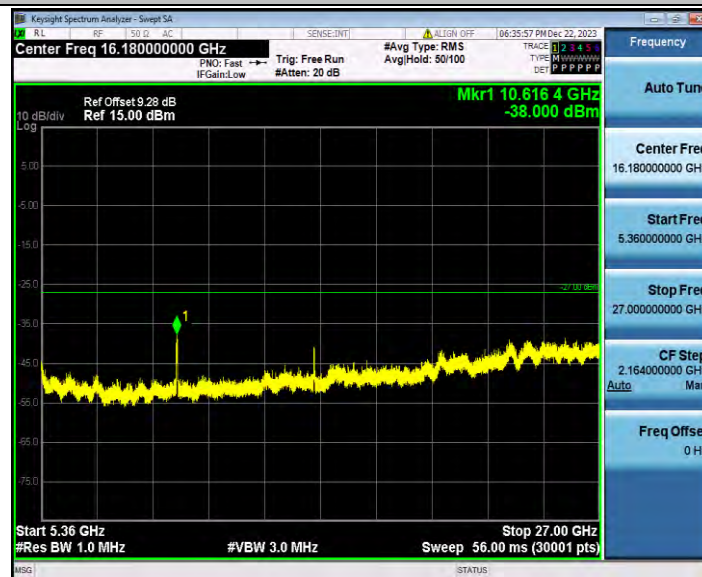
11N40SISO_Ant0_5270_30~5140



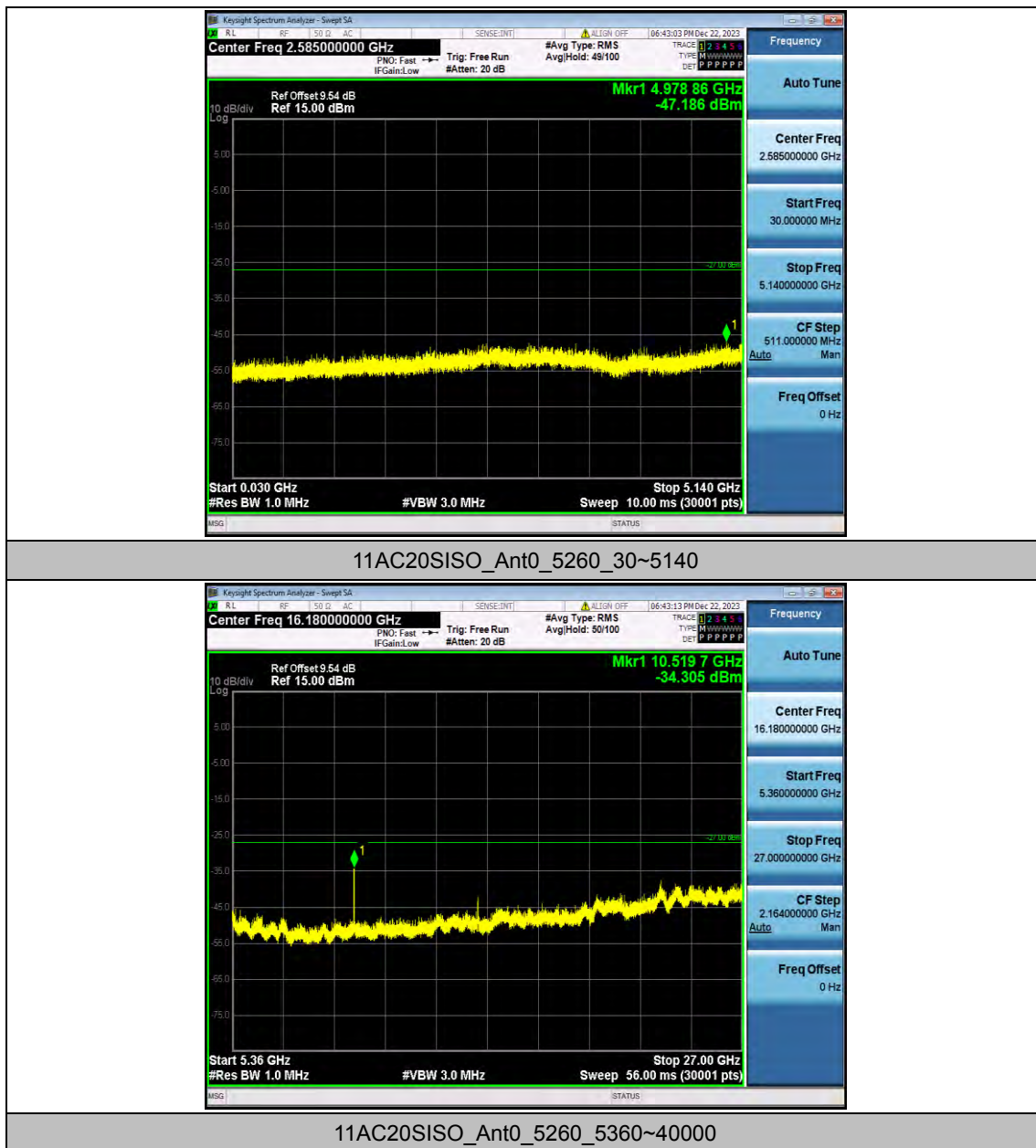
11N40SISO_Ant0_5270_5360~40000

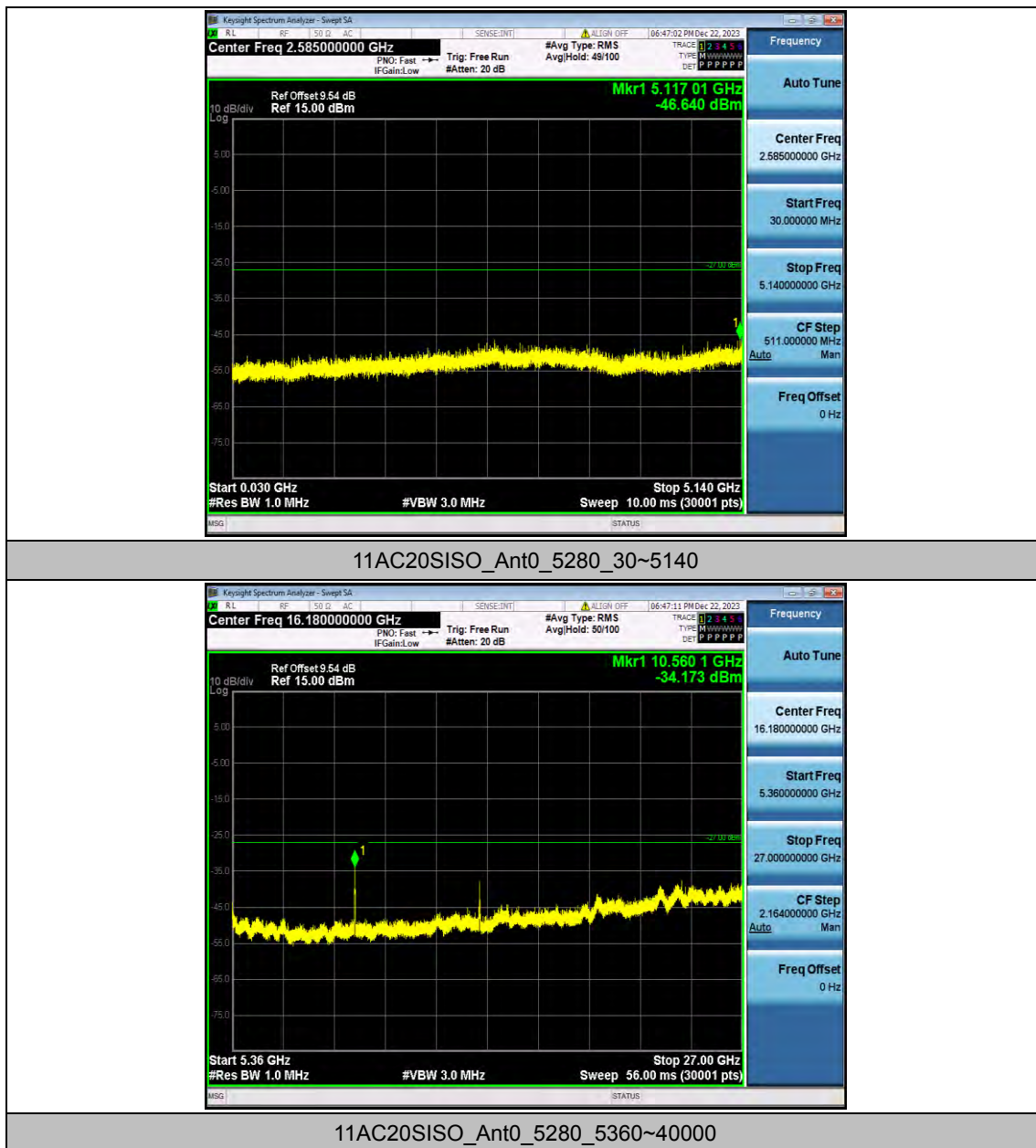


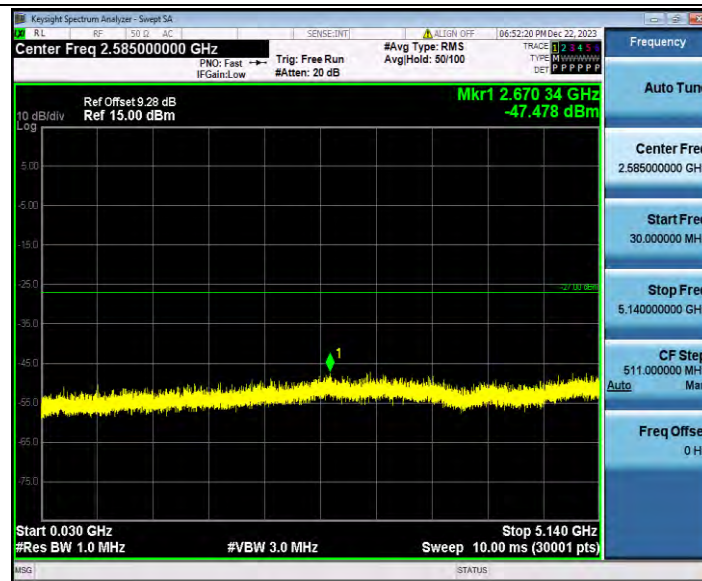
11N40SISO_Ant0_5310_30~5140



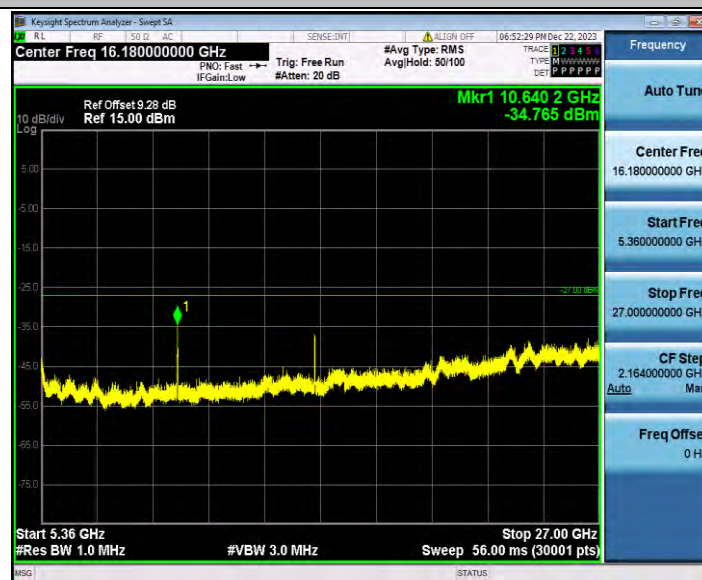
11N40SISO_Ant0_5310_5360~40000



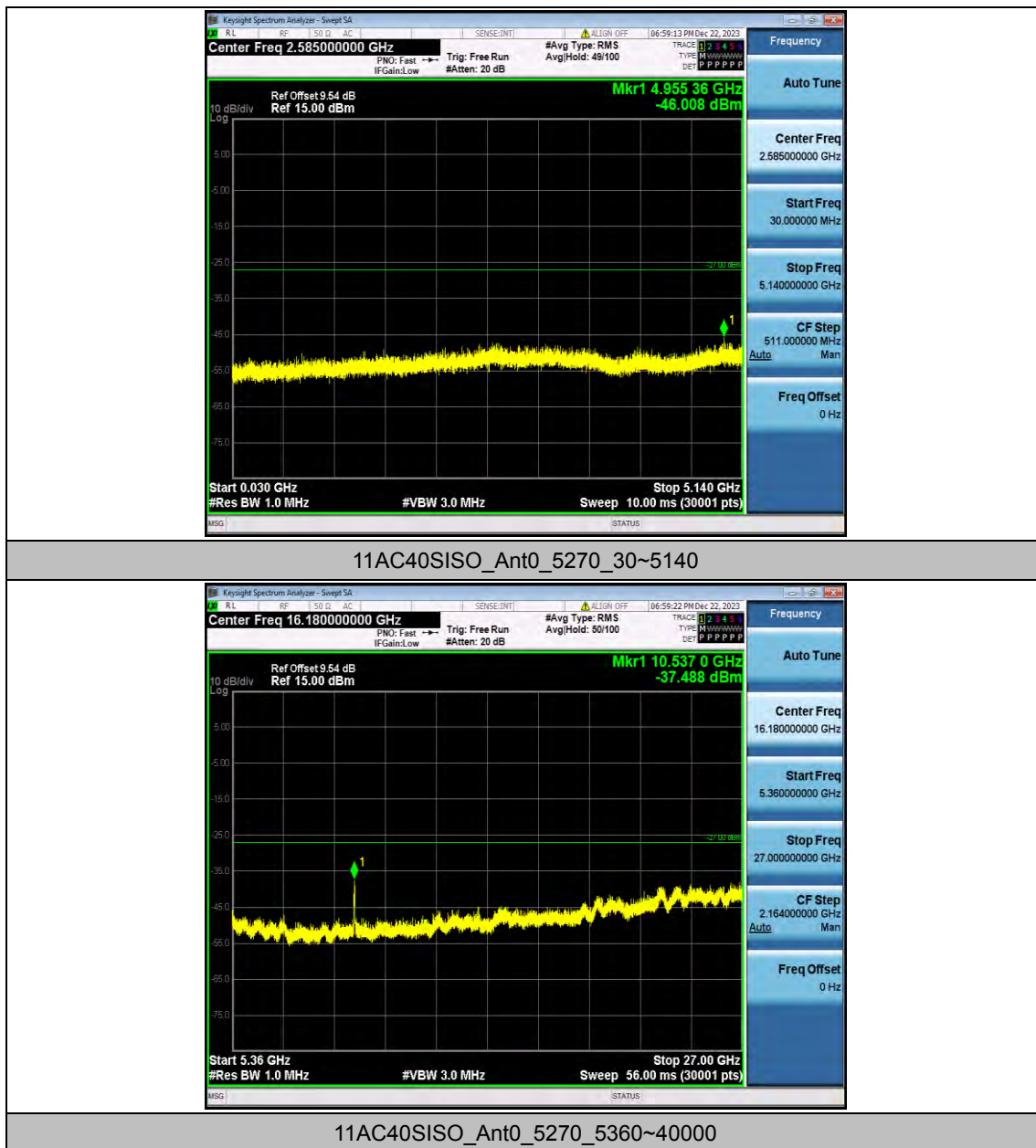




11AC20SISO_Ant0_5320_30~5140

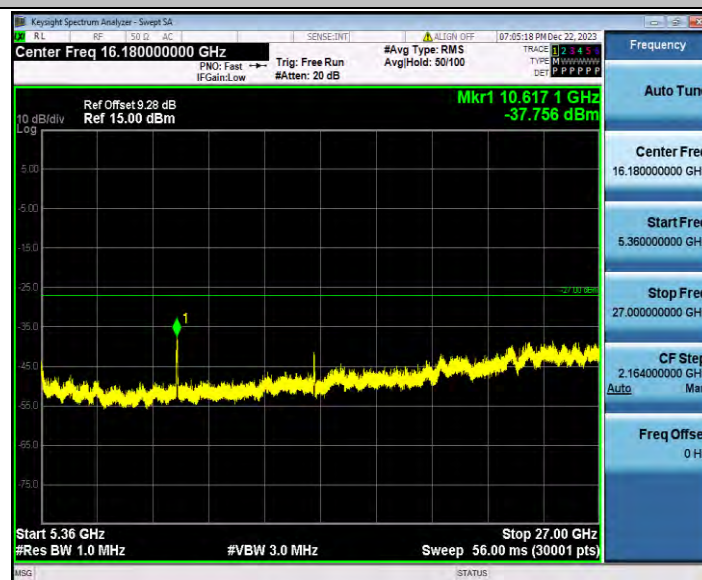


11AC20SISO_Ant0_5320_5360~40000

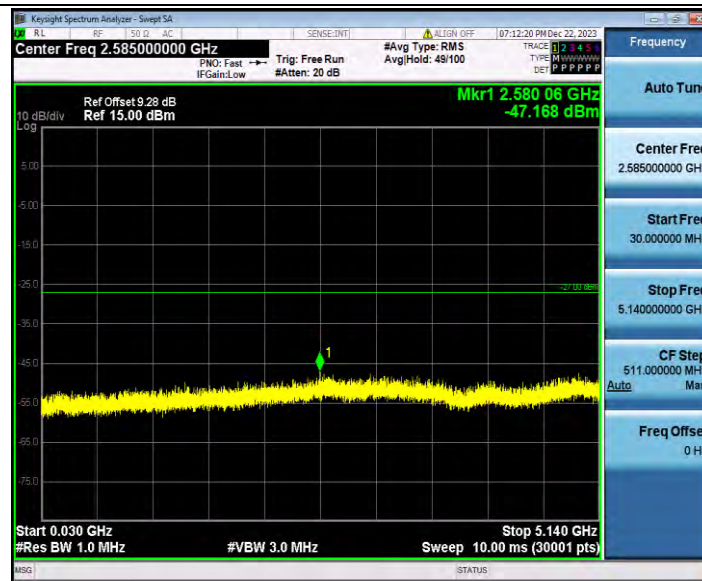




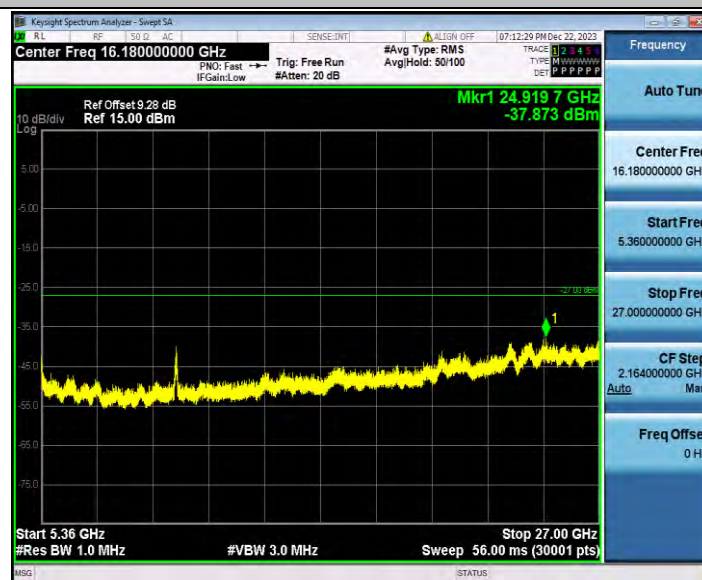
11AC40SISO_Ant0_5310_30~5140



11AC40SISO_Ant0_5310_5360~40000



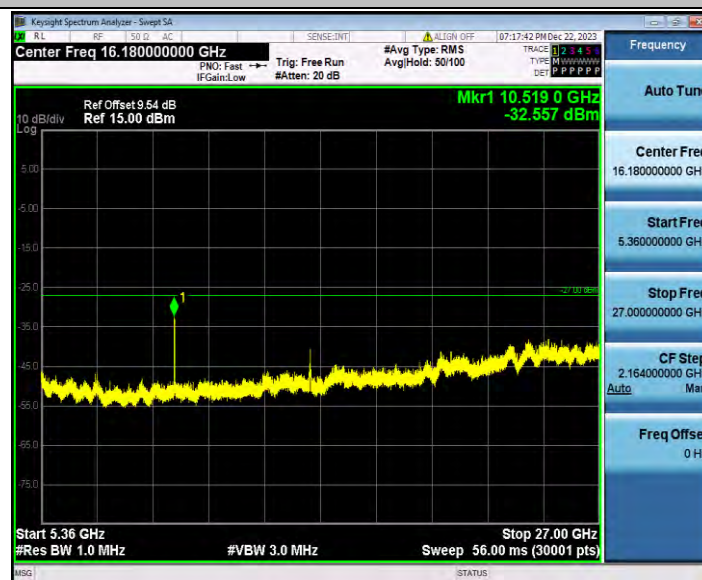
11AC80SISO_Ant0_5290_30~5140



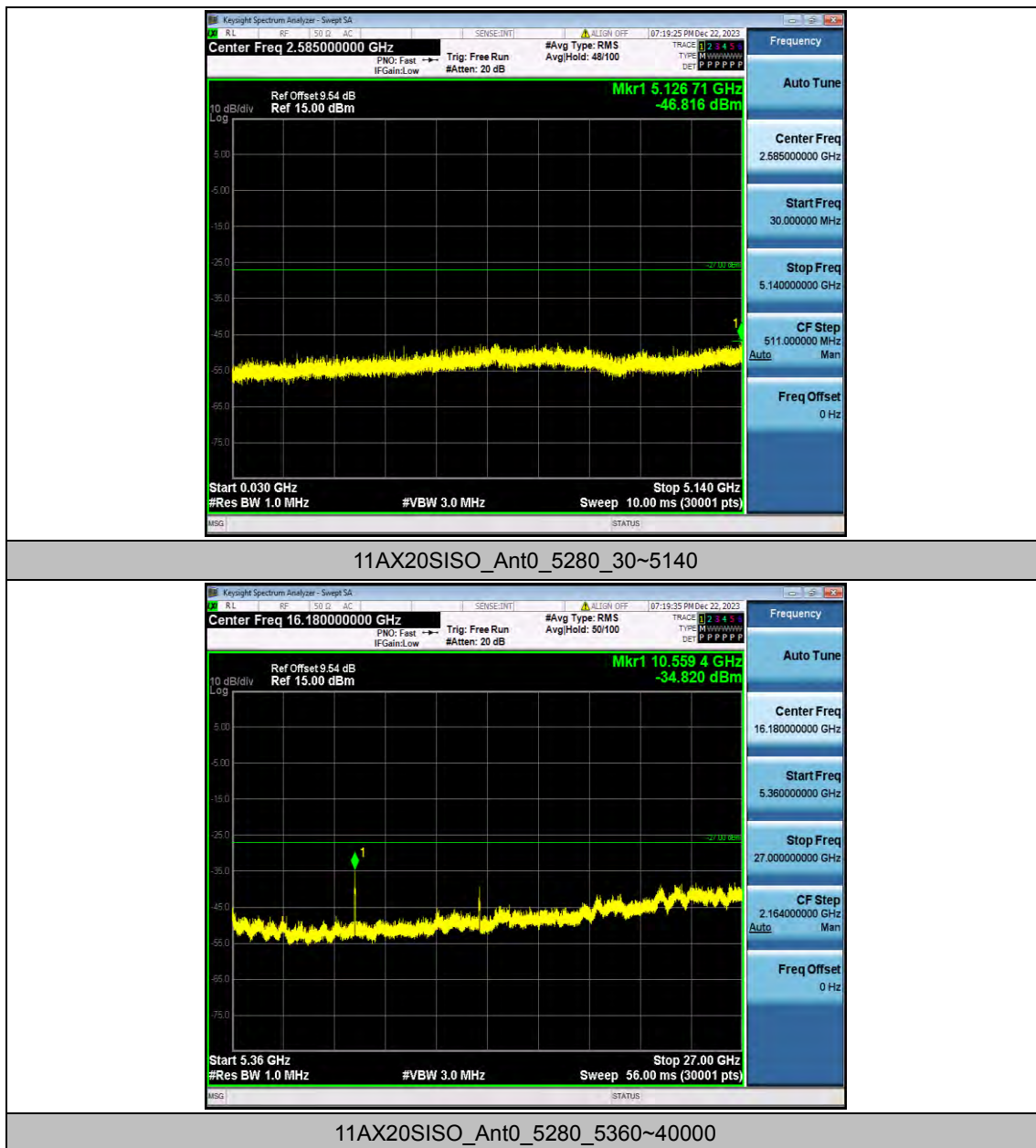
11AC80SISO_Ant0_5290_5360~40000

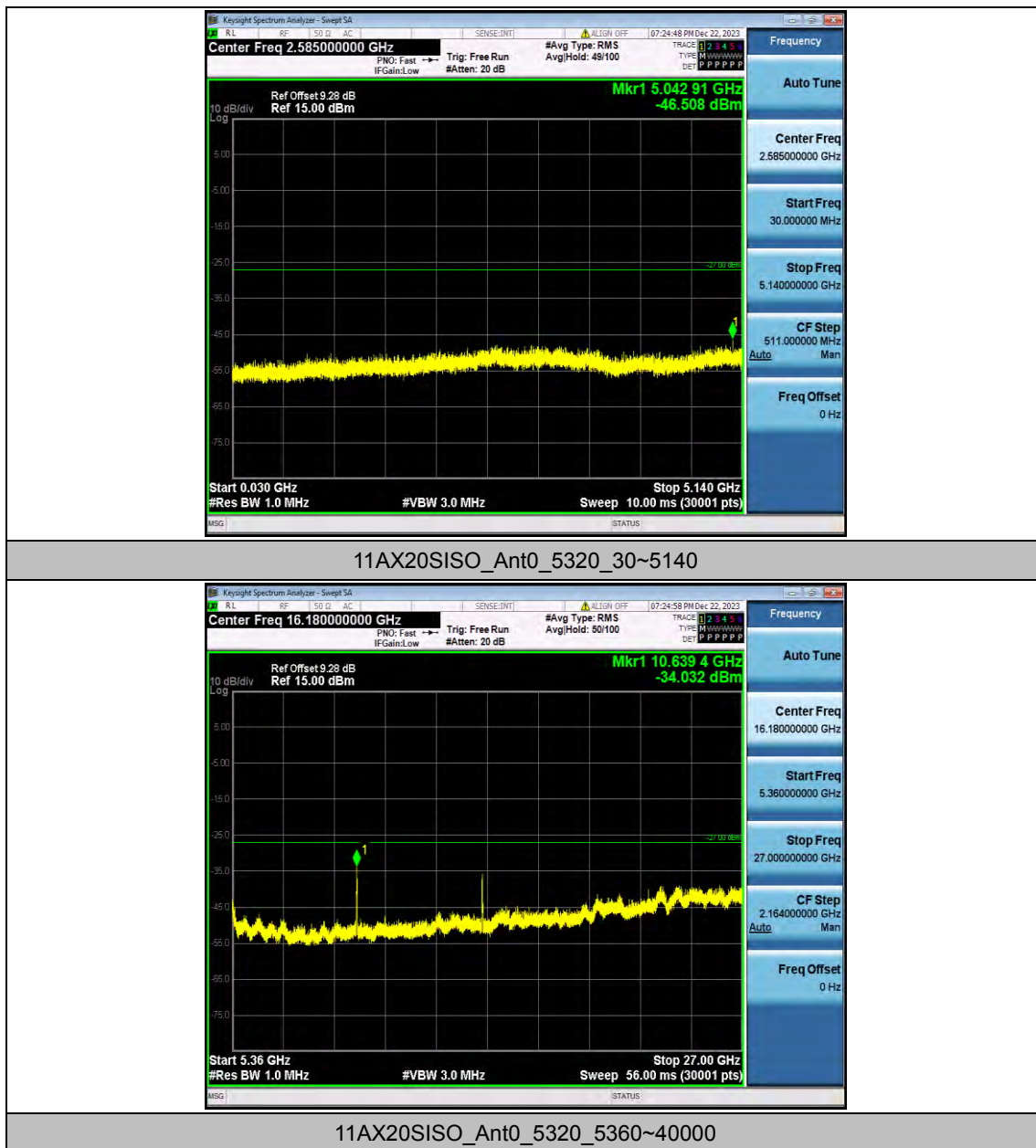


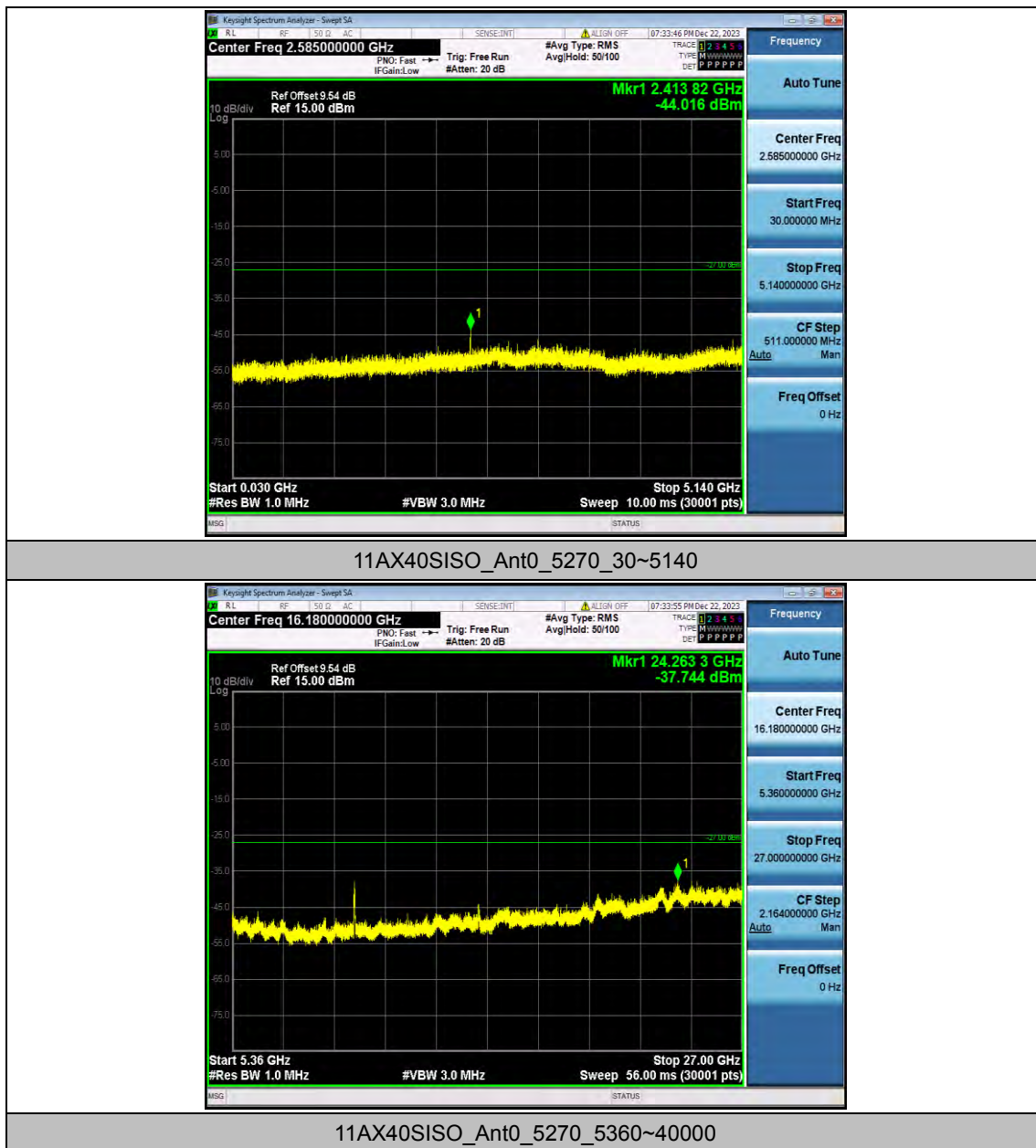
11AX20SISO_Ant0_5260_30~5140

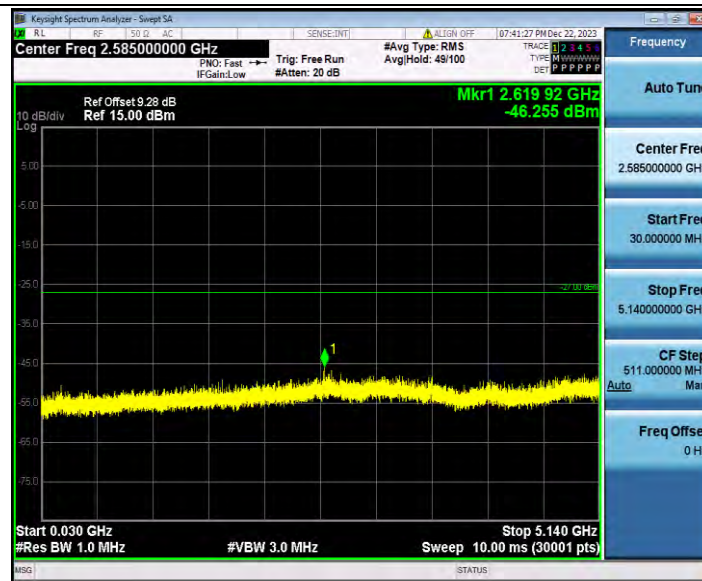


11AX20SISO_Ant0_5260_5360~40000

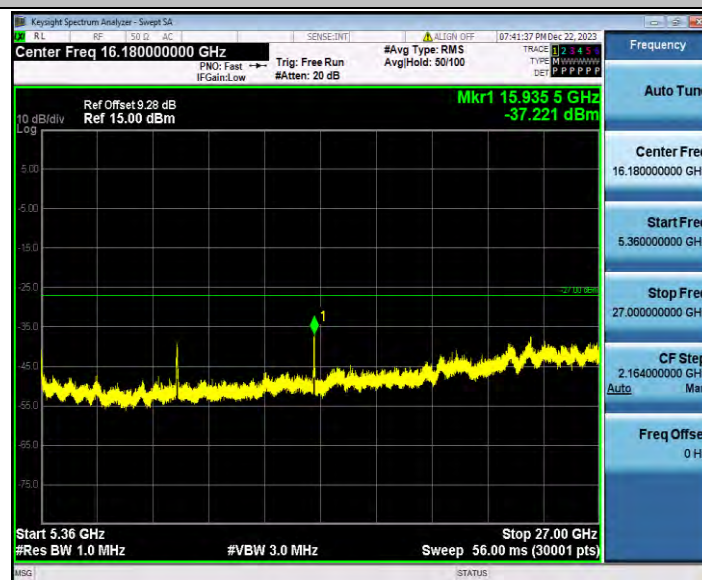








11AX40SISO_Ant0_5310_30~5140



11AX40SISO_Ant0_5310_5360~40000

