

REVISION			
DATE	DESCRIPTION	ECN NO.	REV. NO.

* options

OPTION	USE COMPONENTS
1	Tx Ant, Rx Ant
2	Single Ant, D101, R115~R117
3	Tx/Rx LED D102,D103, R118,R119

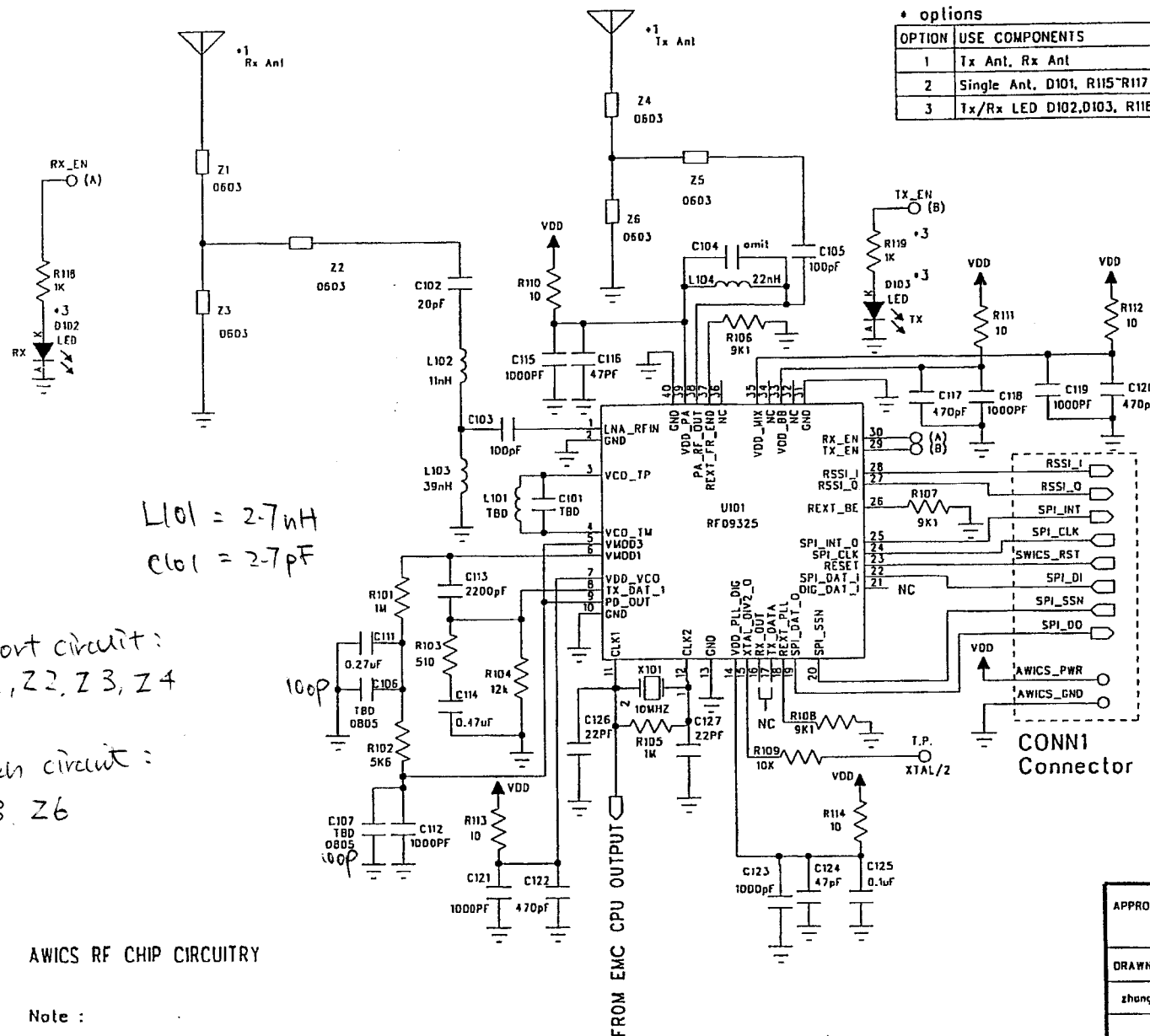
* options

OPTION	USE COMPONENTS
1	Tx Ant, Rx Ant
2	Single Ant, D101, R115~R117
3	Tx/Rx LED D102,D103, R118,R119

THIS AWICS MODULE,
2 PCS IN A SET
OF UNIT PRODUCT.

AS TRANSMITTER MODULE
CONNECT J1 TO RF PAD

AS RECEIVER MODULE
CONNECT J1 TO PSII BODY



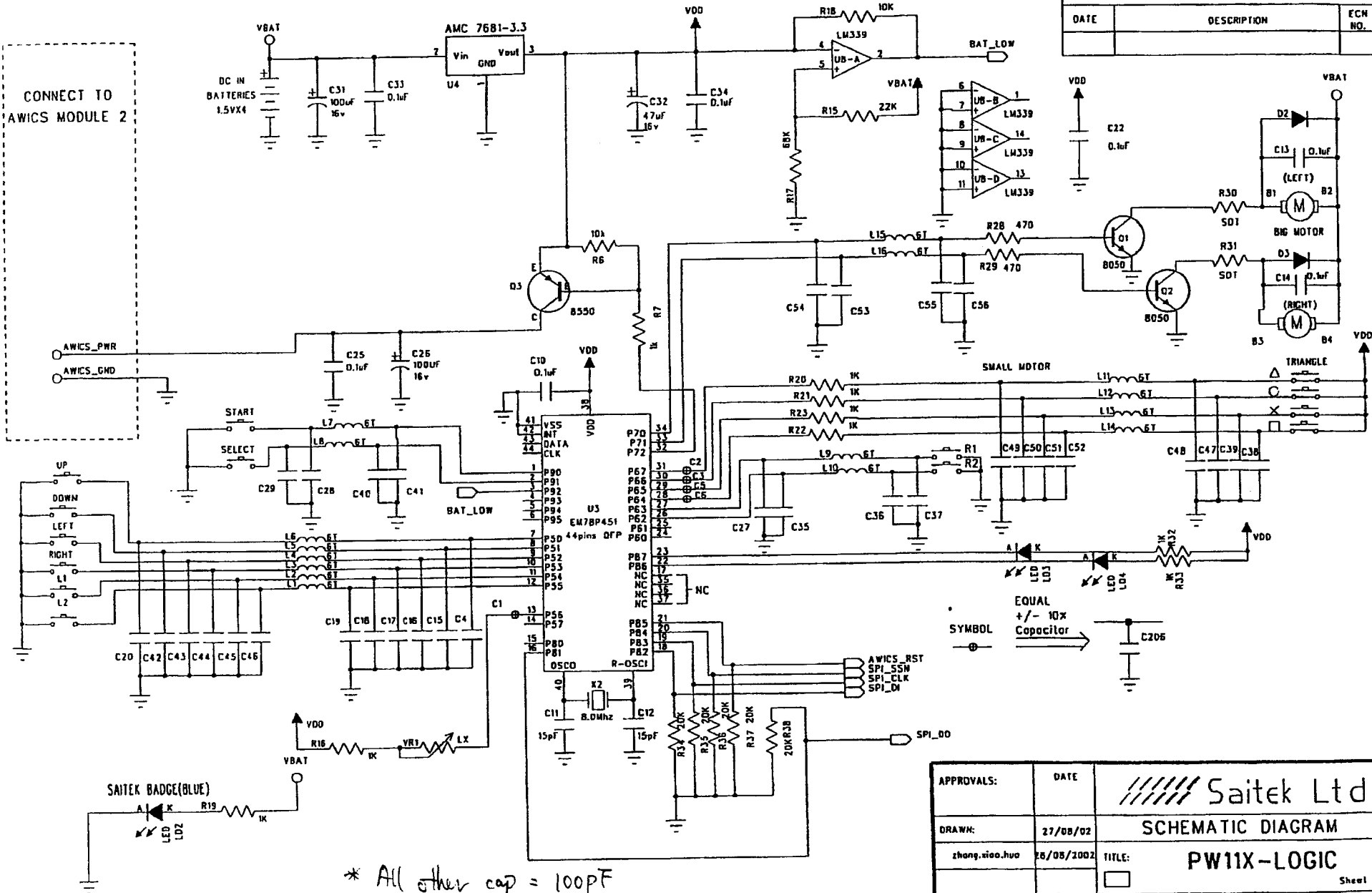
AWICS RF CHIP CIRCUITRY

Note :

All resistors and ceramic capacitor are 0603 package unless specified.

APPROVALS:	DATE	Saitek Ltd.	
DRAWN:	28/08/02	SCHEMATIC DIAGRAM	
zhong.xiao.hua	28/08/02	TITLE:	PW11X-RX-LOGIC
			Sheet 1 of 2
		DWG. NO.	REV. 0

CONNECT TO
AWICS MODULE 2



REVISION			
DATE	DESCRIPTION	ECN NO.	REV. NO.

APPROVALS:	DATE	Saitek Ltd.	
DRAWN:	27/08/02	SCHEMATIC DIAGRAM	
zhong.xiao.hua	26/08/2002	TITLE: PW11X-LOGIC	
		DWG. NO.	REV. 0

* All other cap = 100pF
* All other inductor 6T = 70uH