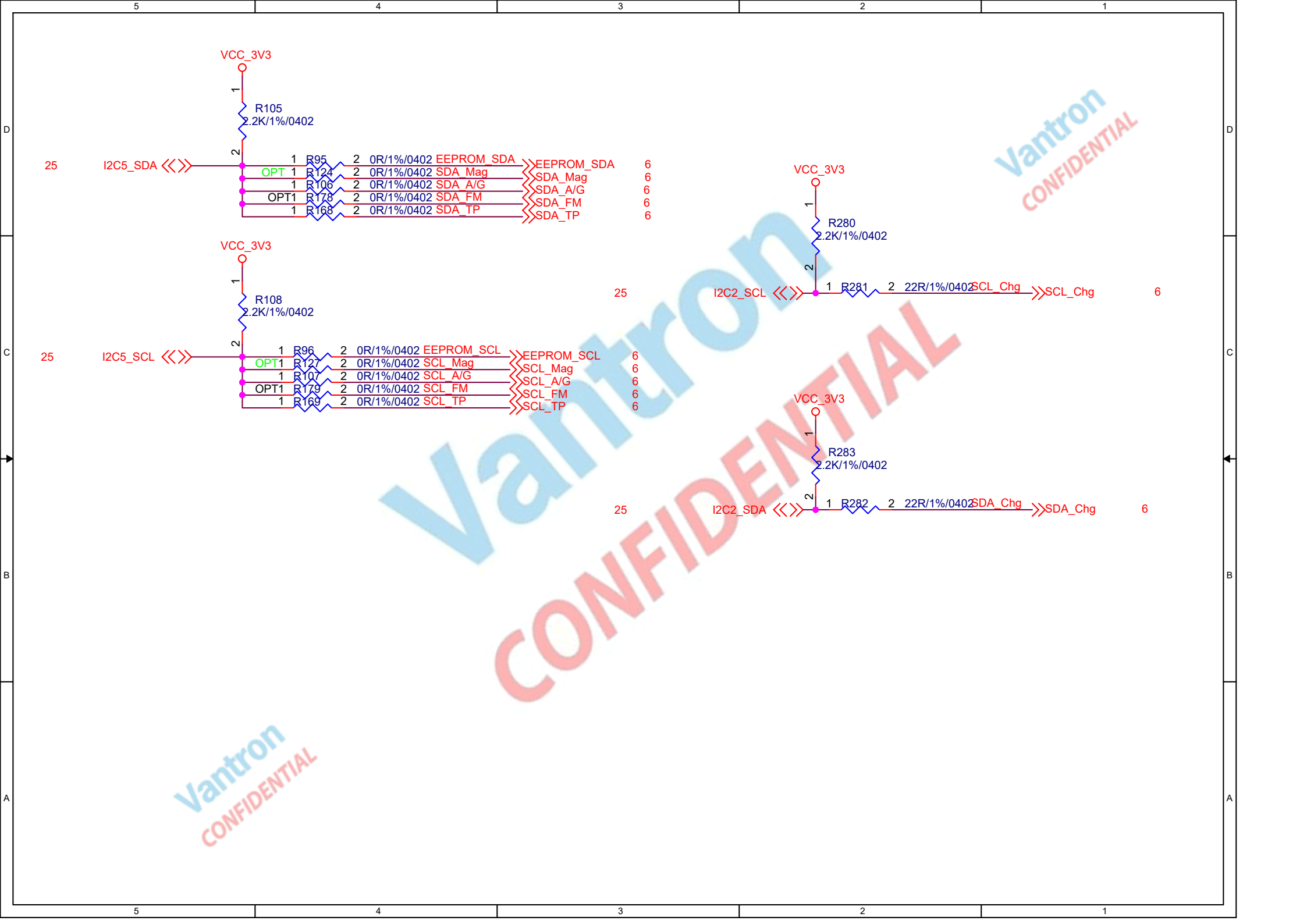
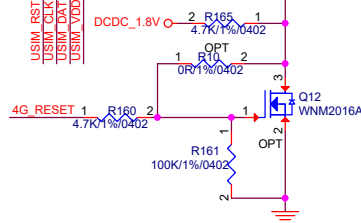
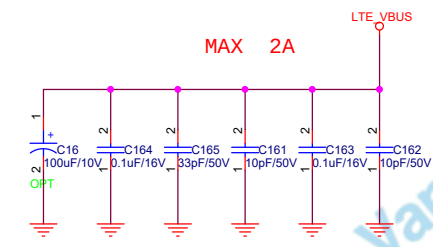
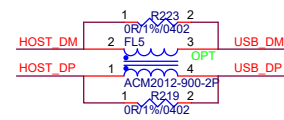
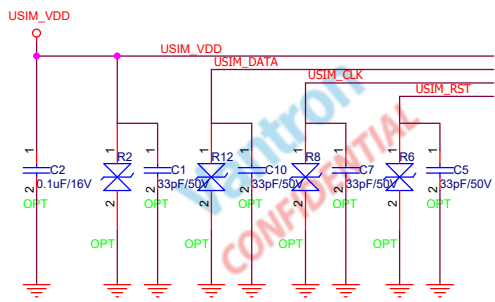
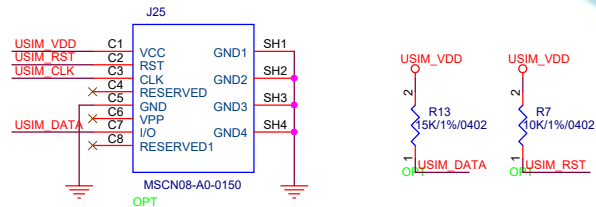
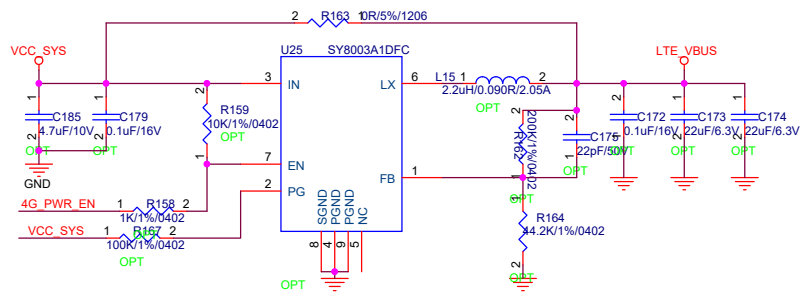
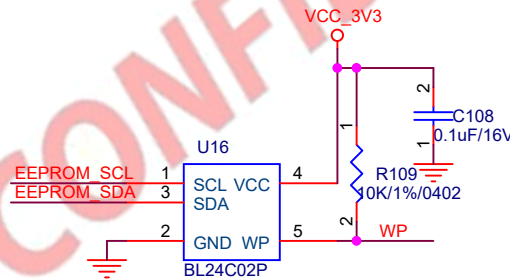
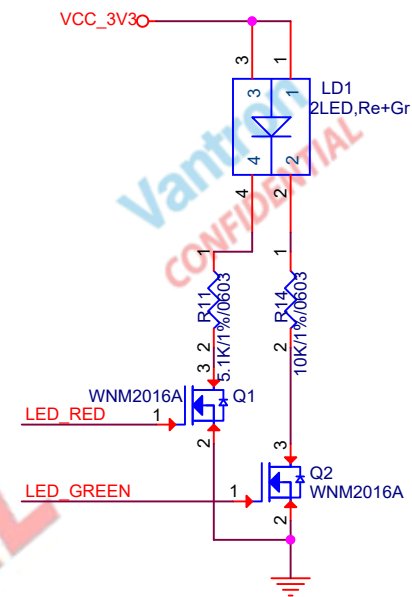
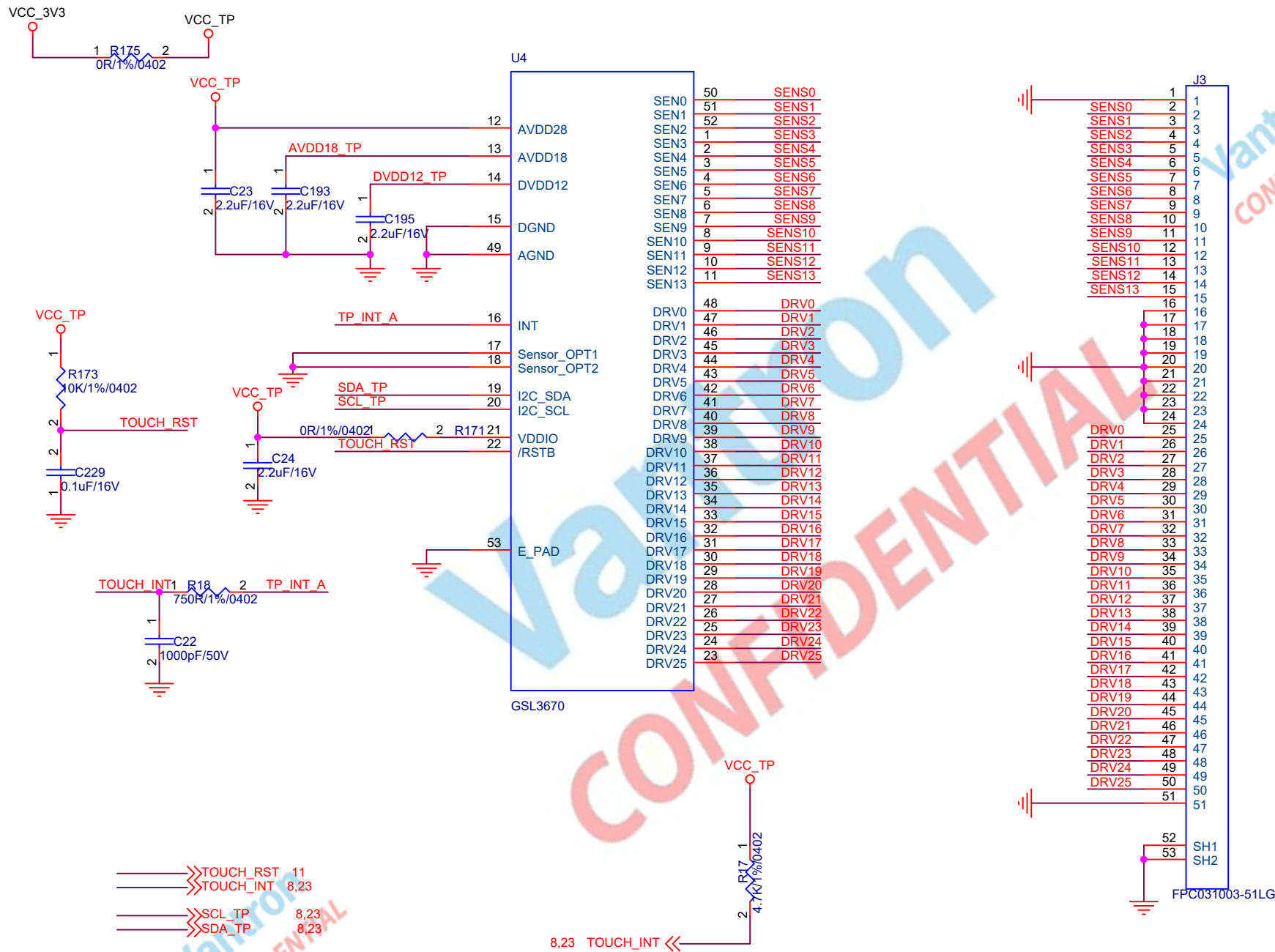








<u>LED_GREEN</u>	<u>LED_GREEN</u>	6
<u>LED_RED</u>	<u>LED_RED</u>	6
<u>WP</u>	<u>WP</u>	6
<u>EEPROM_SCL</u>	<u>EEPROM_SCL</u>	6
<u>EEPROM_SDA</u>	<u>EEPROM_SDA</u>	6
<u>INT_Mag</u>	<u>INT_Mag</u>	6
<u>SCL_Mag</u>	<u>SCL_Mag</u>	6
<u>SDA_Mag</u>	<u>SDA_Mag</u>	6
<u>SCL_A/G</u>	<u>SCL_A/G</u>	6
<u>SDA_A/G</u>	<u>SDA_A/G</u>	6
<u>A/G INT1</u>	<u>A/G INT1</u>	6
<u>A/G INT2</u>	<u>A/G INT2</u>	6



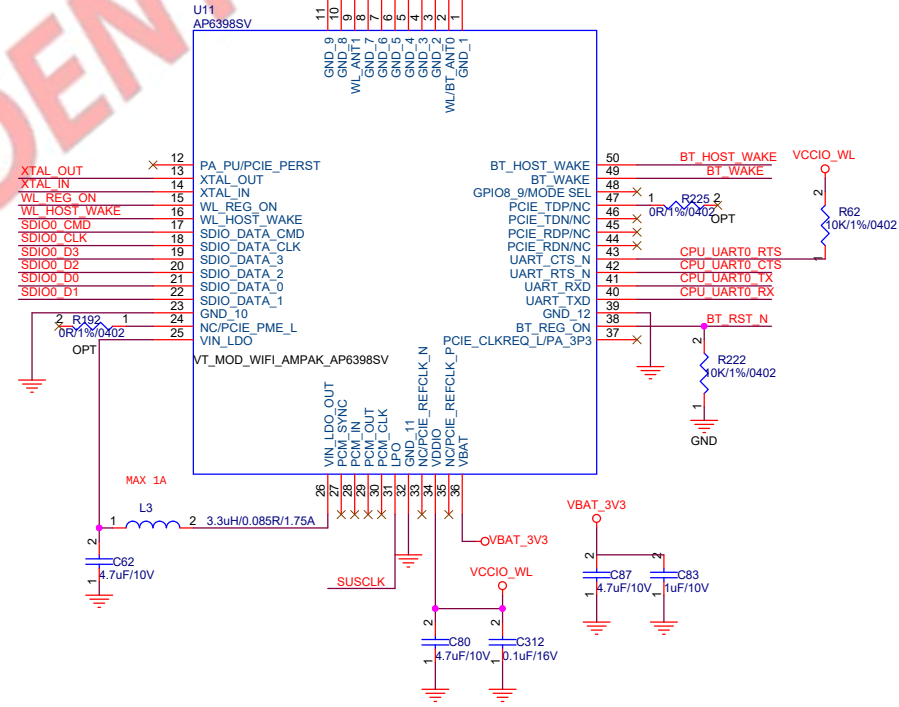
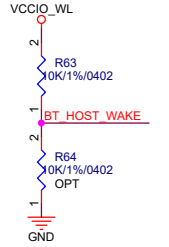
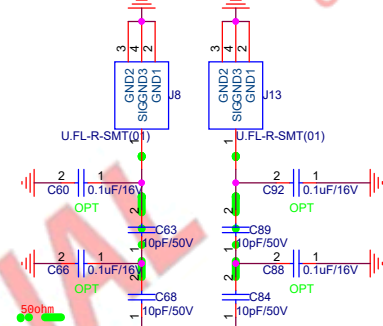
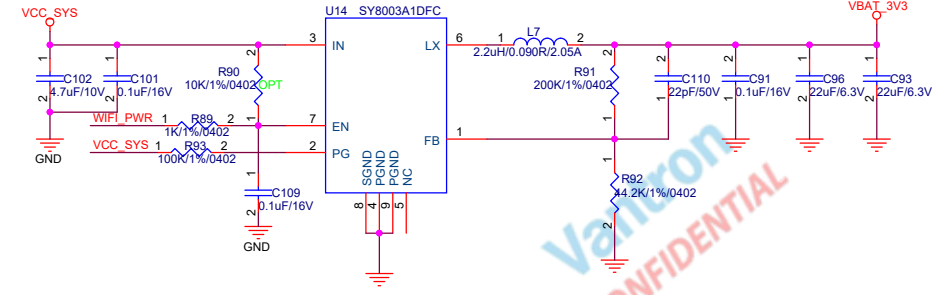
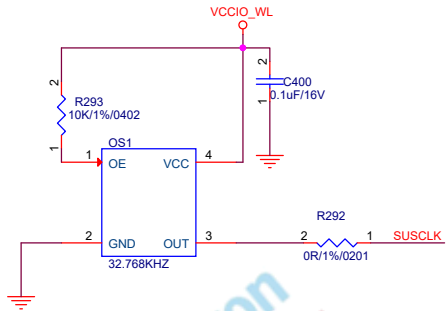
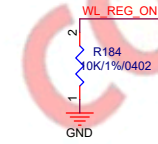
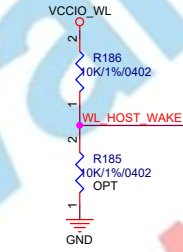
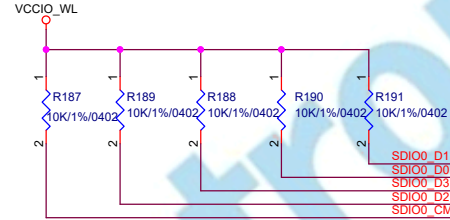
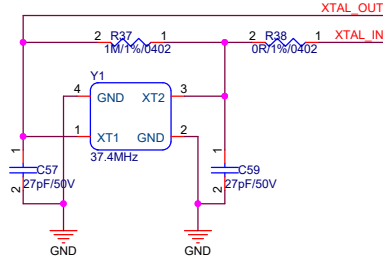
>>SDIO0_D0 11
>>SDIO0_D1 11
>>SDIO0_D2 11
>>SDIO0_D3 11
>>SDIO0_CMD 11
>>SDIO0_CLK 11

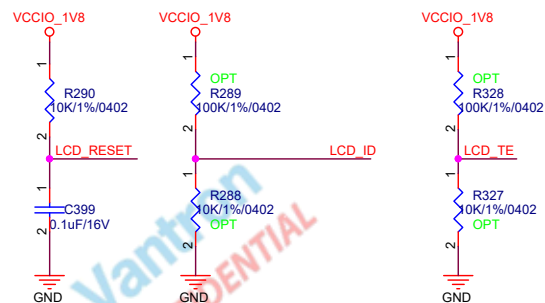
>>BT_HOST_WAKE 11
>>BT_WAKE 11
>>BT_RST_N 11
>>WIFI_PWR 11
>>WIFI_PWR 11
>>WL_REG_ON 11
>>WL_HOST_WAKE 8,11

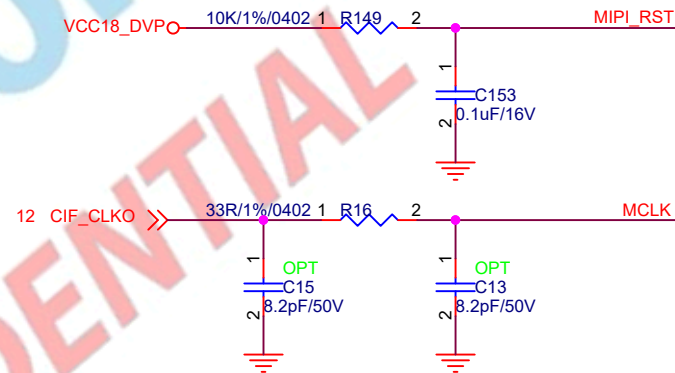
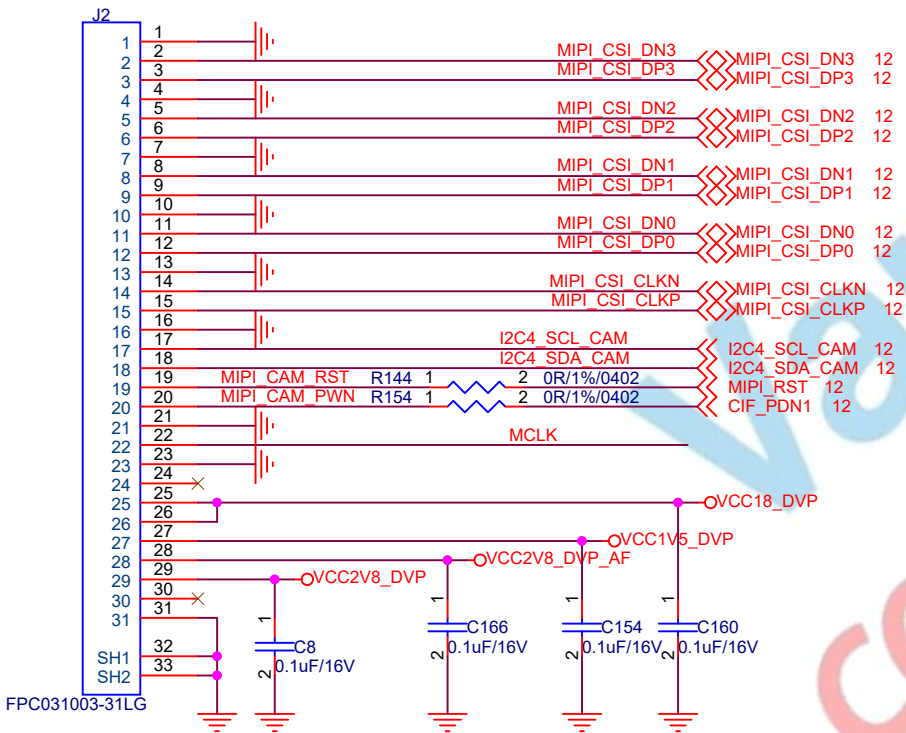
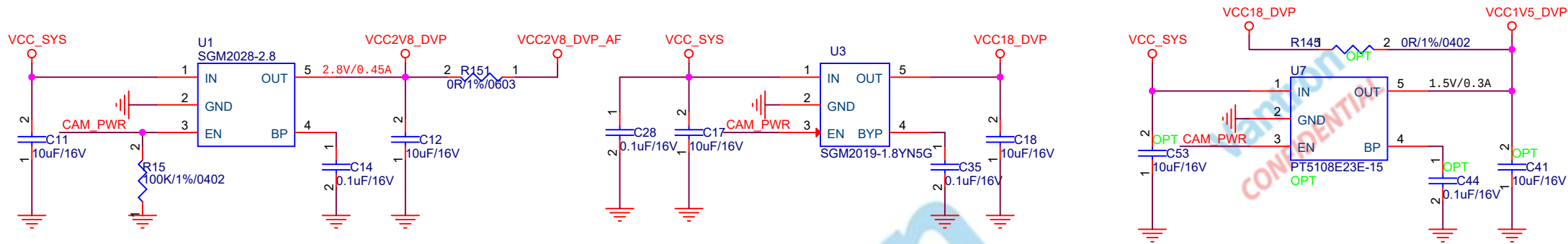
>>CPU_UART0_RTS 11
>>CPU_UART0_CTS 11
>>CPU_UART0_TX 11
>>CPU_UART0_RX 11

>>BT_HOST_WAKE 11
>>BT_WAKE 11
>>BT_RST_N 11
>>WIFI_PWR 11
>>WIFI_PWR 11
>>WL_REG_ON 11
>>WL_HOST_WAKE 8,11

16 PMIC_32KOUT_WK 180R/1%/0402 2 R49 1 OPT SUSCLK







CAM_PWR <>> CAM_PWR 12

U12A

VDD_CPU_1
VDD_CPU_2
VDD_CPU_3
VDD_CPU_4
VDD_CPU_5
VDD_CPU_6
VDD_CPU_7
VDD_CPU_8
VDD_CPU_9
VDD_CPU_10
VDD_CPU_11

1C12
1D11
1D12
1E11
1E12
1E13
1F11
1F12
1F13
1F14
1G13

C305
4.7uF/10V
C297
4.7uF/10V
C378
22uF/6.3V

VDD_CPU

C383
22uF/6.3V
C306
22uF/6.3V

VDD_LOGIC_1
VDD_LOGIC_2
VDD_LOGIC_3
VDD_LOGIC_4
VDD_LOGIC_5
VDD_LOGIC_6
VDD_LOGIC_7
VDD_LOGIC_8
VDD_LOGIC_9

1H7
1H8
1H9
1H10
1J7
1J10
1K7
1K10
1L7

C300
0.1uF/16V
C316
4.7uF/10V
C303
4.7uF/10V
C298
4.7uF/10V
C301
22uF/6.3V

VDD_LOGIC

C47
10uF/16V
C227
22uF/6.3V

VDD_GPU_1
VDD_GPU_2
VDD_GPU_3
VDD_GPU_4
VDD_GPU_5

1L8
1L9
1M7
1M8
1M9

C308
0.1uF/16V
C309
4.7uF/10V
C320
22uF/6.3V

VDD_GPU

C78
10uF/16V
C74
22uF/6.3V

VDD_NPU_1
VDD_NPU_2
VDD_NPU_3
VDD_NPU_4
VDD_NPU_5

1G15
1H14
1H15
1J14
1J15

C311
0.1uF/16V
C79
22uF/6.3V

VDD_NPU

C304
10uF/16V
C323
22uF/6.3V

U12B

VSS_1
VSS_2
VSS_3
VSS_4
VSS_5
VSS_6
VSS_7
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VSS_9
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VSS_49
VSS_50

A1
A2
A38
B1
B2
B6
B11
B14
B18
B21
B28
B31
D2
J2
L2
U2
W2
AA2
AD2
AR1
AP2
AP8
E37
AC37
AE37
AH37
AL37
AP37
AR38
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1D1
1D3
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1E3
1E8
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1E15

U12C

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VSS_52
VSS_53
VSS_54
VSS_55
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VSS_100

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U12D

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VSS_130
VSS_131

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1V1
1V4

RK3566

U12E

AVSS_1
AVSS_2
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AVSS1_14

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AP22
AP25
AP28
AP31
AP34
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1T14
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1T17
1U12
1U13
1U15
1U17
1V7
1V12
1V13
1V18
H37
L37
P37
U37
Y37
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1G16
1G18
1G19
1G20
1J16
1J18
1K18
1L19

RK3566

RK3566

LPDDR4_D0_A << >> DDR DQ0 A G2
LPDDR4_D1_A << >> DDR DQ1 A F2
LPDDR4_D2_A << >> DDR DQ2 A F1
LPDDR4_D3_A << >> DDR DQ3 A E1
LPDDR4_D4_A << >> DDR DQ4 A M2
LPDDR4_D5_A << >> DDR DQ5 A K1
LPDDR4_D6_A << >> DDR DQ6 A K2
LPDDR4_D7_A << >> DDR DQ7 A 1E1

LPDDR4_DM0_A << >> DDR DM0 A 1E2

LPDDR4_DQS0P_A << >> DDR DQS0P A H2
LPDDR4_DQS0N_A << >> DDR DQS0N A H1

LPDDR4_D8_A << >> DDR DQ8 A P2
LPDDR4_D9_A << >> DDR DQ9 A R1
LPDDR4_D10_A << >> DDR DQ10 A R2
LPDDR4_D11_A << >> DDR DQ11 A T2
LPDDR4_D12_A << >> DDR DQ12 A M1
LPDDR4_D13_A << >> DDR DQ13 A 1F2
LPDDR4_D14_A << >> DDR DQ14 A 1F1
LPDDR4_D15_A << >> DDR DQ15 A 1G2

LPDDR4_DM1_A << >> DDR DM1 A 1G1

LPDDR4_DQS1P_A << >> DDR DQS1P A N1
LPDDR4_DQS1N_A << >> DDR DQS1N A N2

LPDDR4_D0_B << >> DDR DQ0 B B12
LPDDR4_D1_B << >> DDR DQ1 B B10
LPDDR4_D2_B << >> DDR DQ2 B 1A8
LPDDR4_D3_B << >> DDR DQ3 B 1A7
LPDDR4_D4_B << >> DDR DQ4 B B13
LPDDR4_D5_B << >> DDR DQ5 B 1A10
LPDDR4_D6_B << >> DDR DQ6 B 1B9
LPDDR4_D7_B << >> DDR DQ7 B 1A9

LPDDR4_DM0_B << >> DDR DM0 B 1B8

LPDDR4_DQS0P_B << >> DDR DQS0P BA13
LPDDR4_DQS0N_B << >> DDR DQS0N BA12

LPDDR4_D8_B << >> DDR DQ8 B B19
LPDDR4_D9_B << >> DDR DQ9 B A19
LPDDR4_D10_B << >> DDR DQ10 B A20
LPDDR4_D11_B << >> DDR DQ11 B B20
LPDDR4_D12_B << >> DDR DQ12 B A15
LPDDR4_D13_B << >> DDR DQ13 B B15
LPDDR4_D14_B << >> DDR DQ14 B1C10
LPDDR4_D15_B << >> DDR DQ15 B B16

LPDDR4_DM1_B << >> DDR DM1 B 1A11

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LPDDR4_DQS1N_B << >> DDR DQS1N BA17

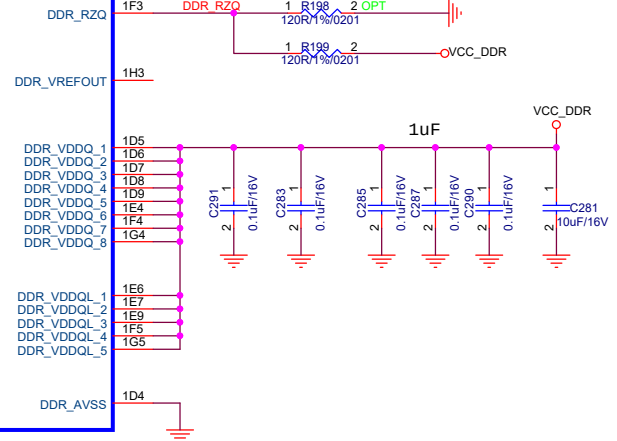
U12F

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DDR DQ0 A / DDR4_DQ0_A	LPDDR4_DQ0_A	DDR DQ0 B / LPDDR3_DQ15	LPDDR3_DQ15
DDR DQ1 A / DDR4_DQ1_A	LPDDR4_DQ1_A	DDR DQ1 B / LPDDR3_DQ14	LPDDR3_DQ14
DDR DQ2 A / DDR4_DQ2_A	LPDDR4_DQ2_A	DDR DQ2 B / LPDDR3_DQ10	LPDDR3_DQ10
DDR DQ3 A / DDR4_DQ3_A	LPDDR4_DQ3_A	DDR DQ3 B / LPDDR3_DQ9	LPDDR3_DQ9
DDR DQ4 A / DDR4_DQ4_A	LPDDR4_DQ4_A	DDR DQ4 B / LPDDR3_DQ13	LPDDR3_DQ13
DDR DQ5 A / DDR4_DQ5_A	LPDDR4_DQ5_A	DDR DQ5 B / LPDDR3_DQ12	LPDDR3_DQ12
DDR DQ6 A / DDR4_DQ6_A	LPDDR4_DQ6_A	DDR DQ6 B / LPDDR3_DQ8	LPDDR3_DQ8
DDR DQ7 A / DDR4_DQ7_A	LPDDR4_DQ7_A	DDR DQ7 B / LPDDR3_DQ11	LPDDR3_DQ11
DDR DM0 A / DDR4_DM0_A	LPDDR4_DM0_A	DDR DM0 B / LPDDR3_DM1	LPDDR3_DM1
DDR DQS0P A / DDR4_DQS0P_A	LPDDR4_DQS0P_A	DDR DQS0P B / LPDDR3_DQS1P	LPDDR3_DQS1P
DDR DQS0N A / DDR4_DQS0N_A	LPDDR4_DQS0N_A	DDR DQS0N B / LPDDR3_DQS1N	LPDDR3_DQS1N
DDR DQ8 A / DDR4_DQ8_A	LPDDR4_DQ8_A	DDR DQ8 B / LPDDR3_DQ25	LPDDR3_DQ25
DDR DQ9 A / DDR4_DQ9_A	LPDDR4_DQ9_A	DDR DQ9 B / LPDDR3_DQ24	LPDDR3_DQ24
DDR DQ10 A / DDR4_DQ10_A	LPDDR4_DQ10_A	DDR DQ10 B / LPDDR3_DQ28	LPDDR3_DQ28
DDR DQ11 A / DDR4_DQ11_A	LPDDR4_DQ11_A	DDR DQ11 B / LPDDR3_DQ29	LPDDR3_DQ29
DDR DQ12 A / DDR4_DQ12_A	LPDDR4_DQ12_A	DDR DQ12 B / LPDDR3_DQ26	LPDDR3_DQ26
DDR DQ13 A / DDR4_DQ13_A	LPDDR4_DQ13_A	DDR DQ13 B / LPDDR3_DQ31	LPDDR3_DQ31
DDR DQ14 A / DDR4_DQ14_A	LPDDR4_DQ14_A	DDR DQ14 B / LPDDR3_DQ30	LPDDR3_DQ30
DDR DQ15 A / DDR4_DQ15_A	LPDDR4_DQ15_A	DDR DQ15 B / LPDDR3_DQ27	LPDDR3_DQ27
DDR DM1 A / DDR4_DM1_A	LPDDR4_DM1_A	DDR DM1 B / LPDDR3_DM3	LPDDR3_DM3
DDR DQS1P A / DDR4_DQS1P_A	LPDDR4_DQS1P_A	DDR DQS1P B / LPDDR3_DQS3P	LPDDR3_DQS3P
DDR DQS1N A / DDR4_DQS1N_A	LPDDR4_DQS1N_A	DDR DQS1N B / LPDDR3_DQS3N	LPDDR3_DQS3N
DDR DQ0 B / DDR4_DQ0_B	LPDDR4_DQ0_B	DDR DQ1 B / LPDDR3_DQ1	LPDDR3_DQ1
DDR DQ1 B / DDR4_DQ1_B	LPDDR4_DQ1_B	DDR DQ2 B / LPDDR3_DQ8	LPDDR3_DQ8
DDR DQ2 B / DDR4_DQ2_B	LPDDR4_DQ2_B	DDR DQ3 B / LPDDR3_DQ18	LPDDR3_DQ18
DDR DQ3 B / DDR4_DQ3_B	LPDDR4_DQ3_B	DDR DQ4 B / LPDDR3_DQ4	LPDDR3_DQ4
DDR DQ4 B / DDR4_DQ4_B	LPDDR4_DQ4_B	DDR DQ5 B / LPDDR3_DQ20	LPDDR3_DQ20
DDR DQ5 B / DDR4_DQ5_B	LPDDR4_DQ5_B	DDR DQ6 B / LPDDR3_DQ3	LPDDR3_DQ3
DDR DQ6 B / DDR4_DQ6_B	LPDDR4_DQ6_B	DDR DQ7 B / LPDDR3_DQ22	LPDDR3_DQ22
DDR DQ7 B / DDR4_DQ7_B	LPDDR4_DQ7_B	DDR DQ8 B / LPDDR3_DQ8	LPDDR3_DQ8
DDR DM0 B / DDR4_DM0_B	LPDDR4_DM0_B	DDR DM1 B / LPDDR3_DM0	LPDDR3_DM0
DDR DQS0P B / DDR4_DQS0P_B	LPDDR4_DQS0P_B	DDR DQS2P B / LPDDR3_DQS0P	LPDDR3_DQS0P
DDR DQS0N B / DDR4_DQS0N_B	LPDDR4_DQS0N_B	DDR DQS2N B / LPDDR3_DQS0N	LPDDR3_DQS0N
DDR DQ8 B / DDR4_DQ8_B	LPDDR4_DQ8_B	DDR DQ24 B / LPDDR3_DQ18	LPDDR3_DQ18
DDR DQ9 B / DDR4_DQ9_B	LPDDR4_DQ9_B	DDR DQ25 B / LPDDR3_DQ19	LPDDR3_DQ19
DDR DQ10 B / DDR4_DQ10_B	LPDDR4_DQ10_B	DDR DQ26 B / LPDDR3_DQ22	LPDDR3_DQ22
DDR DQ11 B / DDR4_DQ11_B	LPDDR4_DQ11_B	DDR DQ27 B / LPDDR3_DQ23	LPDDR3_DQ23
DDR DQ12 B / DDR4_DQ12_B	LPDDR4_DQ12_B	DDR DQ28 B / LPDDR3_DQ16	LPDDR3_DQ16
DDR DQ13 B / DDR4_DQ13_B	LPDDR4_DQ13_B	DDR DQ29 B / LPDDR3_DQ17	LPDDR3_DQ17
DDR DQ14 B / DDR4_DQ14_B	LPDDR4_DQ14_B	DDR DQ30 B / LPDDR3_DQ20	LPDDR3_DQ20
DDR DQ15 B / DDR4_DQ15_B	LPDDR4_DQ15_B	DDR DQ31 B / LPDDR3_DQ21	LPDDR3_DQ21
DDR DM1 B / DDR4_DM1_B	LPDDR4_DM1_B	DDR DM3 B / LPDDR3_DM2	LPDDR3_DM2
DDR DQS1P B / DDR4_DQS1P_B	LPDDR4_DQS1P_B	DDR DQS3P B / LPDDR3_DQS2P	LPDDR3_DQS2P
DDR DQS1N B / DDR4_DQS1N_B	LPDDR4_DQS1N_B	DDR DQS3N B / LPDDR3_DQS2N	LPDDR3_DQS2N

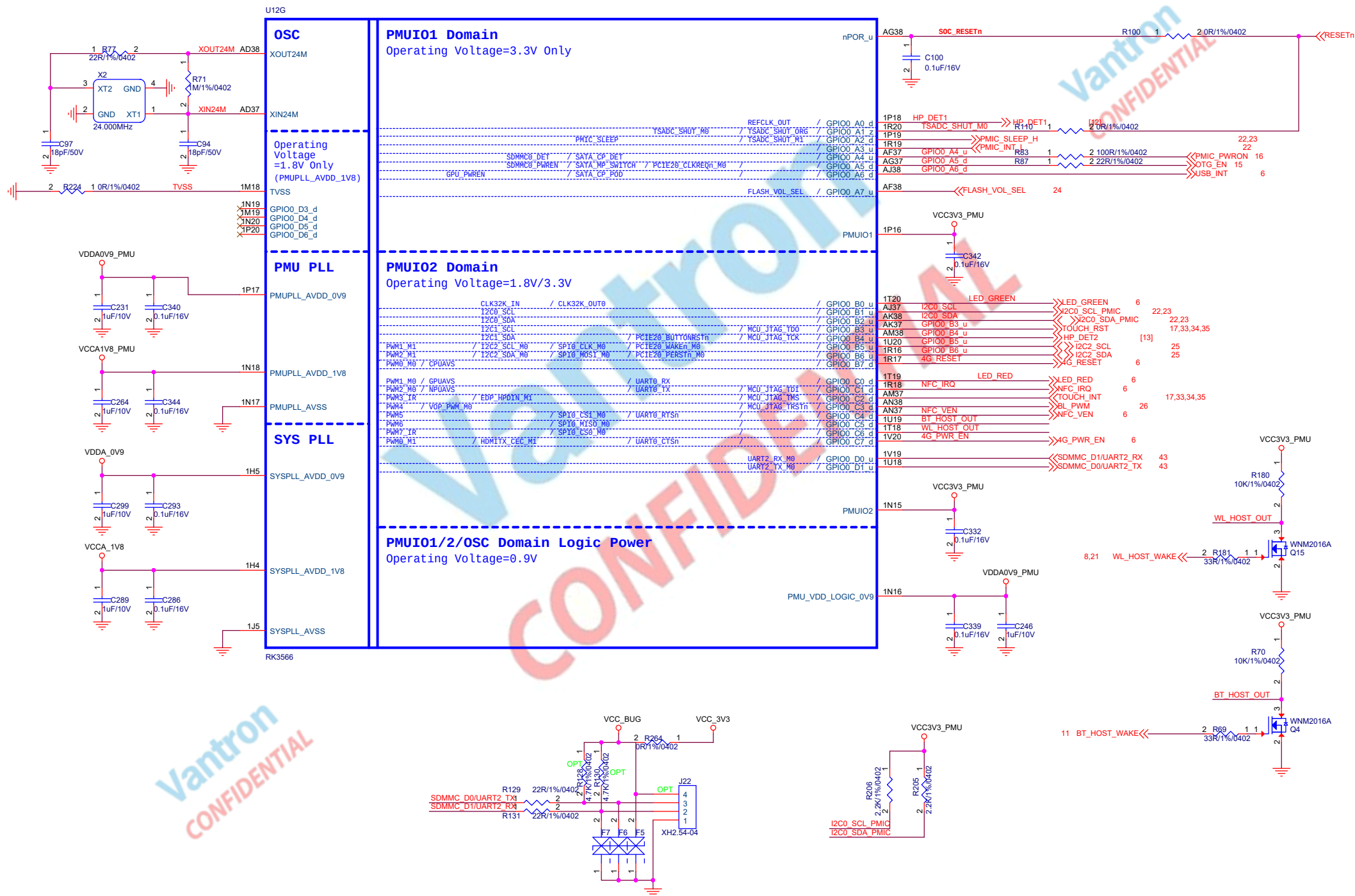
Note:
Except DDR3, other DQ sequences
can not be swap

DDR4	LPDDR4	DDR3	LPDDR3
DDR4_A0 / LPDDR4_CLKP_B	LPDDR4_CLKP_B	DDR3_A9 / ---	AC0
DDR4_A1 / ---	---	DDR3_A2 / ---	AC1
DDR4_A2 / LPDDR4_A1_A	LPDDR4_A1_A	DDR3_A4 / LPDDR3_A8	AC2
DDR4_A3 / LPDDR4_CKE1_A	LPDDR4_CKE1_A	DDR3_A3 / ---	AC3
DDR4_A4 / LPDDR4_A3_B	LPDDR4_A3_B	DDR3_BA1 / LPDDR3_A3	AC4
DDR4_A5 / LPDDR4_A5_B	LPDDR4_A5_B	DDR3_A11 / LPDDR3_A2	AC5
DDR4_A6 / LPDDR4_A1_B	LPDDR4_A1_B	DDR3_A13 / LPDDR3_A1	AC6
DDR4_A7 / LPDDR4_ODT0_CA_B	LPDDR4_ODT0_CA_B	DDR3_A8 / ---	AC7
DDR4_A8 / LPDDR4_ODT0_CA_A	LPDDR4_ODT0_CA_A	DDR3_A6 / LPDDR3_A9	AC8
DDR4_A9 / LPDDR4_CLKN_B	LPDDR4_CLKN_B	DDR3_A5 / ---	AC9
DDR4_A10 / LPDDR4_CKE0_B	LPDDR4_CKE0_B	DDR3_A10 / ---	AC10
DDR4_A11 / LPDDR4_A8_A	LPDDR4_A8_A	DDR3_A7 / LPDDR3_A8	AC11
DDR4_A12 / LPDDR4_A3_A	LPDDR4_A3_A	DDR3_BA2 / ---	AC12
DDR4_A13 / LPDDR4_A8_B	LPDDR4_A8_B	DDR3_A14 / LPDDR3_A8	AC13
DDR4_A14_WEN / LPDDR4_A4_A	LPDDR4_A4_A	DDR3_A15 / LPDDR3_A5	AC14
DDR4_A15_CASH / LPDDR4_A2_A	LPDDR4_A2_A	DDR3_A8 / ---	AC15
DDR4_A16_RASn / LPDDR4_A5_A	LPDDR4_A5_A	DDR3_RASn / LPDDR3_A7	AC16
DDR4_A17n / LPDDR4_CKE1_B	LPDDR4_CKE1_B	DDR3_CASH / ---	AC17
DDR4_BA0 / LPDDR4_A2_B	LPDDR4_A2_B	DDR3_A1 / ---	AC18
DDR4_BA1 / LPDDR4_A4_B	LPDDR4_A4_B	DDR3_A12 / LPDDR3_A4	AC19
DDR4_B08 / LPDDR4_ODT1_CA_B	LPDDR4_ODT1_CA_B	DDR3_WEN / ---	AC20
DDR4_B61 / LPDDR4_ODT1_CA_A	LPDDR4_ODT1_CA_A	DDR3_BA0 / ---	AC21
DDR4_CKE / LPDDR4_CKE0_A	LPDDR4_CKE0_A	DDR3_CKE / LPDDR3_CKE	AC22
DDR4_CLKP / LPDDR4_CLKP_A	LPDDR4_CLKP_A	DDR3_CLKP / LPDDR3_CLKP	AC23
DDR4_CLKN / LPDDR4_CLKN_A	LPDDR4_CLKN_A	DDR3_CLKN / LPDDR3_CLKN	AC24
DDR4_CS0n / LPDDR4_CS0n_A	LPDDR4_CS0n_A	DDR3_ODT1 / LPDDR3_ODT0	AC25
DDR4_CS1n / LPDDR4_CS1n_A	LPDDR4_CS1n_A	DDR3_CS10 / LPDDR3_ODT1	AC26
DDR4_ODT0 / LPDDR4_CS1n_B	LPDDR4_CS1n_B	DDR3_ODT0 / LPDDR3_CS1n	AC27
DDR4_ODT1 / LPDDR4_CS0n_B	LPDDR4_CS0n_B	DDR3_CS0n / LPDDR3_CS0n	AC28
DDR4_RESEtN / LPDDR4_RESEtN	LPDDR4_RESEtN	DDR3_RESEtN / ---	AC29

Note: Sequences can not be swap



	VDDQ	VDDQL
DDR3L	1.35V	1.35V
DDR3	1.50V	1.50V
DDR4	1.20V	1.20V
LPDDR3	1.20V	1.20V
LPDDR4	1.10V	1.10V
LPDDR4X	1.10V	0.60V



U12I

VCCIO2 Domain

Operating Voltage=1.8V/3.3V

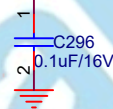
EMMC_D0	/ FLASH_D0	/ GPIO1_B4_u	A32	eMMC D0/FLASH D0/	<<	>>eMMC_D0/FLASH_D0	27,28
EMMC_D1	/ FLASH_D1	/ GPIO1_B5_u	B27	eMMC D1/FLASH D1/	<<	>>eMMC_D1/FLASH_D1	27,28
EMMC_D2	/ FLASH_D2	/ GPIO1_B6_u	B32	eMMC D2/FLASH D2/	<<	>>eMMC_D2/FLASH_D2	27,28
EMMC_D3	/ FLASH_D3	/ GPIO1_B7_u	B29	eMMC D3/FLASH D3/	<<	>>eMMC_D3/FLASH_D3	27,28
EMMC_D4	/ FLASH_D4	/ GPIO1_C0_u	B33	eMMC D4/FLASH D4/	<<	>>eMMC_D4/FLASH_D4	27,28
EMMC_D5	/ FLASH_D5	/ GPIO1_C1_u	A30	eMMC D5/FLASH D5/	<<	>>eMMC_D5/FLASH_D5	27,28
EMMC_D6	/ FLASH_D6	/ GPIO1_C2_u	B30	eMMC D6/FLASH D6/	<<	>>eMMC_D6/FLASH_D6	27,28
EMMC_D7	/ FLASH_D7	/ GPIO1_C3_u	A33	eMMC D7/FLASH D7/	<<	>>eMMC_D7/FLASH_D7	27,28
EMMC_CMD	/ FLASH_WRn	/ GPIO1_C4_u	A27	eMMC_CMD/FLASH_WRn/	<<	>>eMMC_CMD/FLASH_WRn	27,28
EMMC_CLKOUT	/ FLASH_DQS	/ GPIO1_C5_d	A29	eMMC_CLKOUT/FLASH_DQS/	<<	>>eMMC_CLKOUT/FLASH_DQS	27,28
EMMC_DATA_STROBE	/ FSPI_CS1n	/ FLASH_CLE	1A16		<<	>>eMMC_DATA_STROBE/FLASH_CLE	27,28
EMMC_RSTn	/ FSPI_D2	/ FLASH_WPn	1B16		<<	>>eMMC_RSTn/FSPI_D2/FLASH_WPn	27,28,30
	FSPI_CLK	FLASH_ALE	1A15		<<		
	FSPI_D0	FLASH_RDY	1A17		<<		
	FSPI_D1	FLASH_RDn	1A18		<<		
	FSPI_CS0n	FLASH_CS0n	1B17		<<		
	FSPI_D3	FLASH_CS1n	1C15		<<	>>FP_RESET	6

Default is determined by Pin
FLASH_VOL_SEL/GPIO0_A7_u:
L:VCCIO2 must supply 3.3V
H:VCCIO2 must supply 1.8V

VCCIO2

VCCIO_FLASH

1C13



RK3566

U12J

VCCIO3 Domain

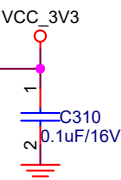
Operating Voltage=1.8V/3.3V

PWM8_M1	/ SDMMC0_D0	/ UART2_TX_M1	/ UART6_TX_M1	/ GPIO1_D5_u	1E20	WP	WP	6
PWM9_M1	/ SDMMC0_D1	/ UART2_RX_M1	/ UART6_RX_M1	/ GPIO1_D6_u	1F19	INT_Mag	INT_Mag	6
	SDMMC0_D2	/ ARM_JTAG_TCK	/ UART5_CTSn_M0	/ GPIO1_D7_u	1D20			
	SDMMC0_D3	/ ARM_JTAG_TMS	/ UART5_RTSn_M0	/ GPIO2_A0_u	1F18			
PWM10_M1	/ SDMMC0_CMD	/ UART5_RX_M0	/ GPIO2_A1_u	1E19				
	SDMMC0_CLK	/ TEST_CLKOUT	/ UART5_TX_M0	/ GPIO2_A2_d	G38	WPC_INT	WPC_INT	6

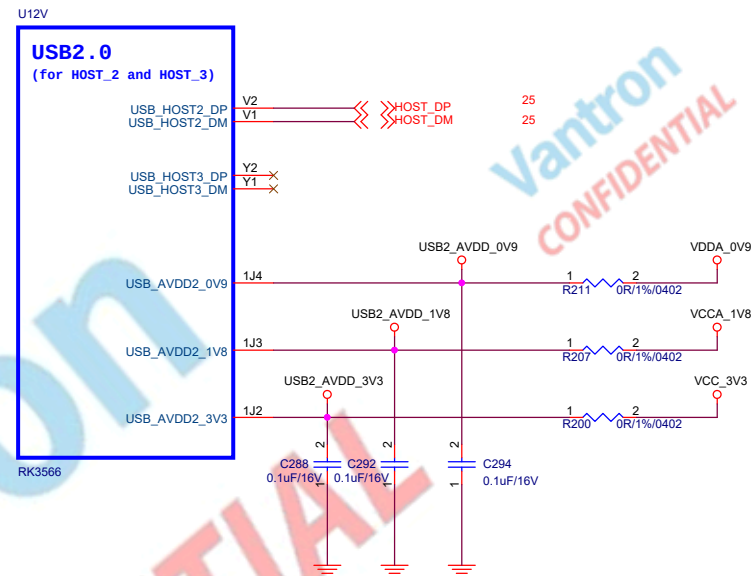
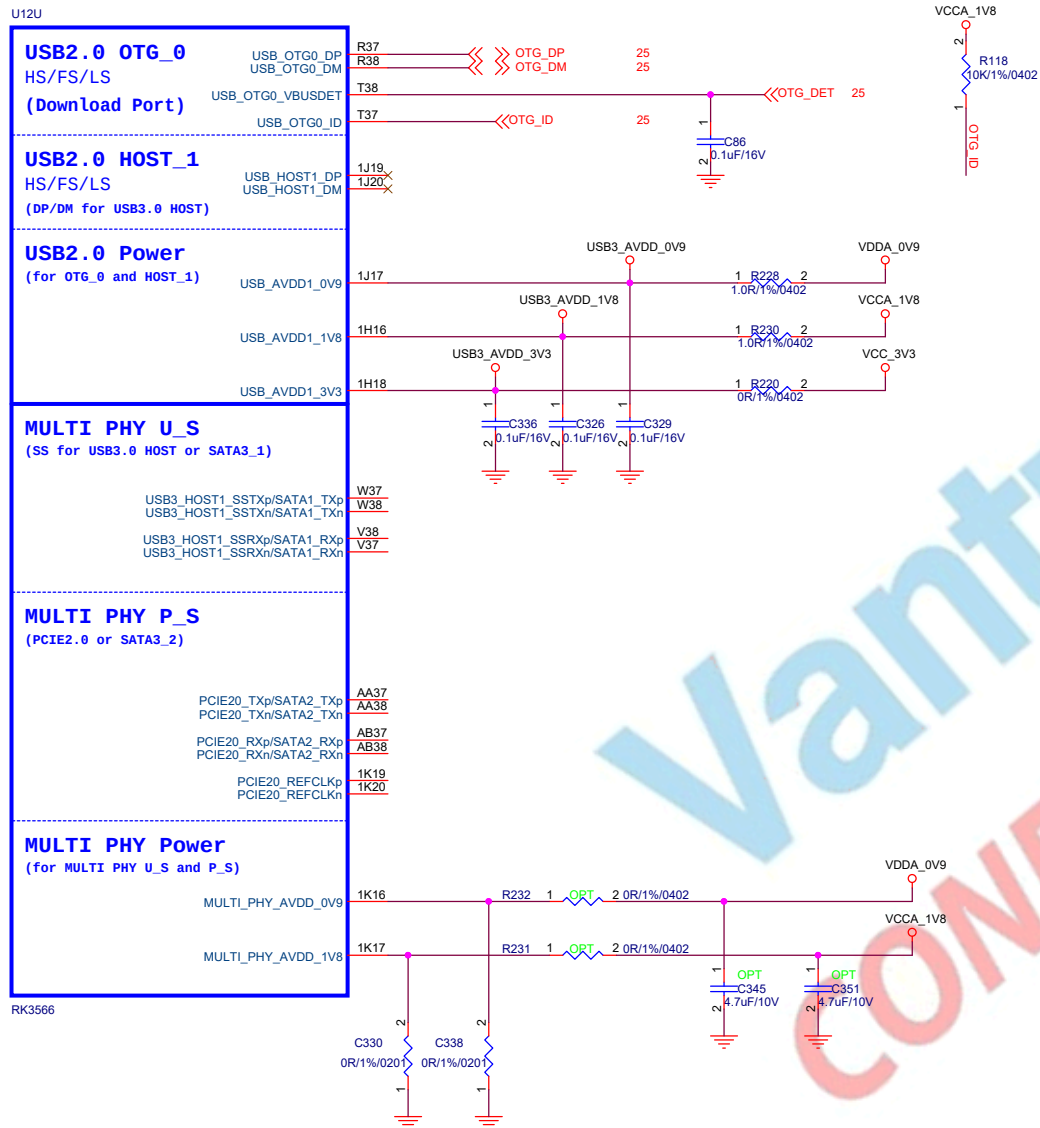
VCCIO3

VCC_3V3

1F17



RK3566



U12K

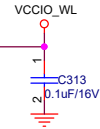
VCCIO4 Domain

Operating Voltage=1.8V/3.3V

UART6_RX_M0	/ SDMMC1_D0	/ GPIO2_A3_u	1C19	GPIO2_A3_u	>>>SDIO0_D0	36
UART6_TX_M0	/ SDMMC1_D1	/ GPIO2_A4_u	1E18	GPIO2_A4_u	>>>SDIO0_D1	36
UART7_RX_M0	/ SDMMC1_D2	/ GPIO2_A5_u	1D18	GPIO2_A5_u	>>>SDIO0_D2	36
UART7_TX_M0	/ SDMMC1_D3	/ GPIO2_A6_u	1A20	GPIO2_A6_u	>>>SDIO0_D3	36
UART9_RX_M0	/ SDMMC1_CMD	/ GPIO2_A7_u	B35	GPIO2_A7_u	>>>SDIO0_CMD	36
UART9_TX_M0	/ SDMMC1_CLK	/ GPIO2_B0_d	B37	GPIO2_B0_d	>>>SDIO0_CLK	36
UART8_RTSn_M0	/ I2C4_SDA_M1	/ SDMMC1_PWREN	A35	GPIO2_B1_d	>>>WL_REG_ON	
UART8_CTSn_M0	/ I2C4_SCL_M1	/ SDMMC1_DEY	B34			
	/ UART1_RX_M0	/ GPIO2_B3_u	1C20	GPIO2_B3_u	>>>CPU_UART0_RX	11
	/ UART1_TX_M0	/ GPIO2_B4_u	F38	GPIO2_B4_u	>>>CPU_UART0_TX	11
	/ UART1_RTSn_M0	/ GPIO2_B5_u	F37	GPIO2_B5_u	>>>CPU_UART0_RTS	11
	/ UART1_CTSn_M0	/ GPIO2_B6_u	G37	GPIO2_B6_u	>>>CPU_UART0_CTS	11
UART6_RTSn_M0	/ SPI1_MOSI_M0	/ I2S2_SCL_RX_M0	D38	GPIO2_C0_d	>>>BT_WAKE	11
UART6_CTSn_M0	/ SPI1_CS0_M0	/ I2S2_LRCK_RX_M0	A37	GPIO2_C1_d	>>>BT_RST_N	11
UART7_RTSn_M0	/ SPI2_CLK_M0	/ I2S2_MCLK_M0	B36			
UART7_CTSn_M0	/ SPI2_MISO_M0	/ I2S2_SCL_TX_M0	C37			
UART8_RTSn_M0	/ SPI2_CS0_M0	/ I2S2_LRCK_TX_M0	1D19			
UART8_CTSn_M0	/ SPI2_MOSI_M0	/ I2S2_SDO_M0	1B20			
UART9_TX_M0	/ SPI2_CS1_M0	/ I2S2_SDI_M0	D37			
UART8_RX_M0	/ SPI1_CS1_M0	/ CLK32K_OUT1	B38			

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vt_bga_15p5x14p4_p4-p65mm_565p

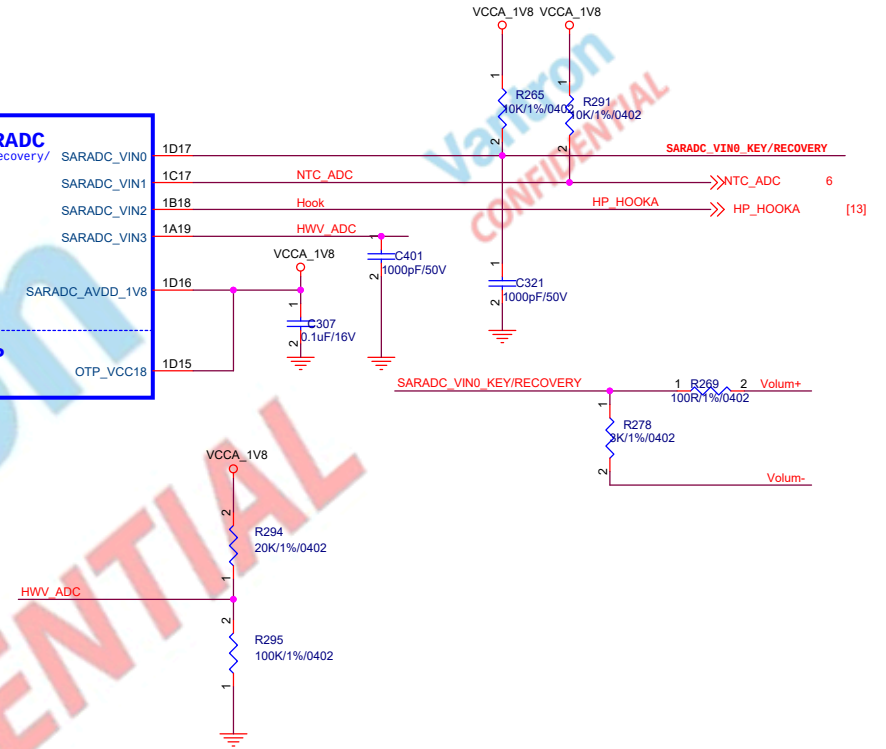
VCCIO4



U12O



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U12N

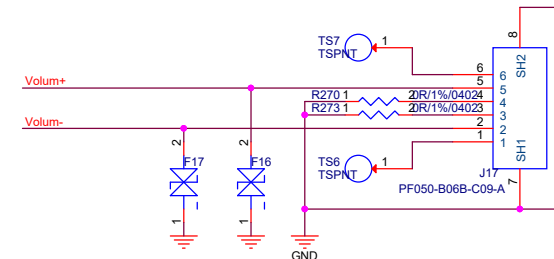
VCCIO7 Domain

Operating Voltage=1.8V/3.3V

PWM14_M1	/ SPI3_CLK_M1	/ I2S3_MCLK_M1	1U7		>>>CAM_PWR	12
PWM15_IR_M1	/ SPI3_MOSI_M1	/ I2S3_SCL_M1	AP12		>>>CH_Status	6
EDP_WD0EN_M0	/ SATA2_ACT_LED	/ SATA0_TX_M2	1V6		>>>Charger_STAT	6
PWM12_M1	/ SPI3_MISO_M1	/ SATA1_ACT_LED	1U6		>>>Charger_nCE	6
PWM13_M1	/ SPI3_CS0_M1	/ SATA0_ACT_LED	1T6			
HDMI_TX_SCL	/ I2C5_SCL_M1	/ GPIO4_C7_u	AR12		>>>I2C5_SCL	23,42
HDMI_TX_SDA	/ I2C5_SDA_M1	/ GPIO4_D0_u	AP11		>>>I2C5_SDA	23,42
HDMI_TX_CEC_M0	/ SPI3_CS1_M1	/ GPIO4_D1_u	1V5		>>>Charge_INT	6

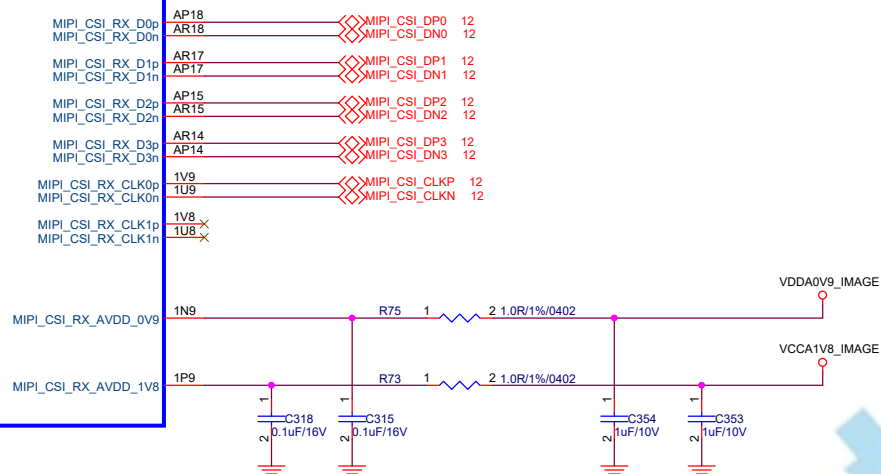
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VCCIO7



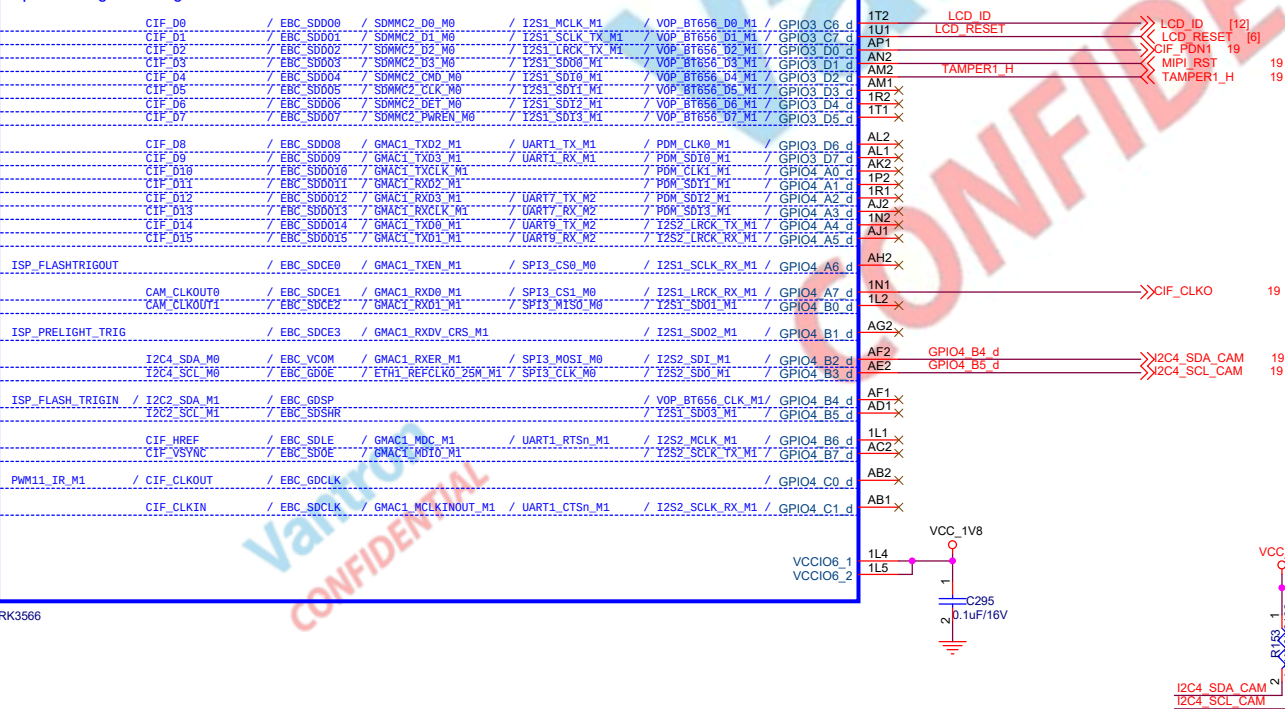


MIPI CSI RX



VCCI06 Domain

Operating Voltage=1.8V/3.3V

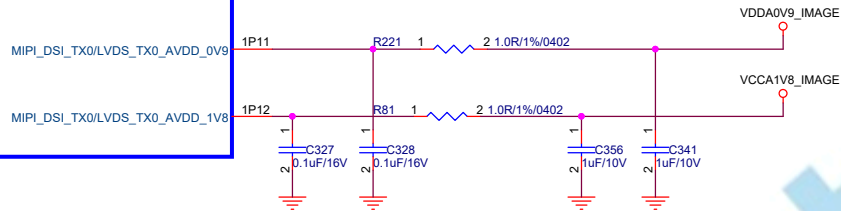


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U12R

MIPI DSI TX0/LVDS TX0

MIPI_DSI_TX0_D0p/LVDS_TX0_D0p AP30 MIPI_TX_D0P << MIPI_TX_D0P [11]
 MIPI_DSI_TX0_D0n/LVDS_TX0_D0n AR30 MIPI_TX_D0N << MIPI_TX_D0N [11]
 MIPI_DSI_TX0_D1p/LVDS_TX0_D1p AR29 MIPI_TX_D1P << MIPI_TX_D1P [11]
 MIPI_DSI_TX0_D1n/LVDS_TX0_D1n AP29 MIPI_TX_D1N << MIPI_TX_D1N [11]
 MIPI_DSI_TX0_D2p/LVDS_TX0_D2p AP27 MIPI_TX_D2P << MIPI_TX_D2P [11]
 MIPI_DSI_TX0_D2n/LVDS_TX0_D2n AR27 MIPI_TX_D2N << MIPI_TX_D2N [11]
 MIPI_DSI_TX0_D3p/LVDS_TX0_D3p AR26 MIPI_TX_D3P << MIPI_TX_D3P [11]
 MIPI_DSI_TX0_D3n/LVDS_TX0_D3n AP26 MIPI_TX_D3N << MIPI_TX_D3N [11]
 MIPI_DSI_TX0_CLKp/LVDS_TX0_CLKp 1V16 MIPI_TX_CLKP << MIPI_TX_CLKP [11]
 MIPI_DSI_TX0_CLKn/LVDS_TX0_CLKn 1V16 MIPI_TX_CLKN << MIPI_TX_CLKN [11]

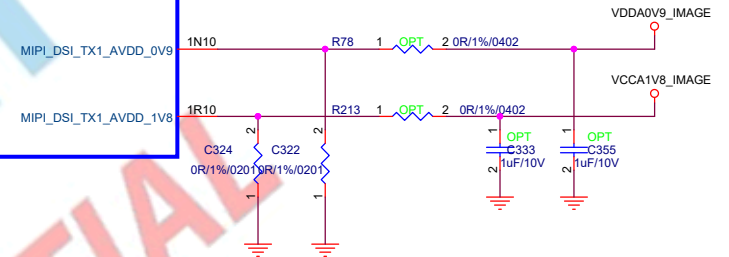


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U12S

MIPI DSI TX1

MIPI_DSI_TX1_D0p AR24 MIPI_DSI_TX1_D0p
 MIPI_DSI_TX1_D0n AR24 MIPI_DSI_TX1_D0n
 MIPI_DSI_TX1_D1p AR23 MIPI_DSI_TX1_D1p
 MIPI_DSI_TX1_D1n AP23 MIPI_DSI_TX1_D1n
 MIPI_DSI_TX1_D2p AP21 MIPI_DSI_TX1_D2p
 MIPI_DSI_TX1_D2n AR21 MIPI_DSI_TX1_D2n
 MIPI_DSI_TX1_D3p AR20 MIPI_DSI_TX1_D3p
 MIPI_DSI_TX1_D3n AP20 MIPI_DSI_TX1_D3n
 MIPI_DSI_TX1_CLKp 1V11 MIPI_DSI_TX1_CLKp
 MIPI_DSI_TX1_CLKn 1V11 MIPI_DSI_TX1_CLKn

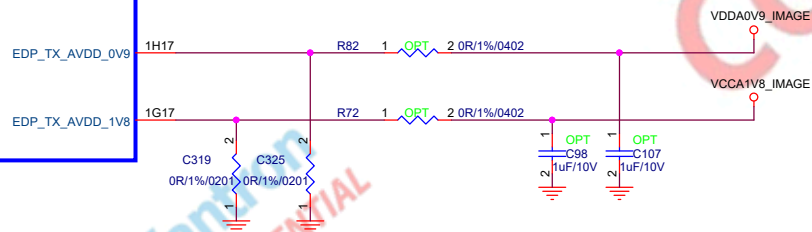


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U12T

eDP_TX

EDP_TX_D0p J37
 EDP_TX_D0n J38
 EDP_TX_D1p K38
 EDP_TX_D1n K37
 EDP_TX_D2p M37
 EDP_TX_D2n M38
 EDP_TX_D3p N37
 EDP_TX_D3n N38
 EDP_TX_AUXp 1H19
 EDP_TX_AUXn 1H20

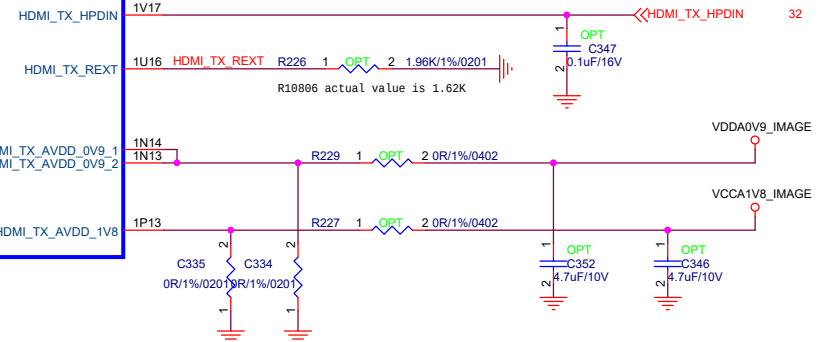


RK3566

U12Q

HDMI2.0 TX

HDMI_TX_D2p AP36
 HDMI_TX_D2n AR36
 HDMI_TX_D1p AR35
 HDMI_TX_D1n AP35
 HDMI_TX_D0p AP33
 HDMI_TX_D0n AR33
 HDMI_TX_CLKp AR32
 HDMI_TX_CLKn AP32



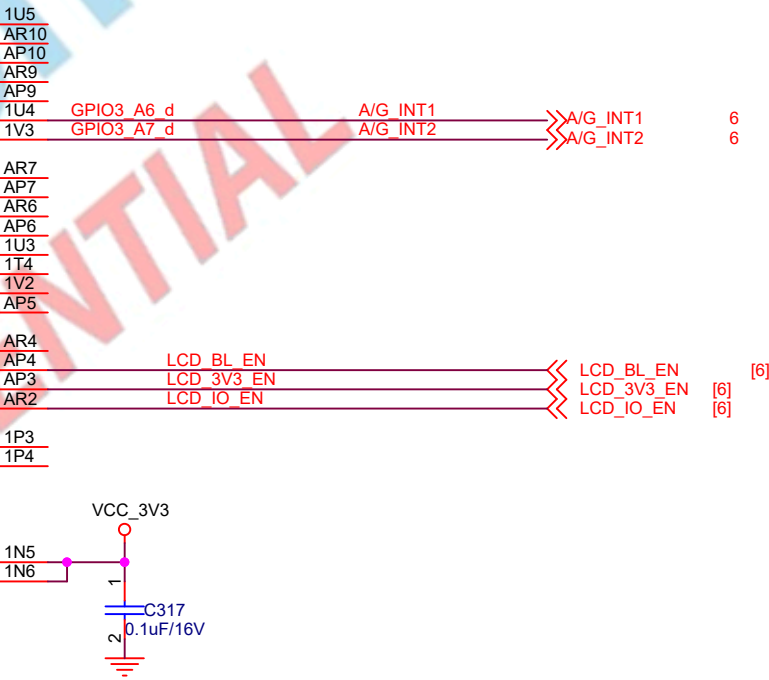
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U12L

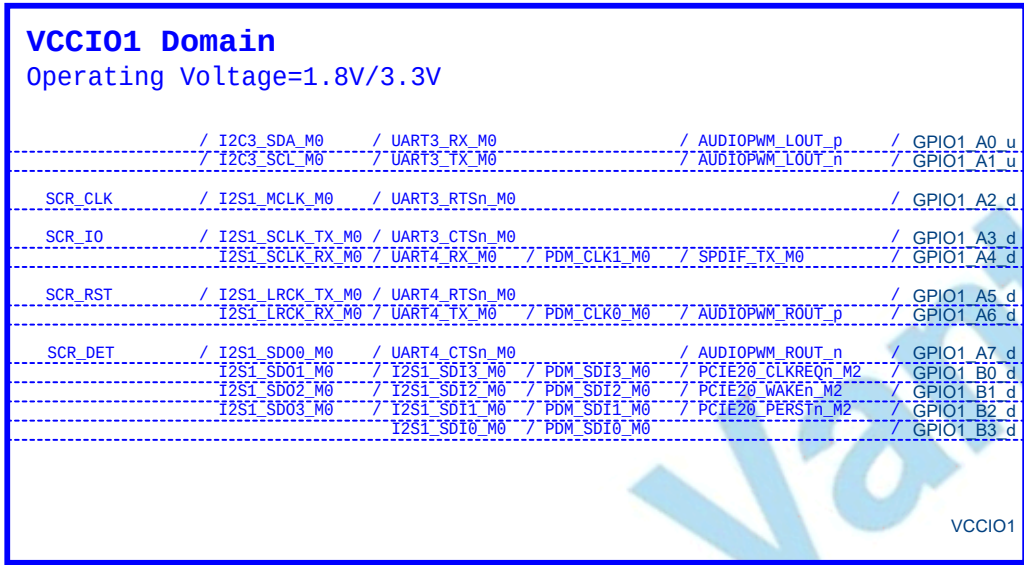
VCCIO5 Domain
Operating Voltage=1.8V/3.3V

VOP_BT1120_D0	/ SPI1_CS0_M1			/ SDMMC2_D0_M1	/ GPIO3_A1_d
VOP_BT1120_D1		/ GMAC1_TXD2_M0	/ I2S3_MCLK_M0	/ SDMMC2_D1_M1	/ GPIO3_A2_d
VOP_BT1120_D2		/ GMAC1_TXD3_M0	/ I2S3_SCLK_M0	/ SDMMC2_D2_M1	/ GPIO3_A3_d
VOP_BT1120_D3		/ GMAC1_RXD2_M0	/ I2S3_LRCK_M0	/ SDMMC2_D3_M1	/ GPIO3_A4_d
VOP_BT1120_D4		/ GMAC1_RXD3_M0	/ I2S3_SDO_M0	/ SDMMC2_CMD_M1	/ GPIO3_A5_d
VOP_BT1120_CLK		/ GMAC1_TXCLK_M0	/ I2S3_SDI_M0	/ SDMMC2_CLK_M1	/ GPIO3_A6_d
VOP_BT1120_D5		/ GMAC1_RXCLK_M0		/ SDMMC2_DET_M1	/ GPIO3_A7_d
VOP_BT1120_D6	/ ETH1_REFCLK0_25M_M0			/ SDMMC2_PWREN_M1	/ GPIO3_B0_d
PWM8_M0	/ VOP_BT1120_D7	/ GMAC1_RXD0_M0	/ UART4_RX_M1		/ GPIO3_B1_d
PWM9_M0	/ VOP_BT1120_D8	/ GMAC1_RXD1_M0	/ UART4_TX_M1		/ GPIO3_B2_d
VOP_BT1120_D9	/ I2C5_SCL_M0	/ GMAC1_RXDV_CRS_M0		/ PDM_SDI0_M2	/ GPIO3_B3_d
VOP_BT1120_D10	/ I2C5_SDA_M0	/ GMAC1_RXER_M0		/ PDM_SDI1_M2	/ GPIO3_B4_d
PWM10_M0	/ VOP_BT1120_D11	/ I2C3_SCL_M1	/ GMAC1_TXD0_M0		/ GPIO3_B5_d
PWM11_IR_M0	/ VOP_BT1120_D12	/ I2C3_SDA_M1	/ GMAC1_TXD1_M0		/ GPIO3_B6_d
PWM12_M0		/ GMAC1_TXEN_M0	/ UART3_TX_M1	/ PDM_SDI2_M2	/ GPIO3_B7_d
PWM13_M0		/ GMAC1_MCLKINOUT_M0	/ UART3_RX_M1	/ PDM_SDI3_M2	/ GPIO3_C0_d
VOP_BT1120_D13	/ SPI1_MOSI_M1		/ PCIE20_PERSTn_M1	/ I2S1_SD02_M2	/ GPIO3_C1_d
VOP_BT1120_D14	/ SPI1_MISO_M1		/ UART5_TX_M1	/ I2S1_SD03_M2	/ GPIO3_C2_d
VOP_BT1120_D15	/ SPI1_CLK_M1		/ UART5_RX_M1	/ I2S1_SCLK_RX_M2	/ GPIO3_C3_d
PWM14_M0	/ VOP_PWM_M1	/ GMAC1_MDC_M0	/ UART7_TX_M1	/ PDM_CLK1_M2	/ GPIO3_C4_d
PWM15_IR_M0	/ SPDIF_TX_M1	/ GMAC1_MDIO_M0	/ UART7_RX_M1	/ I2S1_LRCK_RX_M2	/ GPIO3_C5_d

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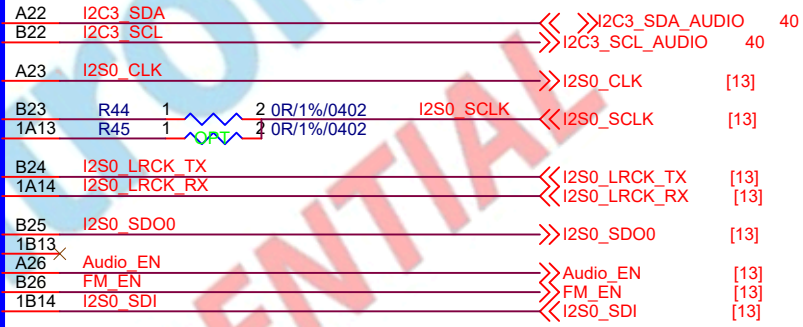
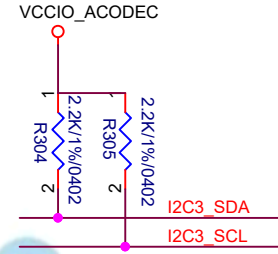


U12H



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VCCIO1



VCCIO_ACODEC

