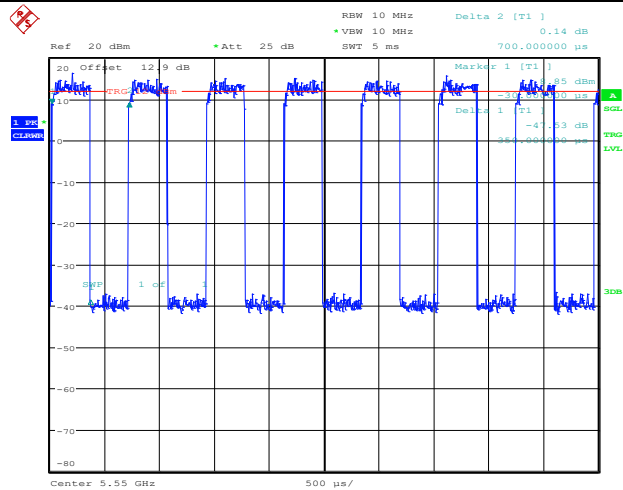
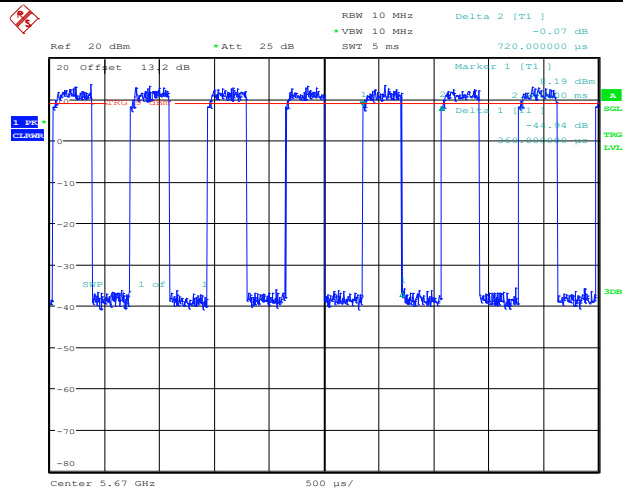


11AC40SISO_Ant1_5550



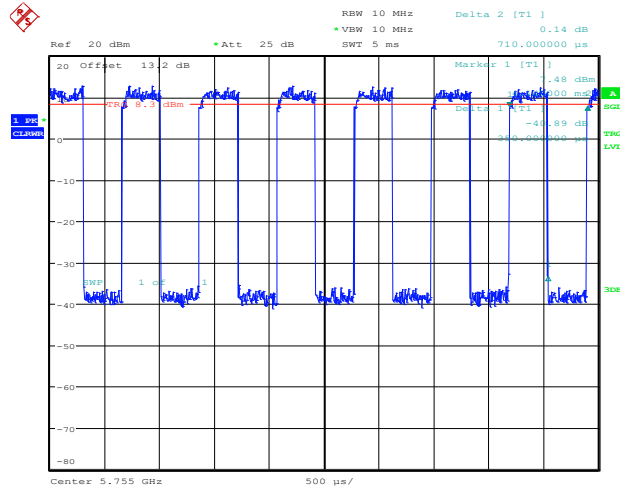
Date: 20.AUG.2024 16:42:37

11AC40SISO_Ant1_5670



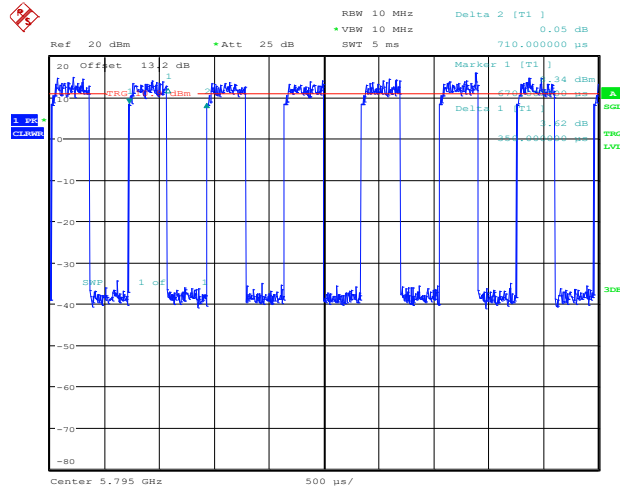
Date: 20.AUG.2024 16:45:00

11AC40SISO_Ant1_5755



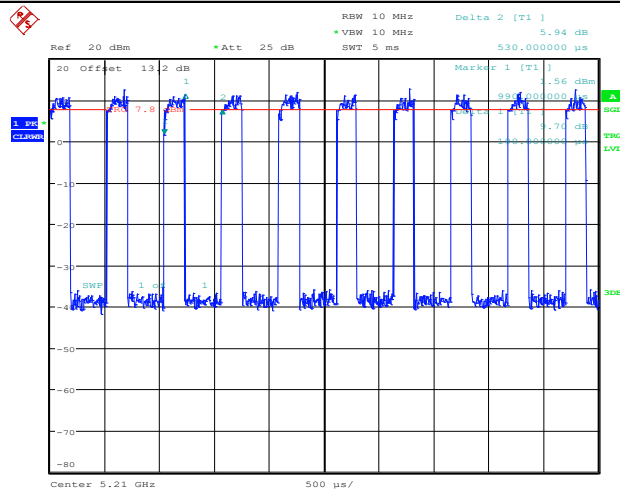
Date: 20.AUG.2024 16:48:54

11AC40SISO_Ant1_5795

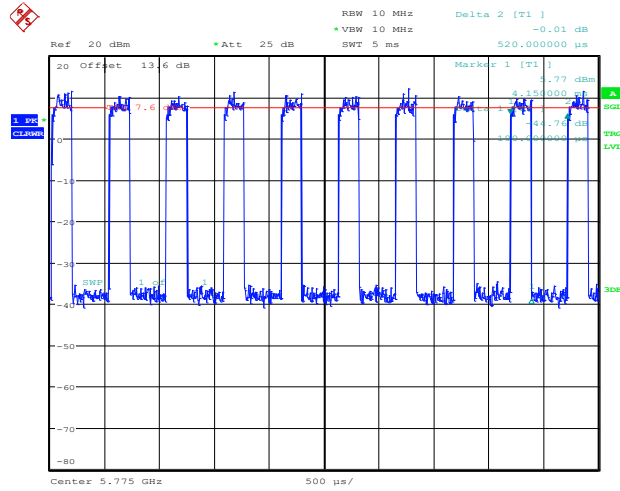


Date: 20.AUG.2024 16:52:03

11AC80SISO_Ant1_5210

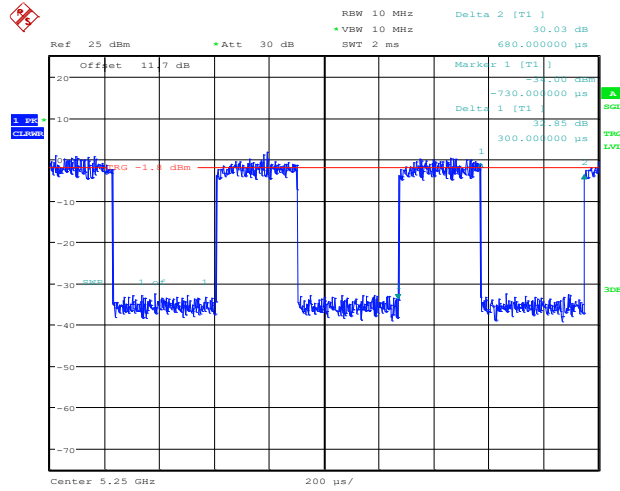


Date: 20.AUG.2024 16:55:27



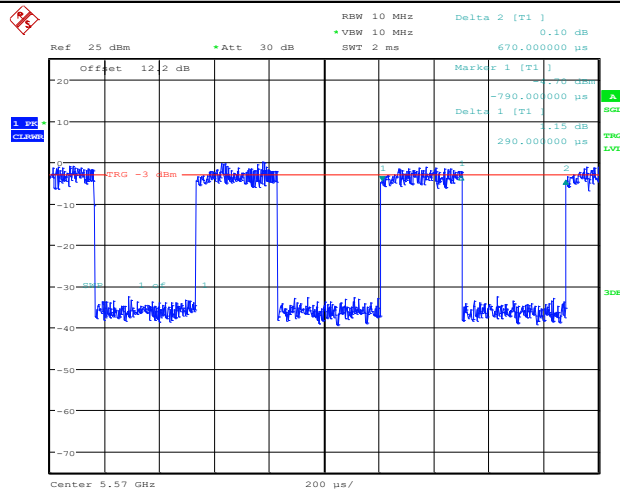
Date: 20.AUG.2024 17:04:10

11AC160SISO_Ant1_5250



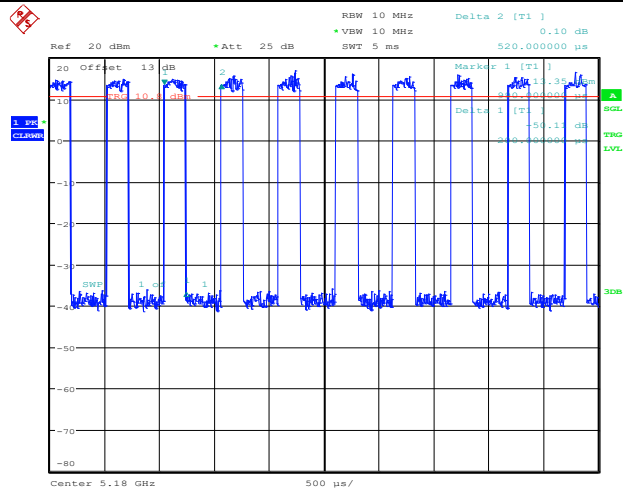
Date: 29.SEP.2024 09:33:02

11AC160SISO_Ant1_5570



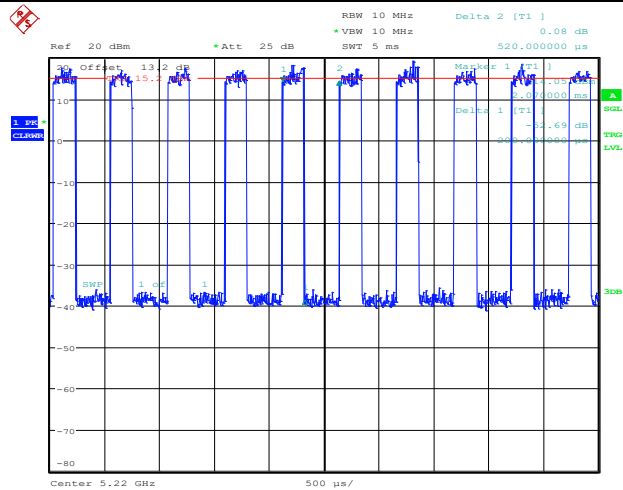
Date: 29.SEP.2024 09:36:05

11AX20SISO_Ant1_5180



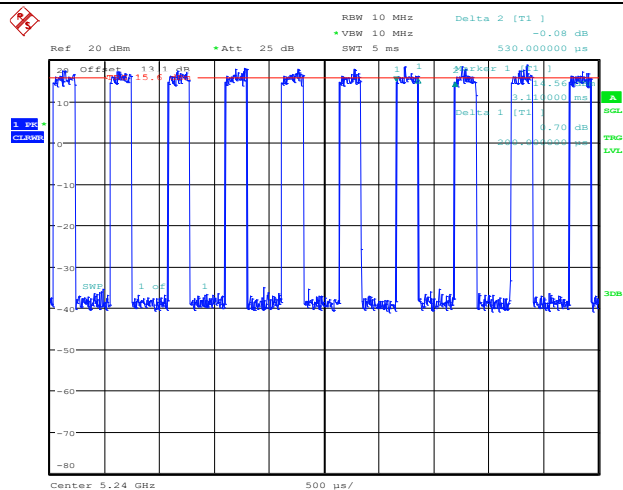
Date: 20.AUG.2024 17:09:18

11AX20SISO_Ant1_5220



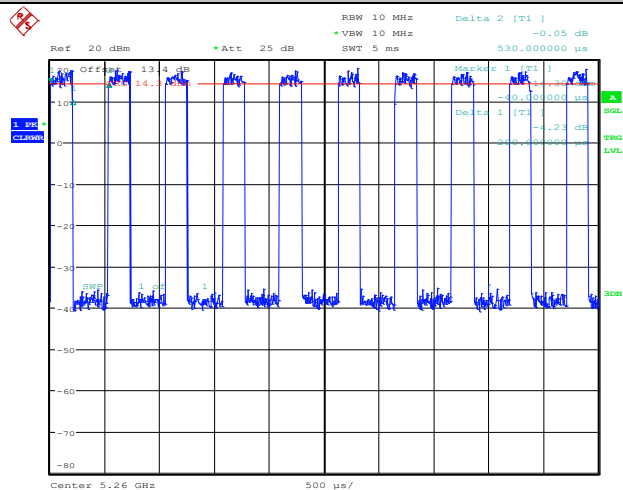
Date: 20.AUG.2024 17:12:58

11AX20SISO_Ant1_5240



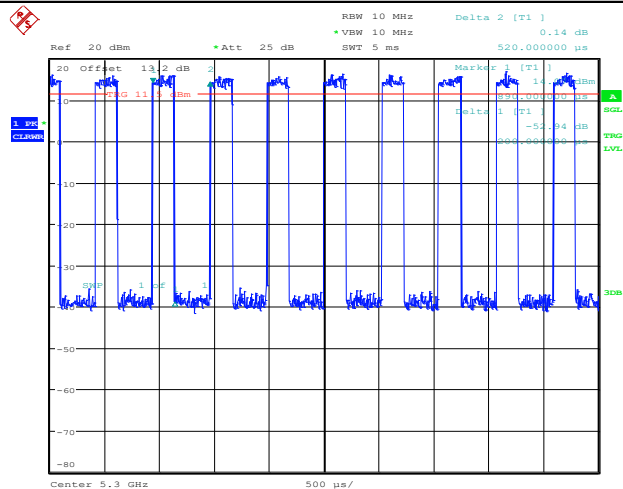
Date: 20.AUG.2024 17:15:15

11AX20SISO_Ant1_5260



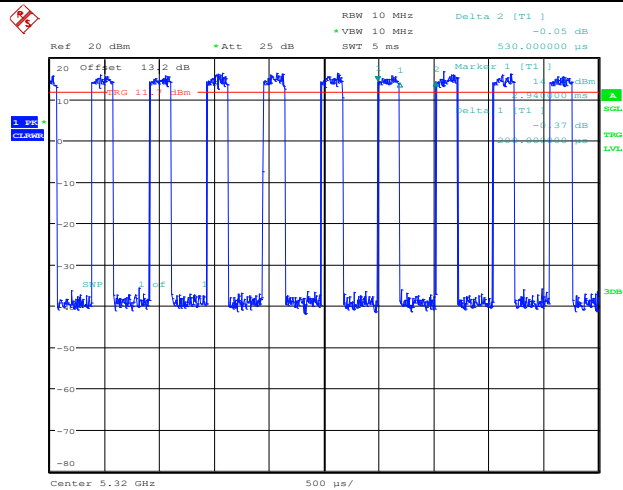
Date: 20.AUG.2024 17:17:30

11AX20SISO_Ant1_5300



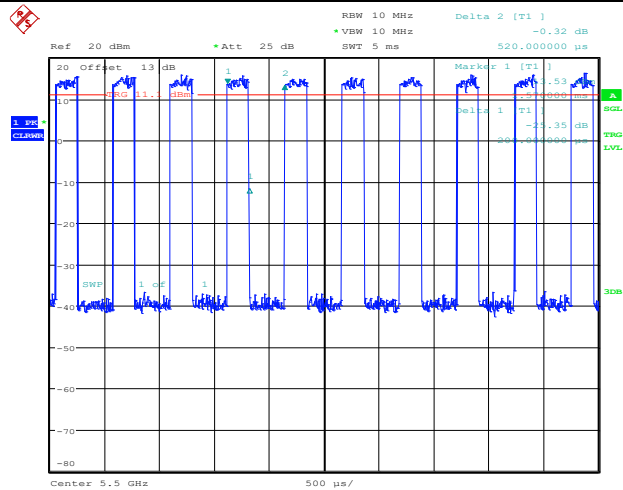
Date: 20.AUG.2024 17:19:49

11AX20SISO_Ant1_5320



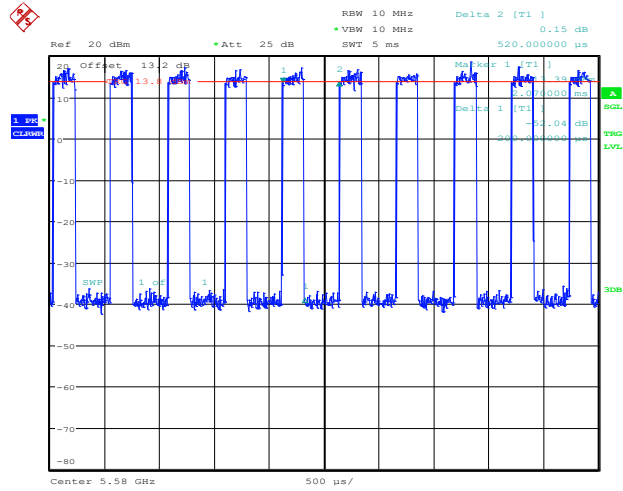
Date: 20.AUG.2024 17:22:00

11AX20SISO_Ant1_5500



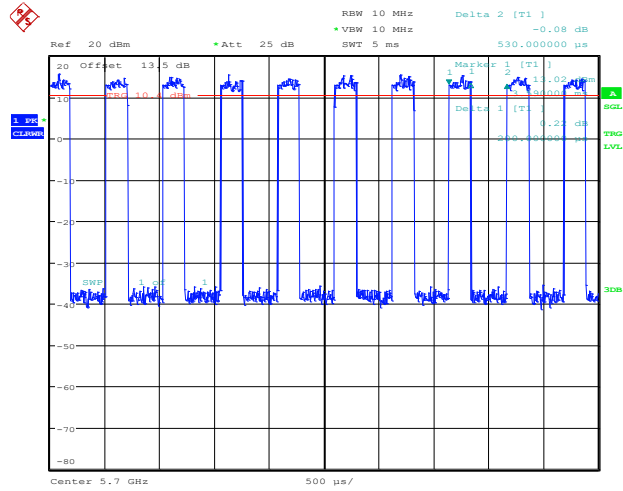
Date: 20.AUG.2024 17:25:34

11AX20SISO_Ant1_5580



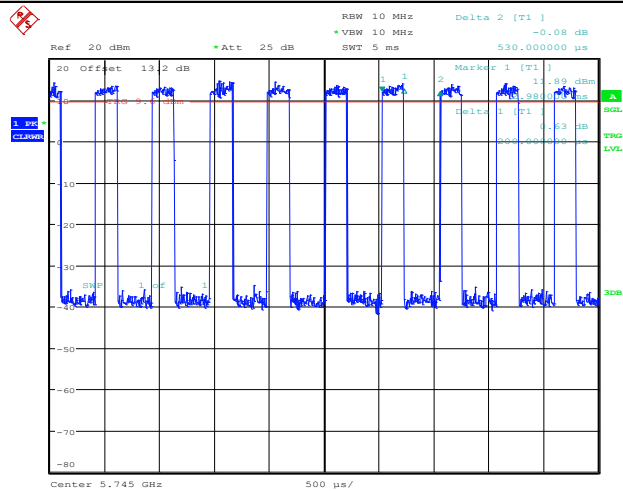
Date: 20.AUG.2024 17:28:15

11AX20SISO_Ant1_5700



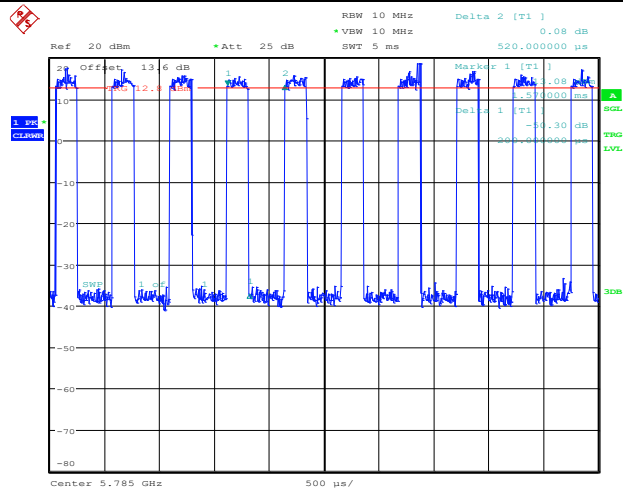
Date: 20.AUG.2024 17:31:33

11AX20SISO_Ant1_5745



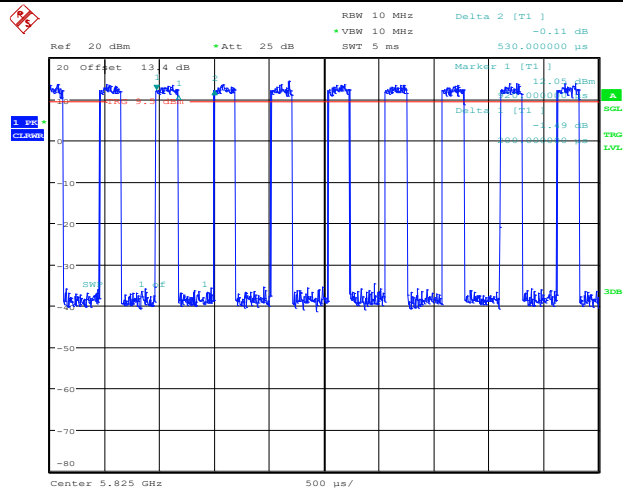
Date: 20.AUG.2024 17:34:52

11AX20SISO_Ant1_5785



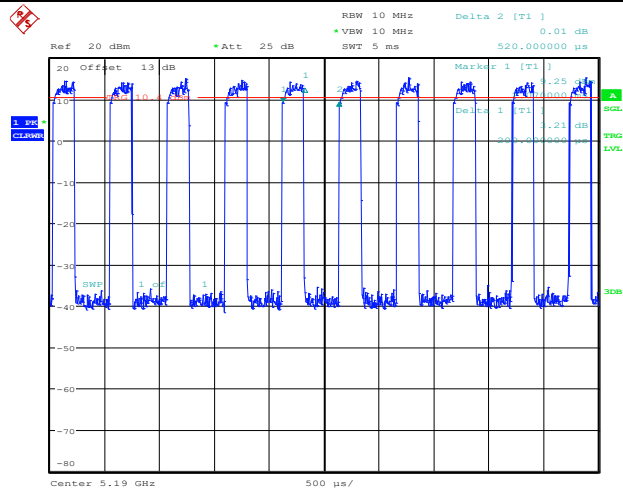
Date: 20.AUG.2024 17:38:03

11AX20SISO_Ant1_5825



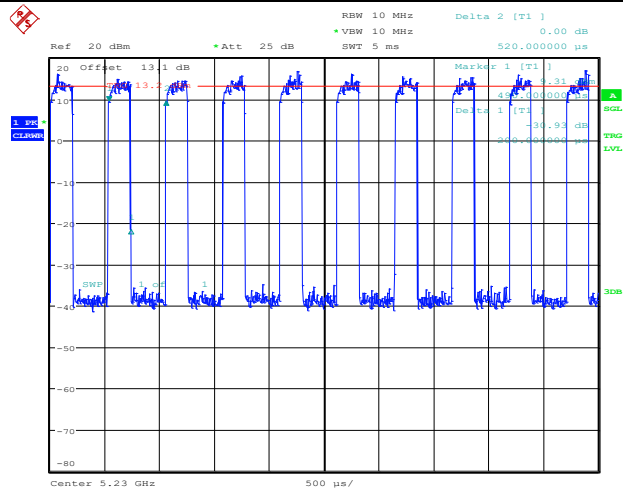
Date: 20.AUG.2024 17:41:11

11AX40SISO_Ant1_5190



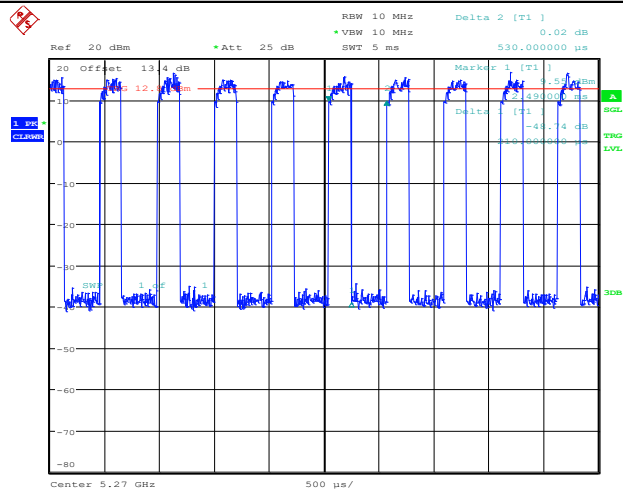
Date: 20.AUG.2024 17:44:13

11AX40SISO_Ant1_5230



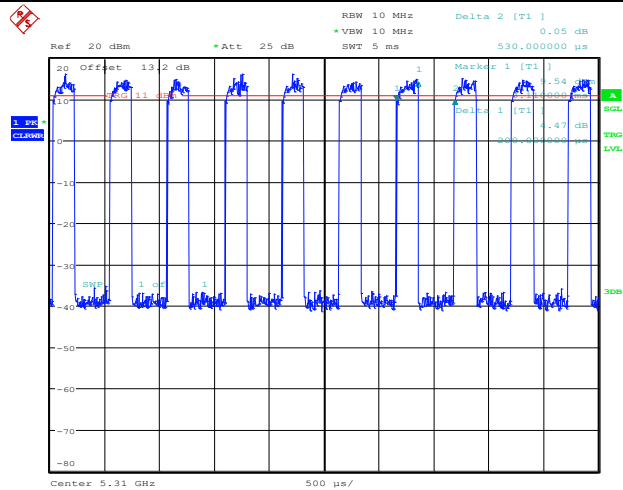
Date: 20.AUG.2024 17:47:16

11AX40SISO_Ant1_5270



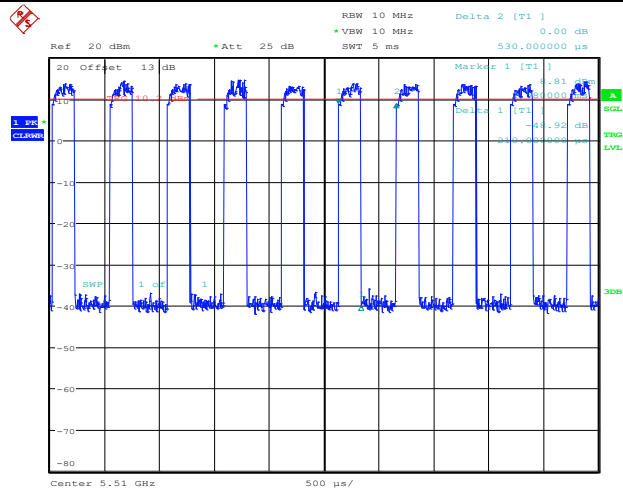
Date: 20.AUG.2024 17:49:35

11AX40SISO_Ant1_5310



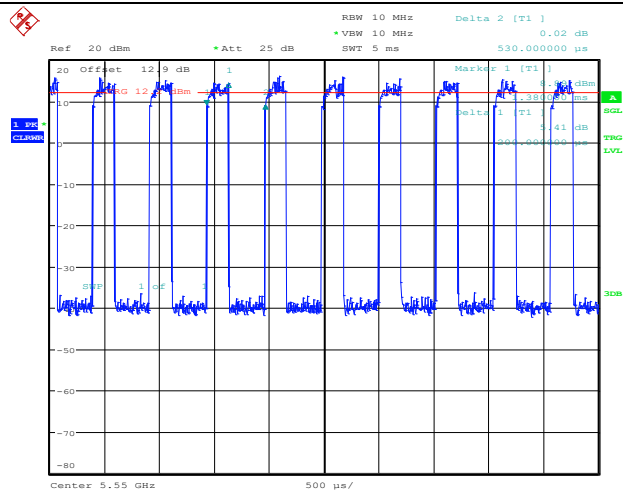
Date: 20.AUG.2024 17:51:47

11AX40SISO_Ant1_5510



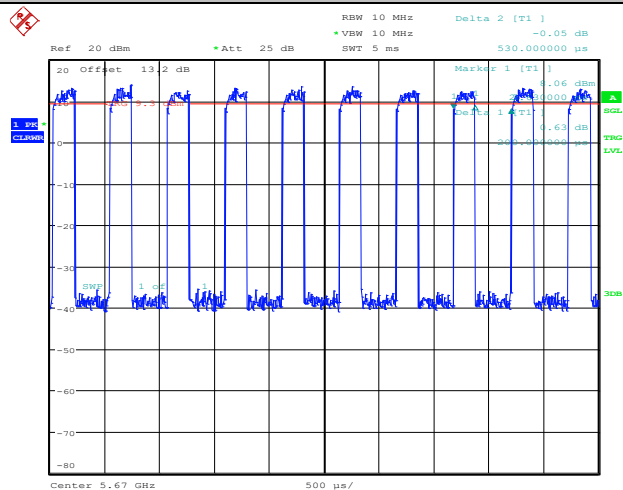
Date: 20.AUG.2024 17:54:56

11AX40SISO_Ant1_5550



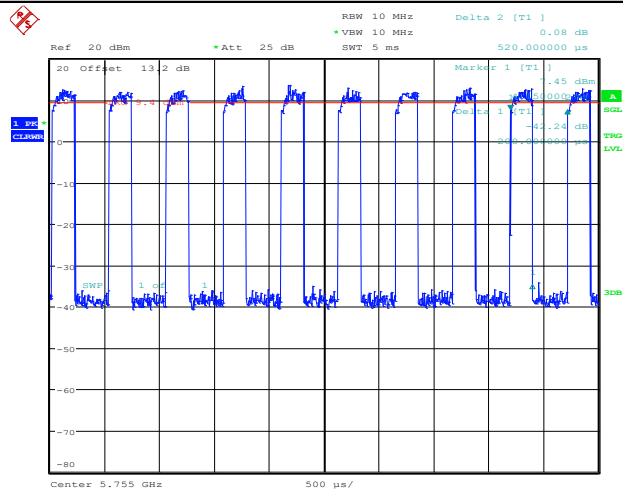
Date: 20.AUG.2024 17:57:42

11AX40SISO_Ant1_5670

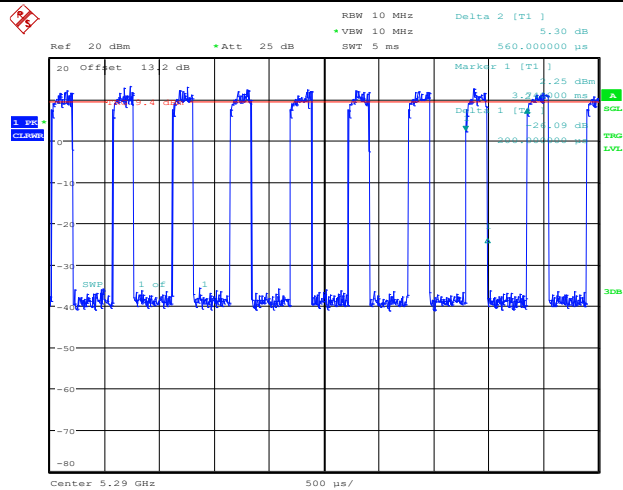


Date: 20.AUG.2024 18:00:05

11AX40SISO_Ant1_5755

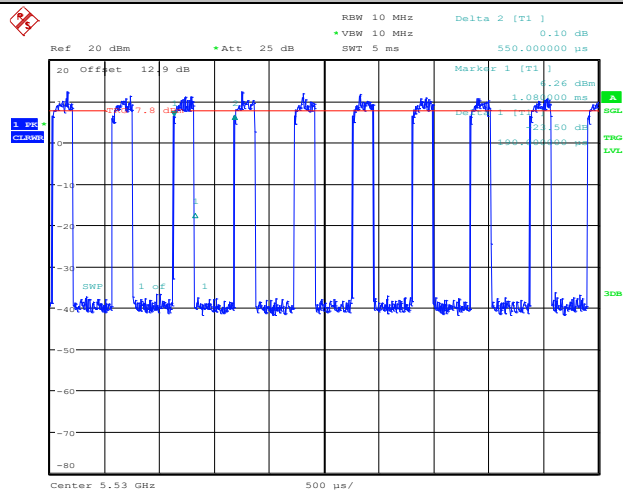


Date: 20.AUG.2024 18:03:34



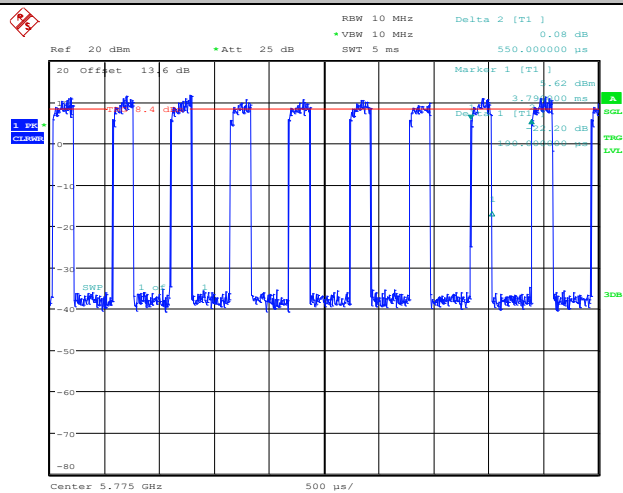
Date: 20.AUG.2024 18:13:22

11AX80SISO_Ant1_5530



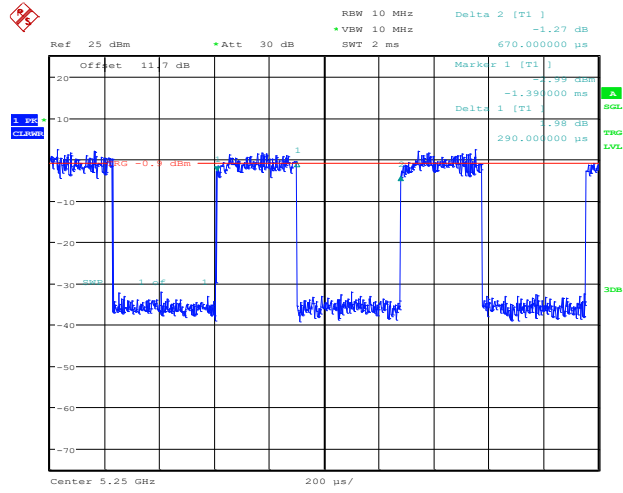
Date: 20.AUG.2024 18:16:59

11AX80SISO_Ant1_5775



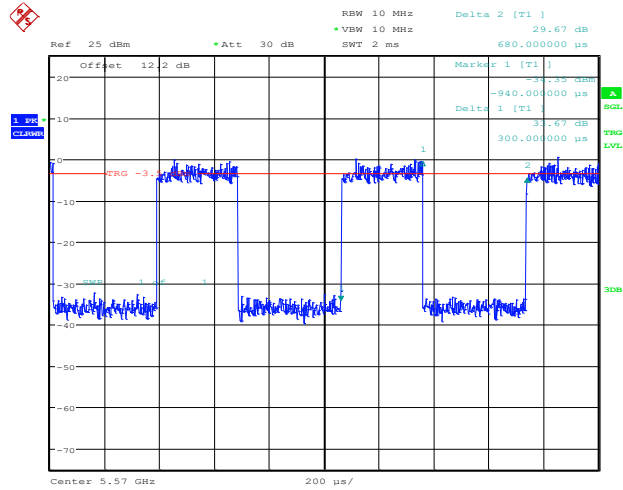
Date: 20.AUG.2024 18:19:59

11AX160SISO_Ant1_5250



Date: 29.SEP.2024 09:46:14

11AX160SISO_Ant1_5570



Date: 29.SEP.2024 09:42:44

Appendix C: Maximum conducted output power

Test Result Channel Power

Test Mode	Antenna	Freq(MHz)	Channel Power [dBm]	Duty Cycle [%]	DC Factor [dBm]	Result [dBm]	Limit [dBm]	Verdict
11A	Ant1	5180	11.50	80.00	0.97	12.47	≤23.98	PASS
		5220	12.54	80.00	0.97	13.51	≤23.98	PASS
		5240	12.75	80.00	0.97	13.72	≤23.98	PASS
		5260	12.74	80.00	0.97	13.71	≤23.98	PASS
		5300	12.35	80.00	0.97	13.32	≤23.98	PASS
		5320	12.42	80.00	0.97	13.39	≤23.98	PASS
		5500	11.58	80.00	0.97	12.55	≤23.98	PASS
		5580	11.87	79.55	0.99	12.86	≤23.98	PASS
		5700	11.02	79.55	0.99	12.01	≤23.98	PASS
		5745	10.05	80.00	0.97	11.02	≤30.00	PASS
		5785	11.66	80.00	0.97	12.63	≤30.00	PASS
		5825	10.29	79.55	0.99	11.28	≤30.00	PASS
11N20SI SO	Ant1	5180	10.36	78.92	1.03	11.39	≤23.98	PASS
		5220	11.55	78.31	1.06	12.61	≤23.98	PASS
		5240	11.80	78.92	1.03	12.83	≤23.98	PASS
		5260	11.72	78.92	1.03	12.75	≤23.98	PASS
		5300	11.34	78.92	1.03	12.37	≤23.98	PASS
		5320	11.27	78.92	1.03	12.30	≤23.98	PASS
		5500	10.64	78.92	1.03	11.67	≤23.98	PASS
		5580	10.98	78.92	1.03	12.01	≤23.98	PASS
		5700	9.78	78.79	1.04	10.82	≤23.98	PASS
		5745	8.98	78.92	1.03	10.01	≤30.00	PASS
		5785	10.55	78.92	1.03	11.58	≤30.00	PASS
		5825	9.23	78.31	1.06	10.29	≤30.00	PASS
11N40SI SO	Ant1	5190	10.23	64.36	1.91	12.14	≤23.98	PASS
		5230	10.93	64.36	1.91	12.84	≤23.98	PASS
		5270	10.98	64.36	1.91	12.89	≤23.98	PASS
		5310	10.89	64.36	1.91	12.80	≤23.98	PASS
		5510	10.15	64.36	1.91	12.06	≤23.98	PASS
		5550	10.39	64.36	1.91	12.30	≤23.98	PASS
		5670	9.30	64.36	1.91	11.21	≤23.98	PASS
		5755	8.73	64.36	1.91	10.64	≤30.00	PASS
		5795	9.59	64.36	1.91	11.50	≤30.00	PASS
11AC20S ISO	Ant1	5180	9.62	65.71	1.82	11.44	≤23.98	PASS
		5220	10.76	65.38	1.85	12.61	≤23.98	PASS
		5240	11.07	65.71	1.82	12.89	≤23.98	PASS

		5260	11.01	65.38	1.85	12.86	≤23.98	PASS
		5300	10.65	65.71	1.82	12.47	≤23.98	PASS
		5320	10.61	65.38	1.85	12.46	≤23.98	PASS
		5500	9.88	65.38	1.85	11.73	≤23.98	PASS
		5580	10.13	65.38	1.85	11.98	≤23.98	PASS
		5700	8.98	65.38	1.85	10.83	≤23.98	PASS
		5745	8.15	65.38	1.85	10.00	≤30.00	PASS
		5785	9.97	65.38	1.85	11.82	≤30.00	PASS
		5825	8.36	65.38	1.85	10.21	≤30.00	PASS
11AC40S ISO	Ant1	5190	9.21	49.30	3.07	12.28	≤23.98	PASS
		5230	9.89	49.30	3.07	12.96	≤23.98	PASS
		5270	9.92	49.30	3.07	12.99	≤23.98	PASS
		5310	9.79	49.30	3.07	12.86	≤23.98	PASS
		5510	9.13	49.30	3.07	12.20	≤23.98	PASS
		5550	9.35	50.00	3.01	12.36	≤23.98	PASS
		5670	8.09	50.00	3.01	11.10	≤23.98	PASS
		5755	7.47	49.30	3.07	10.54	≤30.00	PASS
		5795	8.38	49.30	3.07	11.45	≤30.00	PASS
11AC80S ISO	Ant1	5210	7.97	35.85	4.46	12.43	≤23.98	PASS
		5290	8.16	36.54	4.37	12.53	≤23.98	PASS
		5530	7.30	34.62	4.61	11.91	≤23.98	PASS
		5775	6.53	36.54	4.37	10.90	≤30.00	PASS
11AC160 SISO	Ant1	5250_UN II-1	1.01	44.12	3.55	4.56	≤23.98	PASS
		5250_UN II-2A	3.83	44.12	3.55	7.38	≤23.98	PASS
		5570	3.72	43.28	3.64	7.36	≤23.98	PASS
11AX20S ISO	Ant1	5180	7.67	38.46	4.15	11.82	≤23.98	PASS
		5220	8.63	38.46	4.15	12.78	≤23.98	PASS
		5240	8.95	37.74	4.23	13.18	≤23.98	PASS
		5260	8.74	37.74	4.23	12.97	≤23.98	PASS
		5300	8.44	38.46	4.15	12.59	≤23.98	PASS
		5320	8.40	37.74	4.23	12.63	≤23.98	PASS
		5500	7.77	38.46	4.15	11.92	≤23.98	PASS
		5580	8.02	38.46	4.15	12.17	≤23.98	PASS
		5700	6.97	37.74	4.23	11.20	≤23.98	PASS
		5745	6.17	37.74	4.23	10.40	≤30.00	PASS
		5785	7.65	38.46	4.15	11.80	≤30.00	PASS
		5825	6.30	37.74	4.23	10.53	≤30.00	PASS
11AX40S ISO	Ant1	5190	8.35	38.46	4.15	12.50	≤23.98	PASS
		5230	8.93	38.46	4.15	13.08	≤23.98	PASS
		5270	9.03	39.62	4.02	13.05	≤23.98	PASS
		5310	9.00	37.74	4.23	13.23	≤23.98	PASS

		5510	8.15	39.62	4.02	12.17	≤ 23.98	PASS
		5550	8.52	37.74	4.23	12.75	≤ 23.98	PASS
		5670	7.27	37.74	4.23	11.50	≤ 23.98	PASS
		5755	6.67	38.46	4.15	10.82	≤ 30.00	PASS
		5795	7.58	38.46	4.15	11.73	≤ 30.00	PASS
11AX80S ISO	Ant1	5210	8.20	34.55	4.62	12.82	≤ 23.98	PASS
		5290	8.32	35.71	4.47	12.79	≤ 23.98	PASS
		5530	7.52	34.55	4.62	12.14	≤ 23.98	PASS
		5775	6.74	34.55	4.62	11.36	≤ 30.00	PASS
11AX160 SISO	Ant1	5250_UN II-1	0.28	43.28	3.64	3.92	≤ 23.98	PASS
		5250_UN II-2A	2.91	43.28	3.64	6.55	≤ 23.98	PASS
		5570	3.79	44.12	3.55	7.34	≤ 23.98	PASS

Note: The Duty Cycle Factor is compensated in the graph.

Appendix D: Maximum power spectral density

Test Result

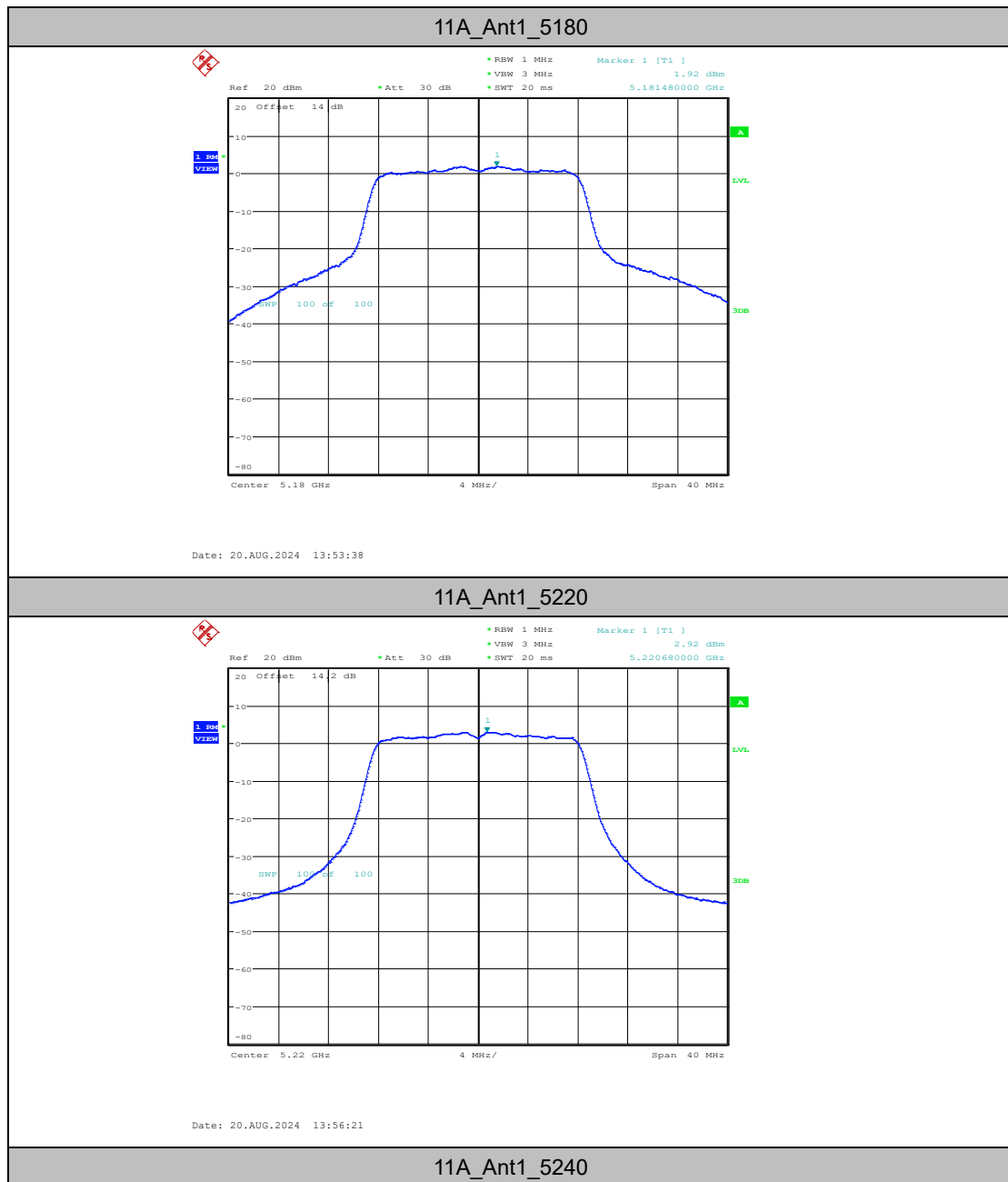
TestMode	Antenna	Freq(MHz)	Result [dBm/MHz]	Limit[dBm/MHz] FCC	Limit[dBm/MHz] ISED	Verdict
11A	Ant1	5180	1.92	≤11.00	≤10.00	PASS
		5220	2.92	≤11.00	≤10.00	PASS
		5240	2.97	≤11.00	≤10.00	PASS
		5260	3.29	≤11.00	≤11.00	PASS
		5300	2.77	≤11.00	≤11.00	PASS
		5320	2.77	≤11.00	≤11.00	PASS
		5500	2.30	≤11.00	≤11.00	PASS
		5580	2.53	≤11.00	≤11.00	PASS
		5700	1.40	≤11.00	≤11.00	PASS
		5745	-2.48	≤30.00	≤30.00	PASS
		5785	-0.77	≤30.00	≤30.00	PASS
		5825	-2.12	≤30.00	≤30.00	PASS
11N20SISO	Ant1	5180	0.71	≤11.00	≤10.00	PASS
		5220	1.74	≤11.00	≤10.00	PASS
		5240	1.83	≤11.00	≤10.00	PASS
		5260	1.95	≤11.00	≤11.00	PASS
		5300	1.60	≤11.00	≤11.00	PASS
		5320	1.45	≤11.00	≤11.00	PASS
		5500	0.91	≤11.00	≤11.00	PASS
		5580	1.34	≤11.00	≤11.00	PASS
		5700	0.09	≤11.00	≤11.00	PASS
		5745	-3.68	≤30.00	≤30.00	PASS
		5785	-1.97	≤30.00	≤30.00	PASS
		5825	-3.31	≤30.00	≤30.00	PASS
11N40SISO	Ant1	5190	-0.66	≤11.00	≤10.00	PASS
		5230	-0.43	≤11.00	≤10.00	PASS
		5270	-0.56	≤11.00	≤11.00	PASS
		5310	-0.37	≤11.00	≤11.00	PASS
		5510	-1.15	≤11.00	≤11.00	PASS
		5550	-0.67	≤11.00	≤11.00	PASS
		5670	-1.79	≤11.00	≤11.00	PASS
		5755	-5.20	≤30.00	≤30.00	PASS
		5795	-4.56	≤30.00	≤30.00	PASS
11AC20SISO	Ant1	5180	0.78	≤11.00	≤10.00	PASS
		5220	2.03	≤11.00	≤10.00	PASS
		5240	2.16	≤11.00	≤10.00	PASS

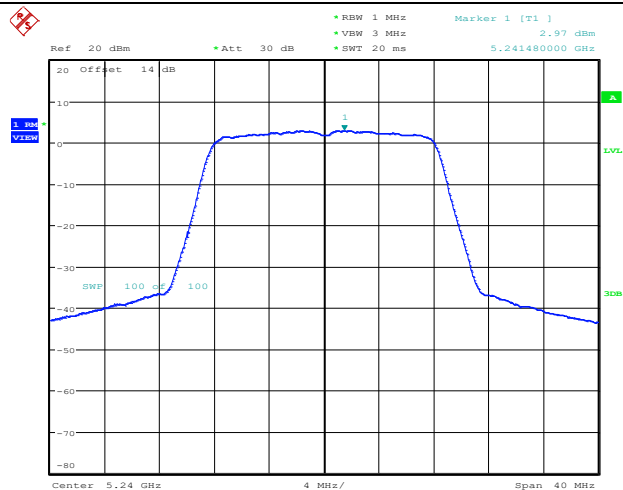
		5260	2.17	≤11.00	≤11.00	PASS
		5300	1.78	≤11.00	≤11.00	PASS
		5320	1.76	≤11.00	≤11.00	PASS
		5500	1.15	≤11.00	≤11.00	PASS
		5580	1.15	≤11.00	≤11.00	PASS
		5700	0.18	≤11.00	≤11.00	PASS
		5745	-3.41	≤30.00	≤30.00	PASS
		5785	-1.98	≤30.00	≤30.00	PASS
		5825	-3.21	≤30.00	≤30.00	PASS
11AC40SISO	Ant1	5190	-0.62	≤11.00	≤10.00	PASS
		5230	-0.18	≤11.00	≤10.00	PASS
		5270	-0.15	≤11.00	≤11.00	PASS
		5310	-0.20	≤11.00	≤11.00	PASS
		5510	-0.70	≤11.00	≤11.00	PASS
		5550	-0.47	≤11.00	≤11.00	PASS
		5670	-1.91	≤11.00	≤11.00	PASS
		5755	-5.33	≤30.00	≤30.00	PASS
		5795	-4.53	≤30.00	≤30.00	PASS
11AC80SISO	Ant1	5210	-3.09	≤11.00	≤10.00	PASS
		5290	-3.12	≤11.00	≤11.00	PASS
		5530	-3.99	≤11.00	≤11.00	PASS
		5775	-7.12	≤30.00	≤30.00	PASS
11AC160SISO	Ant1	5250_UNII-1	-10.61	≤11.00	≤10.00	PASS
		5250_UNII-2A	-8.81	≤11.00	≤11.00	PASS
		5570	-10.16	≤11.00	≤11.00	PASS
11AX20SISO	Ant1	5180	1.10	≤11.00	≤10.00	PASS
		5220	1.75	≤11.00	≤10.00	PASS
		5240	2.64	≤11.00	≤10.00	PASS
		5260	2.12	≤11.00	≤11.00	PASS
		5300	1.67	≤11.00	≤11.00	PASS
		5320	1.56	≤11.00	≤11.00	PASS
		5500	1.14	≤11.00	≤11.00	PASS
		5580	1.54	≤11.00	≤11.00	PASS
		5700	0.18	≤11.00	≤11.00	PASS
		5745	-3.28	≤30.00	≤30.00	PASS
		5785	-1.58	≤30.00	≤30.00	PASS
		5825	-2.67	≤30.00	≤30.00	PASS
11AX40SISO	Ant1	5190	-0.68	≤11.00	≤10.00	PASS
		5230	0.46	≤11.00	≤10.00	PASS
		5270	0.13	≤11.00	≤11.00	PASS
		5310	-0.02	≤11.00	≤11.00	PASS

		5510	-1.16	≤11.00	≤11.00	PASS
		5550	-0.31	≤11.00	≤11.00	PASS
		5670	-1.60	≤11.00	≤11.00	PASS
		5755	-4.61	≤30.00	≤30.00	PASS
		5795	-3.75	≤30.00	≤30.00	PASS
11AX80SISO	Ant1	5210	-2.75	≤11.00	≤10.00	PASS
		5290	-2.92	≤11.00	≤11.00	PASS
		5530	-3.94	≤11.00	≤11.00	PASS
		5775	-6.90	≤30.00	≤30.00	PASS
11AX160SISO	Ant1	5250_UNII-1	-10.72	≤11.00	≤10.00	PASS
		5250_UNII-2A	-9.70	≤11.00	≤11.00	PASS
		5570	-9.93	≤11.00	≤11.00	PASS

Note: 1.The Result and Limit Unit is dBm/500 kHz in the band 5.725–5.85 GHz.
2.The Duty Cycle Factor and RBW Factor is compensated in the graph.

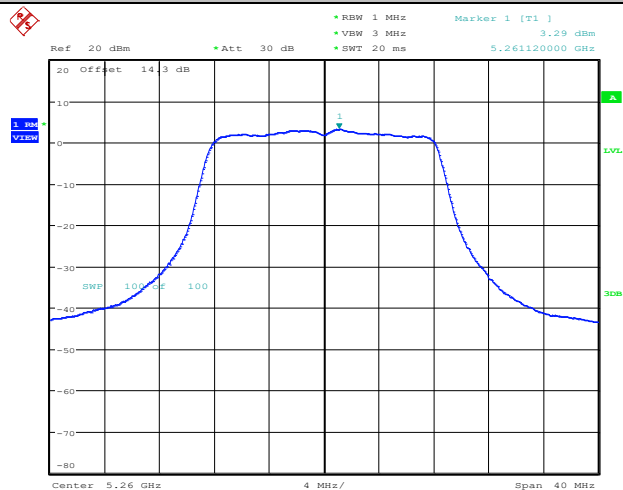
Test Graphs





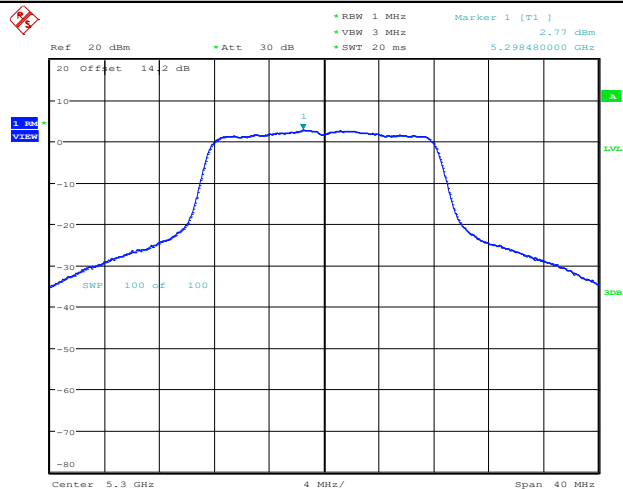
Date: 20.AUG.2024 13:58:41

11A_Ant1_5260

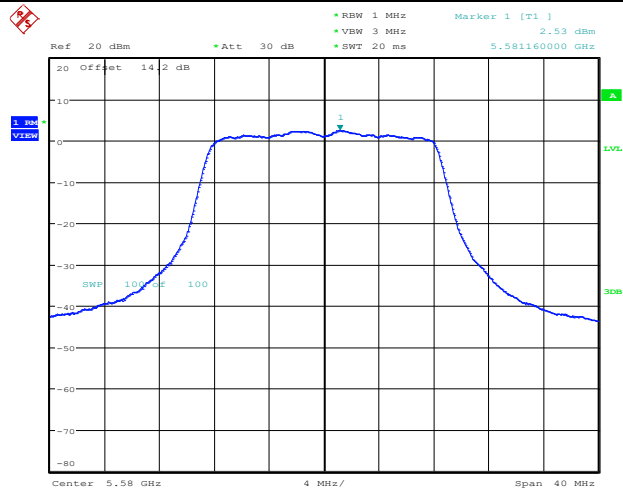


Date: 20.AUG.2024 14:12:38

11A_Ant1_5300

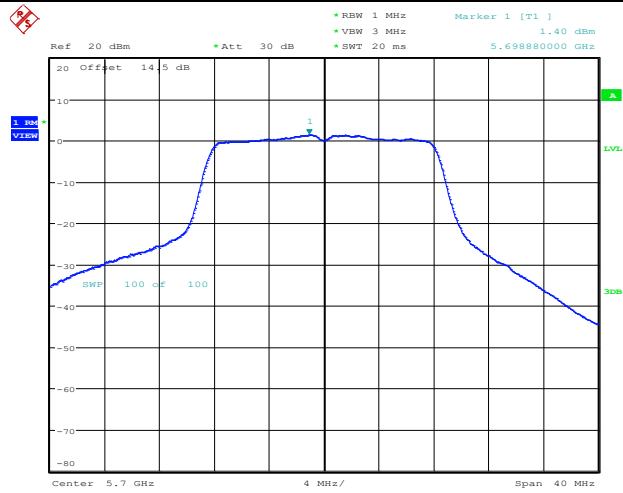


Date: 20.AUG.2024 14:15:53



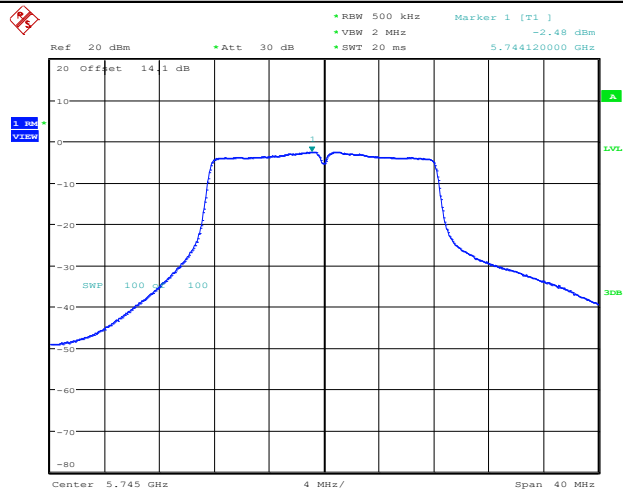
Date: 20.AUG.2024 14:28:11

11A_Ant1_5700



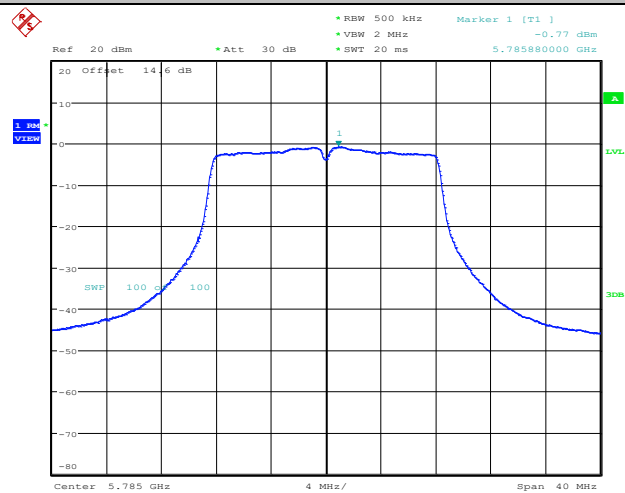
Date: 20.AUG.2024 14:31:03

11A_Ant1_5745



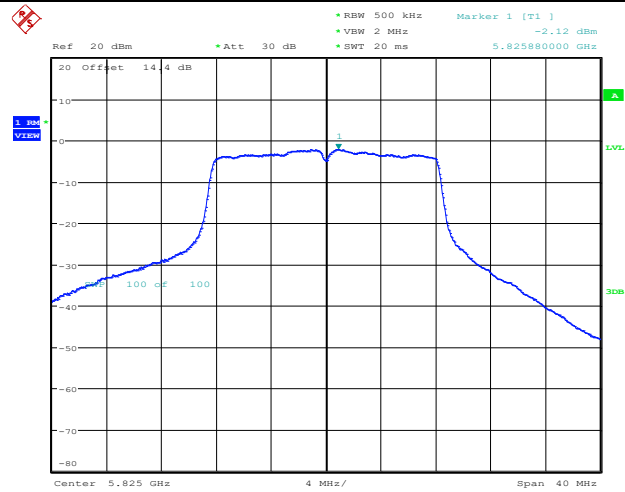
Date: 20.AUG.2024 14:34:49

11A_Ant1_5785



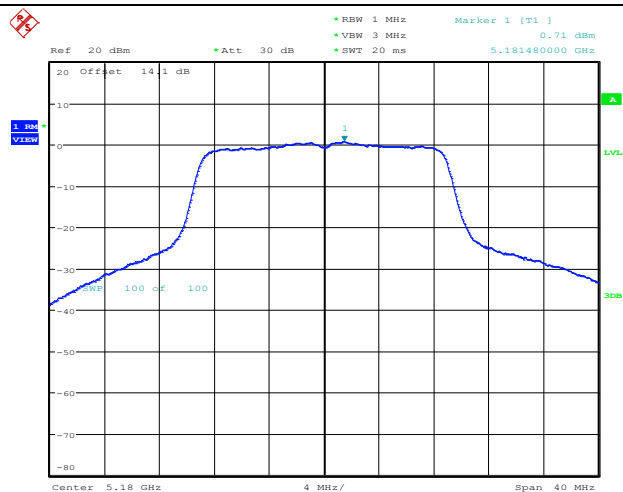
Date: 20.AUG.2024 14:37:52

11A_Ant1_5825



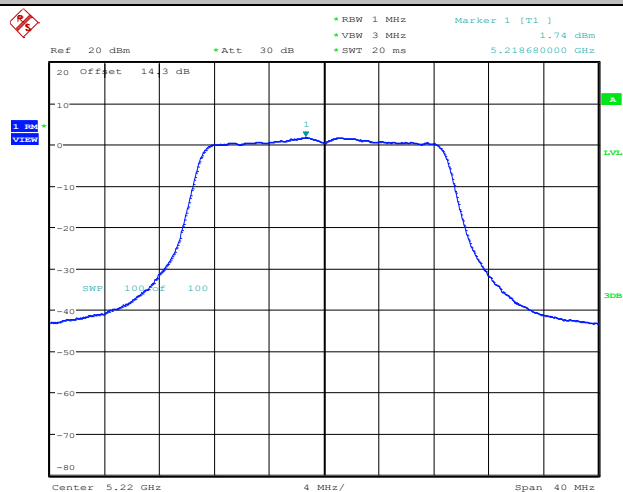
Date: 20.AUG.2024 14:40:44

11N20SISO_Ant1_5180



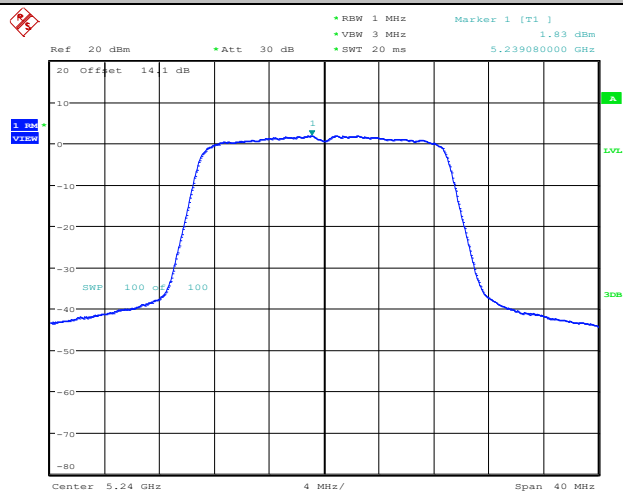
Date: 20.AUG.2024 14:43:38

11N20SISO_Ant1_5220



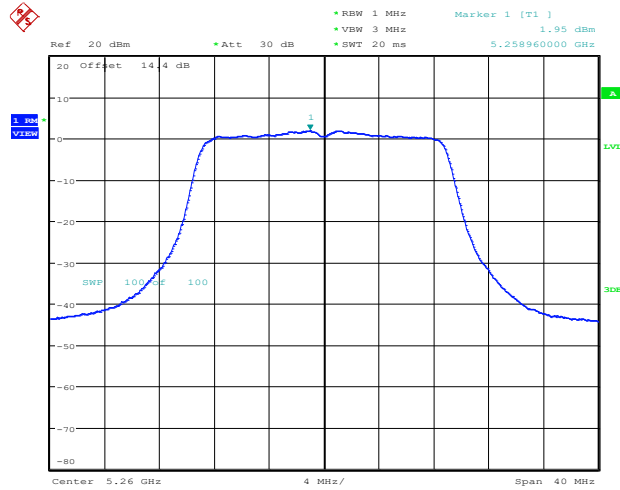
Date: 20.AUG.2024 14:47:15

11N20SISO_Ant1_5240



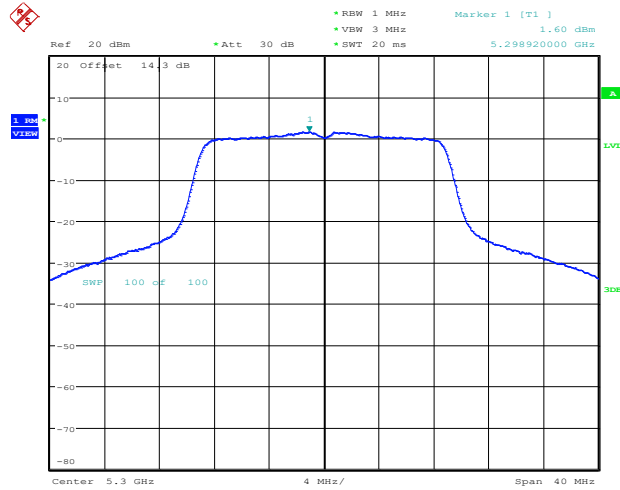
Date: 20.AUG.2024 14:49:27

11N20SISO_Ant1_5260



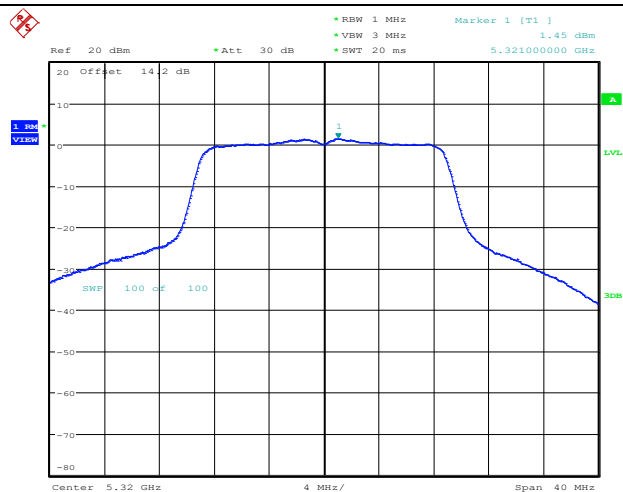
Date: 20.AUG.2024 14:51:48

11N20SISO_Ant1_5300



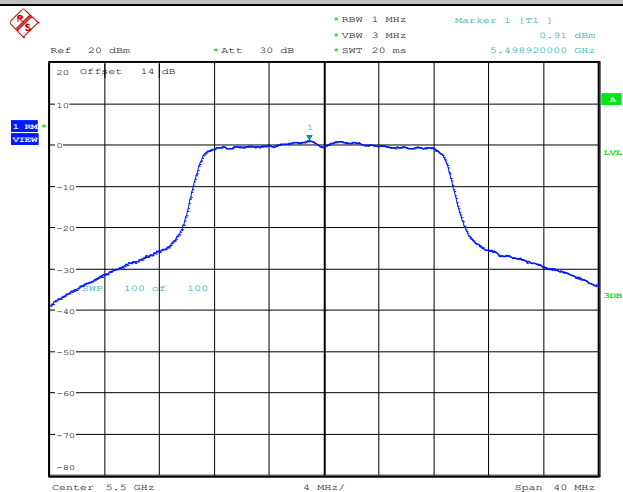
Date: 20.AUG.2024 14:54:05

11N20SISO_Ant1_5320



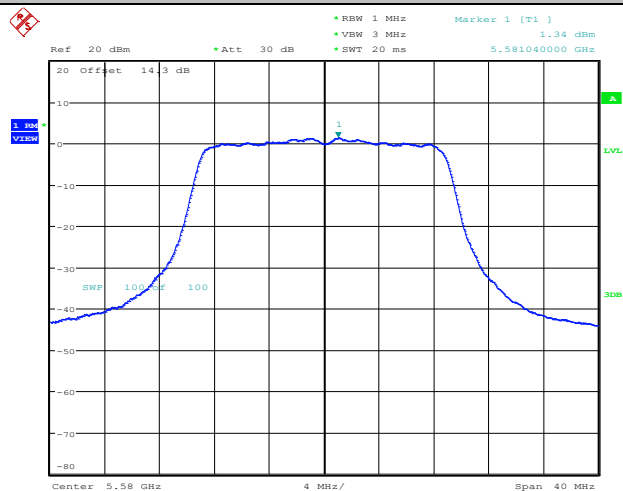
Date: 20.AUG.2024 14:56:23

11N20SISO_Ant1_5500



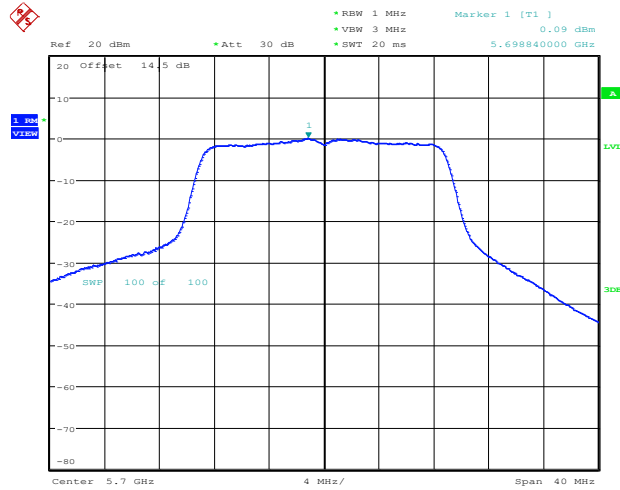
Date: 20.AUG.2024 14:59:38

11N20SISO_Ant1_5580



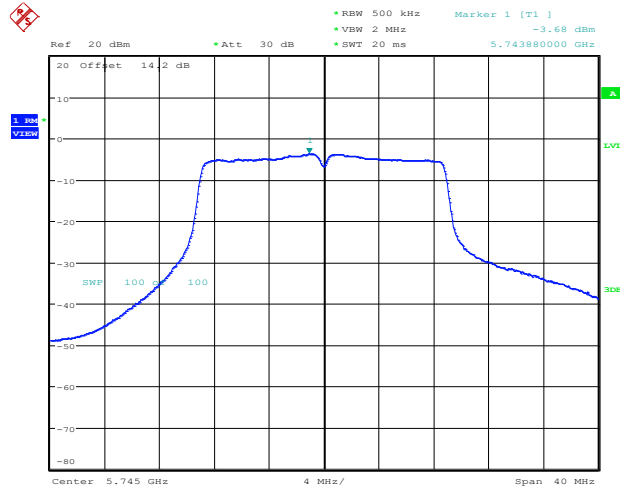
Date: 20.AUG.2024 15:03:20

11N20SISO_Ant1_5700



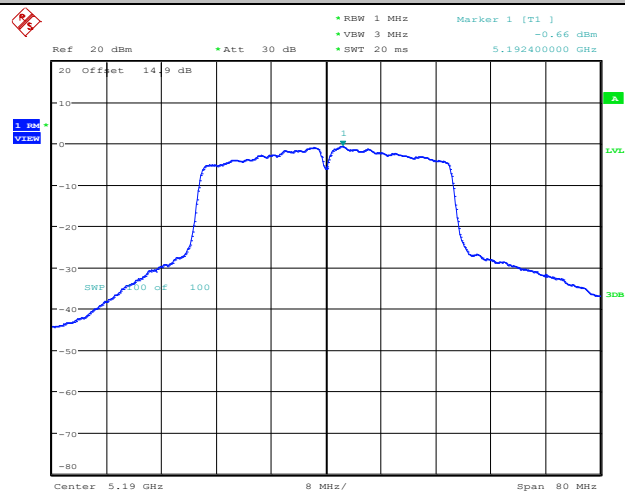
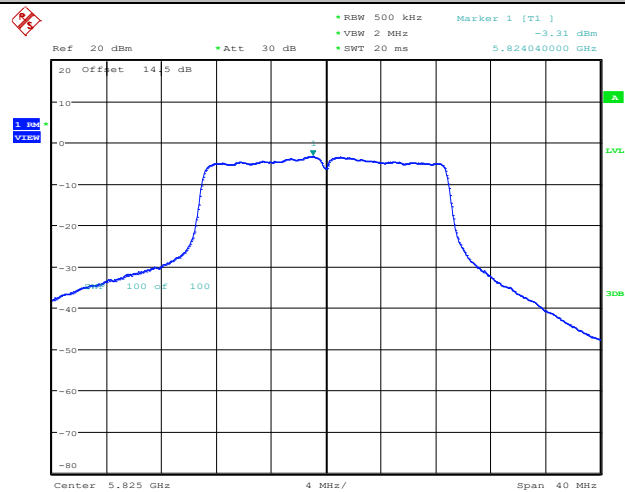
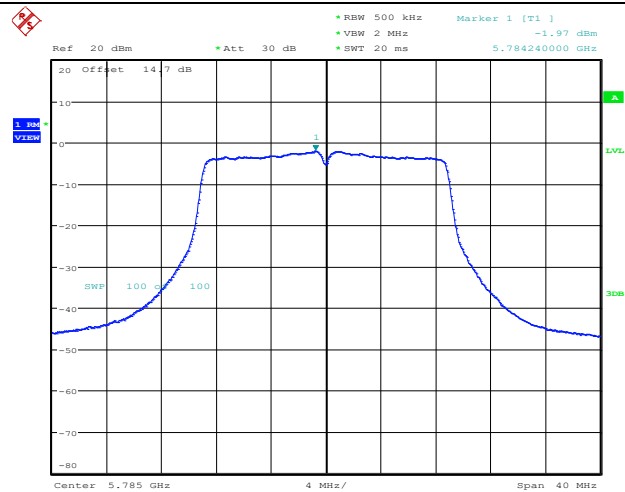
Date: 20.AUG.2024 15:05:39

11N20SISO_Ant1_5745

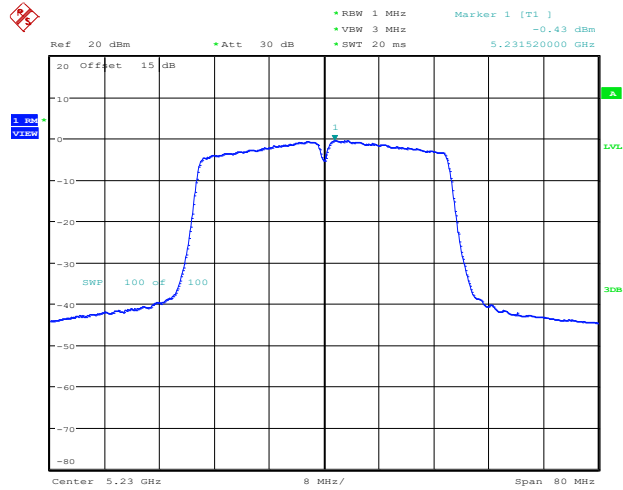


Date: 20.AUG.2024 15:09:59

11N20SISO_Ant1_5785

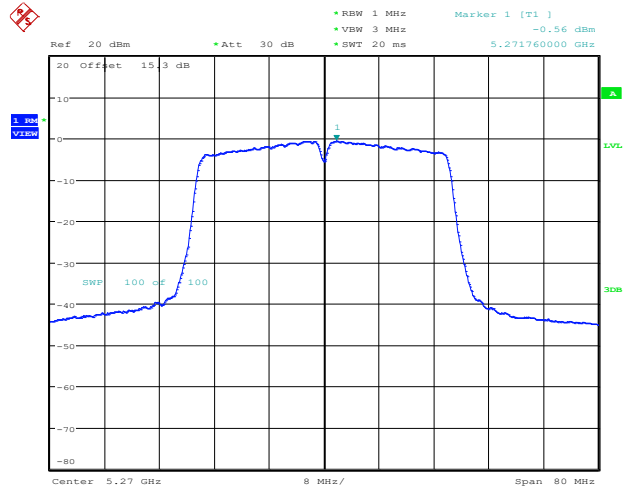


11N40SISO_Ant1_5230



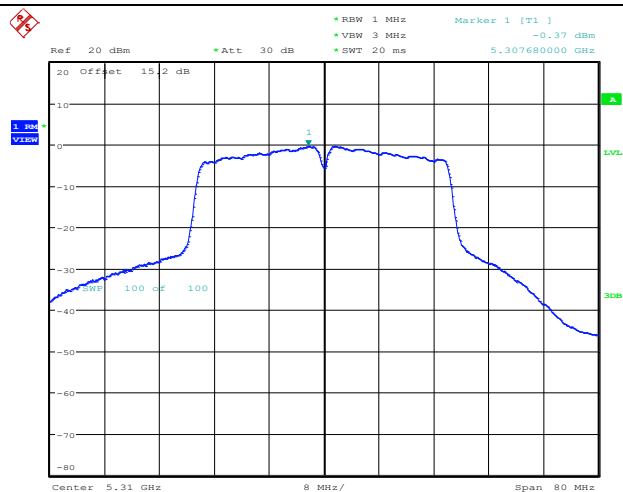
Date: 20.AUG.2024 15:23:18

11N40SISO_Ant1_5270



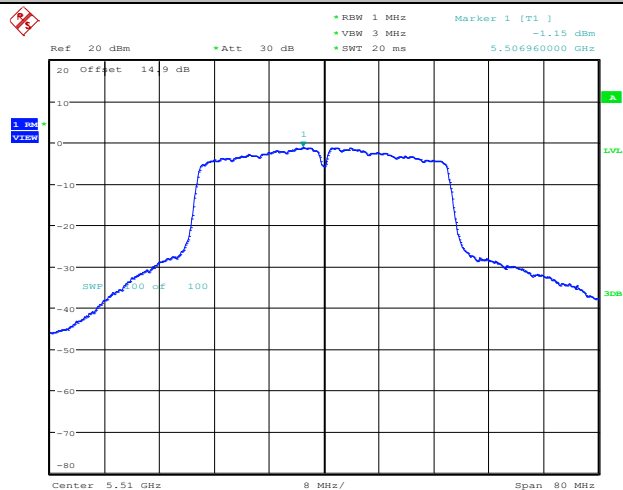
Date: 20.AUG.2024 15:26:01

11N40SISO_Ant1_5310



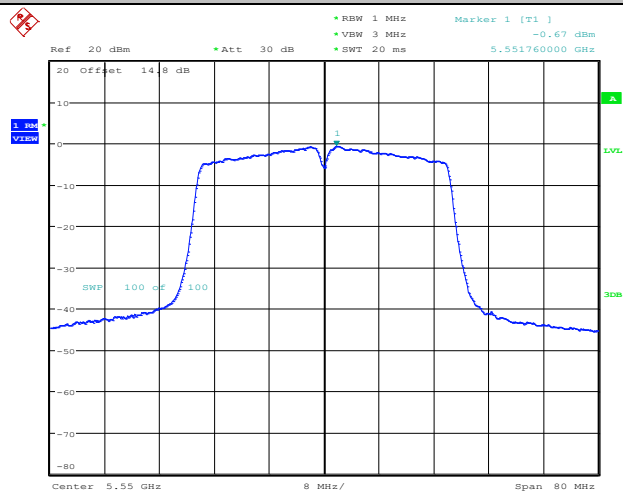
Date: 20.AUG.2024 15:28:41

11N40SISO_Ant1_5510



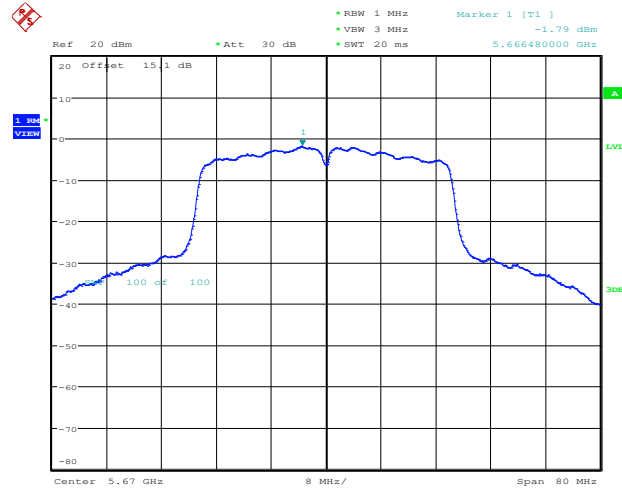
Date: 20.AUG.2024 15:32:01

11N40SISO_Ant1_5550



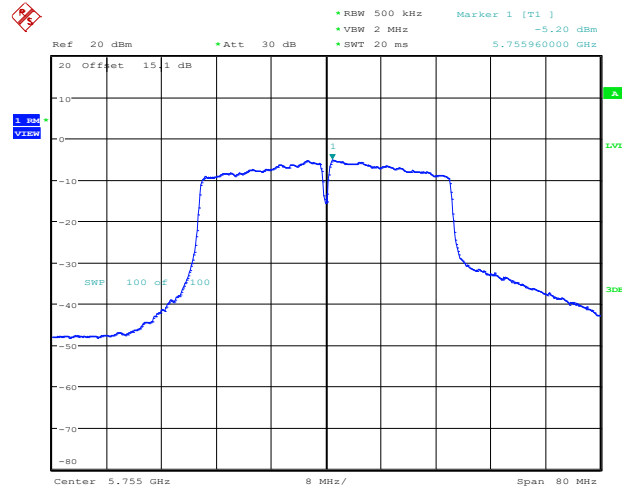
Date: 20.AUG.2024 15:35:09

11N40SISO_Ant1_5670



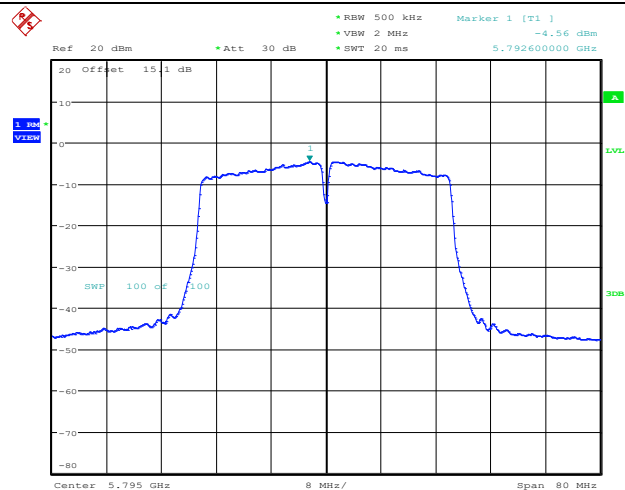
Date: 20.AUG.2024 15:37:30

11N40SISO_Ant1_5755



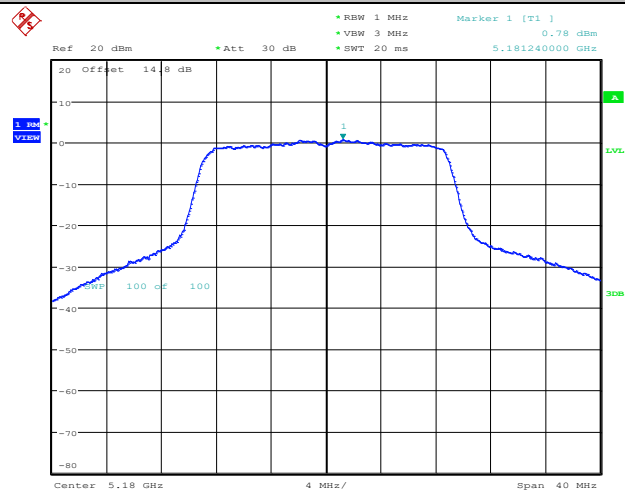
Date: 20.AUG.2024 15:42:01

11N40SISO_Ant1_5795



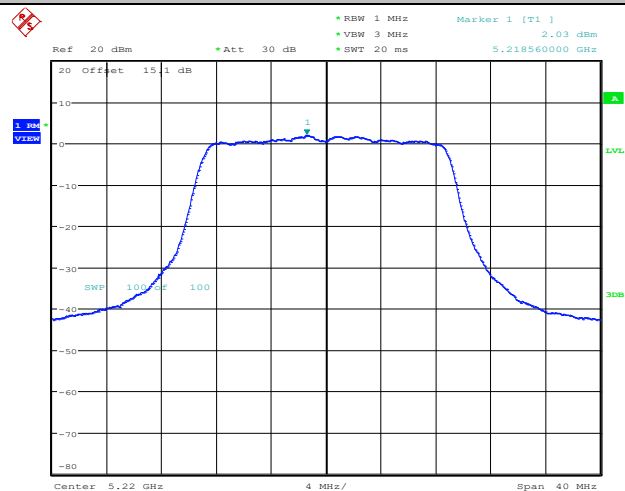
Date: 20.AUG.2024 15:45:34

11AC20SISO_Ant1_5180



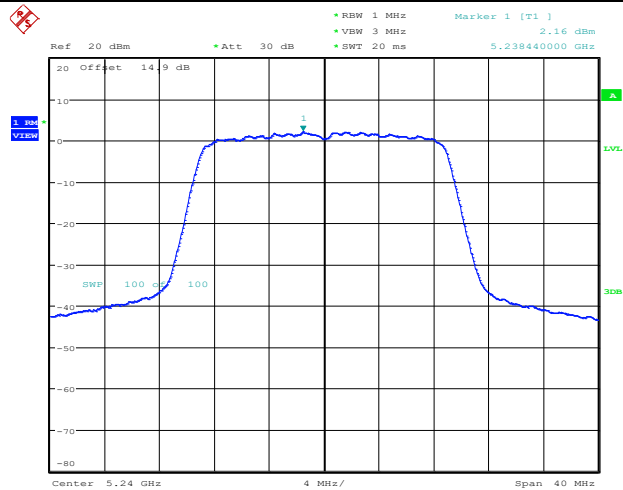
Date: 20.AUG.2024 15:48:49

11AC20SISO_Ant1_5220



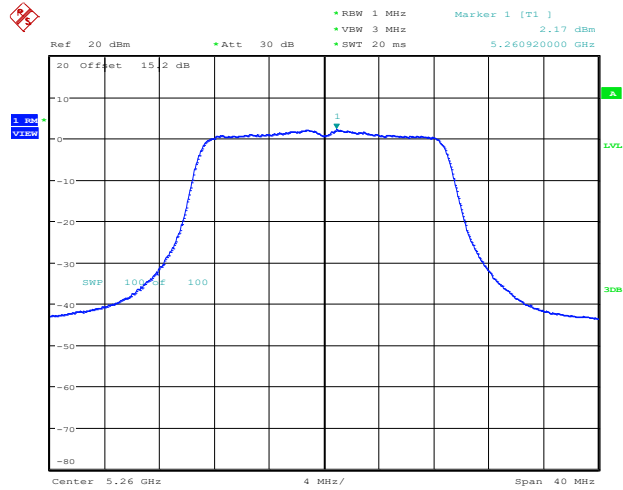
Date: 20.AUG.2024 15:52:26

11AC20SISO_Ant1_5240



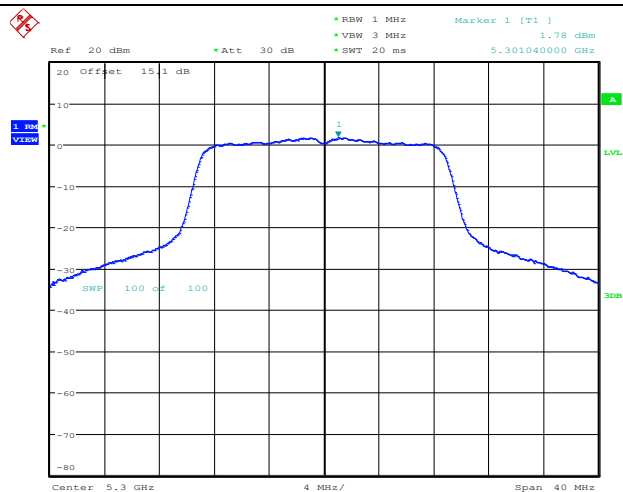
Date: 20.AUG.2024 15:54:44

11AC20SISO_Ant1_5260



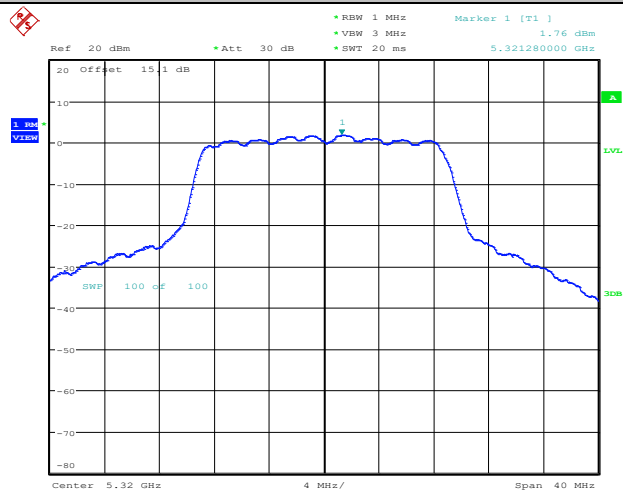
Date: 20.AUG.2024 15:56:58

11AC20SISO_Ant1_5300



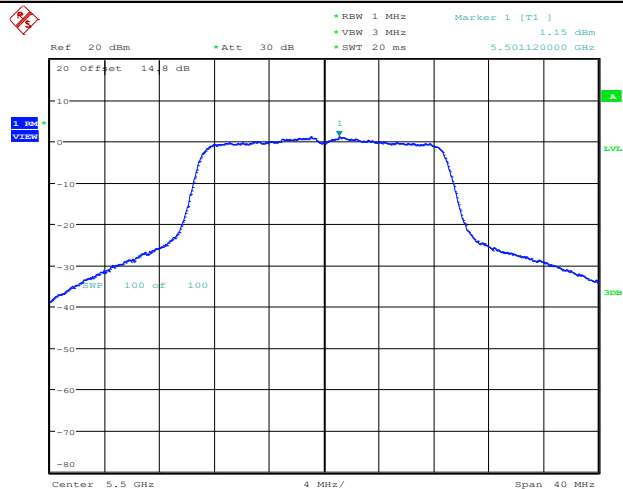
Date: 20.AUG.2024 16:00:10

11AC20SISO_Ant1_5320



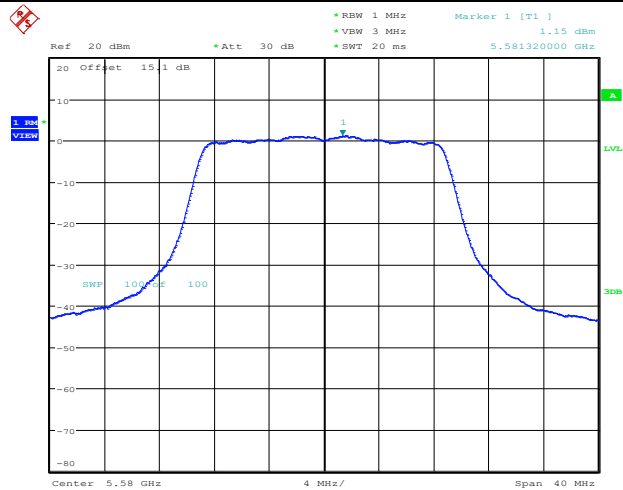
Date: 20.AUG.2024 16:03:01

11AC20SISO_Ant1_5500



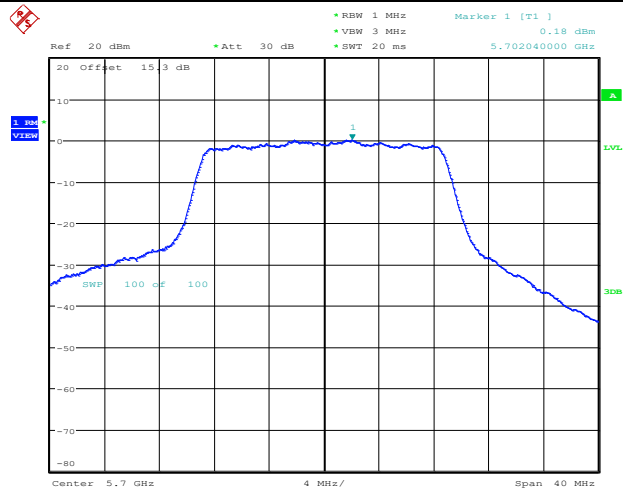
Date: 20.AUG.2024 16:07:03

11AC20SISO_Ant1_5580



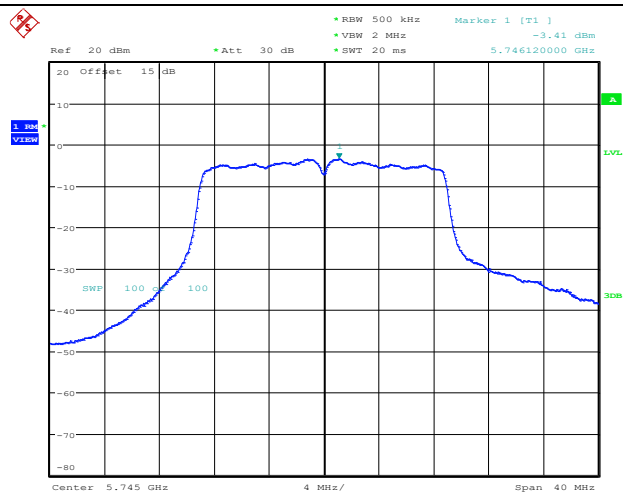
Date: 20.AUG.2024 16:09:58

11AC20SISO_Ant1_5700



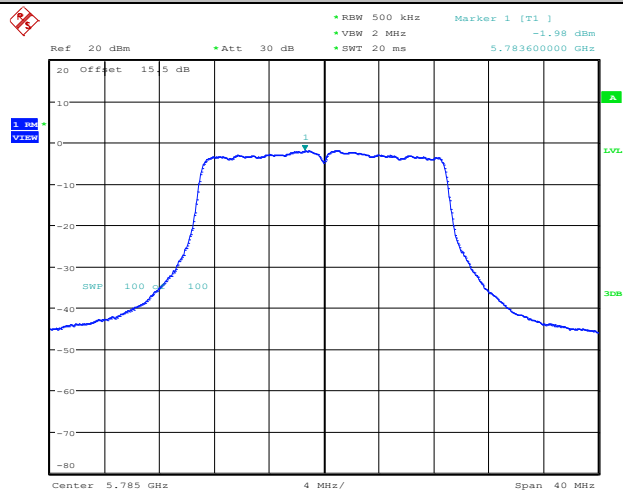
Date: 20.AUG.2024 16:12:56

11AC20SISO_Ant1_5745



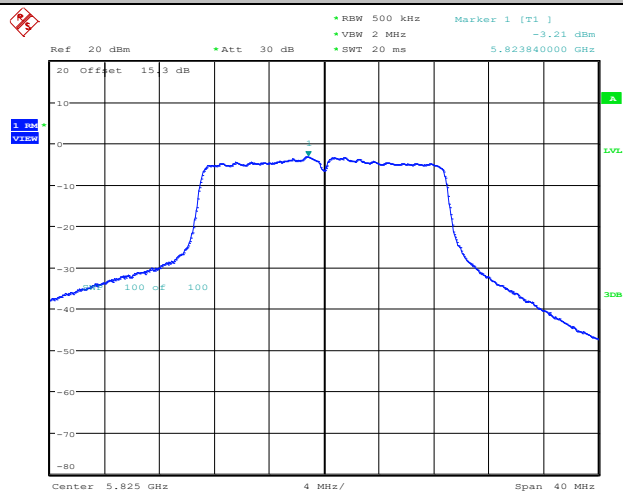
Date: 20.AUG.2024 16:15:58

11AC20SISO_Ant1_5785



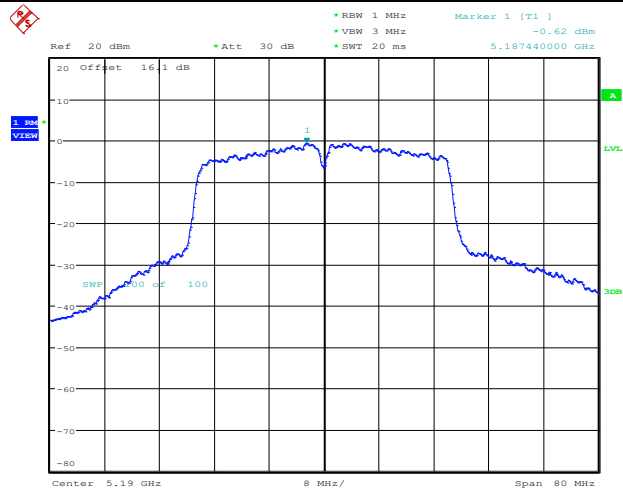
Date: 20.AUG.2024 16:19:56

11AC20SISO_Ant1_5825



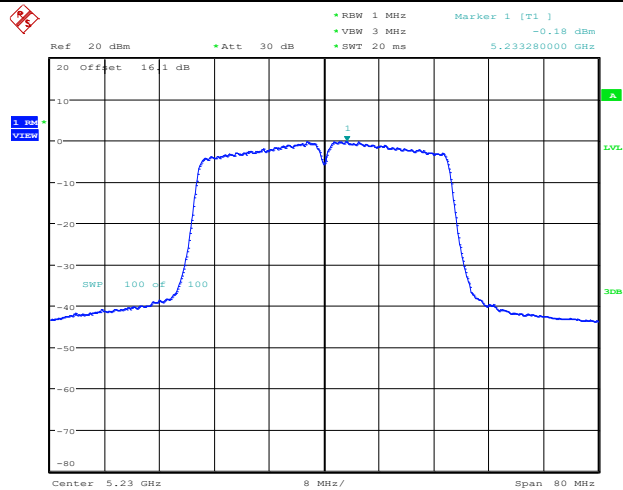
Date: 20.AUG.2024 16:23:11

11AC40SISO_Ant1_5190



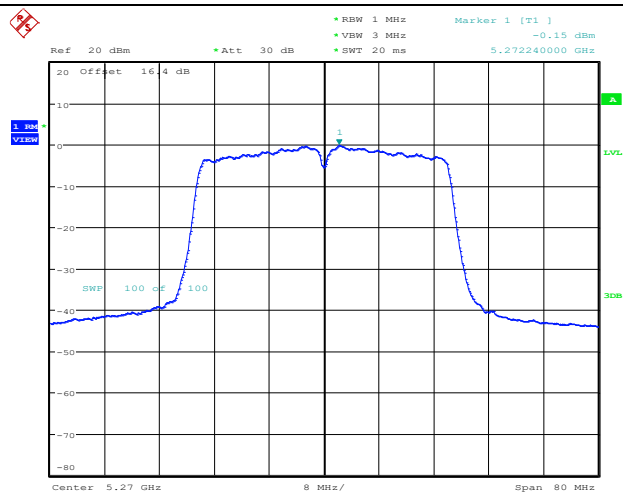
Date: 20.AUG.2024 16:26:06

11AC40SISO_Ant1_5230



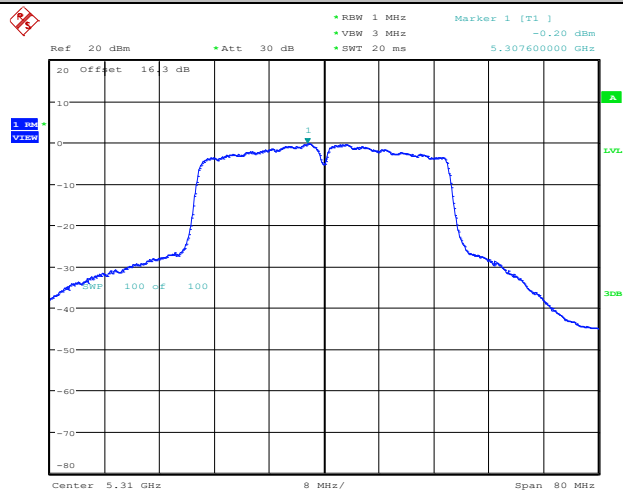
Date: 20.AUG.2024 16:31:20

11AC40SISO_Ant1_5270



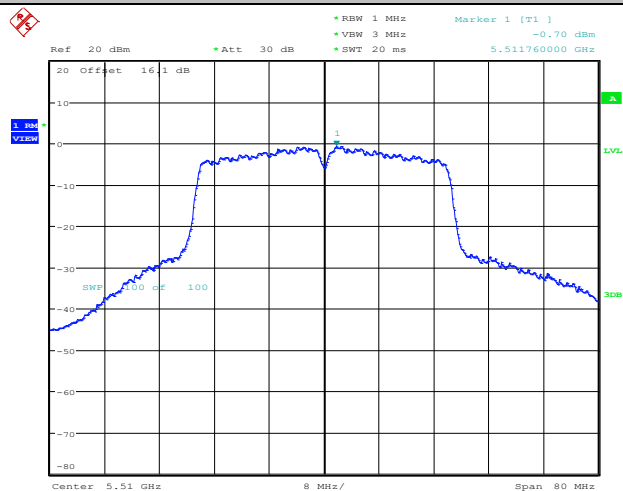
Date: 20.AUG.2024 16:33:52

11AC40SISO_Ant1_5310



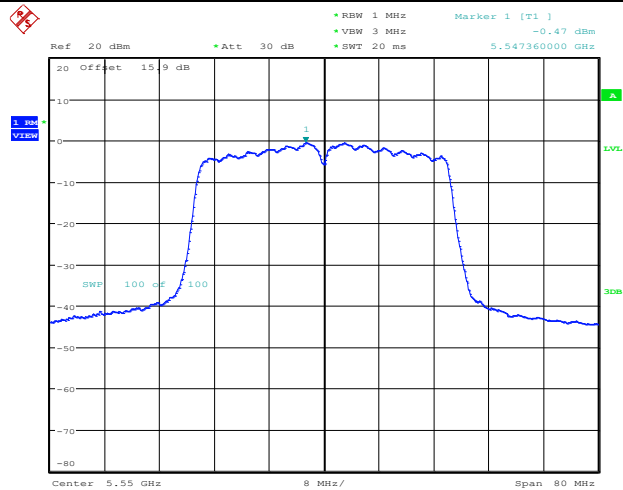
Date: 20.AUG.2024 16:37:01

11AC40SISO_Ant1_5510



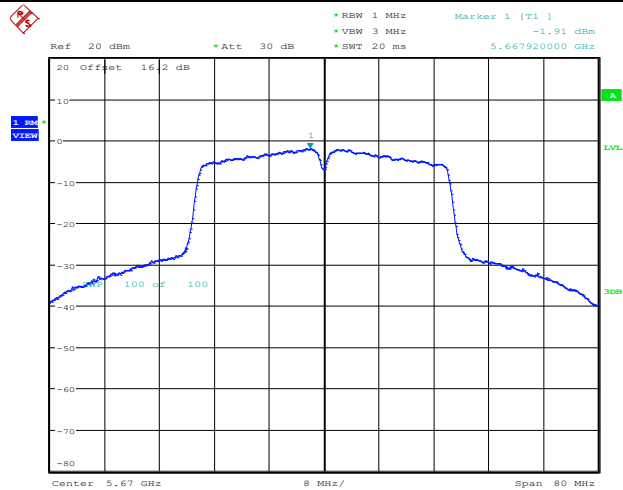
Date: 20.AUG.2024 16:40:11

11AC40SISO_Ant1_5550



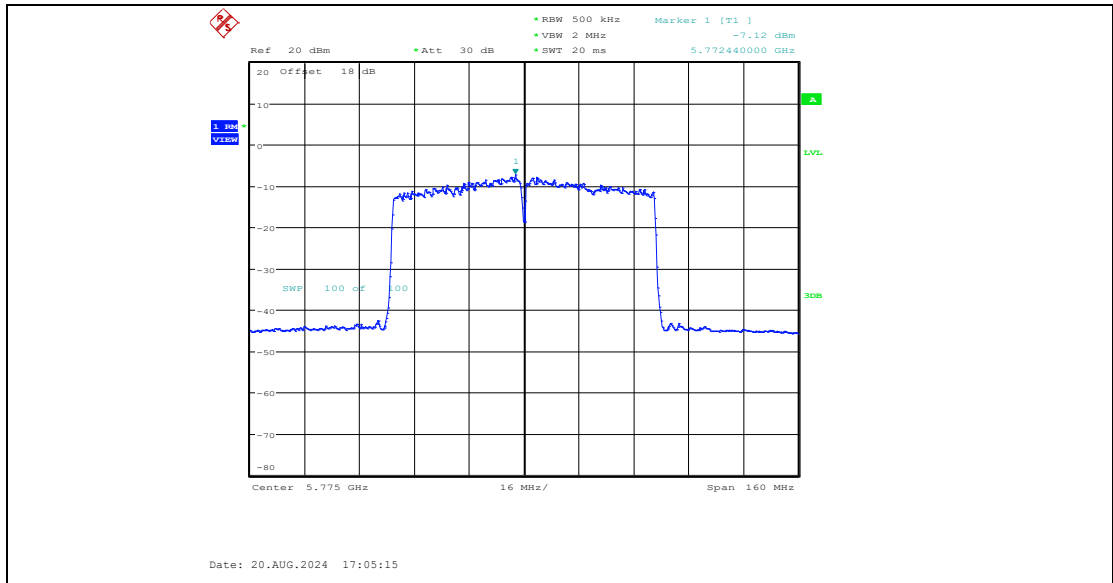
Date: 20.AUG.2024 16:43:41

11AC40SISO_Ant1_5670

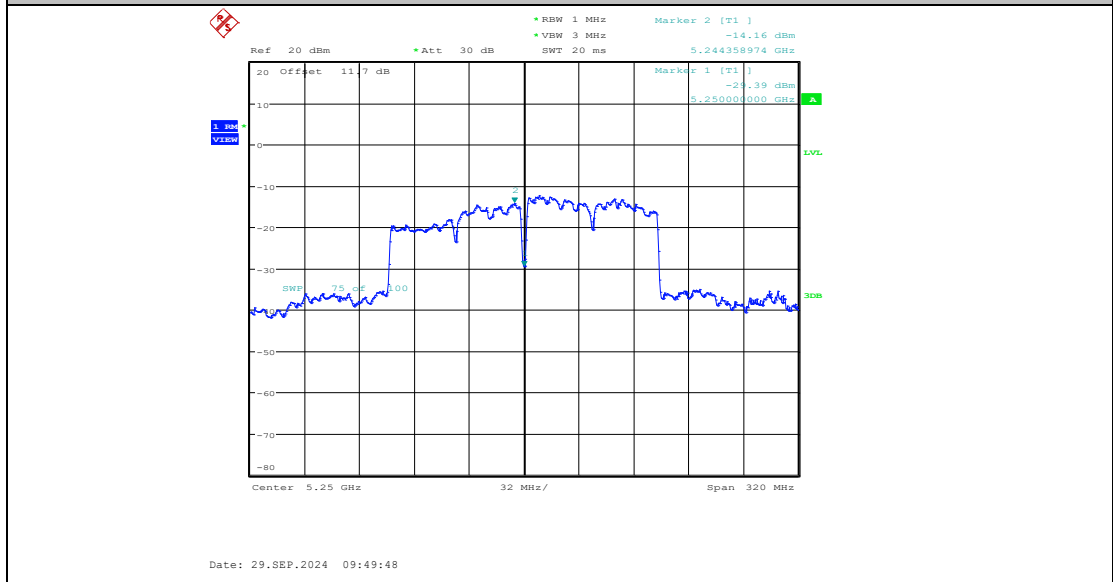


Date: 20.AUG.2024 16:46:05

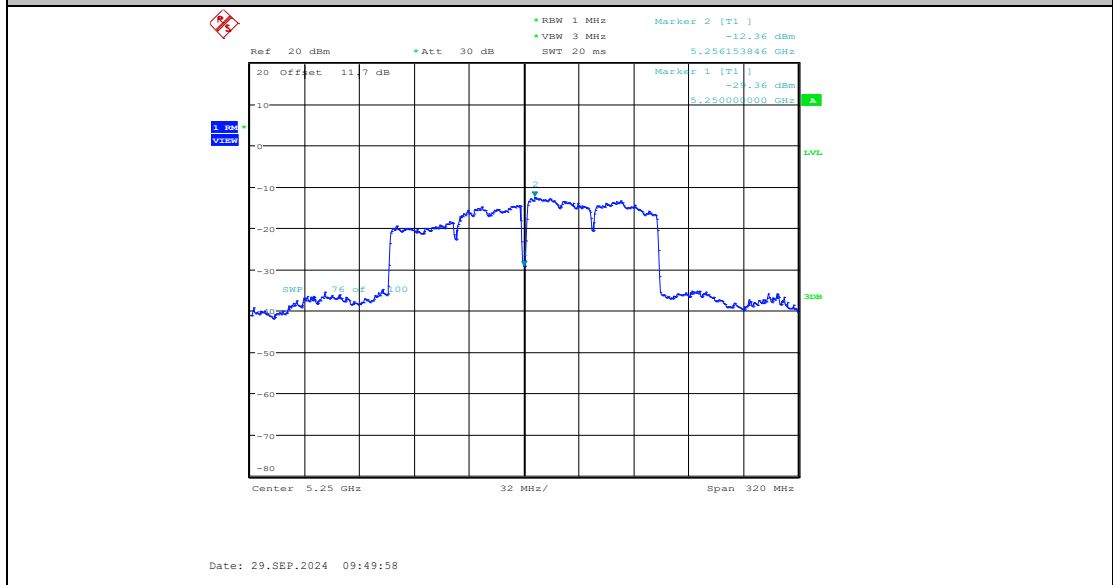
11AC40SISO_Ant1_5755



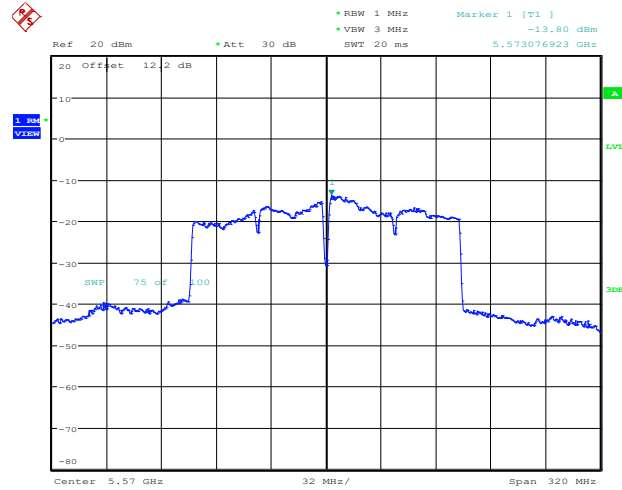
11AC160SISO_Ant1_5250_UNII-1



11AC160SISO_Ant1_5250_UNII-2A

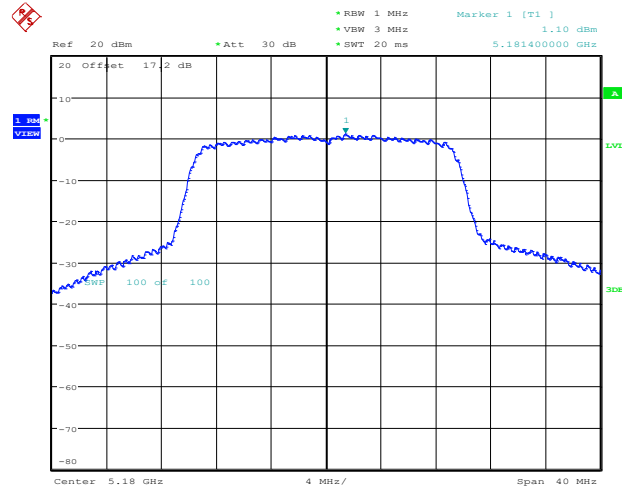


11AC160SISO_Ant1_5570



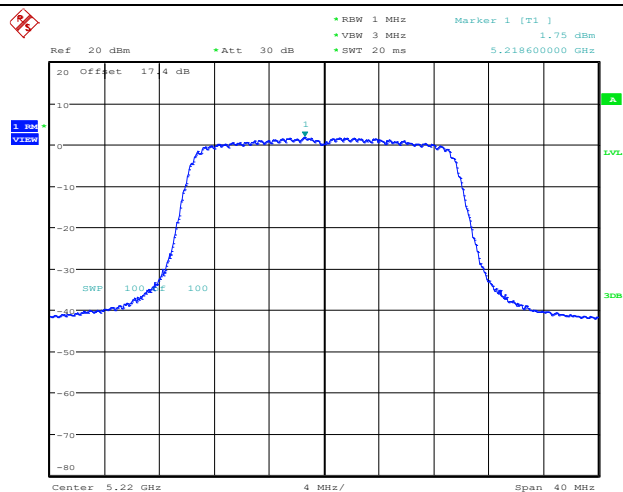
Date: 29.SEP.2024 09:51:29

11AX20SISO_Ant1_5180



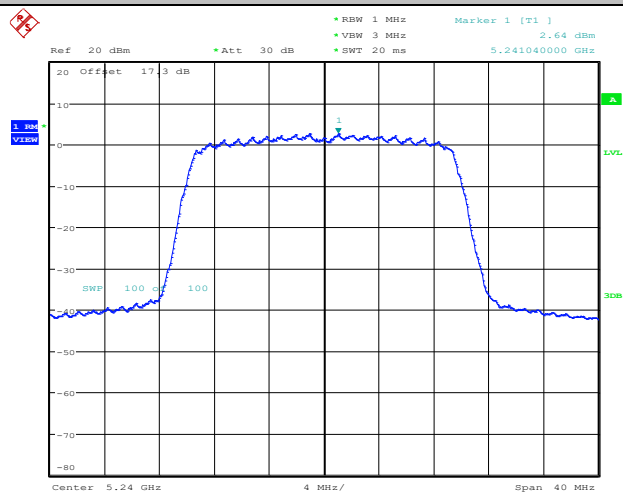
Date: 20.AUG.2024 17:10:23

11AX20SISO_Ant1_5220



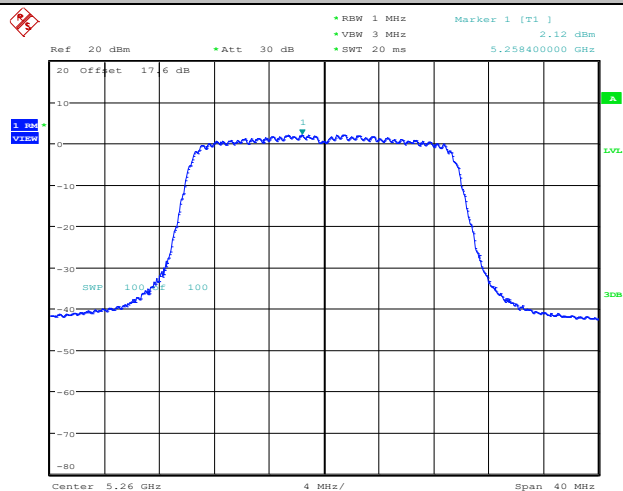
Date: 20.AUG.2024 17:14:02

11AX20SISO_Ant1_5240



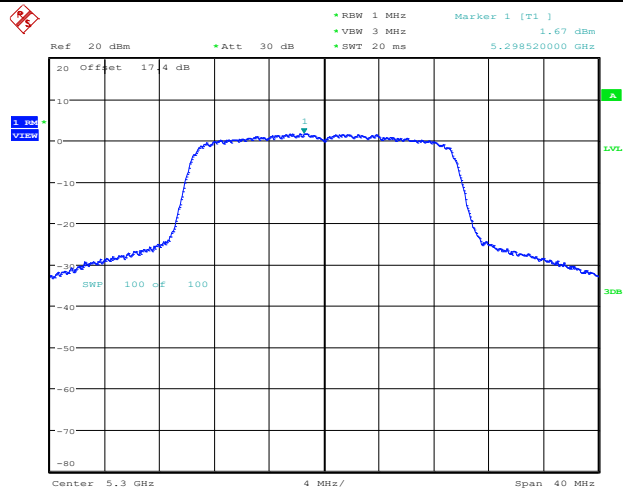
Date: 20.AUG.2024 17:16:20

11AX20SISO_Ant1_5260



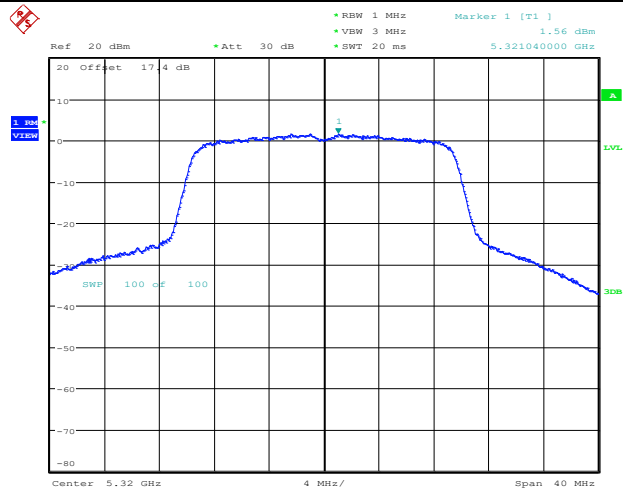
Date: 20.AUG.2024 17:18:35

11AX20SISO_Ant1_5300



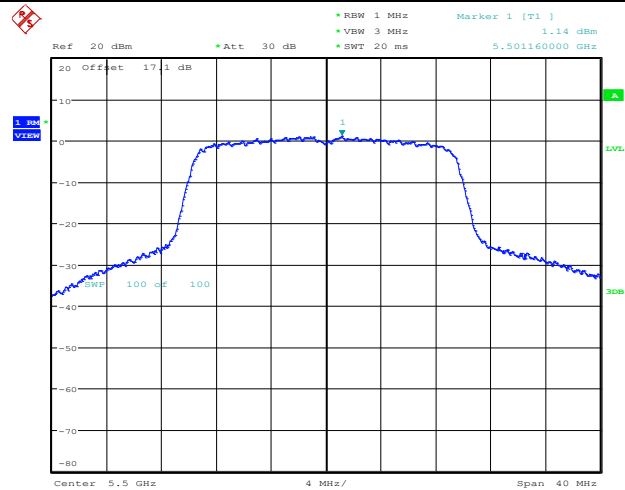
Date: 20.AUG.2024 17:20:54

11AX20SISO_Ant1_5320



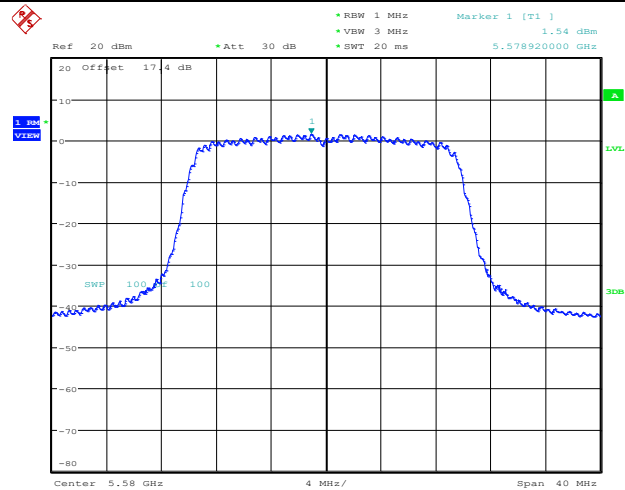
Date: 20.AUG.2024 17:23:04

11AX20SISO_Ant1_5500



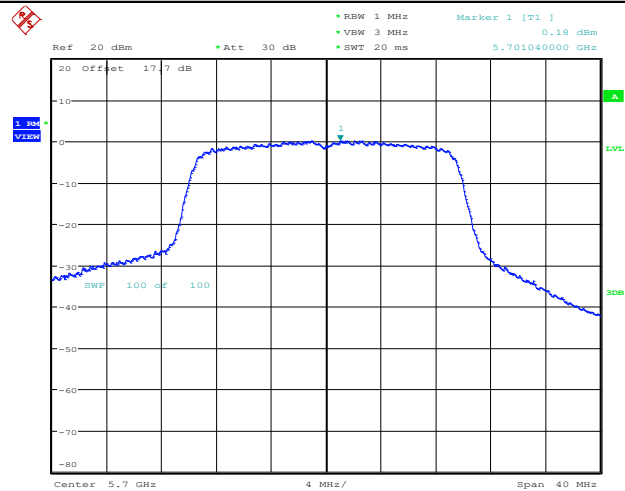
Date: 20.AUG.2024 17:26:39

11AX20SISO_Ant1_5580



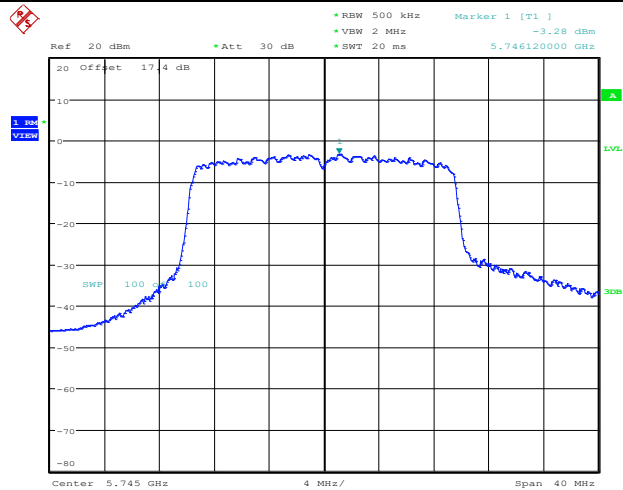
Date: 20.AUG.2024 17:29:19

11AX20SISO_Ant1_5700



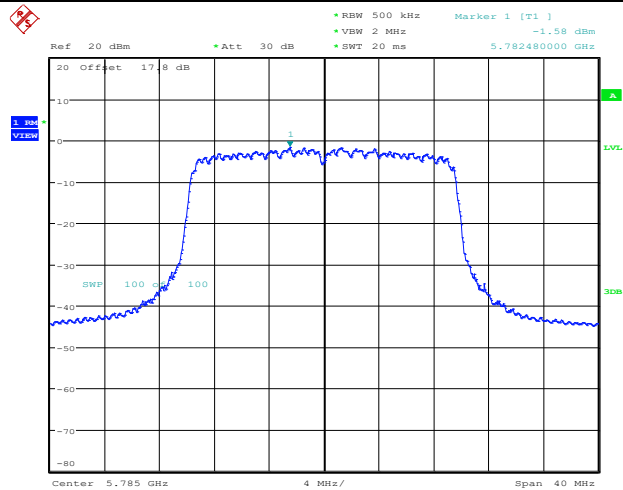
Date: 20.AUG.2024 17:32:38

11AX20SISO_Ant1_5745



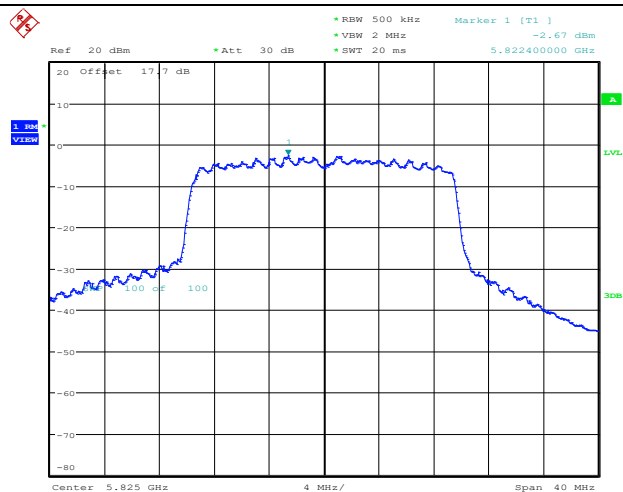
Date: 20.AUG.2024 17:35:57

11AX20SISO_Ant1_5785



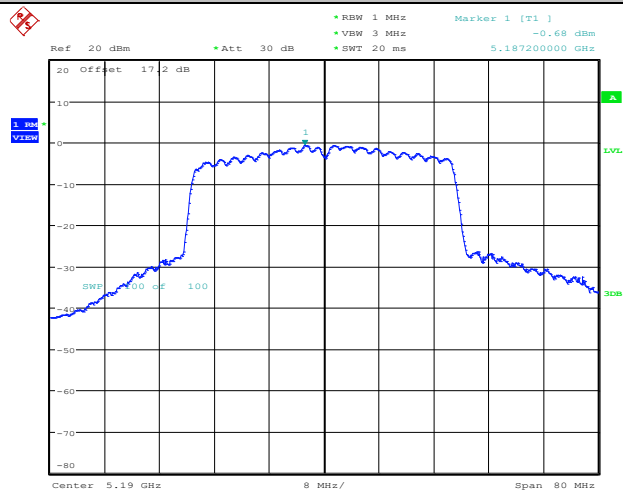
Date: 20.AUG.2024 17:39:08

11AX20SISO_Ant1_5825



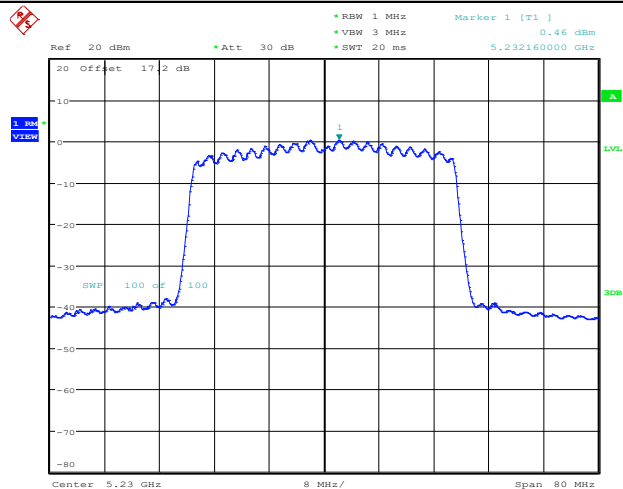
Date: 20.AUG.2024 17:42:16

11AX40SISO_Ant1_5190



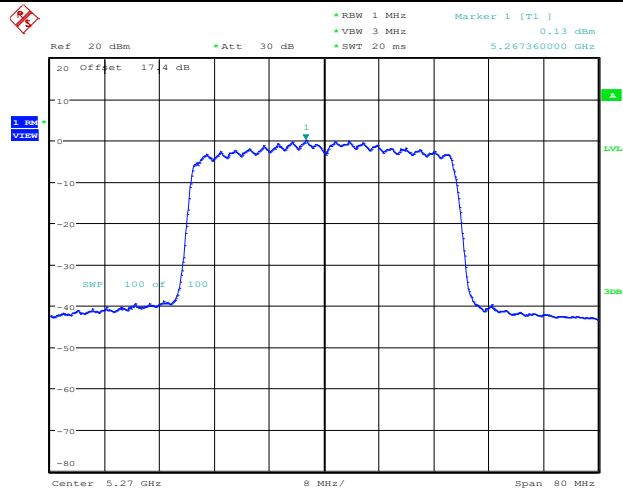
Date: 20.AUG.2024 17:45:18

11AX40SISO_Ant1_5230



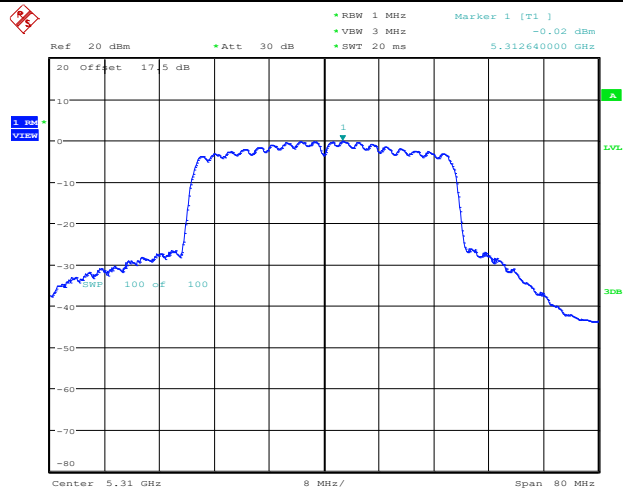
Date: 20.AUG.2024 17:48:21

11AX40SISO_Ant1_5270



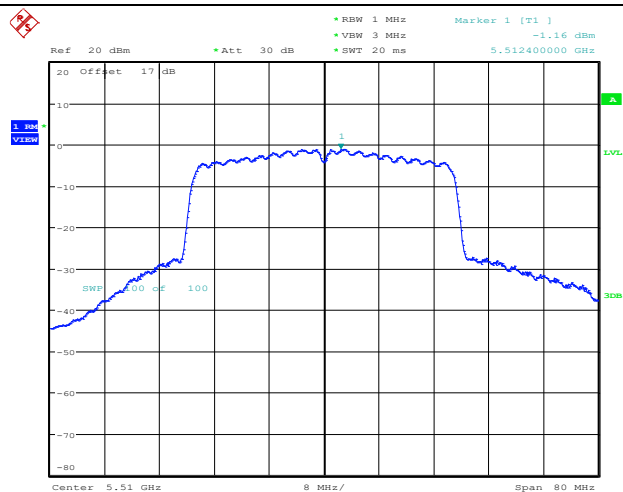
Date: 20.AUG.2024 17:50:39

11AX40SISO_Ant1_5310



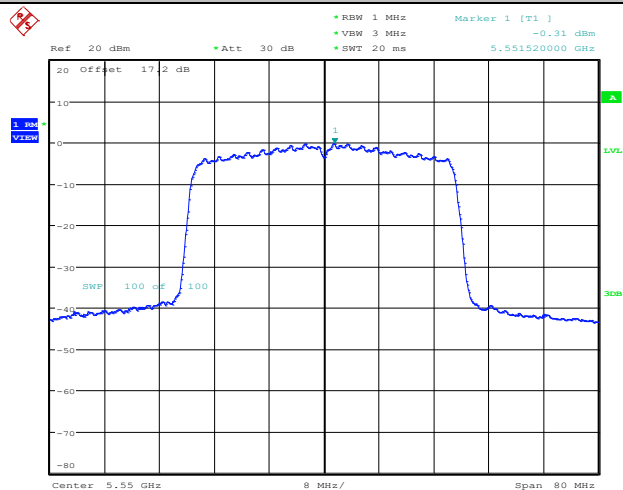
Date: 20.AUG.2024 17:52:52

11AX40SISO_Ant1_5510



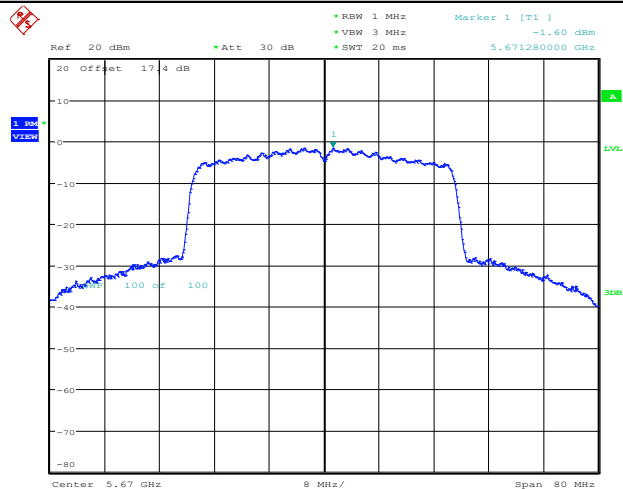
Date: 20.AUG.2024 17:56:00

11AX40SISO_Ant1_5550



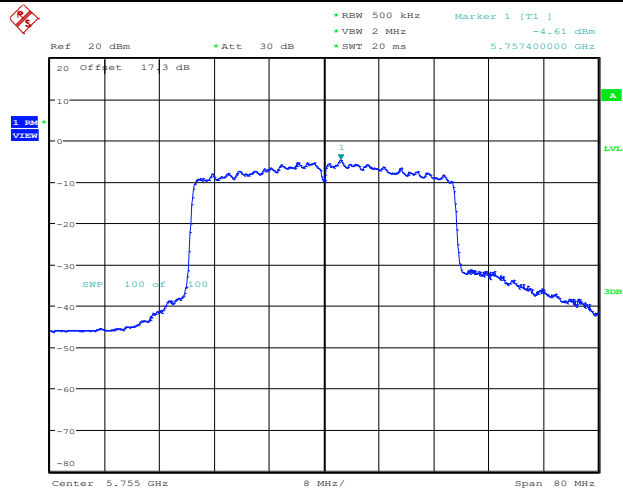
Date: 20.AUG.2024 17:58:46

11AX40SISO_Ant1_5670



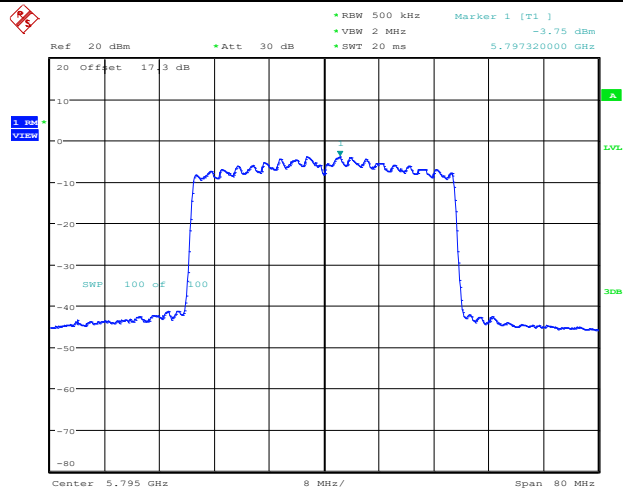
Date: 20.AUG.2024 18:01:09

11AX40SISO_Ant1_5755



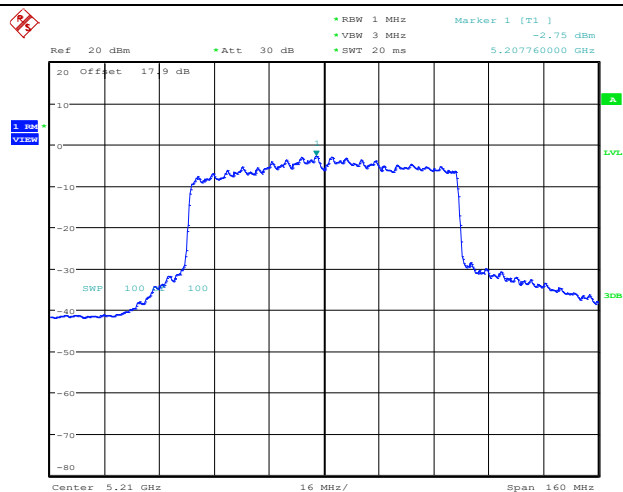
Date: 20.AUG.2024 18:04:39

11AX40SISO_Ant1_5795



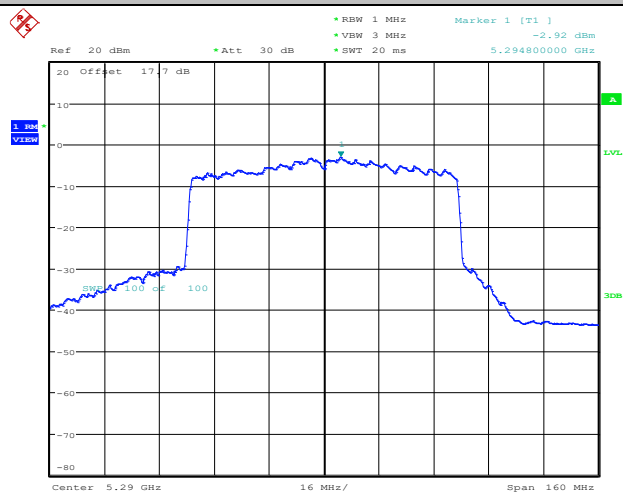
Date: 20.AUG.2024 18:08:07

11AX80SISO_Ant1_5210



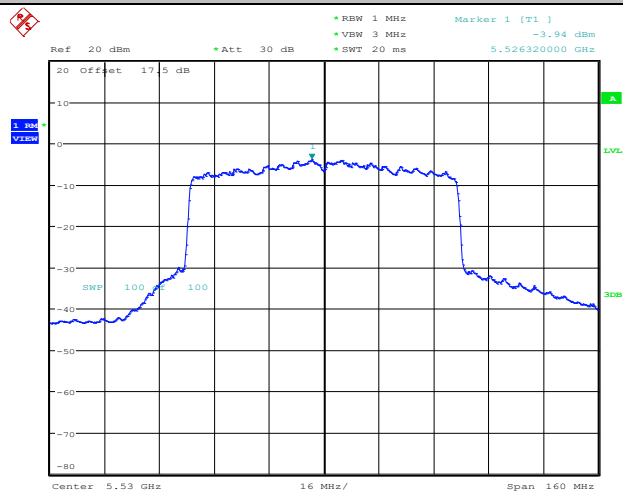
Date: 20.AUG.2024 18:11:08

11AX80SISO_Ant1_5290



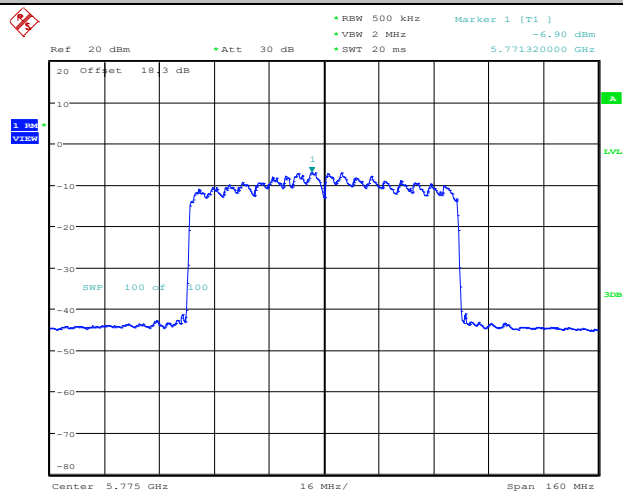
Date: 20.AUG.2024 18:14:26

11AX80SISO_Ant1_5530



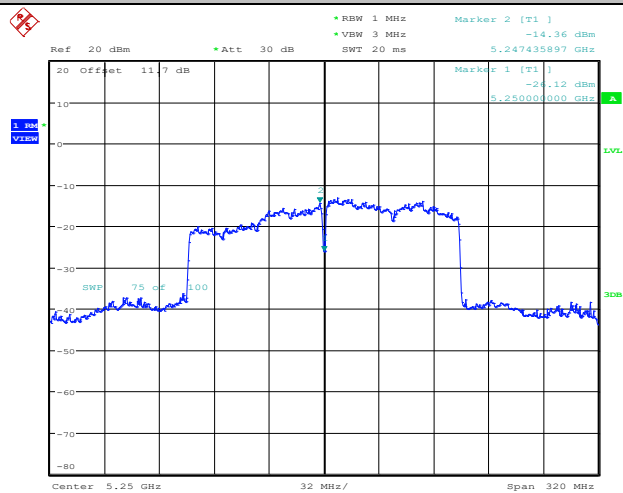
Date: 20.AUG.2024 18:18:04

11AX80SISO_Ant1_5775



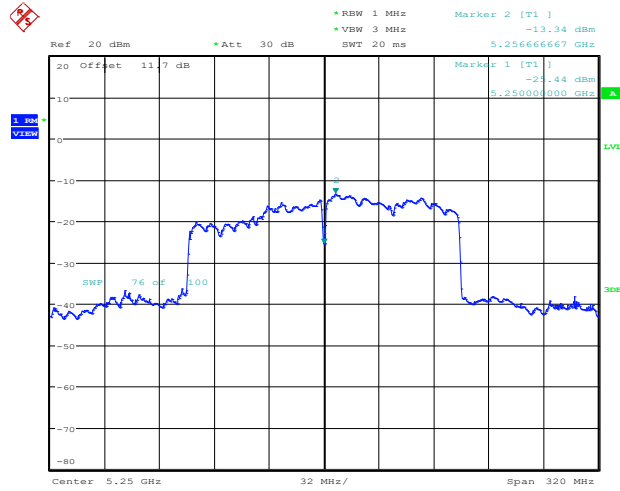
Date: 20.AUG.2024 18:21:04

11AX160SISO_Ant1_5250_UNII-1



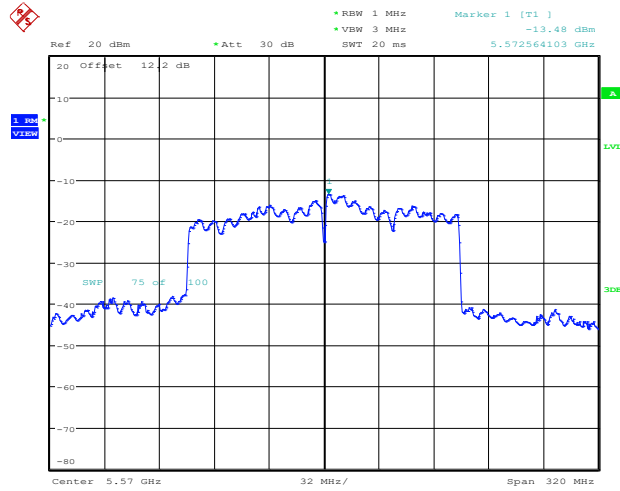
Date: 29.SEP.2024 09:53:04

11AX160SISO_Ant1_5250_UNII-2A



Date: 29.SEP.2024 09:53:14

11AX160SISO_Ant1_5570



Date: 29.SEP.2024 09:53:59

Appendix E: Band edge measurements

Test Result B1/2/3

TestMode	Antenna	ChName	Freq(MHz)	Result[dBm]	Limit[dBm]	Verdict
11A	Ant1	Low	5180	-42.6	≤-27	PASS
		High	5320	-43.79	≤-27	PASS
		Low	5500	-43.35	≤-27	PASS
		High	5700	-42.85	≤-27	PASS
11N20SISO	Ant1	Low	5180	-42.78	≤-27	PASS
		High	5320	-43.35	≤-27	PASS
		Low	5500	-44.05	≤-27	PASS
		High	5700	-42.99	≤-27	PASS
11N40SISO	Ant1	Low	5190	-42.9	≤-27	PASS
		High	5310	-43.94	≤-27	PASS
		Low	5510	-43.75	≤-27	PASS
		High	5670	-43.86	≤-27	PASS
11AC20SISO	Ant1	Low	5180	-42.97	≤-27	PASS
		High	5320	-43.44	≤-27	PASS
		Low	5500	-44.12	≤-27	PASS
		High	5700	-42.89	≤-27	PASS
11AC40SISO	Ant1	Low	5190	-43.53	≤-27	PASS
		High	5310	-43.93	≤-27	PASS
		Low	5510	-43.08	≤-27	PASS
		High	5670	-43.8	≤-27	PASS
11AC80SISO	Ant1	Low	5210	-42.24	≤-27	PASS
		High	5290	-43.43	≤-27	PASS
		Low	5530	-42.23	≤-27	PASS
11AC160SISO	Ant1	Low	5250	-35.85	≤-27	PASS
		High	5250	-34.27	≤-27	PASS
		Low	5570	-41.2	≤-27	PASS
		High	5570	-46.28	≤-27	PASS
11AX20SISO	Ant1	Low	5180	-43.28	≤-27	PASS
		High	5320	-43.76	≤-27	PASS
		Low	5500	-44.03	≤-27	PASS
		High	5700	-42.14	≤-27	PASS
11AX40SISO	Ant1	Low	5190	-43	≤-27	PASS
		High	5310	-43.87	≤-27	PASS
		Low	5510	-43.71	≤-27	PASS
		High	5670	-43.68	≤-27	PASS
11AX80SISO	Ant1	Low	5210	-42.16	≤-27	PASS
		High	5290	-42.8	≤-27	PASS
		Low	5530	-43.04	≤-27	PASS

11AX160SISO	Ant1	Low	5250	-38.81	≤-27	PASS
		High	5250	-38.06	≤-27	PASS
		Low	5570	-42.35	≤-27	PASS
		High	5570	-51.34	≤-27	PASS

Test Result B4

TestMode	Antenna	ChName	Freq(MHz)	FreqRange [MHz]	Result [dBm]	Limit [dBm]	Verdict
11A	Ant1	Low	5745	5650~5700	-44.56	≤-26.75	PASS
				5700~5720	-44.26	≤10.80	PASS
				5720~5725	-45.04	≤15.62	PASS
				5760~5650	-44.18	≤-27	PASS
		High	5825	5850~5855	-44.09	≤16.70	PASS
				5855~5875	-44.29	≤11.01	PASS
				5875~5925	-43.73	≤-26.56	PASS
				5925~5935	-44.07	≤-27	PASS
11N20SISO	Ant1	Low	5745	5650~5700	-44.9	≤-26.88	PASS
				5700~5720	-43.77	≤10.13	PASS
				5720~5725	-45.28	≤15.62	PASS
				5760~5650	-44.16	≤-27	PASS
		High	5825	5850~5855	-45.07	≤15.71	PASS
				5855~5875	-44.96	≤10.22	PASS
				5875~5925	-44.61	≤-26.88	PASS
				5925~5935	-44.15	≤-27	PASS
11N40SISO	Ant1	Low	5755	5650~5700	-44.47	≤-25.90	PASS
				5700~5720	-44.46	≤10.53	PASS
				5720~5725	-44.7	≤15.76	PASS
				5780~5650	-44.66	≤-27	PASS
		High	5795	5850~5855	-45.9	≤15.87	PASS
				5855~5875	-44.22	≤10.67	PASS
				5875~5925	-44.65	≤-26.18	PASS
				5925~5935	-43.5	≤-27	PASS
11AC20SISO	Ant1	Low	5745	5650~5700	-45.27	≤-26.88	PASS
				5700~5720	-43.55	≤10.80	PASS
				5720~5725	-44.81	≤16.46	PASS
				5760~5650	-44.29	≤-27	PASS
		High	5825	5850~5855	-44.7	≤15.71	PASS
				5855~5875	-44.23	≤10.89	PASS
				5875~5925	-44.49	≤-26.56	PASS
				5925~5935	-43.66	≤-27	PASS
11AC40SISO	Ant1	Low	5755	5650~5700	-44.56	≤-26.70	PASS
				5700~5720	-43.96	≤10.96	PASS

				5720~5725	-44.47	≤ 15.76	PASS
				5780~5650	-44.74	≤ -27	PASS
		High	5795	5850~5855	-46.16	≤ 15.87	PASS
				5855~5875	-45.32	≤ 10.08	PASS
				5875~5925	-44.65	≤ -25.99	PASS
				5925~5935	-44.54	≤ -27	PASS
11AC80S ISO	Ant1	Low	5775	5650~5700	-43.4	≤ -26.47	PASS
				5700~5720	-43.1	≤ 10.53	PASS
				5720~5725	-42.98	≤ 15.64	PASS
				5800~5650	-44.29	≤ -27	PASS
		High	5775	5850~5855	-43.94	≤ 15.71	PASS
				5855~5875	-42.72	≤ 11.79	PASS
				5875~5925	-45.18	≤ -26.72	PASS
				5925~5935	-44.49	≤ -27	PASS
11AX20S ISO	Ant1	Low	5745	5650~5700	-45.4	≤ -26.75	PASS
				5700~5720	-44.76	≤ 10.85	PASS
				5720~5725	-45.57	≤ 15.62	PASS
				5760~5650	-44.31	≤ -27	PASS
		High	5825	5850~5855	-45.83	≤ 15.71	PASS
				5855~5875	-45.53	≤ 10.10	PASS
				5875~5925	-45.78	≤ -26.88	PASS
				5925~5935	-42.85	≤ -27	PASS
11AX40S ISO	Ant1	Low	5755	5650~5700	-44.16	≤ -26.38	PASS
				5700~5720	-44.01	≤ 10.11	PASS
				5720~5725	-45.16	≤ 15.76	PASS
				5780~5650	-44.23	≤ -27	PASS
		High	5795	5850~5855	-45.13	≤ 15.87	PASS
				5855~5875	-43.88	≤ 10.45	PASS
				5875~5925	-45.24	≤ -26.77	PASS
				5925~5935	-43.22	≤ -27	PASS
11AX80S ISO	Ant1	Low	5775	5650~5700	-43.61	≤ -26.10	PASS
				5700~5720	-42.62	≤ 10.67	PASS
				5720~5725	-42.45	≤ 15.64	PASS
				5800~5650	-44.65	≤ -27	PASS

11A_Ant1_Low_5180

Ref 20 dBm Att 30 dB SWT 70 ms 4.895993590 GHz

Marker 1 [T1] -42.60 dBm 5.178698397 GHz

Marker 2 [T1] -45.33 dBm 5.180000000 GHz

Marker 3 [T1] -43.79 dBm 5.357435897 GHz

Offset 13 dB

SWP 100 kHz

VBW 300 kHz

SWT 70 ms

Start 4.5 GHz Stop 5.2 GHz

1 dB View

01 -27 dBm

SWP 100 cf 100

3

2

1

20

10

0

-10

-20

-30

-40

-50

-60

-70

-80

dB

70 MHz/

LVL

30dB

Date: 20.AUG.2024 13:54:04

11A_Ant1_High_5320

Ref 20 dBm Att 30 dB SWT 20 ms 5.357435897 GHz

Marker 1 [T1] -43.79 dBm 5.357435897 GHz

Marker 2 [T1] -45.98 dBm 5.360000000 GHz

Marker 3 [T1] -45.00 dBm 5.31846538 GHz

Offset 13.2 dB

SWP 100 kHz

VBW 300 kHz

SWT 20 ms

Start 5.3 GHz Stop 5.46 GHz

1 dB View

01 -27 dBm

SWP 100 cf 100

3

2

1

20

10

0

-10

-20

-30

-40

-50

-60

-70

-80

dB

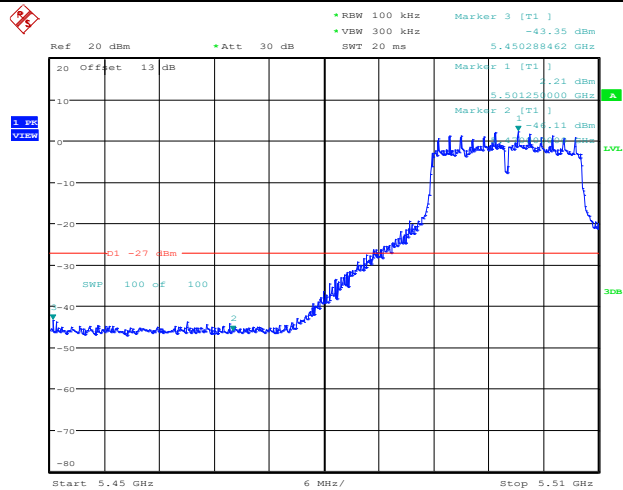
16 MHz/

LVL

30dB

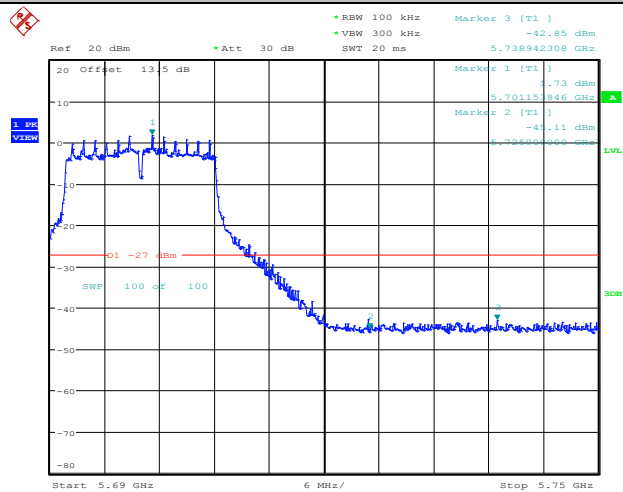
Date: 20.AUG.2024 14:18:39

11A_Ant1_Low_5500



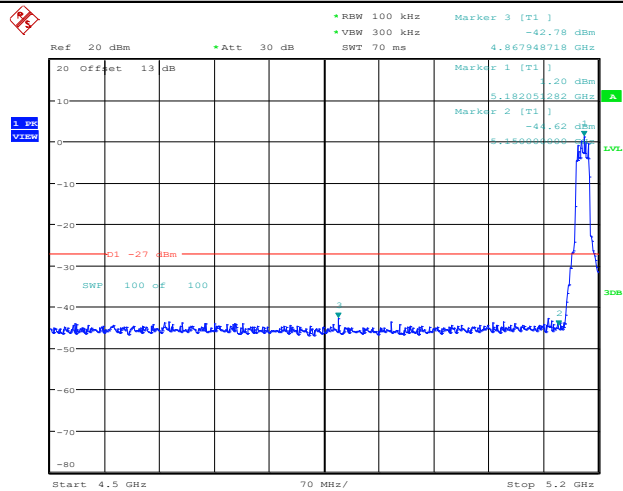
Date: 20.AUG.2024 14:21:42

11A_Ant1_High_5700



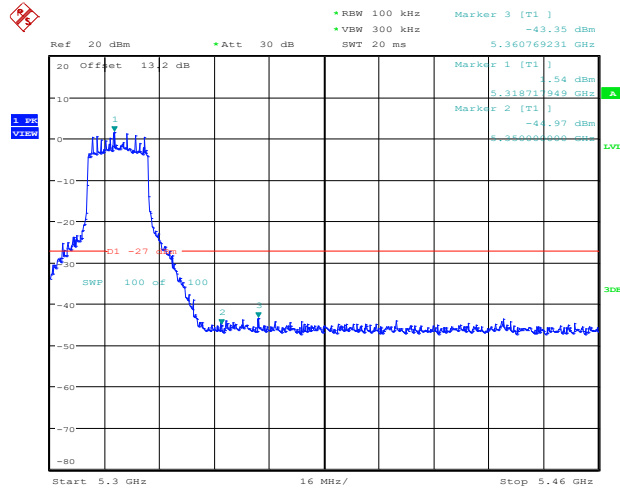
Date: 20.AUG.2024 14:31:29

11N20SISO_Ant1_Low_5180



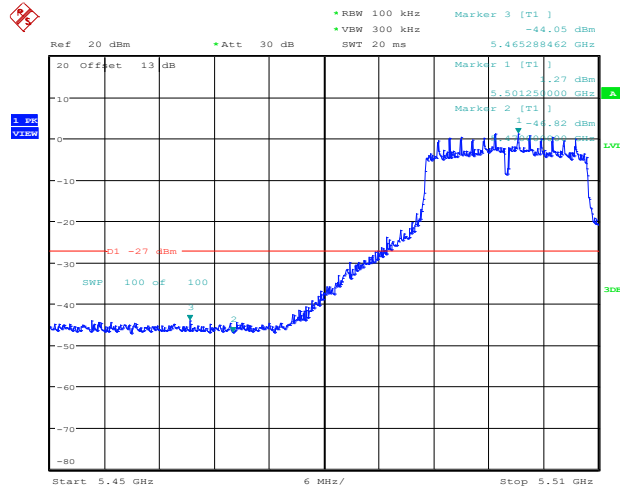
Date: 20.AUG.2024 14:44:04

11N20SISO_Ant1_High_5320



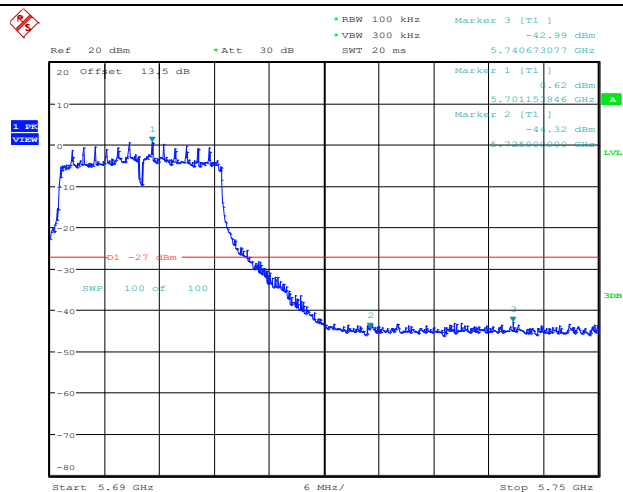
Date: 20.AUG.2024 14:56:50

11N20SISO_Ant1_Low_5500



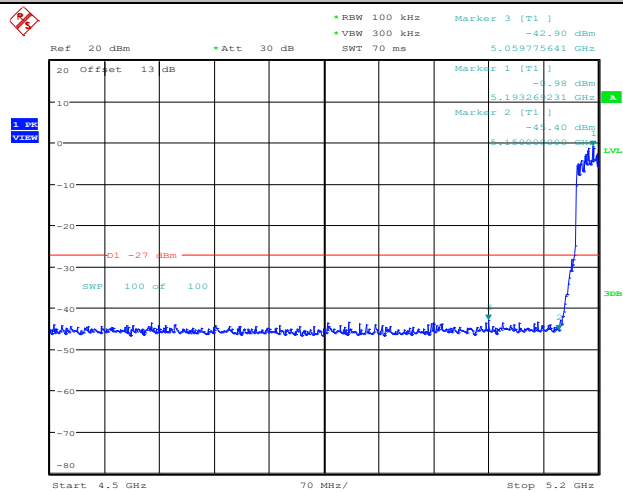
Date: 20.AUG.2024 15:00:05

11N20SISO_Ant1_High_5700



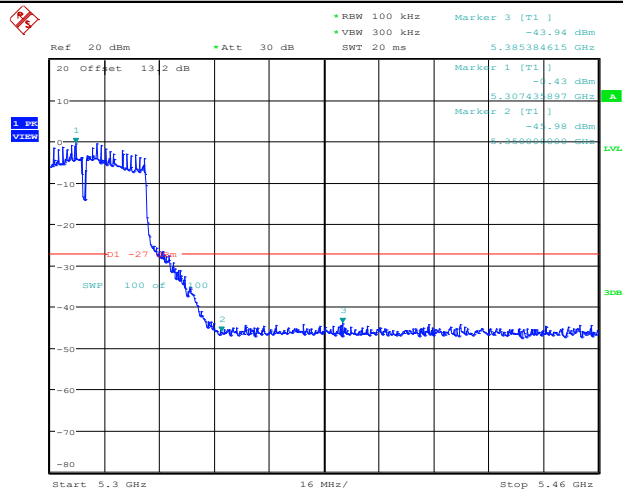
Date: 20.AUG.2024 15:06:05

11N40SISO_Ant1_Low_5190



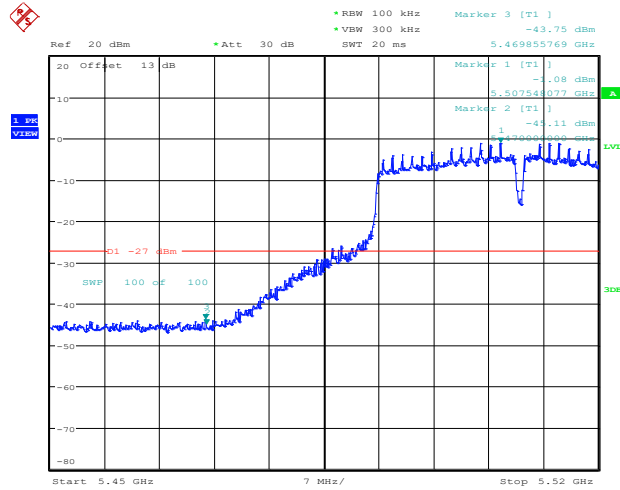
Date: 20.AUG.2024 15:20:14

11N40SISO_Ant1_High_5310



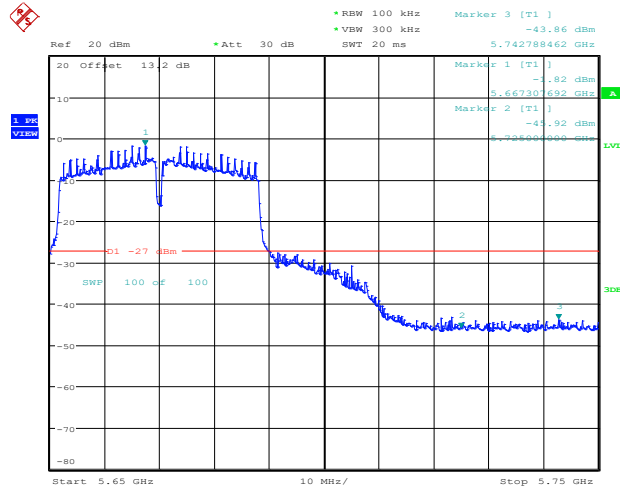
Date: 20.AUG.2024 15:29:07

11N40SISO_Ant1_Low_5510



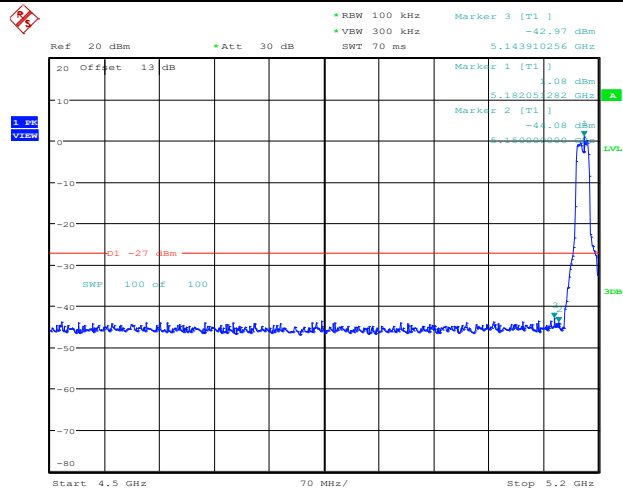
Date: 20.AUG.2024 15:32:27

11N40SISO_Ant1_High_5670



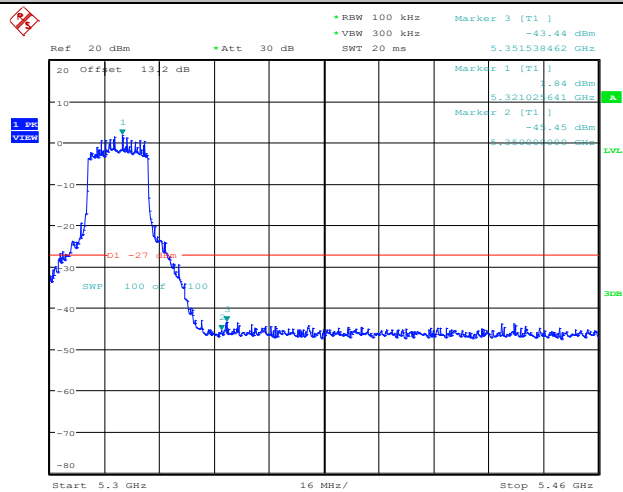
Date: 20.AUG.2024 15:37:56

11AC20SISO_Ant1_Low_5180



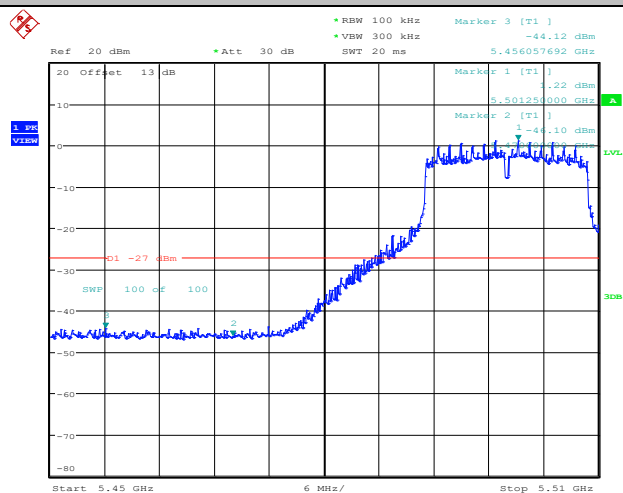
Date: 20.AUG.2024 15:49:16

11AC20SISO_Ant1_High_5320

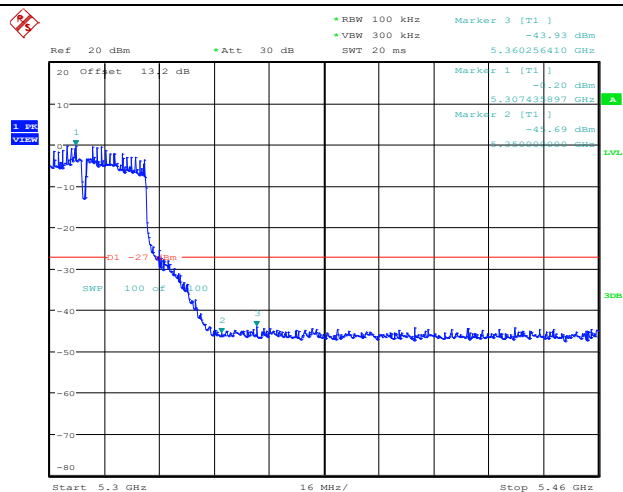


Date: 20.AUG.2024 16:03:27

11AC20SISO_Ant1_Low_5500

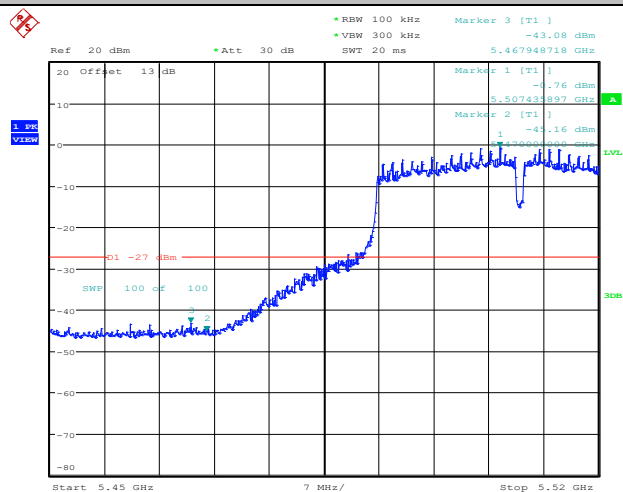


Date: 20.AUG.2024 16:07:29



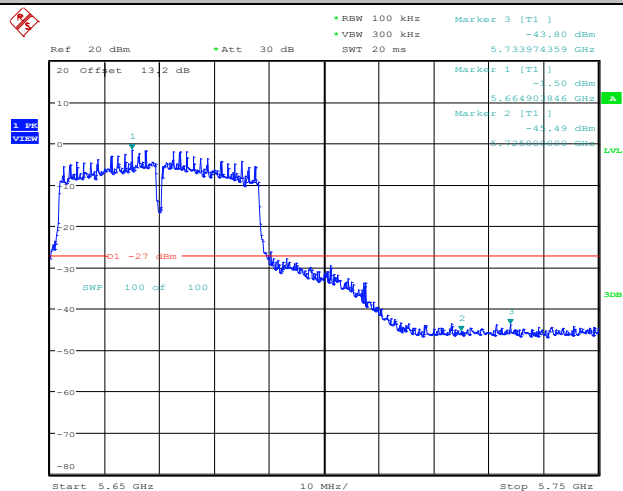
Date: 20.AUG.2024 16:37:28

11AC40SISO_Ant1_Low_5510



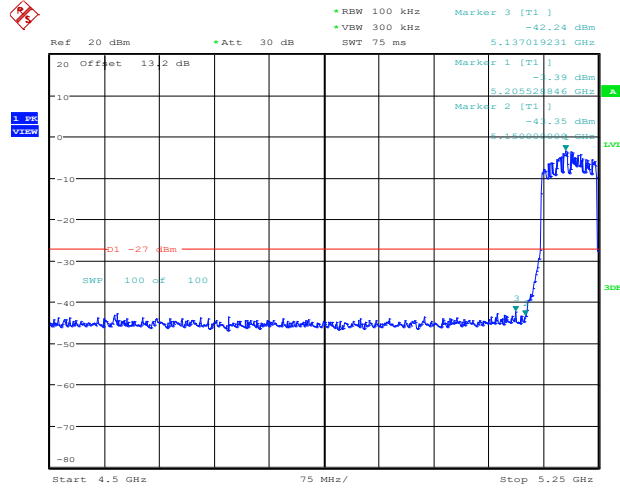
Date: 20.AUG.2024 16:40:37

11AC40SISO_Ant1_High_5670



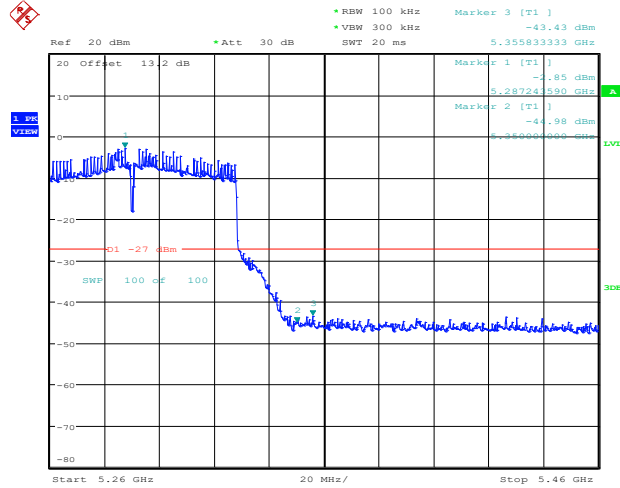
Date: 20.AUG.2024 16:46:31

11AC80SISO_Ant1_Low_5210



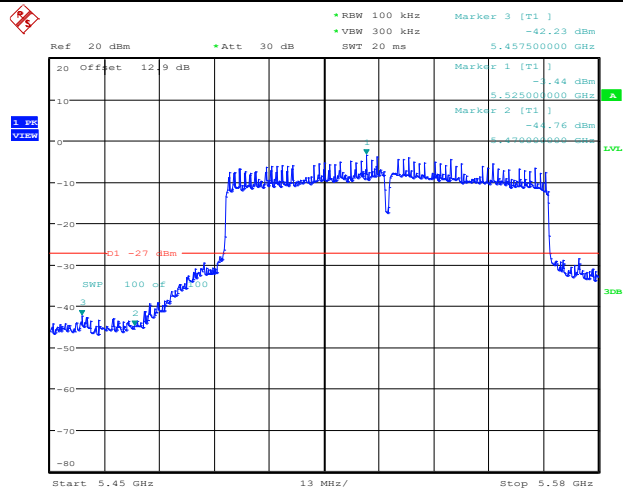
Date: 20.AUG.2024 16:56:57

11AC80SISO_Ant1_High_5290



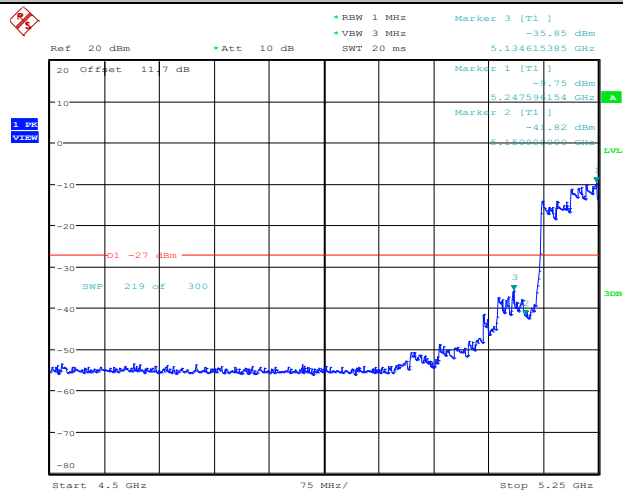
Date: 20.AUG.2024 16:59:41

11AC80SISO_Ant1_Low_5530



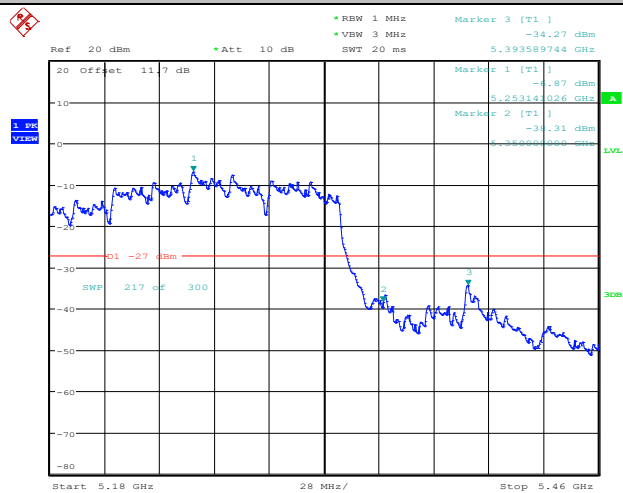
Date: 20.AUG.2024 17:02:40

11AC160SISO_Ant1_Low_5250



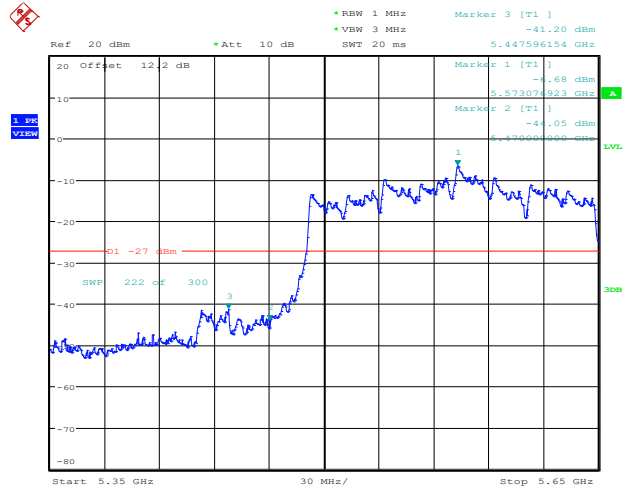
Date: 29.SEP.2024 09:56:20

11AC160SISO_Ant1_High_5250



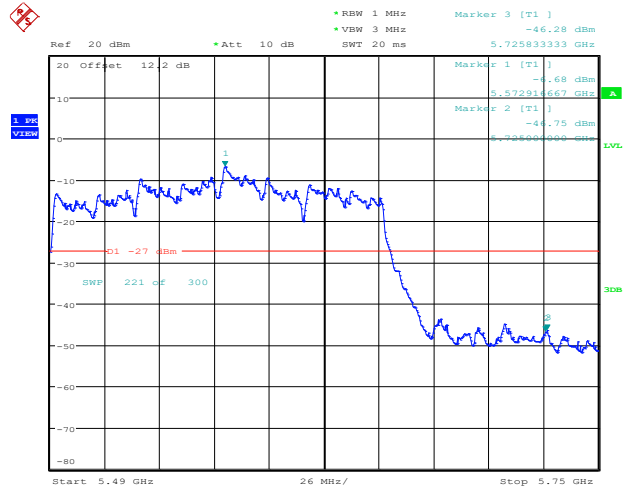
Date: 29.SEP.2024 09:56:42

11AC160SISO_Ant1_Low_5570



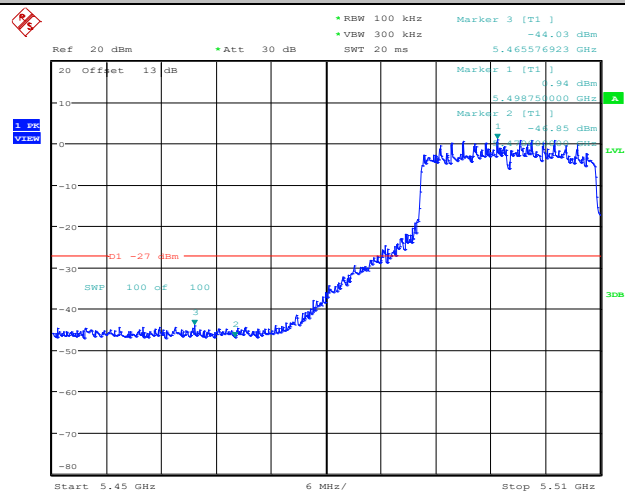
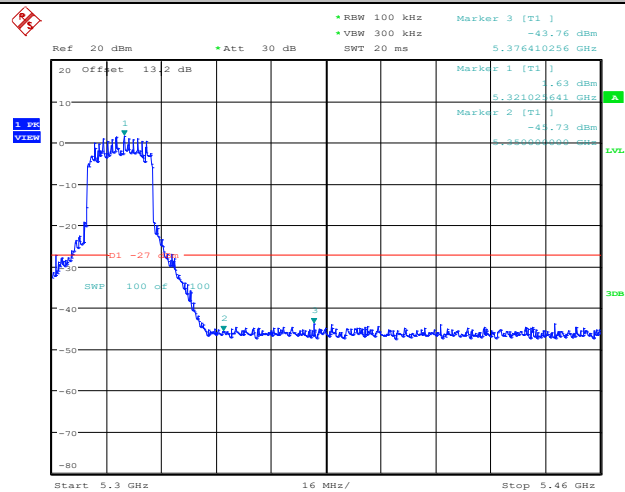
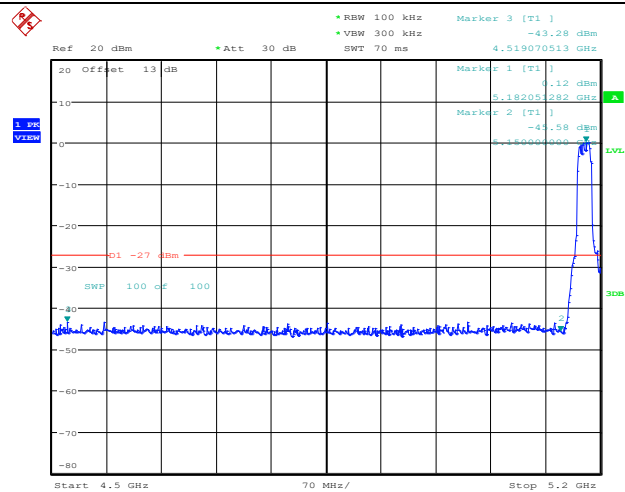
Date: 29.SEP.2024 09:58:00

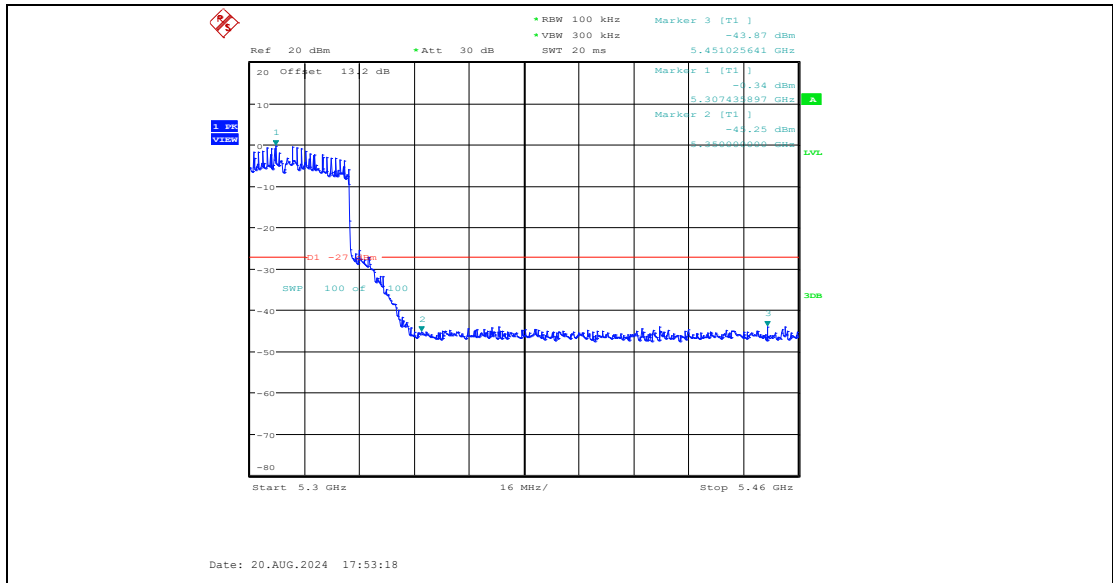
11AC160SISO_Ant1_High_5570



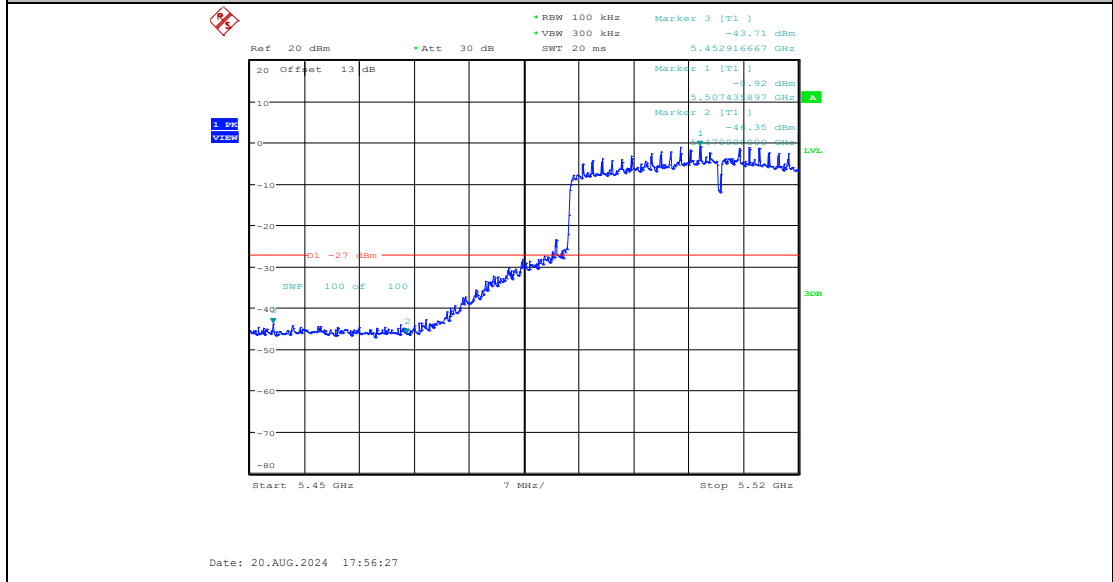
Date: 29.SEP.2024 09:58:22

11AX20SISO_Ant1_Low_5180

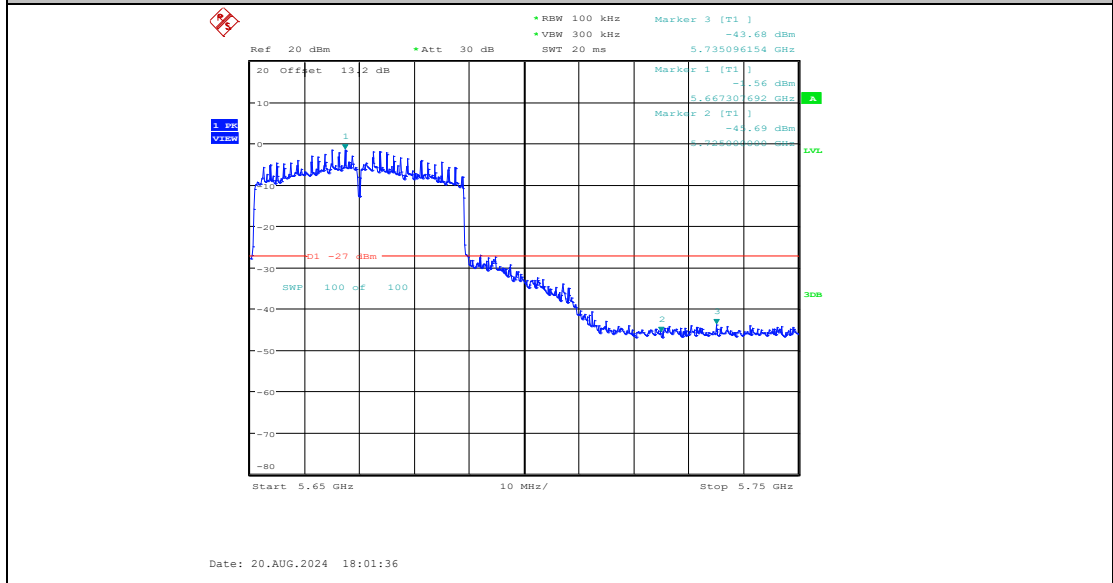




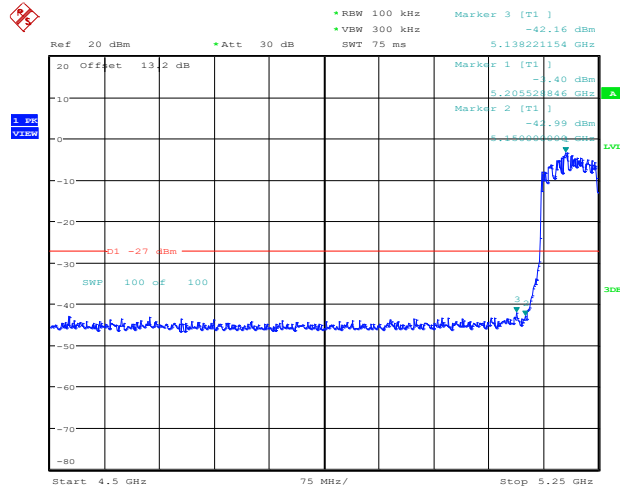
11AX40SISO_Ant1_Low_5510



11AX40SISO_Ant1_High_5670

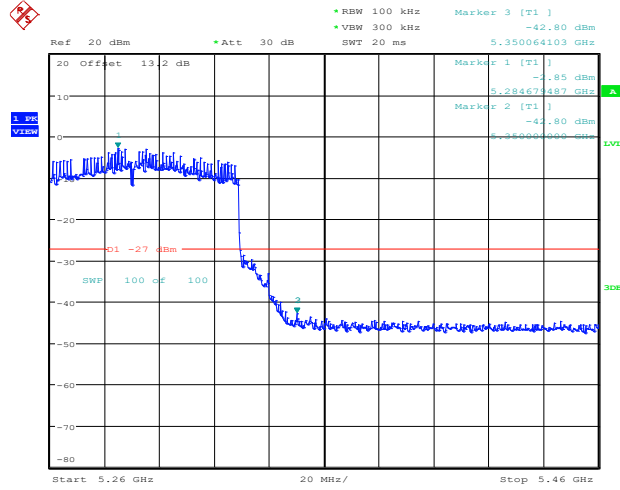


11AX80SISO_Ant1_Low_5210



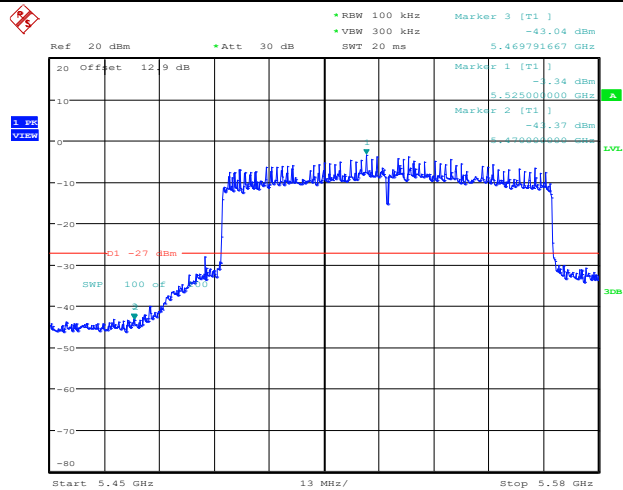
Date: 20.AUG.2024 18:11:34

11AX80SISO_Ant1_High_5290



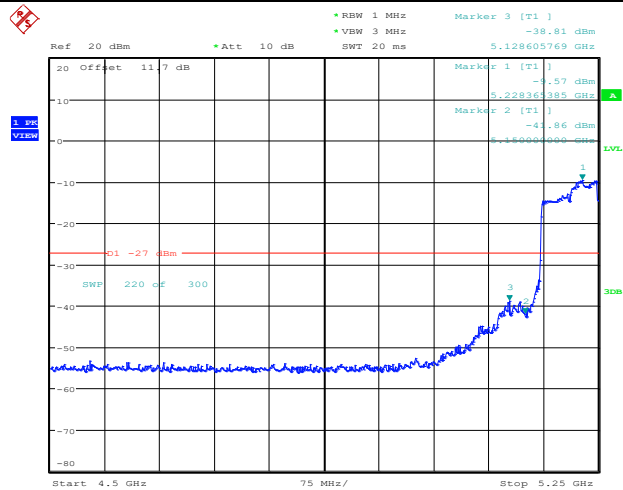
Date: 20.AUG.2024 18:14:53

11AX80SISO_Ant1_Low_5530



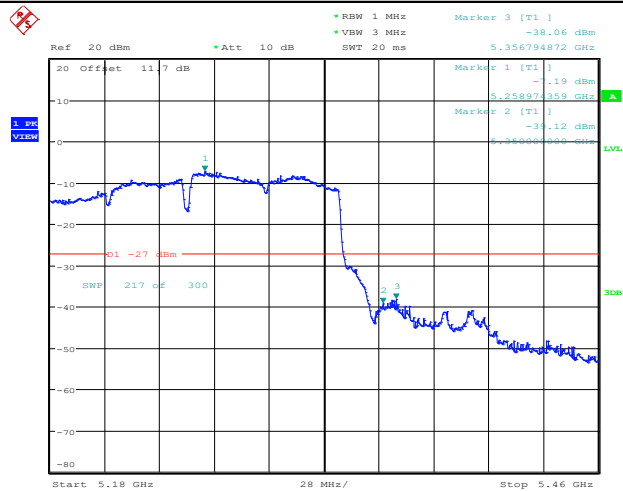
Date: 20.AUG.2024 18:18:30

11AX160SISO_Ant1_Low_5250



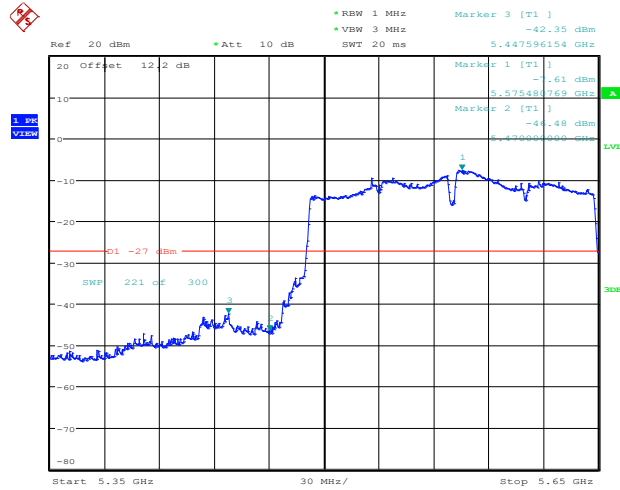
Date: 29.SEP.2024 09:59:44

11AX160SISO_Ant1_High_5250



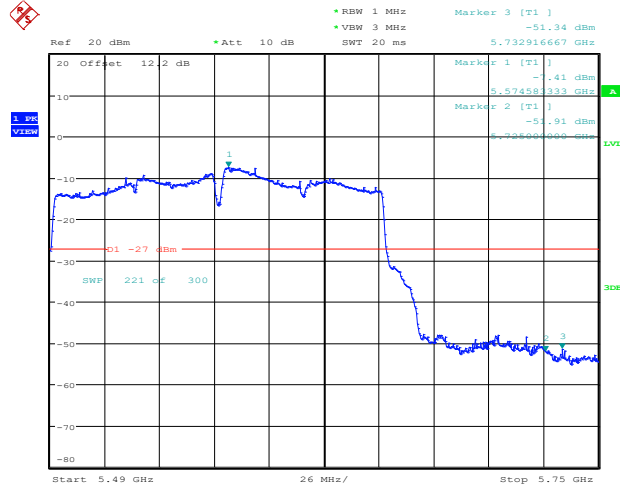
Date: 29.SEP.2024 10:00:06

11AX160SISO_Ant1_Low_5570



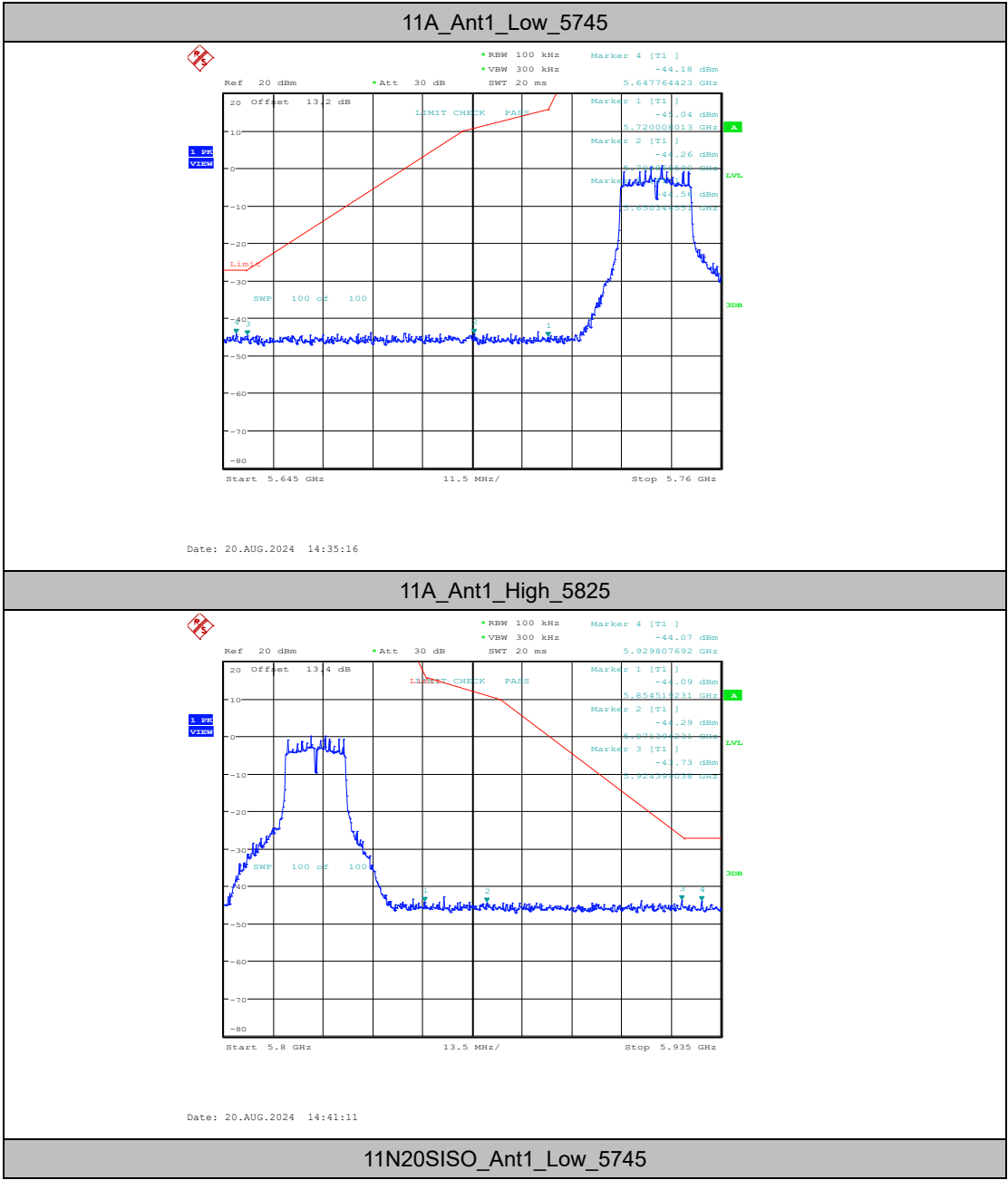
Date: 29.SEP.2024 10:01:22

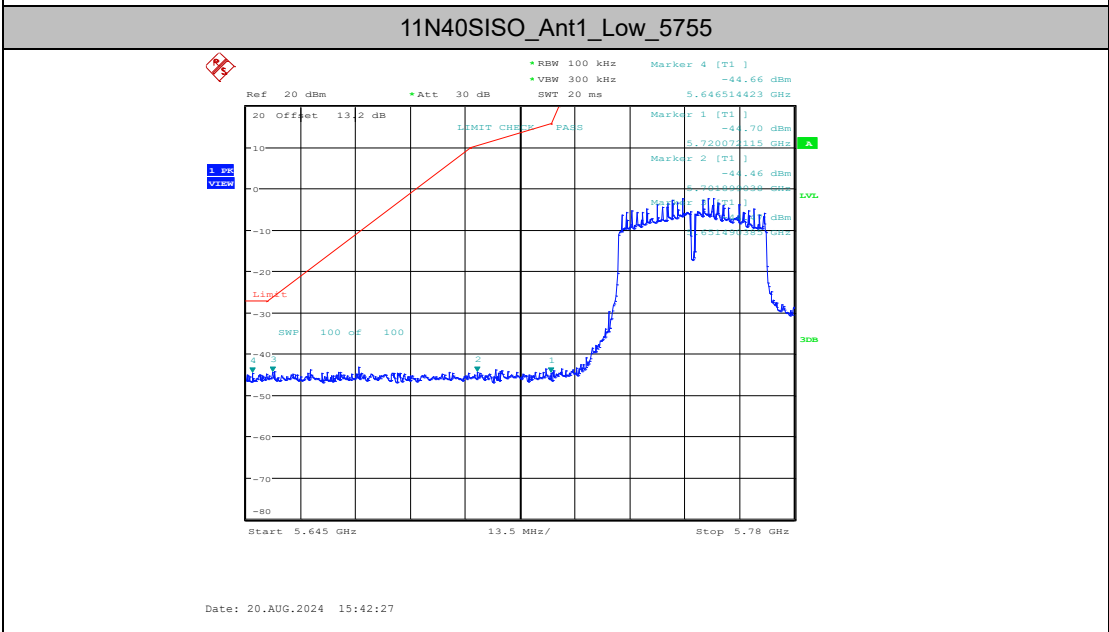
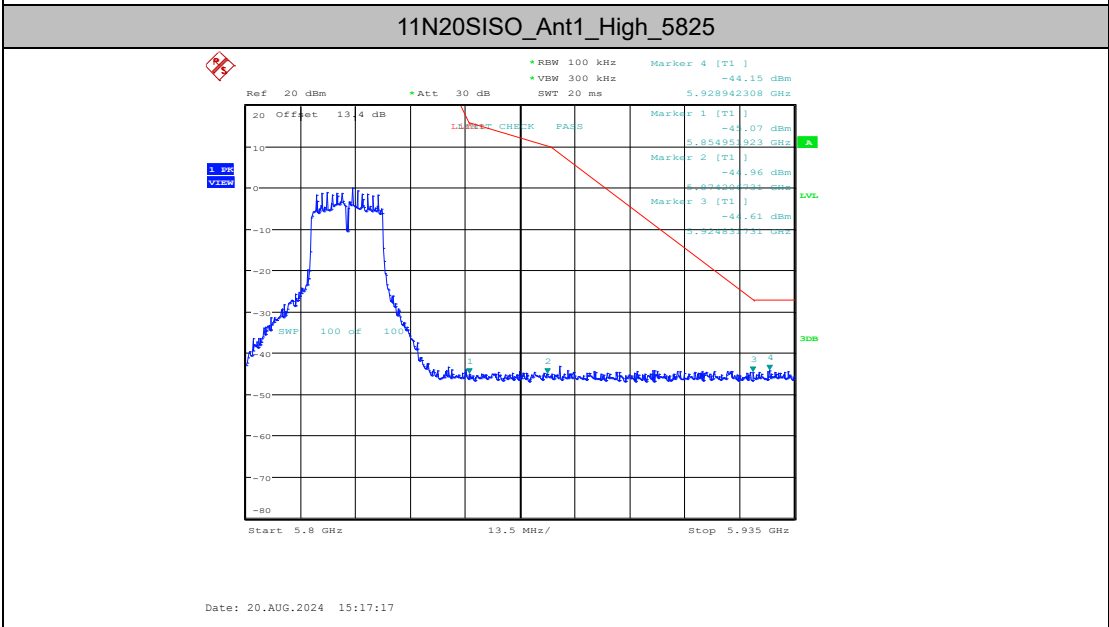
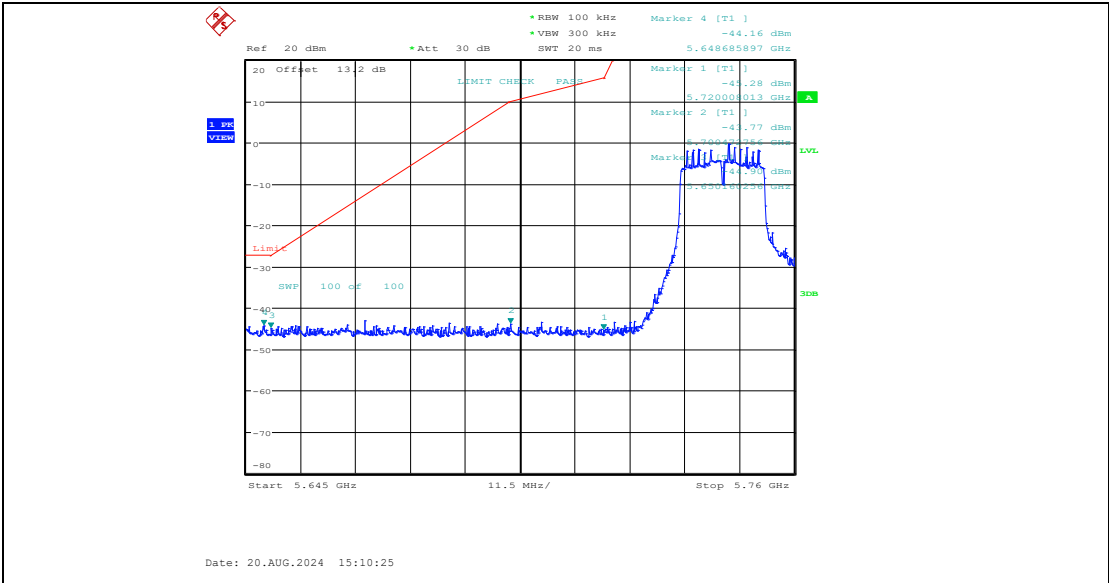
11AX160SISO_Ant1_High_5570



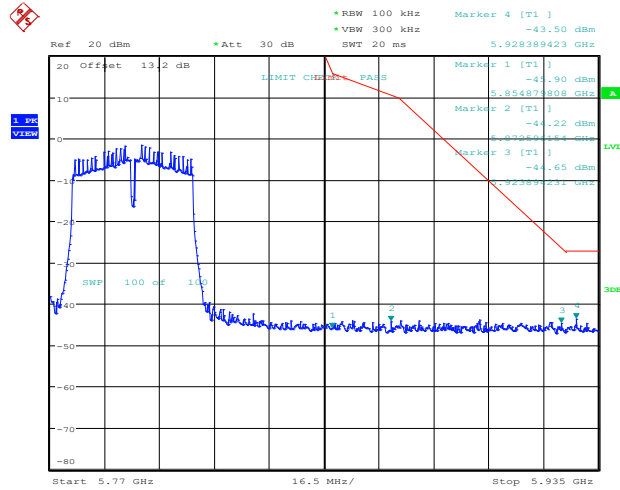
Date: 29.SEP.2024 10:01:44

Test Graphs B4



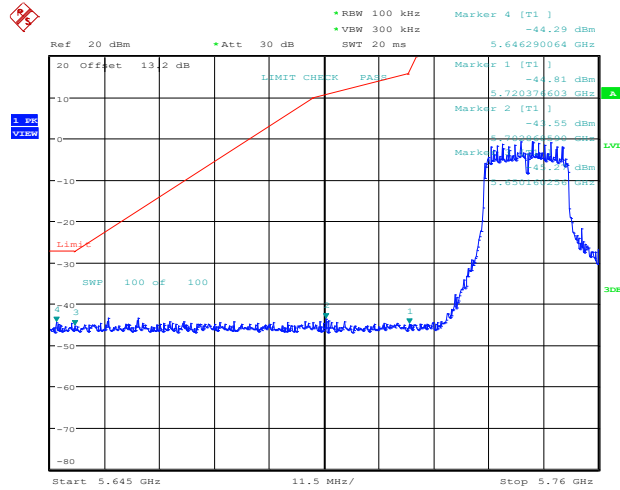


11N40SISO_Ant1_High_5795



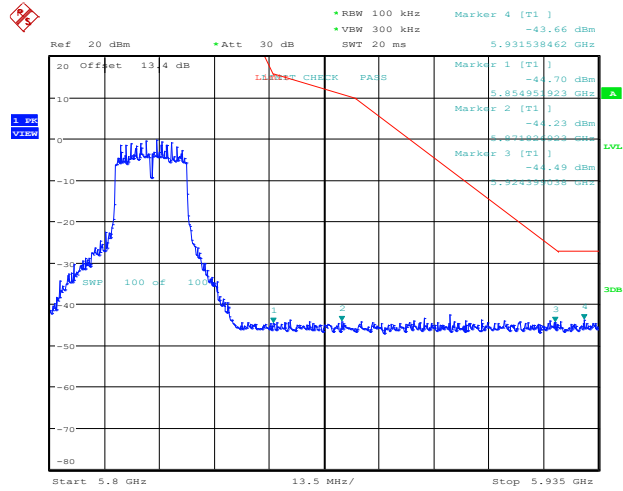
Date: 20.AUG.2024 15:46:01

11AC20SISO_Ant1_Low_5745



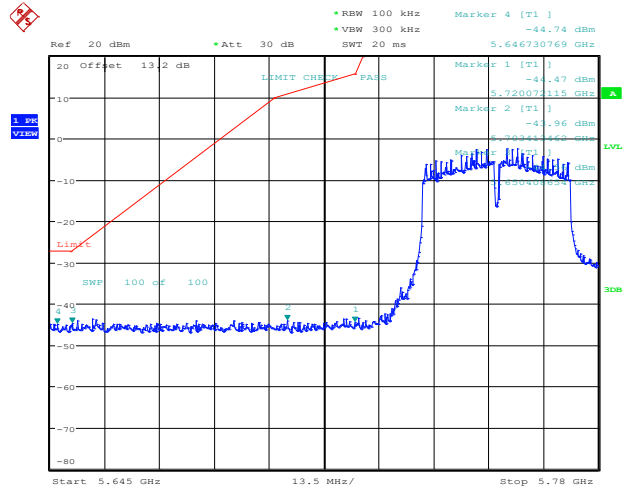
Date: 20.AUG.2024 16:16:25

11AC20SISO_Ant1_High_5825



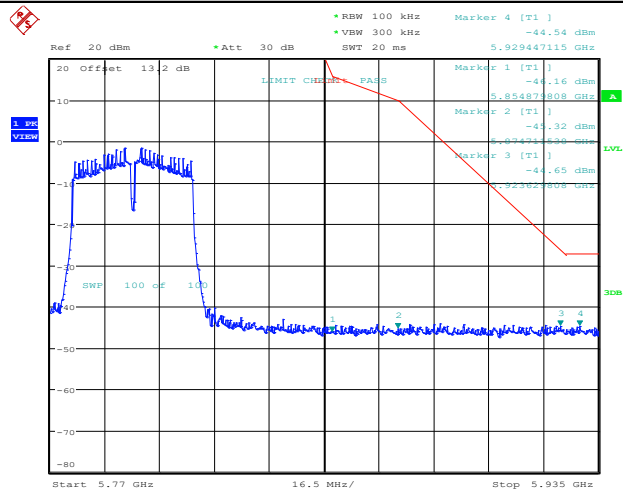
Date: 20.AUG.2024 16:23:38

11AC40SISO_Ant1_Low_5755



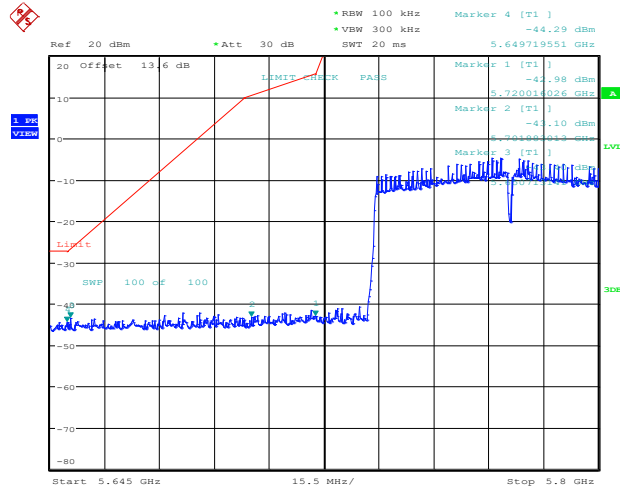
Date: 20.AUG.2024 16:50:26

11AC40SISO_Ant1_High_5795



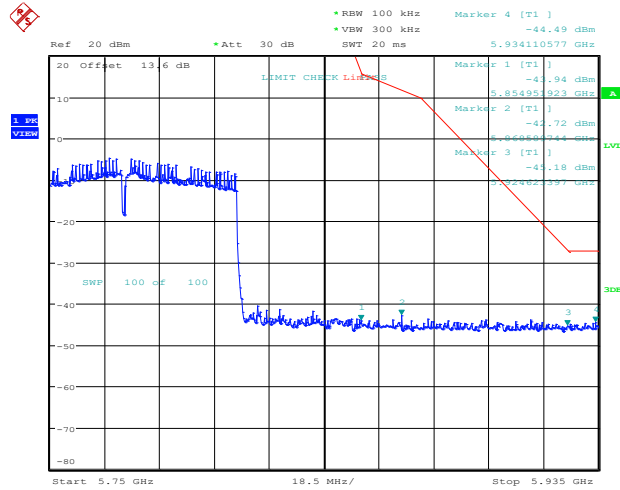
Date: 20.AUG.2024 16:53:34

11AC80SISO_Ant1_Low_5775



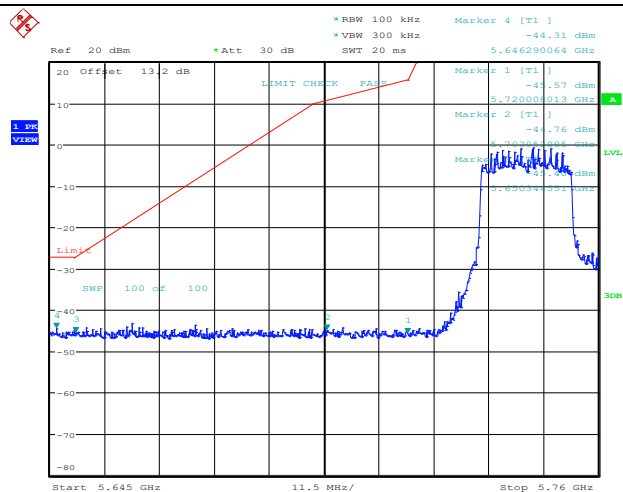
Date: 20.AUG.2024 17:05:42

11AC80SISO_Ant1_High_5775



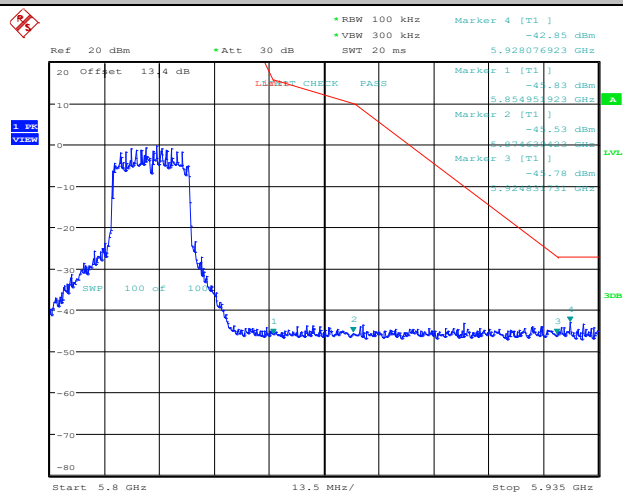
Date: 20.AUG.2024 17:06:12

11AX20SISO_Ant1_Low_5745



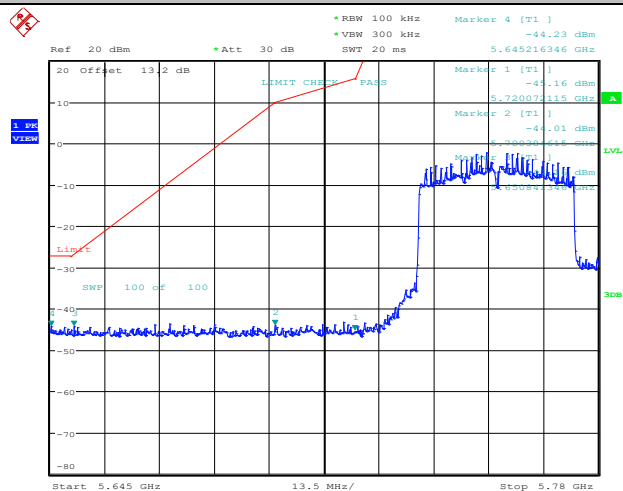
Date: 20.AUG.2024 17:36:24

11AX20SISO_Ant1_High_5825



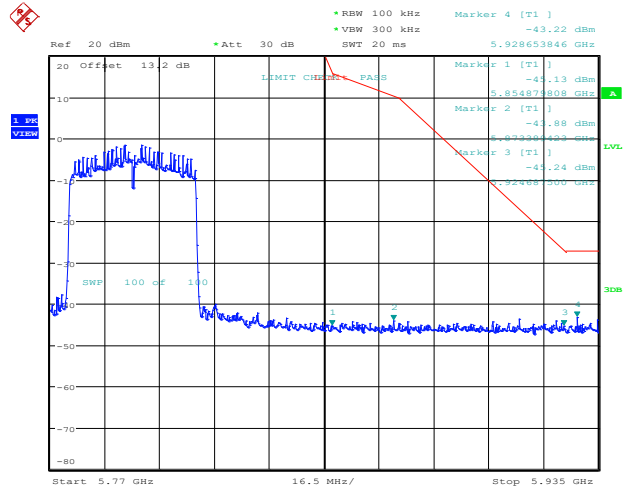
Date: 20.AUG.2024 17:42:43

11AX40SISO_Ant1_Low_5755



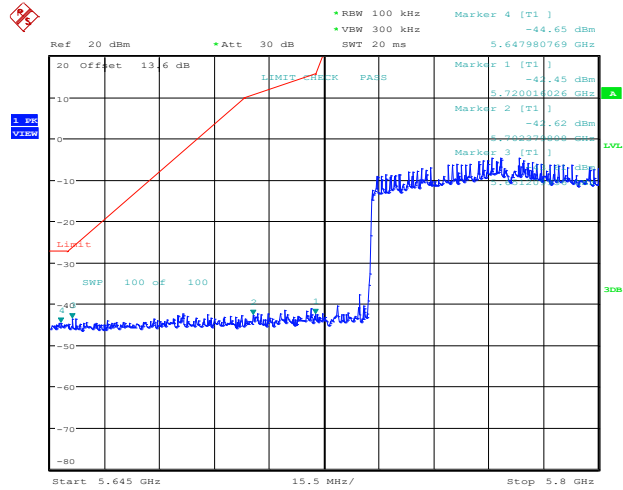
Date: 20.AUG.2024 18:05:06

11AX40SISO_Ant1_High_5795



Date: 20.AUG.2024 18:08:33

11AX80SISO_Ant1_Low_5775



Date: 20.AUG.2024 18:21:31