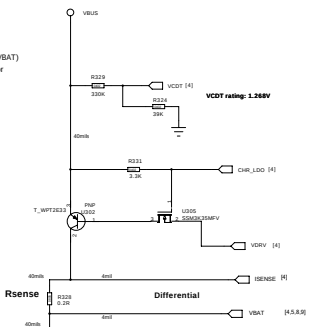
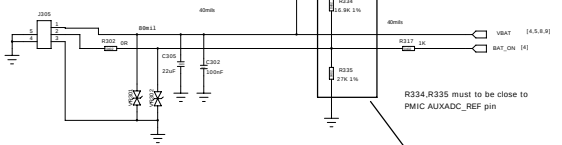


Charger

1. Close to Battery Connector.
(Rsense (R328) <10mm)
2. Main path should be 40mil.
(VBUS -> U303's E, -> U303's C -> R328 -> VBAT)
3. Star connection from R328 to BAT Connector



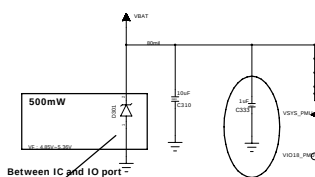
BATTERY CONNECTOR



Based on your system level design , if better ESD performance is needed on your system, please refer to ESD performance enhance proposal

if battery NTC is 10kohm, R334=39K, R335=90K
if battery NTC is 47kohm, R334=190K, R335=390K
Refer to MT6323 HW design notice

Add Zenar D
Place on the
from VBAT to
(Battery con
or test point
connector)



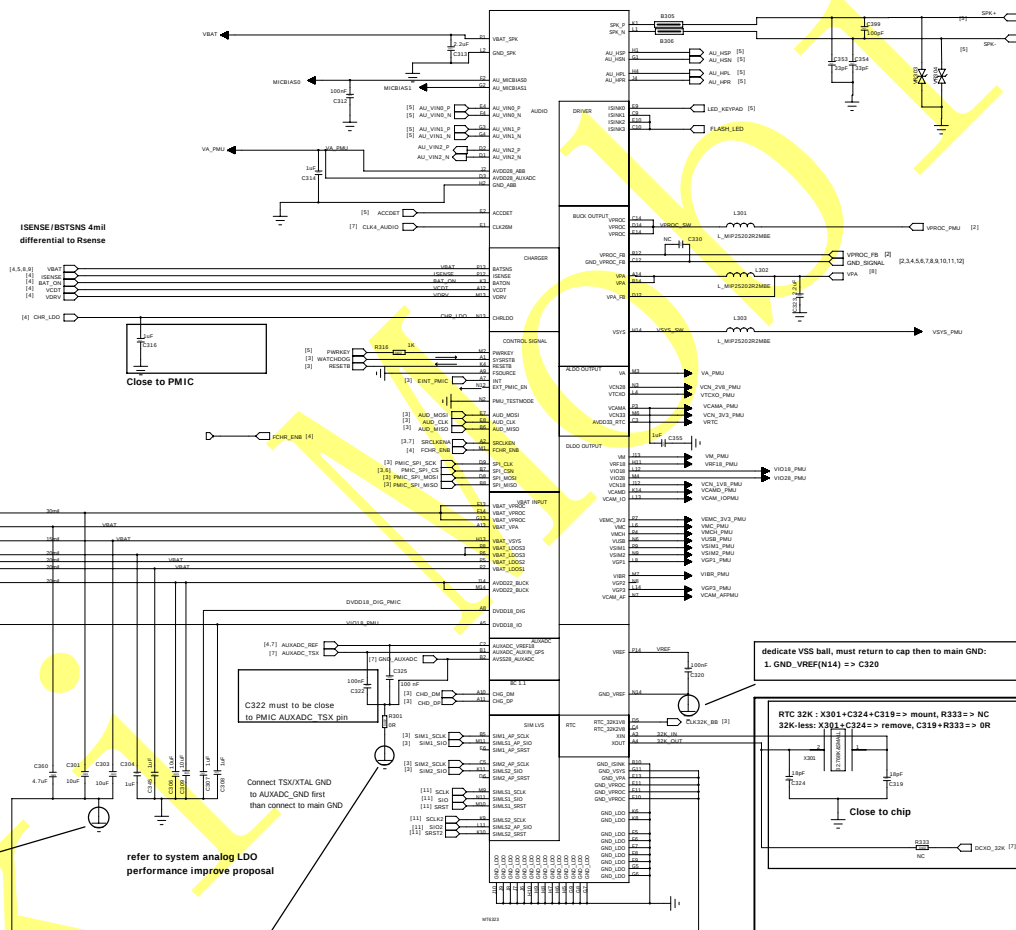
Refer to MT6323 design
notice for Zener selection

Based on your system level design , if better EOS performance is needed on your system, please refer to EOS performance enhance proposal

Refer to MT6323 design notice
for Buck GND layout rule

refer to system analog LDO
performance improve proposal

Refer to GPS co-clock layout rule



dedicate VSS ball, must return to cap then to main GND:
1. GND_VREF(N14) => C320

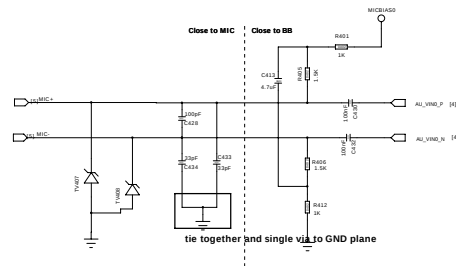
RTC 32K : X301+C324+C319=> mount, R333=> NC
32K-less: X301+C324=> remove, C319+R333=> 0R

==> for longer RTC time sustain after battery remove, please refer to RTC design notice

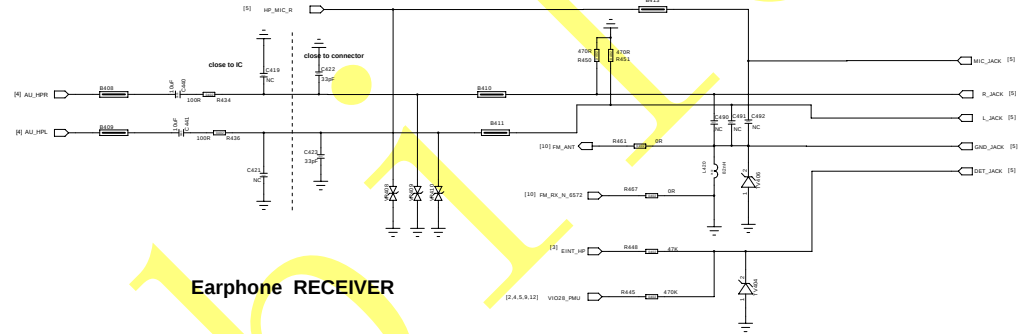
RTC

Title PMIC									
Size F		Document Number AX705 Mobile PHONE						Rev V1	
Date	Friday	June	28	2013	6	Sheet	4	of	13

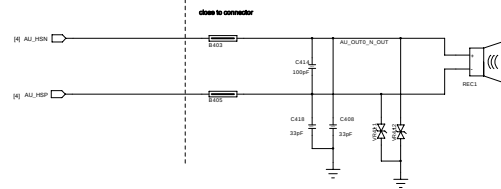
Handset Microphone 1



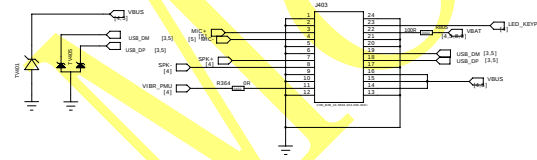
Earphone RECEIVER



HANDSET RECEIVER



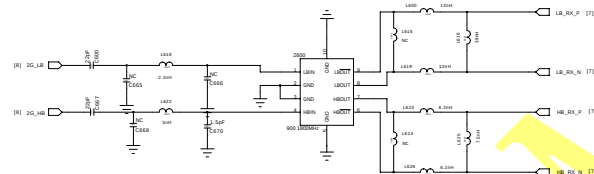
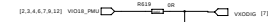
B2B



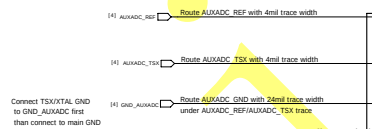
SKY77590 control logic table

MODE	Enable Vc1C Vc1B Vc1A		
LB_GMSK_TX	H	L	H
HB_GMSK_TX	H	L	H
LB_EDGE_TX	H	H	L
HB_EDGE_TX	H	H	L
TRX1	L	H	L
TRX2	L	H	L
TRX3	L	H	L
TRX4	L	H	H
TRX5	L	L	H
TRX6	L	L	H

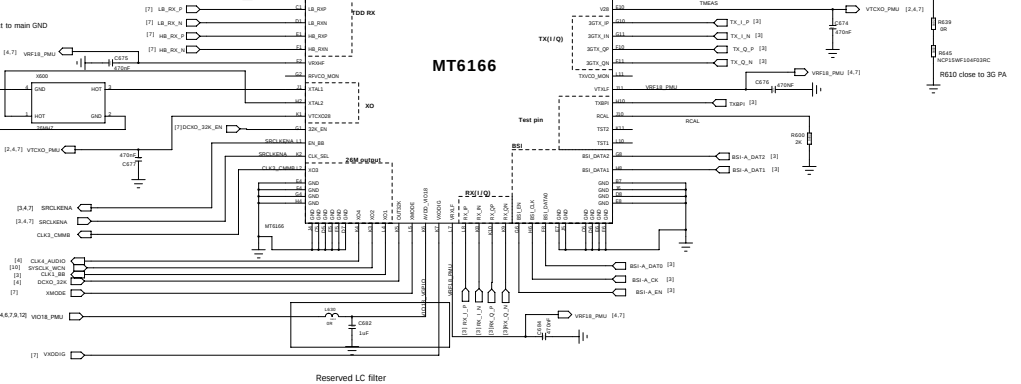
MODE	LC1C	LC1B	LC1A	Vc1C Vc1B Vc1A
DCXO + 32K XO	1(V1018)	1(V1018)	1(V1018)	
DCXO + 32K-Less	1(V1018)	1(V1018)	1(V1018)	

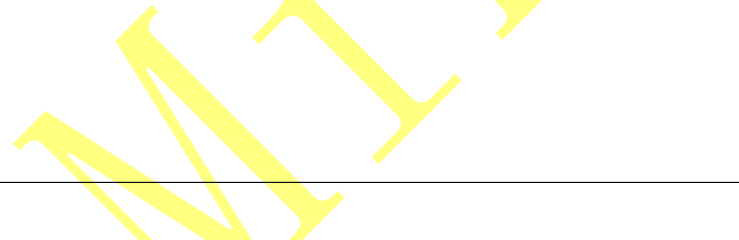
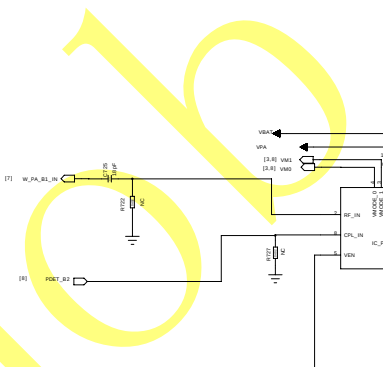


Two Application Circuit Conditions,
 1.TSX Circuit : X600=TSX, R653=R656=NC, R654=100K+1%, R655=R657=0ohm
 2.XTAL Circuit :X600=Mobile XTAL, R653=R656=0ohm, R654=R655=R657=NC

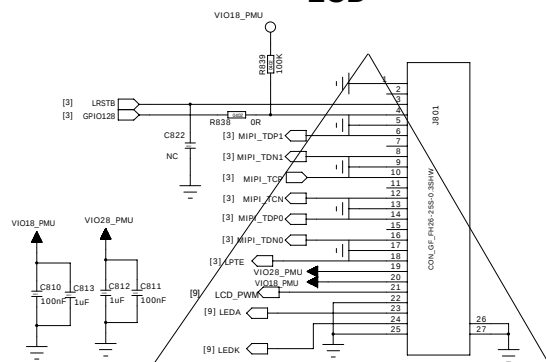


Route AUXADC_REF/AUXADC_TSX as differential trace with well GND shielding and route AUXADC_GND with 24mil trace width under AUXADC_REF/AUXADC_TSX trace.



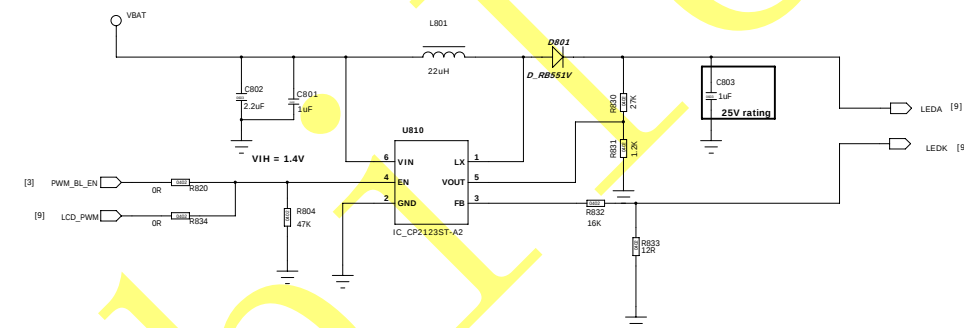


LCD

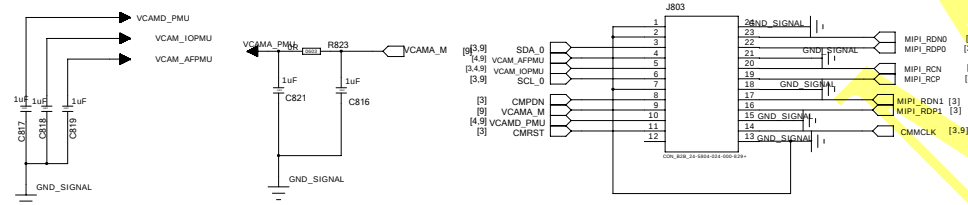


9 LED series

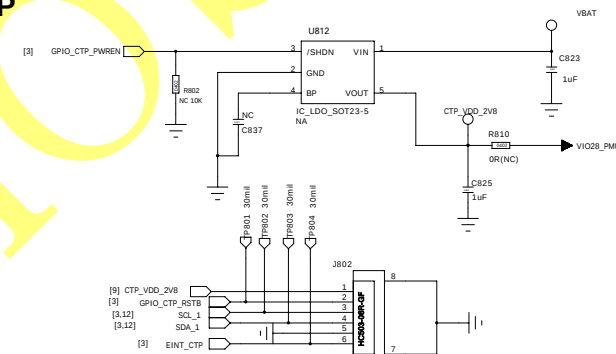
BACKLIGHT DRIVER



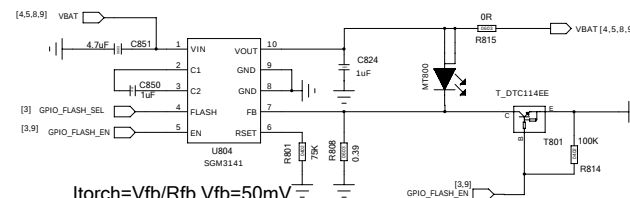
Camera

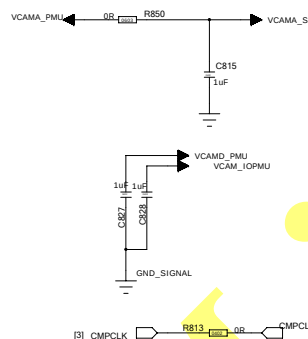


CTP



FLASH LED

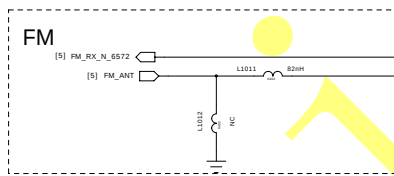
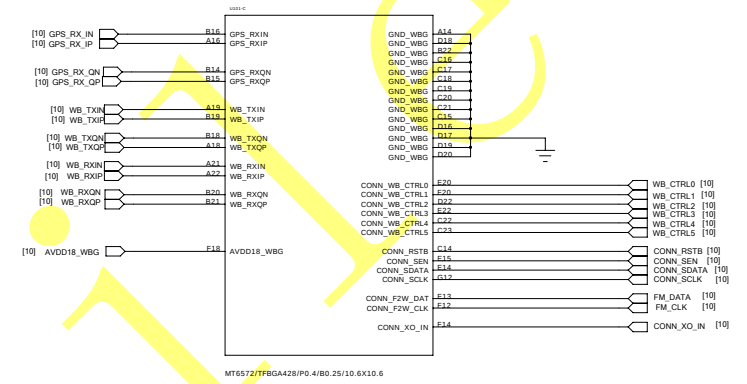

$$I_{\text{torch}} = V_{\text{fb}} / R_{\text{fb}} \quad V_{\text{fb}} = 50 \text{ mV}$$

$$I_{\text{flash}} = V_{\text{fb}} / R_{\text{fb}} \quad V_{\text{fb}} = 1.23 \times 11.2 \text{ K} / R_{\text{set}}$$


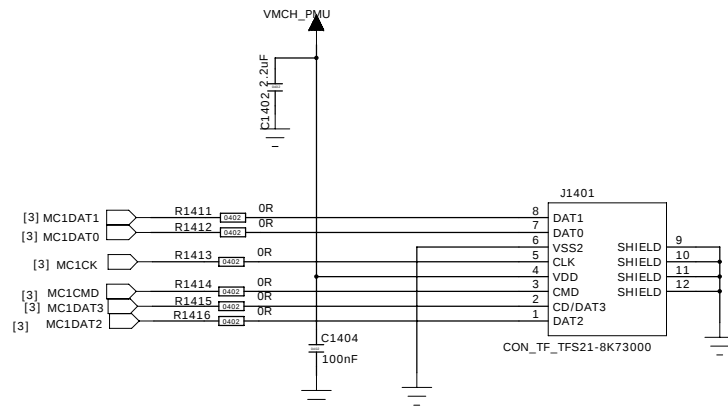
[3,9]	SDA_0	CAM_SDA	1
[9]	VCAMA_5	VCAMA	2
[3,9]	SCL_0	CAM_SCL	3
[3]	SUB_CMRST	CMRST	4
[3]	SUB_CMVSTN	VSTN	5
[3]	SUB_CMDFN	CMDFN	6
[3]	CMVSTN	CMVSTN	7
[3,4,9]	VCAM_IOPMU	VCAMO	8
[4,9]	VCAMO_IOPMU	VCAMO	9
[3]	CMUAT7	CAMUAT7	10
[3]	CMUAT6	CMUAT6	11
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[3]	CMUAT6	CMUAT6	347

same as L052

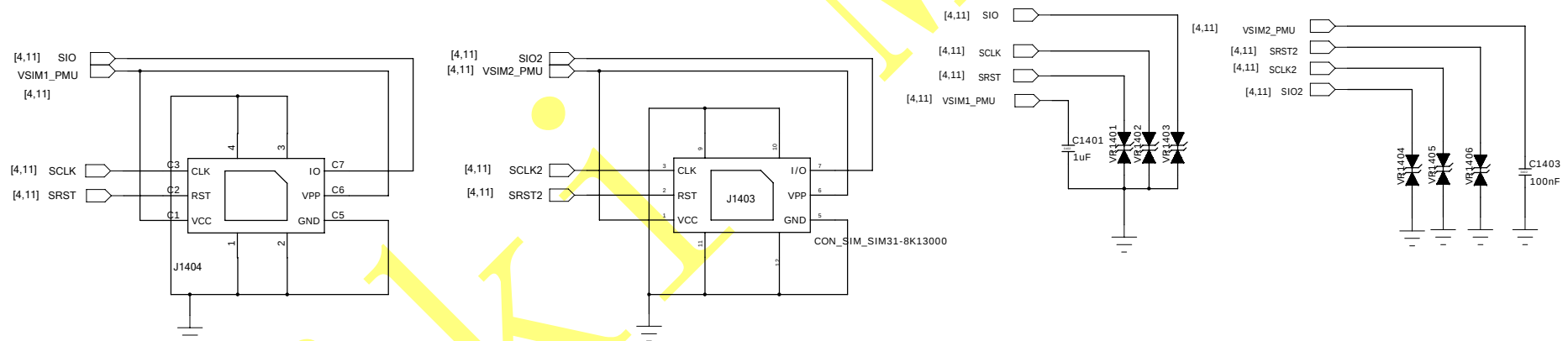
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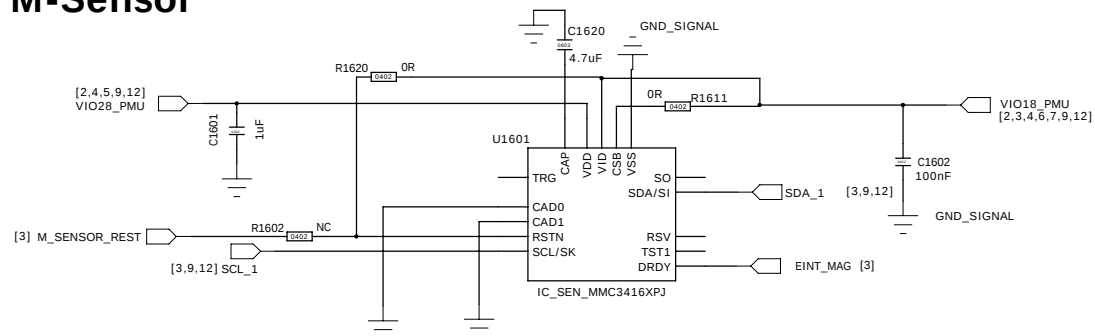
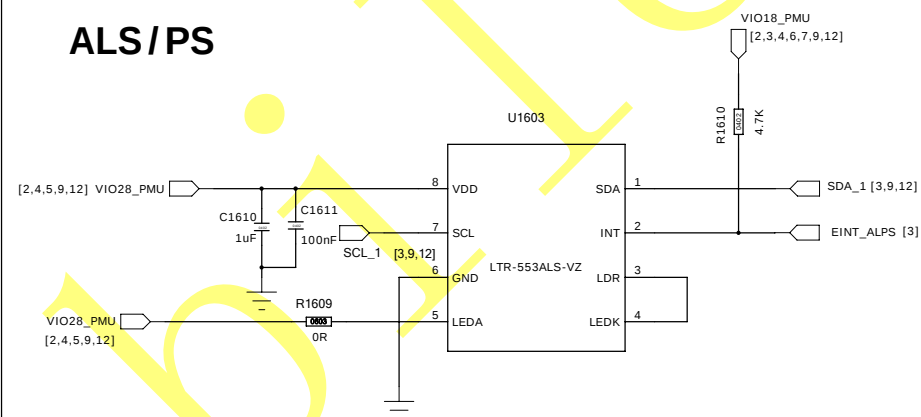
SD CARD



SIM CARD



M-Sensor

**ALS/PS**

G-Sensor

