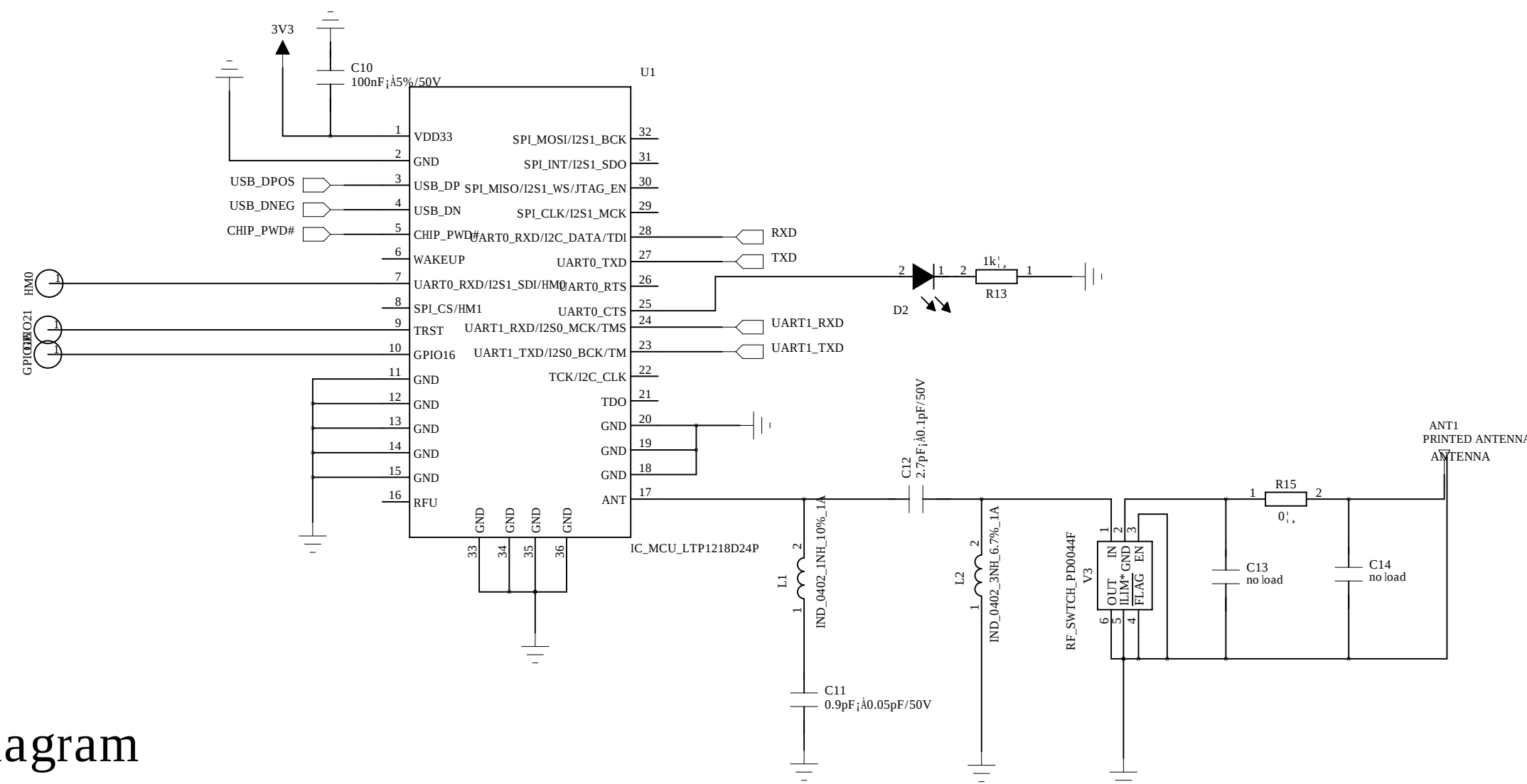
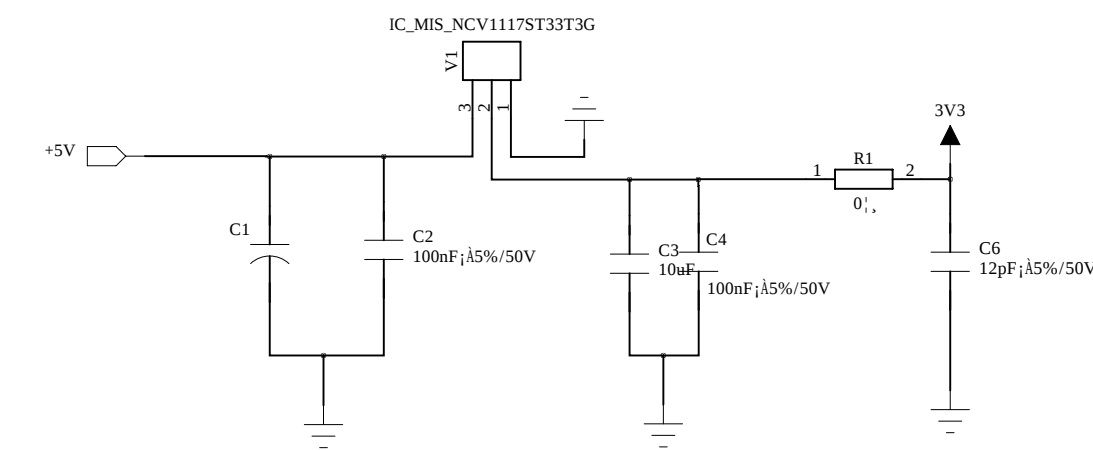
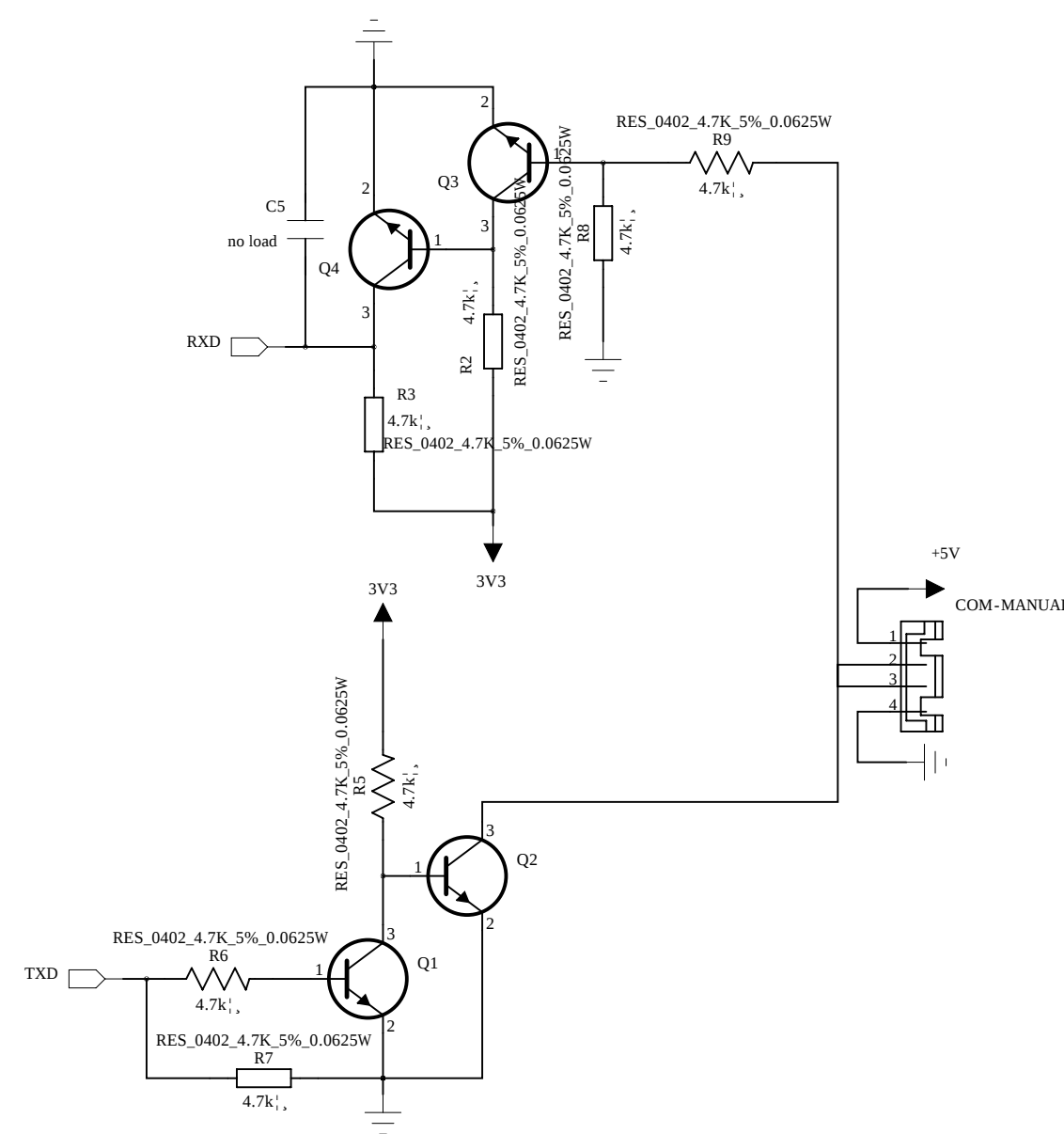




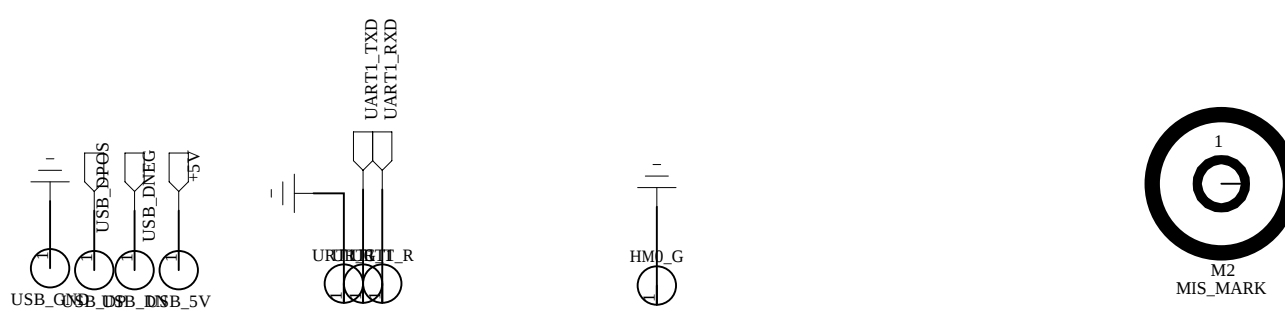
Main Circuit Diagram



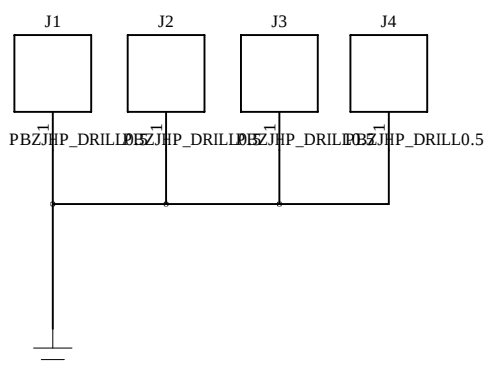
5V to 3V3 POWER



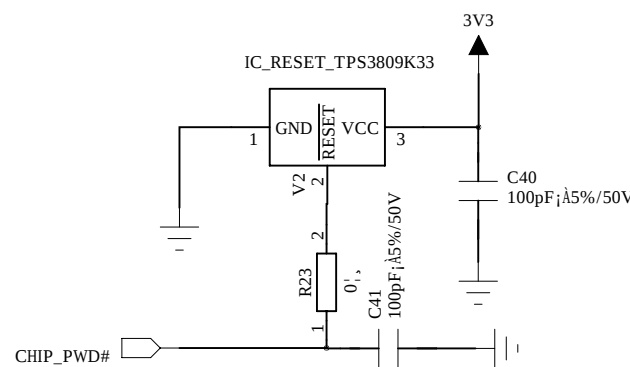
UART IO Switch



Test Point



Low_Voltage Reset



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ēēāēēŲŲ DESIGN ORIGIN	ēŲŲŲ TRIAL ☐	ēŲāŲ PRODUCTION ☐	
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