

Appendix D.4: Maximum conducted output power

Test Result Channel Power

Test Mode	Antenna	Frequency[M Hz]	Channel Power [dBm]	Duty Cycle [%]	DC Factor [dBm]	Result [dBm]	Limit [dBm]	Verdict
11A	Ant2	5180	13.38	98.11	0.08	13.46	≤23.98	PASS
		5200	13.78	98.58	0.06	13.84	≤23.98	PASS
		5240	13.46	98.11	0.08	13.54	≤23.98	PASS
11N20SISO	Ant2	5180	13.34	98.87	0.05	13.39	≤23.98	PASS
		5200	13.68	98.87	0.05	13.73	≤23.98	PASS
		5240	13.36	98.87	0.05	13.41	≤23.98	PASS
11N40SISO	Ant2	5190	13.58	98.11	0.08	13.66	≤23.98	PASS
		5230	13.97	98.49	0.07	14.04	≤23.98	PASS
11AC20SISO	Ant2	5180	13.32	98.86	0.05	13.37	≤23.98	PASS
		5200	13.65	98.86	0.05	13.70	≤23.98	PASS
		5240	13.38	98.86	0.05	13.43	≤23.98	PASS
11AC40SISO	Ant2	5190	13.54	98.48	0.07	13.61	≤23.98	PASS
		5230	10.02	98.48	0.07	10.09	≤23.98	PASS
11AC80SISO	Ant2	5210	13.90	98.48	0.07	13.97	≤23.98	PASS
11AX20SISO	Ant2	5180	13.13	98.49	0.07	13.20	≤23.98	PASS
		5200	13.50	98.49	0.07	13.57	≤23.98	PASS
		5240	13.23	98.49	0.07	13.30	≤23.98	PASS
11AX40SISO	Ant2	5190	13.21	98.11	0.08	13.29	≤23.98	PASS
		5230	13.73	98.86	0.05	13.78	≤23.98	PASS
11AX80SISO	Ant2	5210	13.52	98.48	0.07	13.59	≤23.98	PASS

Note: The Duty Cycle Factor is compensated in the graph.

Appendix D.5: Maximum power spectral density

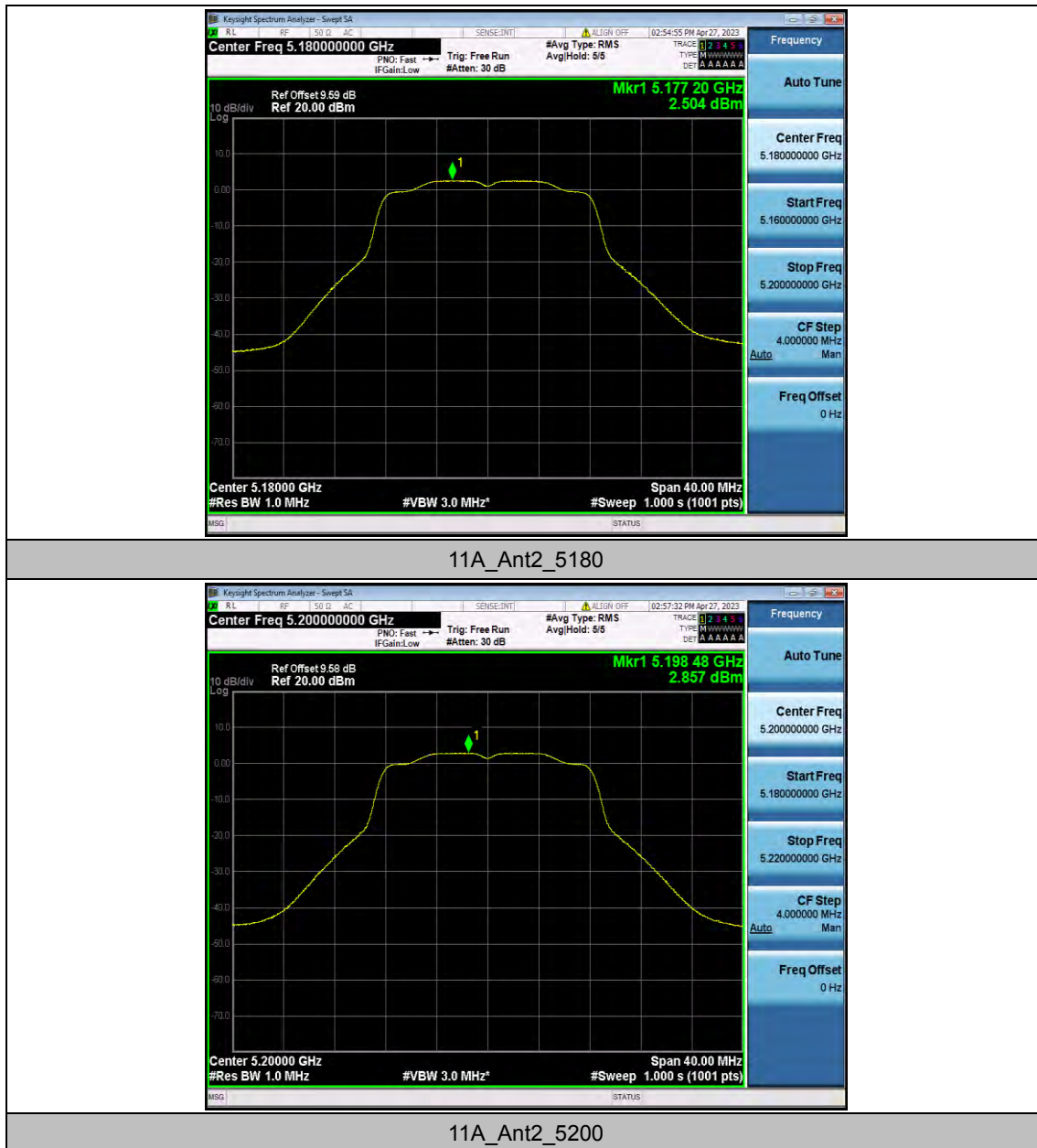
Test Result

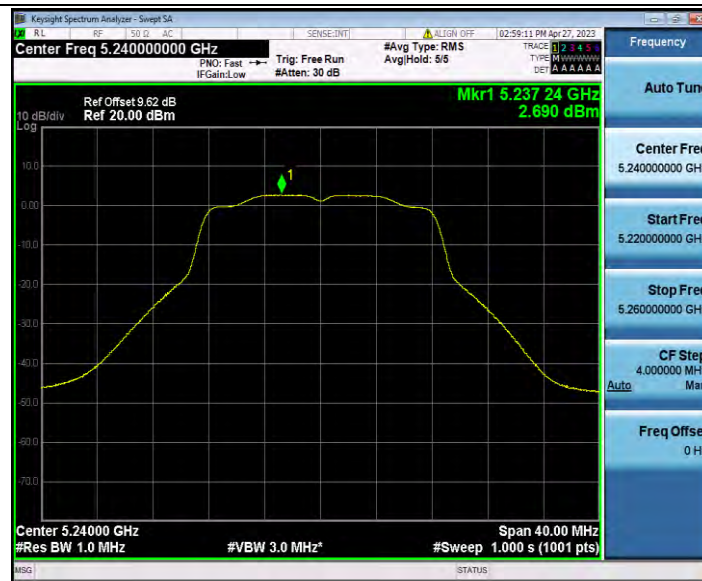
TestMode	Antenna	Frequency[MHz]	Result [dBm/MHz]	Limit[dBm/MHz]	Verdict
11A	Ant2	5180	2.5	≤11.00	PASS
		5200	2.86	≤11.00	PASS
		5240	2.69	≤11.00	PASS
11N20SISO	Ant2	5180	2.2	≤11.00	PASS
		5200	2.55	≤11.00	PASS
		5240	2.43	≤11.00	PASS
11N40SISO	Ant2	5190	-0.69	≤11.00	PASS
		5230	-0.3	≤11.00	PASS
11AC20SISO	Ant2	5180	2.2	≤11.00	PASS
		5200	2.52	≤11.00	PASS
		5240	2.45	≤11.00	PASS
11AC40SISO	Ant2	5190	-0.66	≤11.00	PASS
		5230	-4.19	≤11.00	PASS
11AC80SISO	Ant2	5210	-3.23	≤11.00	PASS
11AX20SISO	Ant2	5180	1.81	≤11.00	PASS
		5200	2.14	≤11.00	PASS
		5240	2.03	≤11.00	PASS
11AX40SISO	Ant2	5190	-1.23	≤11.00	PASS
		5230	-0.81	≤11.00	PASS
11AX80SISO	Ant2	5210	-3.67	≤11.00	PASS

Note: 1.The Result and Limit Unit is dBm/500 kHz in the band 5.725–5.85 GHz.

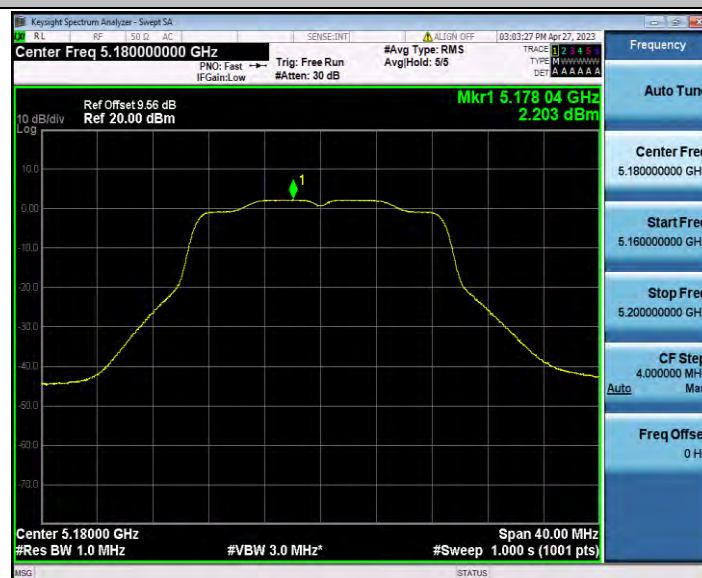
2.The Duty Cycle Factor and RBW Factor is compensated in the graph.

Test Graphs





11A_Ant2_5240



11N20SISO_Ant2_5180



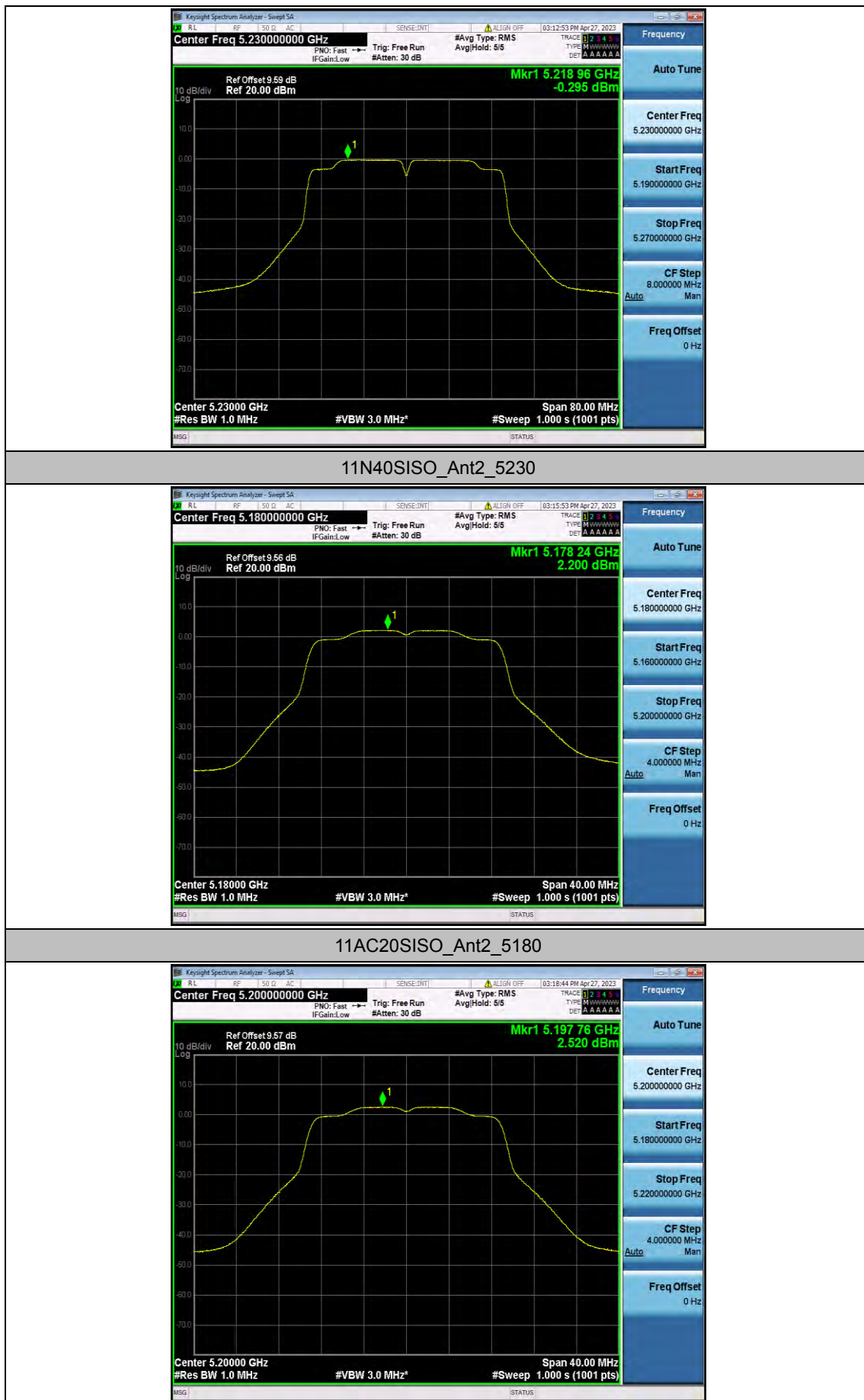
11N20SISO_Ant2_5200



11N20SISO_Ant2_5240



11N40SISO_Ant2_5190



11AC20SISO_Ant2_5200



11AC20SISO_Ant2_5240



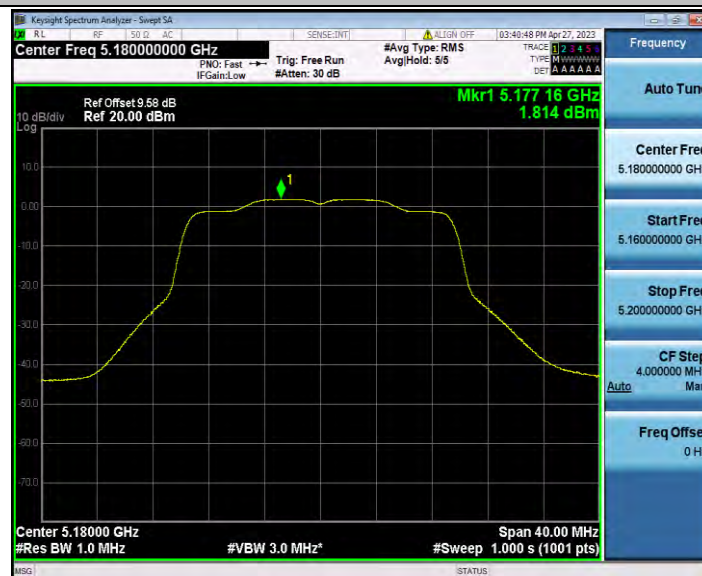
11AC40SISO_Ant2_5190



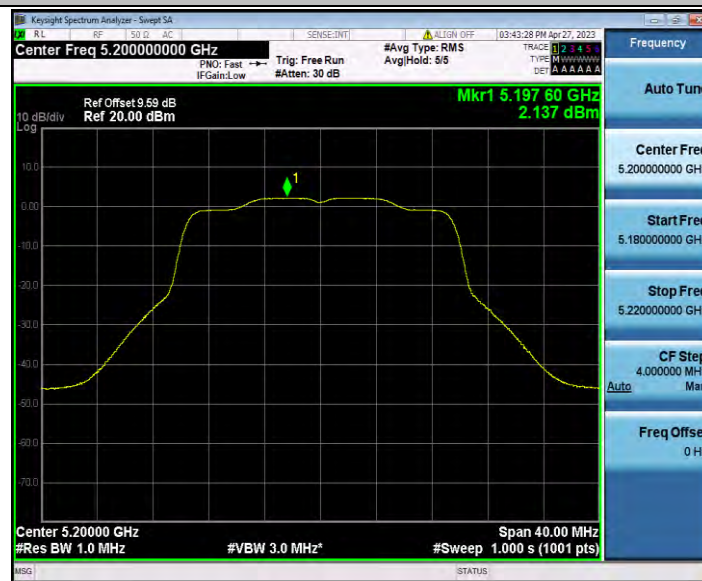
11AC40SISO_Ant2_5230



11AC80SISO_Ant2_5210



11AX20SISO_Ant2_5180



11AX20SISO_Ant2_5200



11AX20SISO_Ant2_5240



11AX40SISO_Ant2_5190



11AX40SISO_Ant2_5230



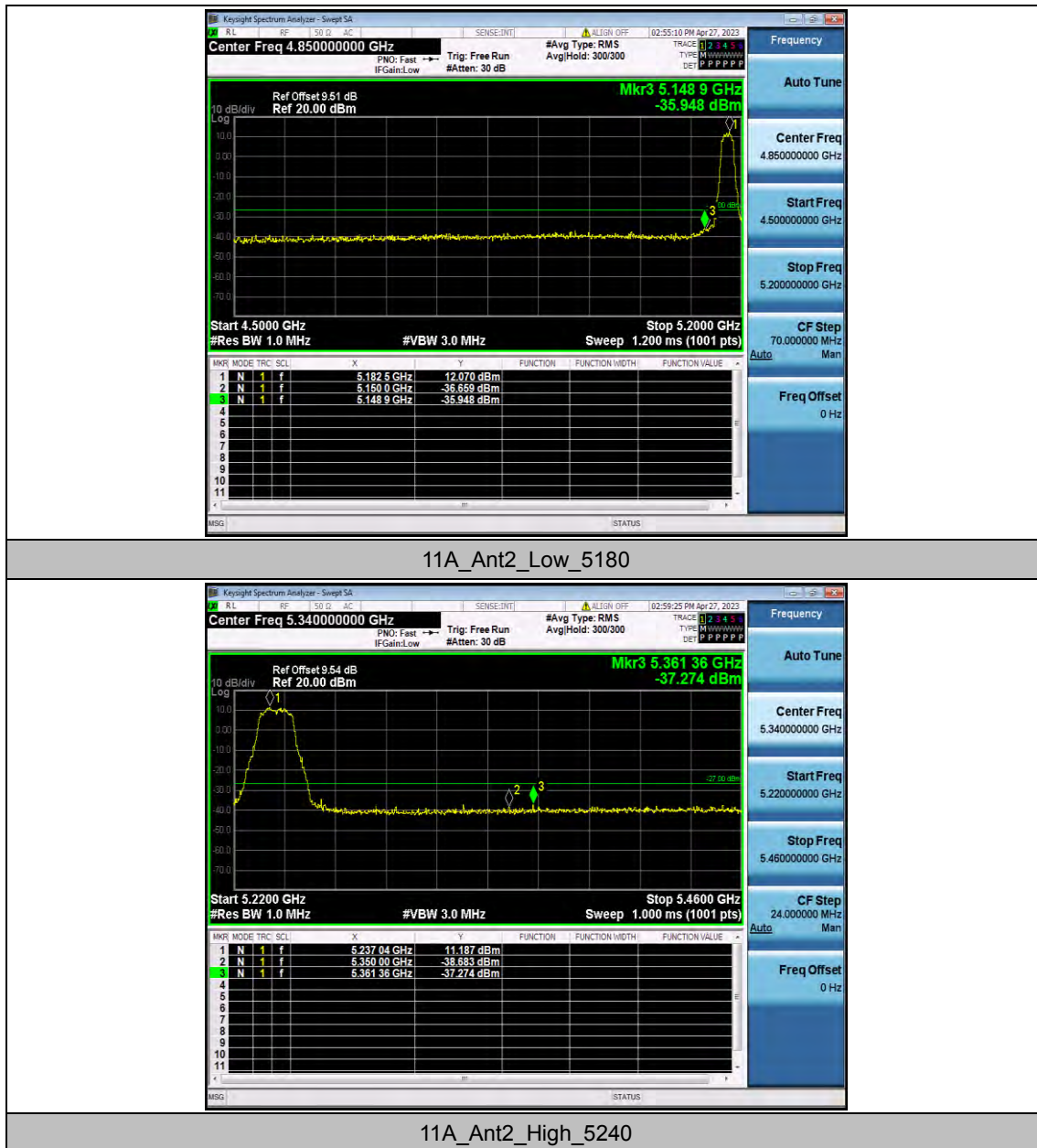
11AX80SISO_Ant2_5210

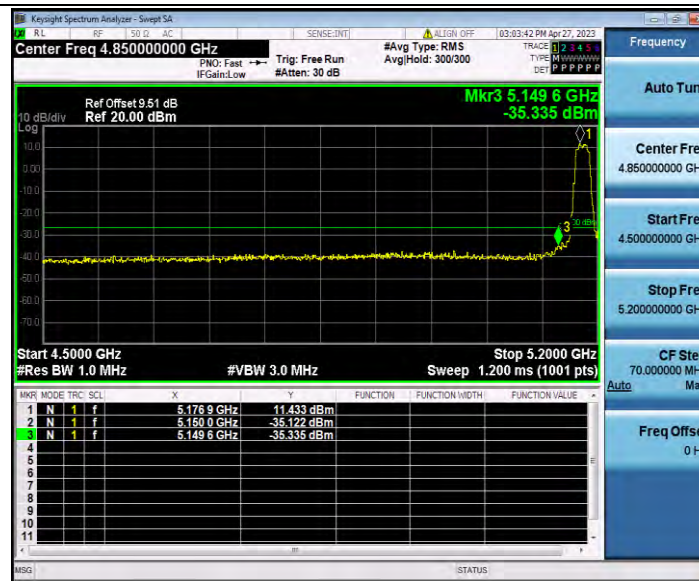
Appendix D.6: Band edge measurements

Test Result B1

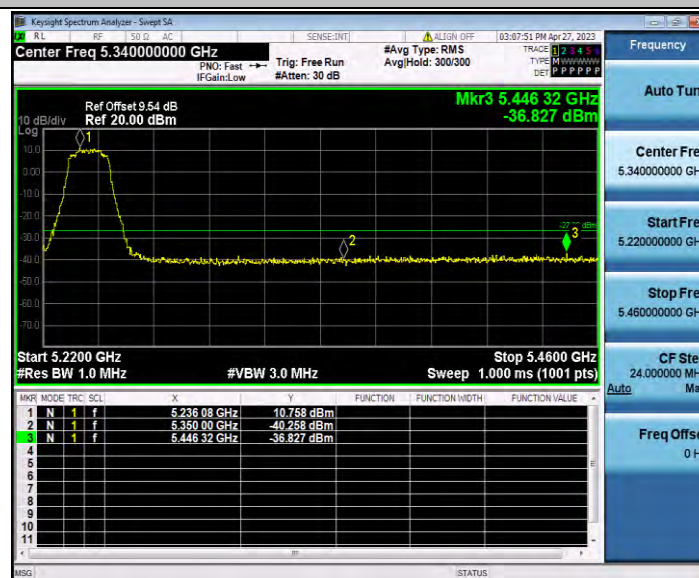
TestMode	Antenna	ChName	Frequency[MHz]	Result[dBm]	Limit[dBm]	Verdict
11A	Ant2	Low	5180	-35.95	≤ -27	PASS
		High	5240	-37.27	≤ -27	PASS
11N20SISO	Ant2	Low	5180	-35.34	≤ -27	PASS
		High	5240	-36.83	≤ -27	PASS
11N40SISO	Ant2	Low	5190	-34.36	≤ -27	PASS
		High	5230	-37.61	≤ -27	PASS
11AC20SISO	Ant2	Low	5180	-37.07	≤ -27	PASS
		High	5240	-37.8	≤ -27	PASS
11AC40SISO	Ant2	Low	5190	-33.87	≤ -27	PASS
		High	5230	-37.52	≤ -27	PASS
11AC80SISO	Ant2	Low	5210	-32.62	≤ -27	PASS
		High	5210	-37.06	≤ -27	PASS
11AX20SISO	Ant2	Low	5180	-35.88	≤ -27	PASS
		High	5240	-37.31	≤ -27	PASS
11AX40SISO	Ant2	Low	5190	-33.69	≤ -27	PASS
		High	5230	-37.86	≤ -27	PASS
11AX80SISO	Ant2	Low	5210	-33.36	≤ -27	PASS
		High	5210	-37.77	≤ -27	PASS

Test Graphs B1

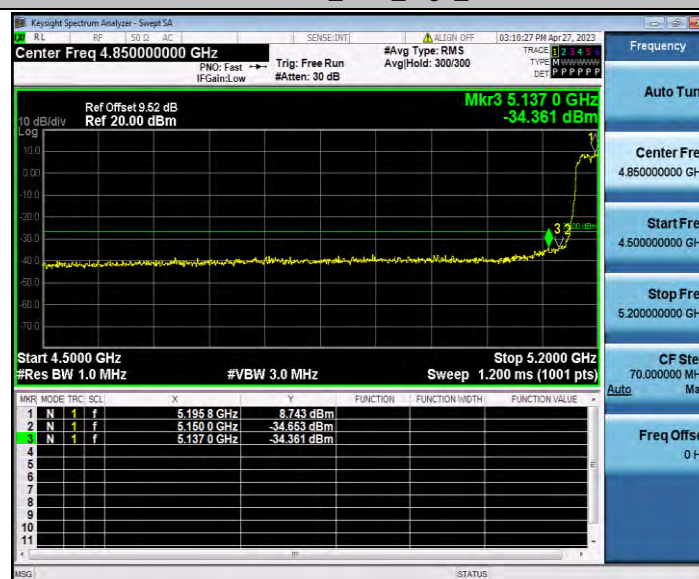




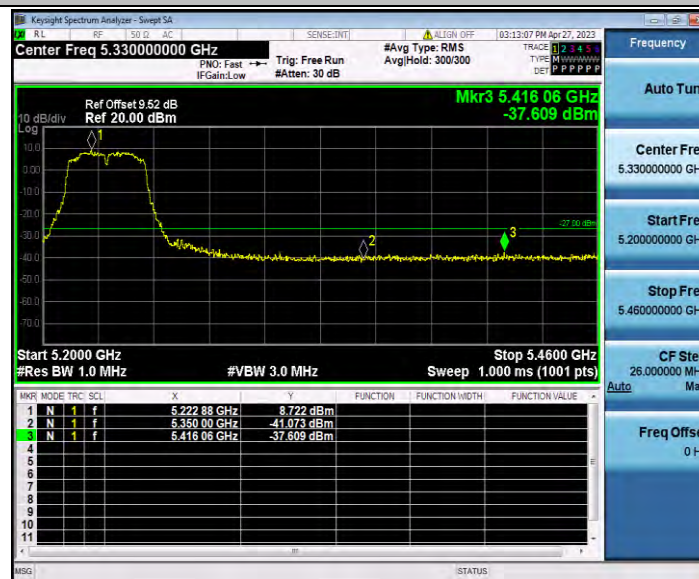
11N20SISO_Ant2_Low_5180



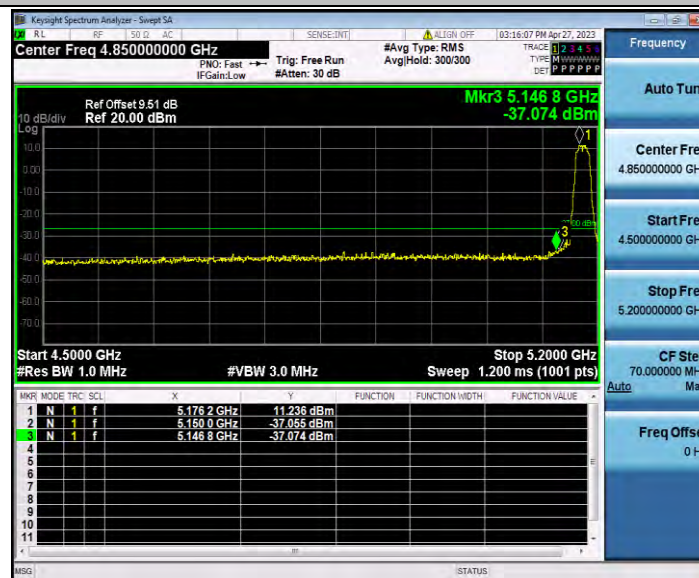
11N20SISO_Ant2_High_5240



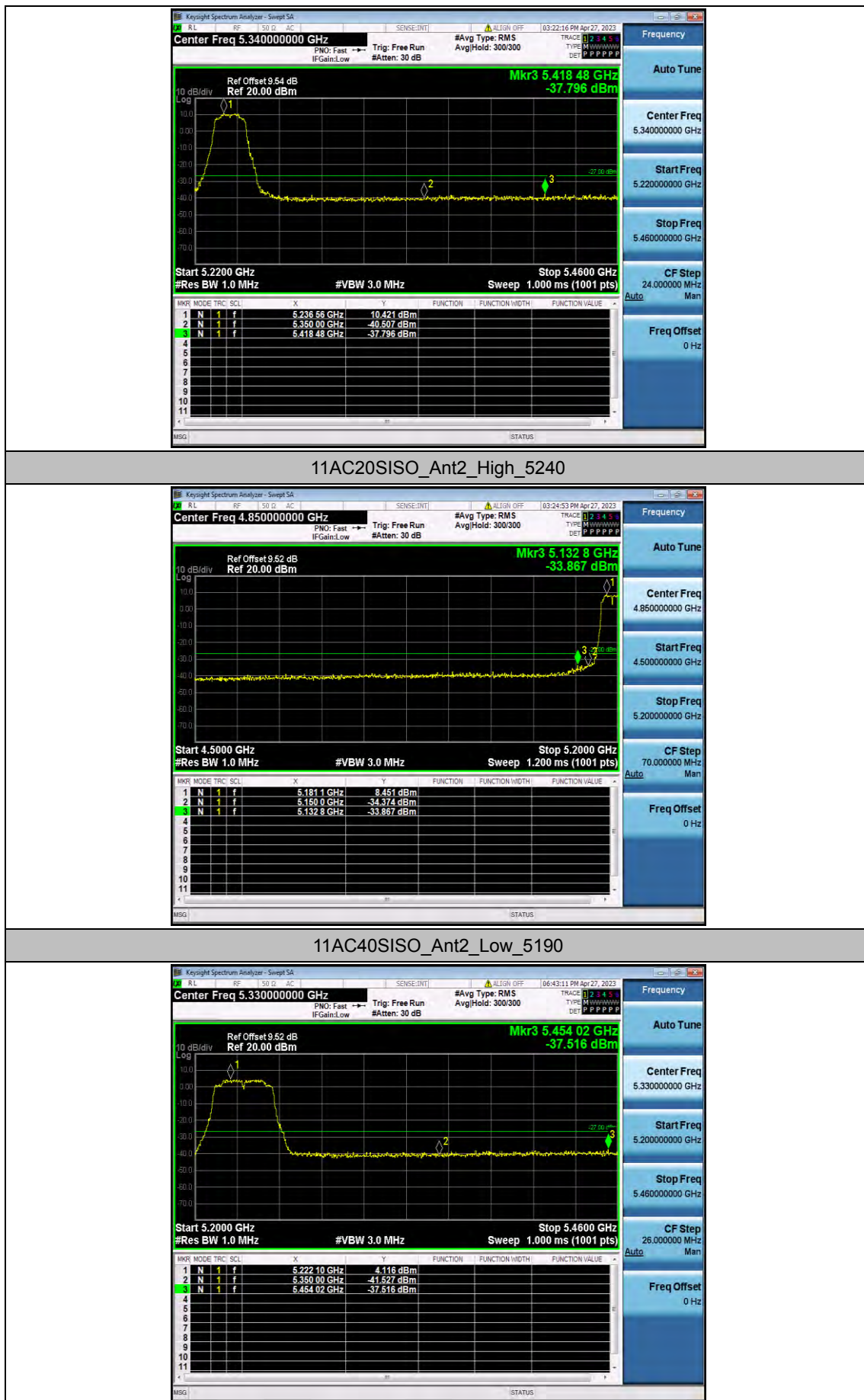
11N40SISO_Ant2_Low_5190



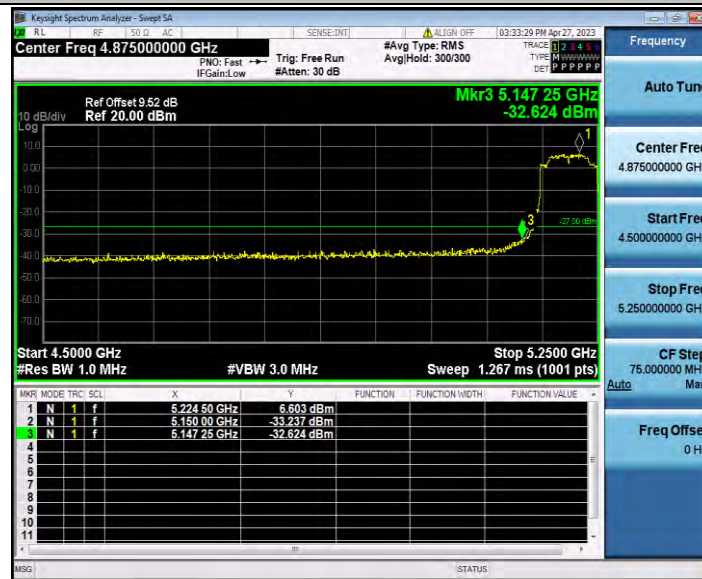
11N40SISO_Ant2_High_5230



11AC20SISO_Ant2_Low_5180



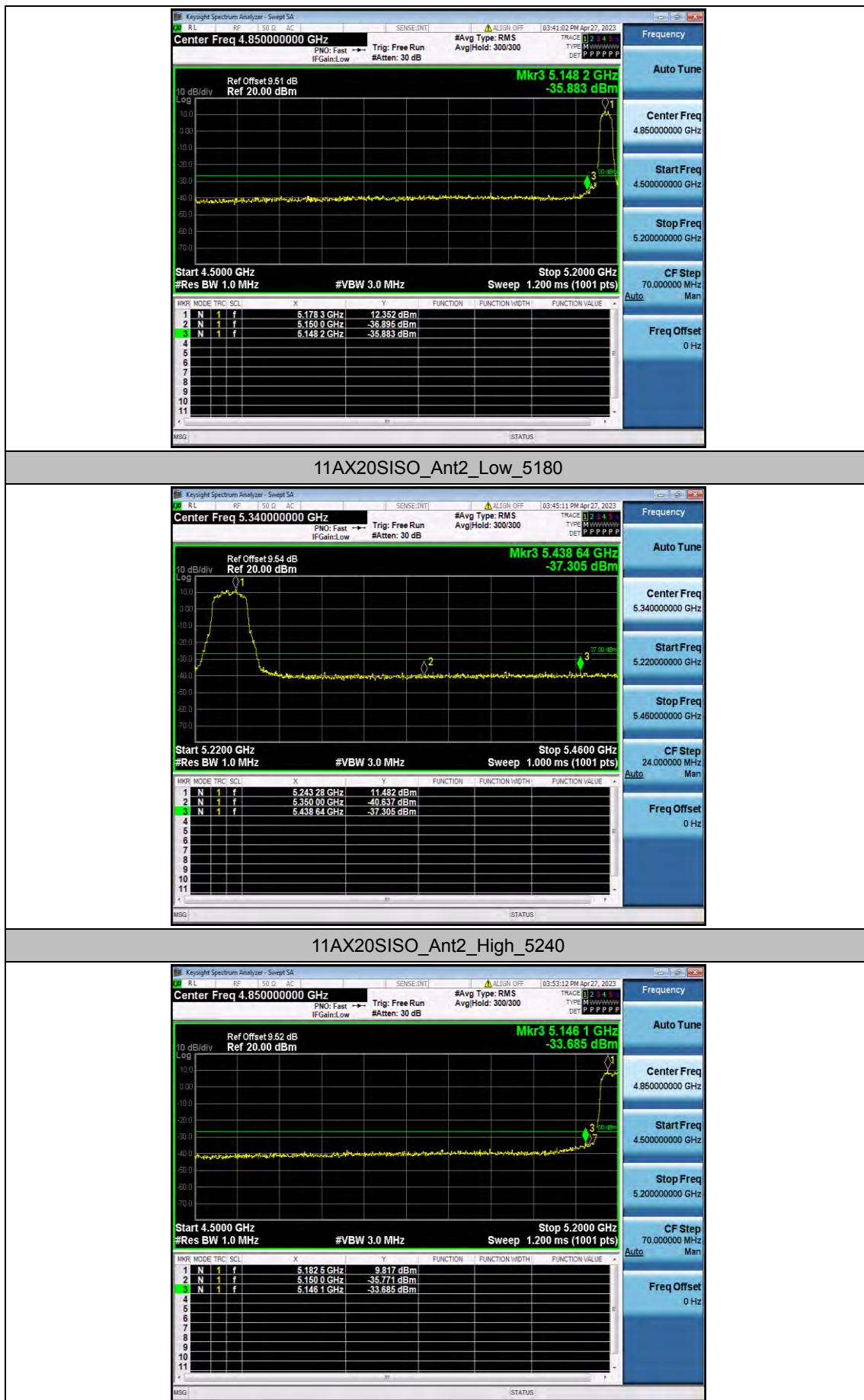
11AC40SISO_Ant2_High_5230



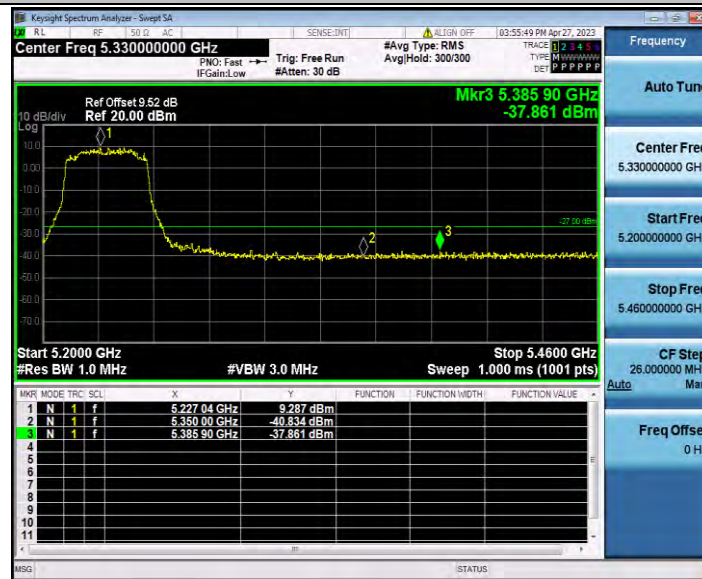
11AC80SISO_Ant2_Low_5210



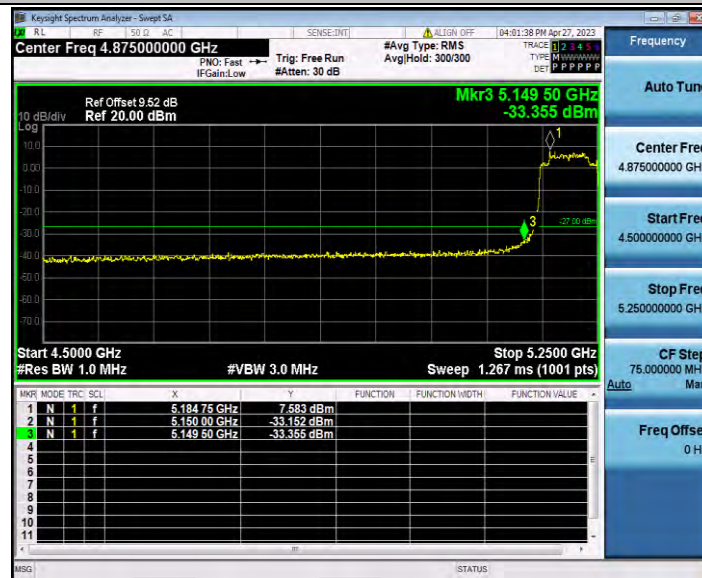
11AC80SISO_Ant2_High_5210



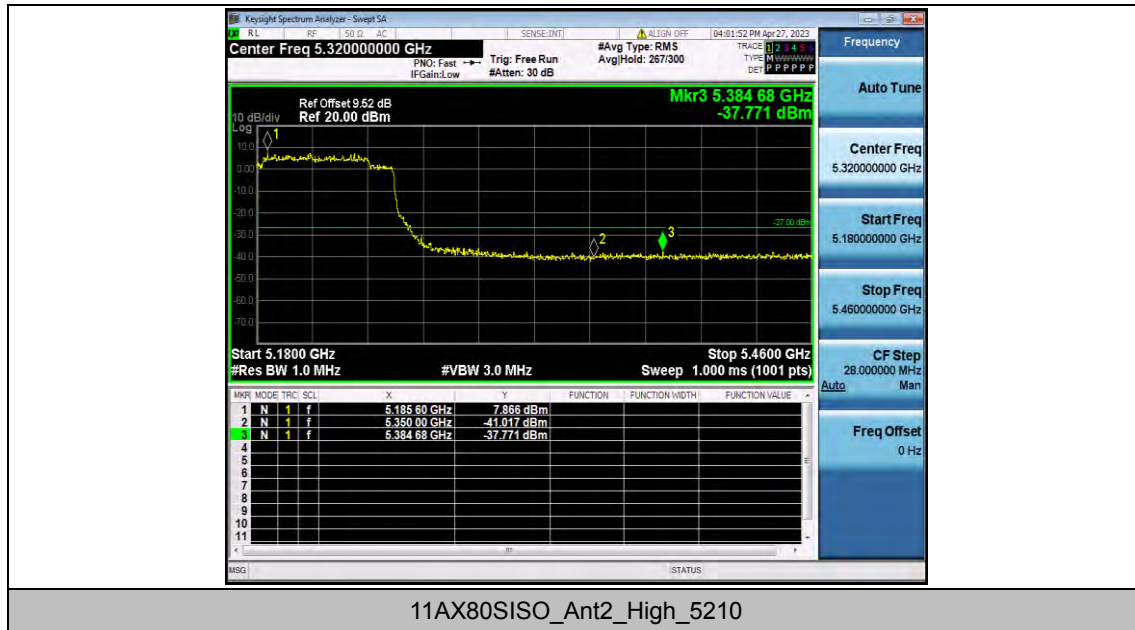
11AX40SISO_Ant2_Low_5190



11AX40SISO_Ant2_High_5230



11AX80SISO_Ant2_Low_5210



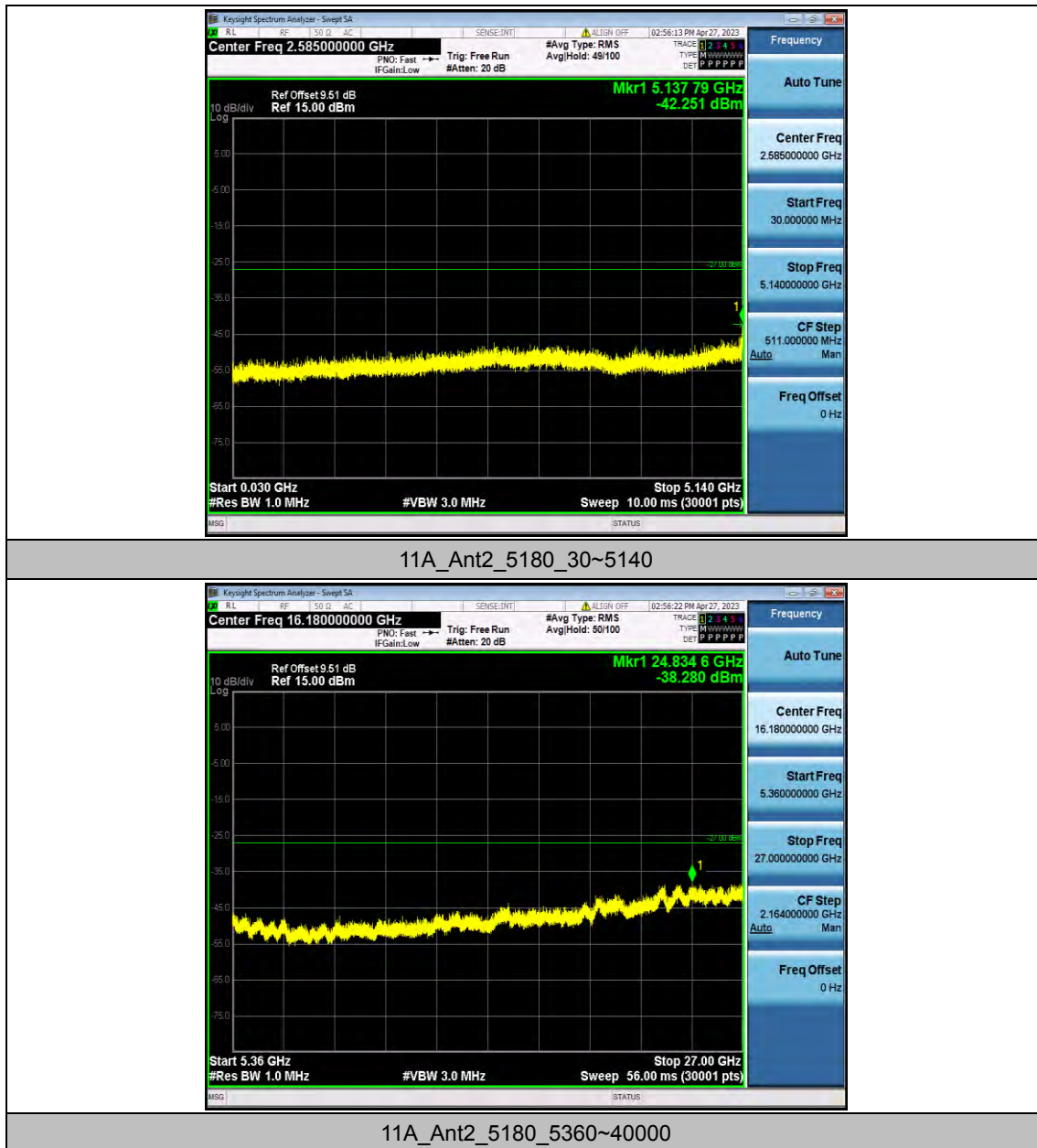
Appendix D.7: Conducted Spurious Emission

Test Result

TestMode	Antenna	Frequency[MHz]	FreqRange [MHz]	Max. Fre [MHz]	Max. Level [dBm]	Limit [dBm]	Verdict
11A	Ant2	5180	30~5140	5137.79	-42.25	≤-27	PASS
			5360~40000	24834.56	-38.28	≤-27	PASS
		5200	30~5140	2546.5	-46.42	≤-27	PASS
			5360~40000	25224.08	-37.01	≤-27	PASS
		5240	30~5140	4958.6	-47.17	≤-27	PASS
			5360~40000	24294.28	-38.2	≤-27	PASS
11N20SISO	Ant2	5180	30~5140	5137.96	-43.59	≤-27	PASS
			5360~40000	24251.72	-37.64	≤-27	PASS
		5200	30~5140	904.83	-41.63	≤-27	PASS
			5360~40000	26484.97	-37.6	≤-27	PASS
		5240	30~5140	4837.15	-47.24	≤-27	PASS
			5360~40000	25253.65	-37.78	≤-27	PASS
11N40SISO	Ant2	5190	30~5140	5137.27	-37.85	≤-27	PASS
			5360~40000	24226.47	-37.87	≤-27	PASS
		5230	30~5140	5136.25	-44.21	≤-27	PASS
			5360~40000	26557.82	-37.95	≤-27	PASS
11AC20SISO	Ant2	5180	30~5140	5138.3	-42.41	≤-27	PASS
			5360~40000	24882.17	-37.88	≤-27	PASS
		5200	30~5140	5138.64	-46.69	≤-27	PASS
			5360~40000	25009.84	-37.41	≤-27	PASS
		5240	30~5140	5112.75	-46.78	≤-27	PASS
			5360~40000	24913.9	-37.08	≤-27	PASS
11AC40SISO	Ant2	5190	30~5140	904.83	-34.17	≤-27	PASS
			5360~40000	26039.91	-37.57	≤-27	PASS
		5230	30~5140	2685.5	-47.77	≤-27	PASS
			5360~40000	24805.7	-36.79	≤-27	PASS
11AC80SISO	Ant2	5210	30~5140	5134.04	-37.54	≤-27	PASS
			5360~40000	24182.47	-38.14	≤-27	PASS
11AX20SISO	Ant2	5180	30~5140	5136.25	-43.08	≤-27	PASS
			5360~40000	26521.03	-38.08	≤-27	PASS
		5200	30~5140	3142.67	-47.15	≤-27	PASS
			5360~40000	26019.71	-38.1	≤-27	PASS
		5240	30~5140	2726.21	-47.02	≤-27	PASS
			5360~40000	24277.69	-37.85	≤-27	PASS
11AX40SISO	Ant2	5190	30~5140	5135.74	-37.2	≤-27	PASS
			5360~40000	24802.82	-38.04	≤-27	PASS

		5230	30~5140	913.52	-37.31	≤-27	PASS
			5360~40000	26596.77	-37.52	≤-27	PASS
11AX80SISO	Ant2	5210	30~5140	5129.95	-37.21	≤-27	PASS
			5360~40000	26697.04	-37.94	≤-27	PASS

Test Graphs

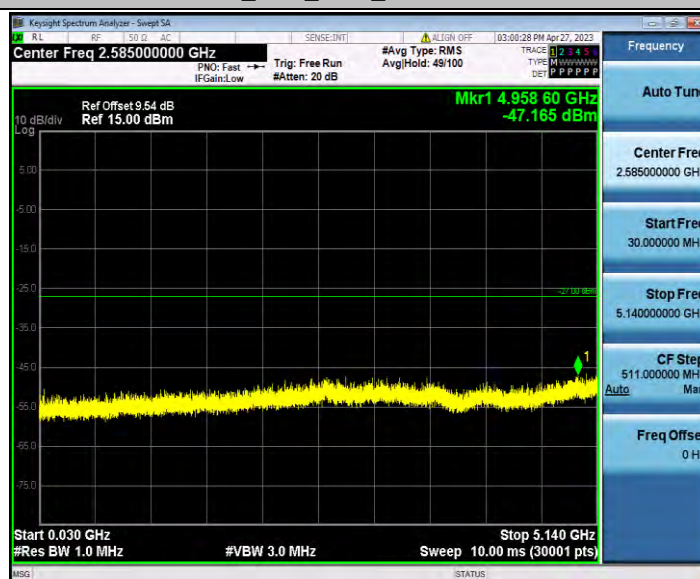




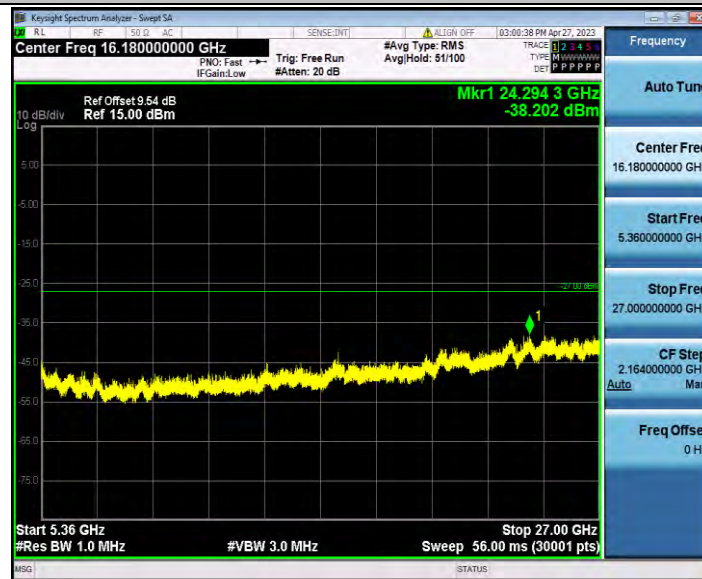
11A_Ant2_5200_30~5140



11A_Ant2_5200_5360~40000



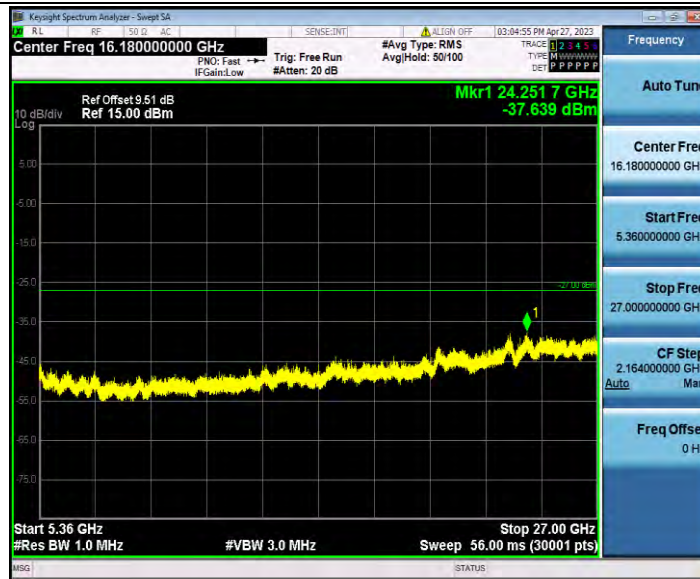
11A_Ant2_5240_30~5140



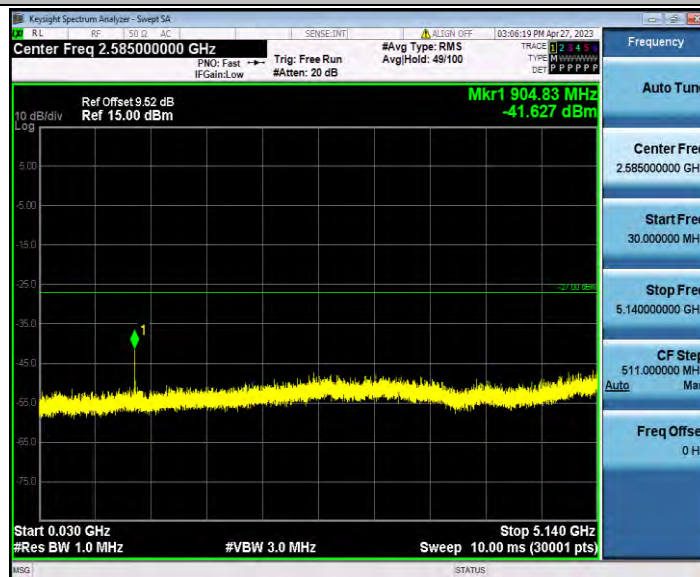
11A_Ant2_5240_5360~40000



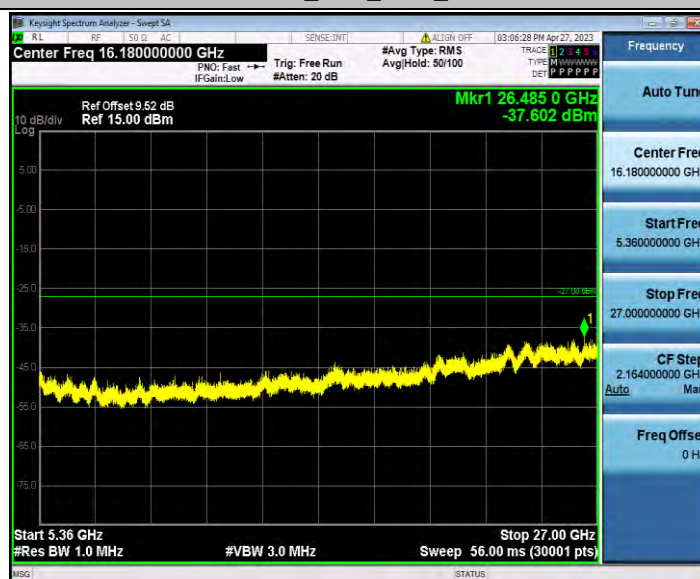
11N20SISO_Ant2_5180_30~5140



11N20SISO_Ant2_5180_5360~40000



11N20SISO_Ant2_5200_30~5140



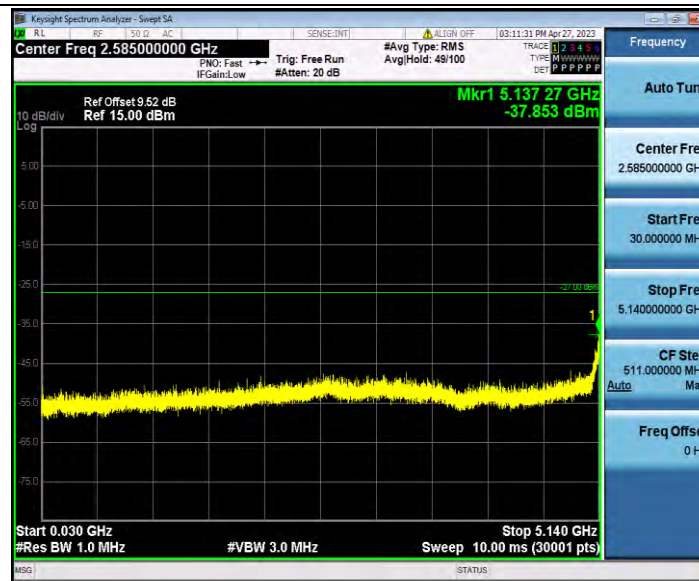
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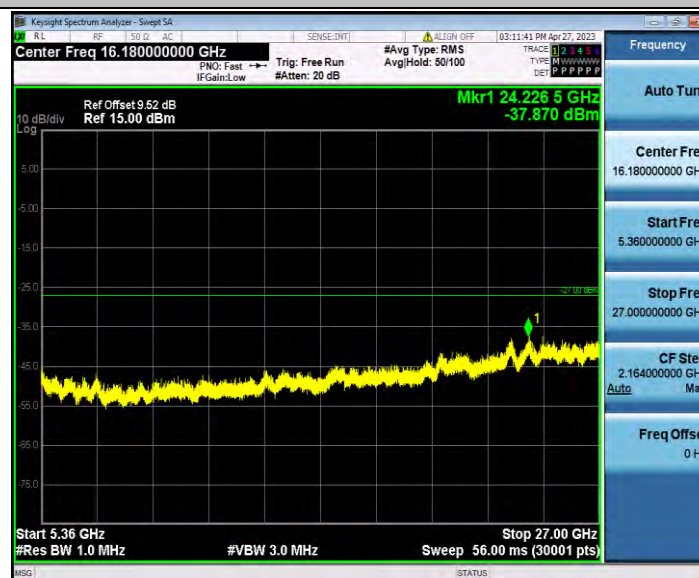
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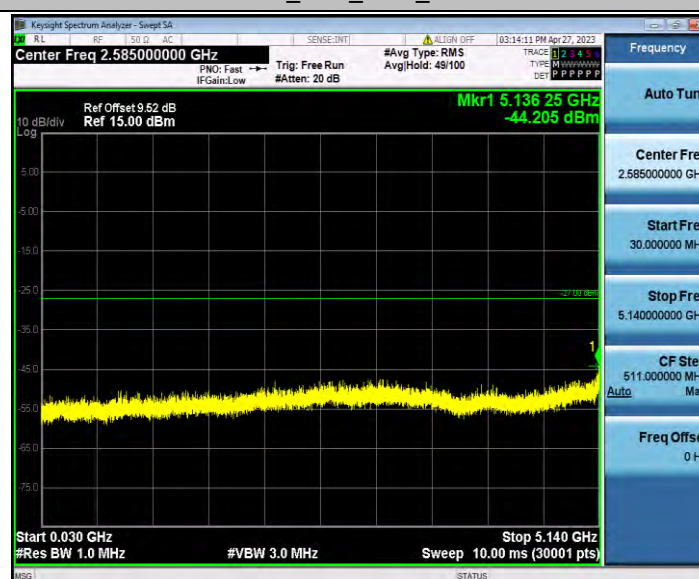
11N20SISO_Ant2_5240_5360~40000



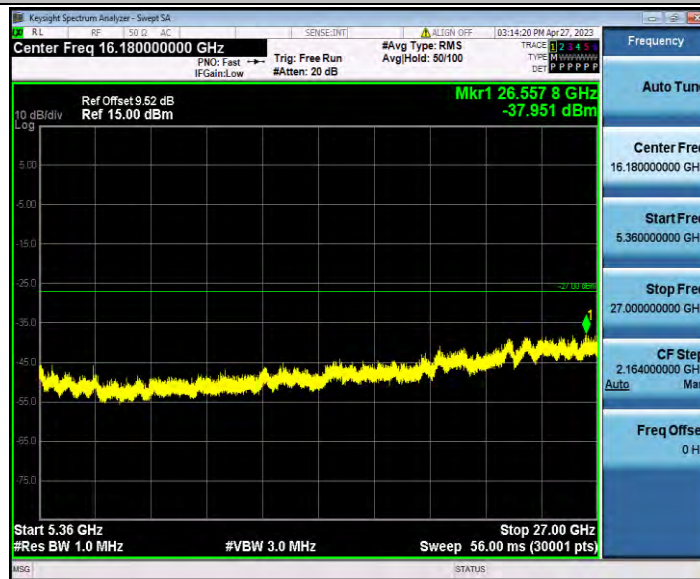
11N40SISO_Ant2_5190_30~5140



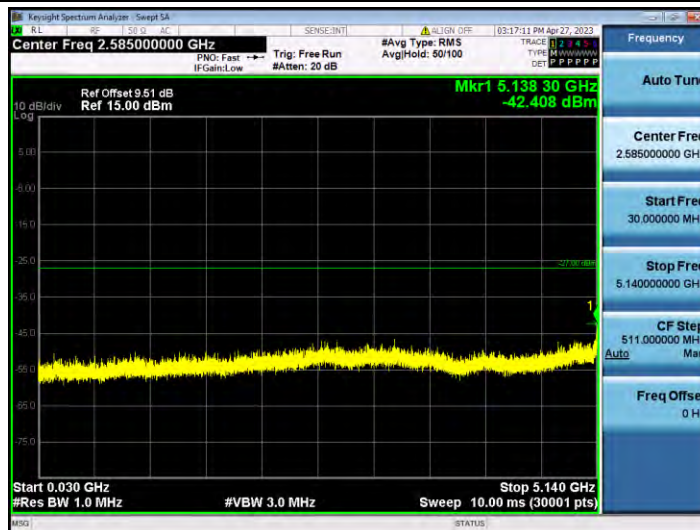
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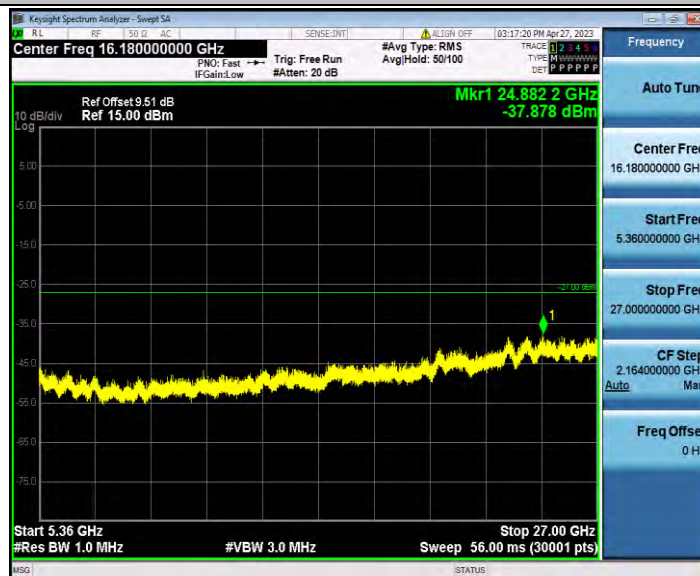
11N40SISO_Ant2_5230_30~5140



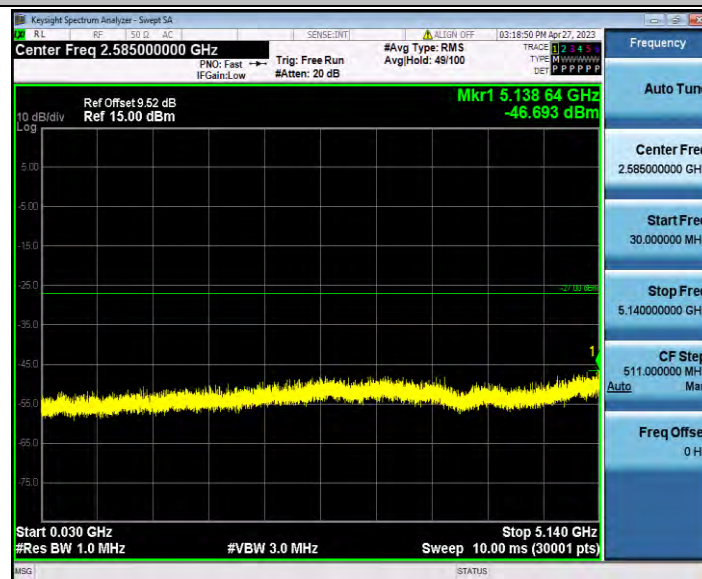
11N40SISO_Ant2_5230_5360~40000



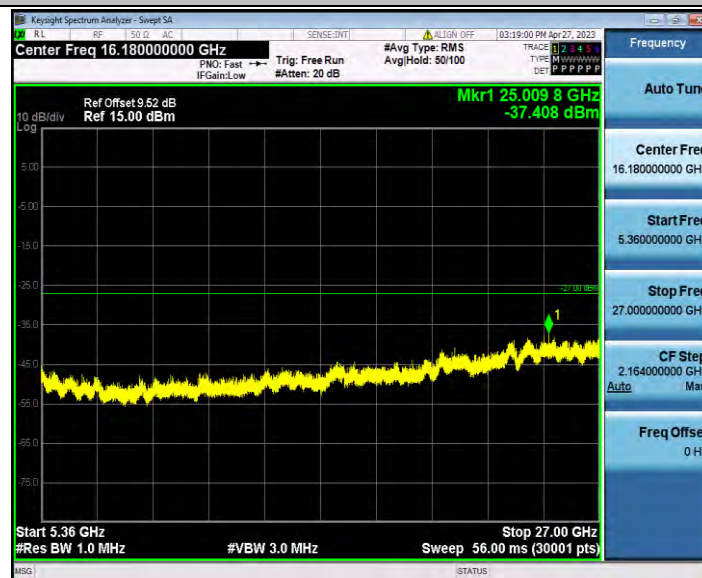
11AC20SISO_Ant2_5180_30~5140



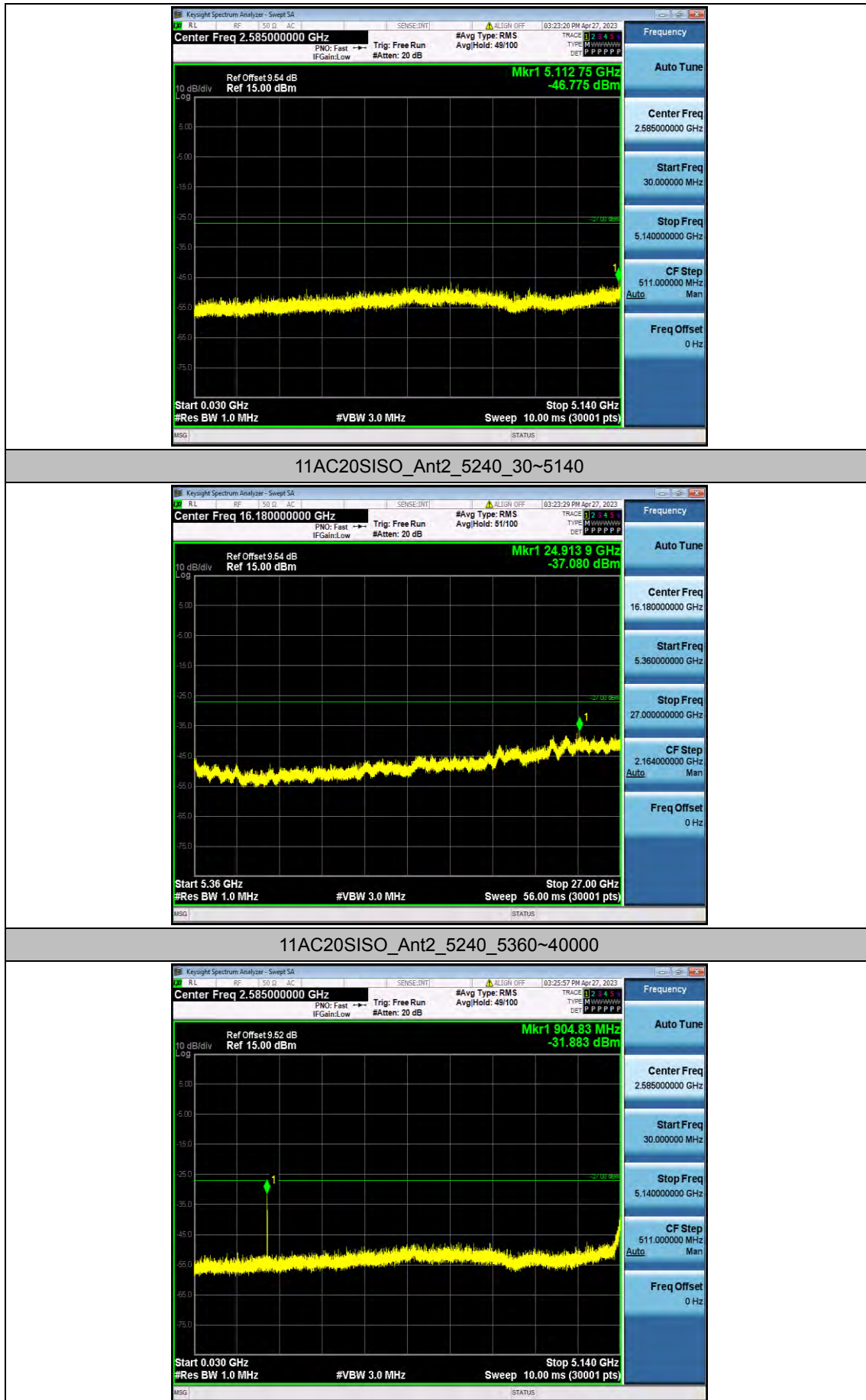
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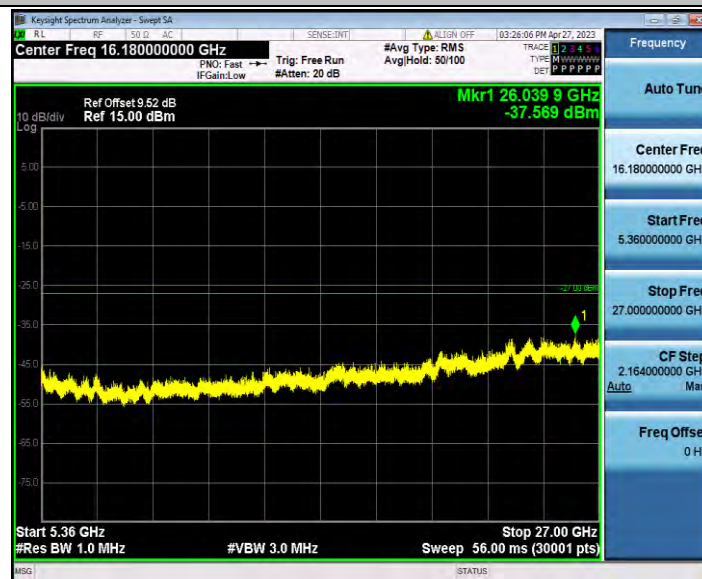
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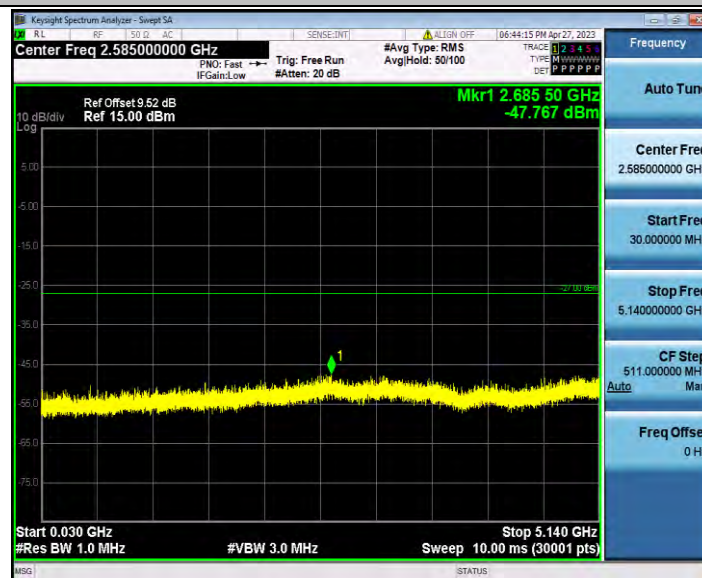
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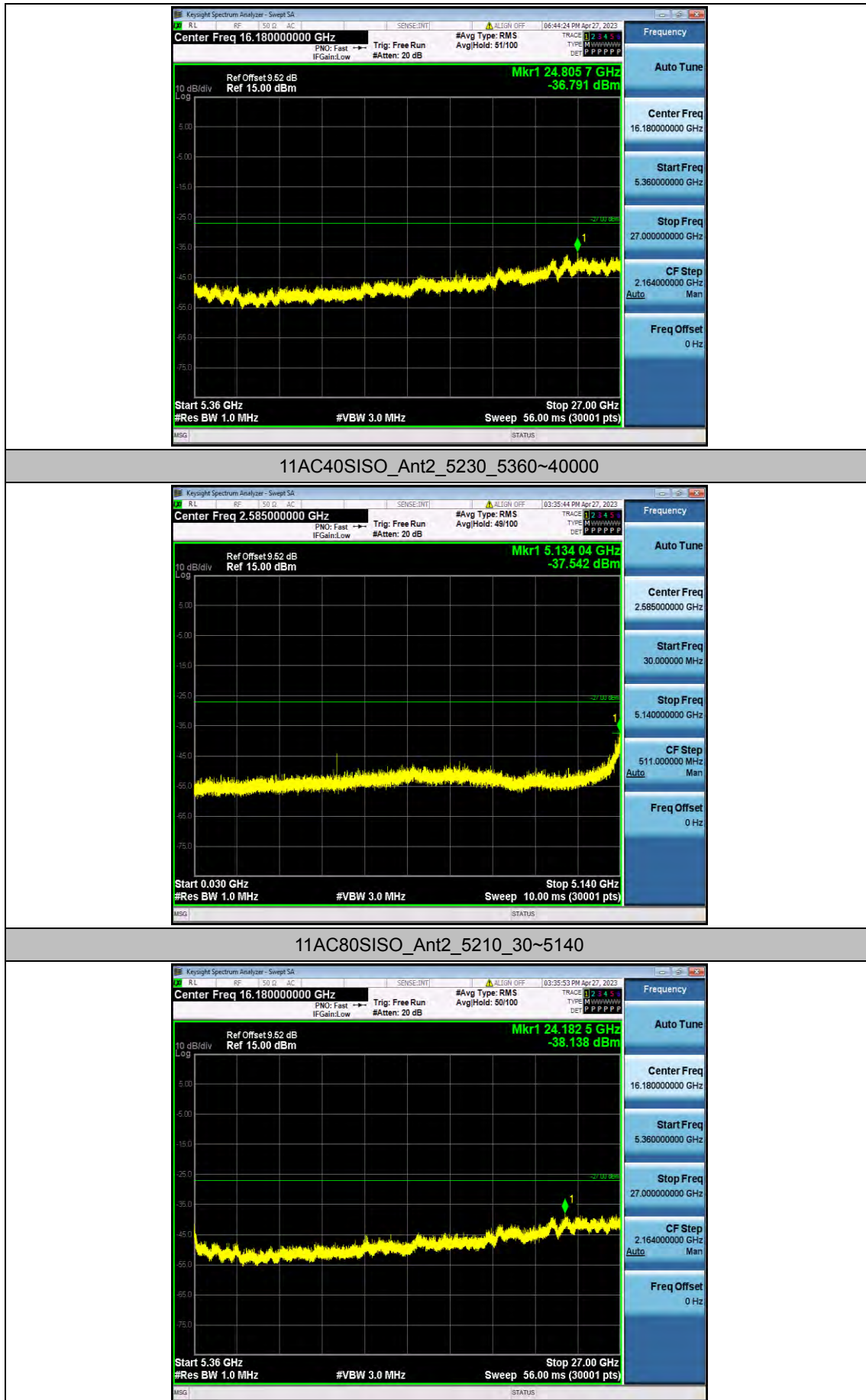
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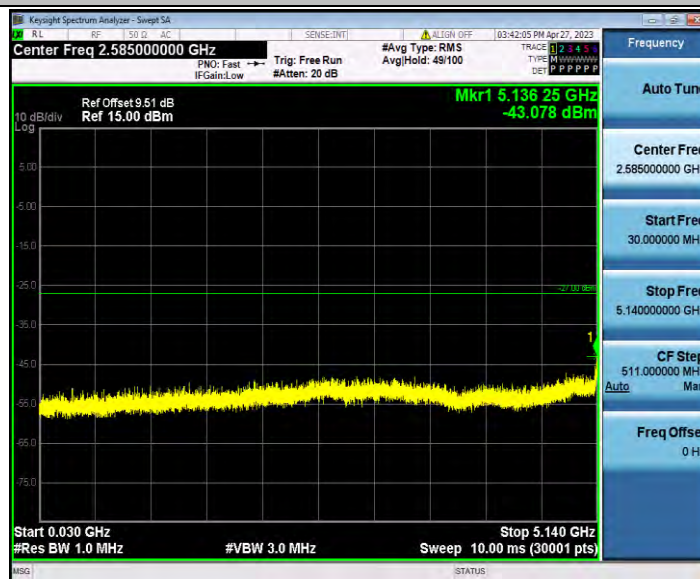
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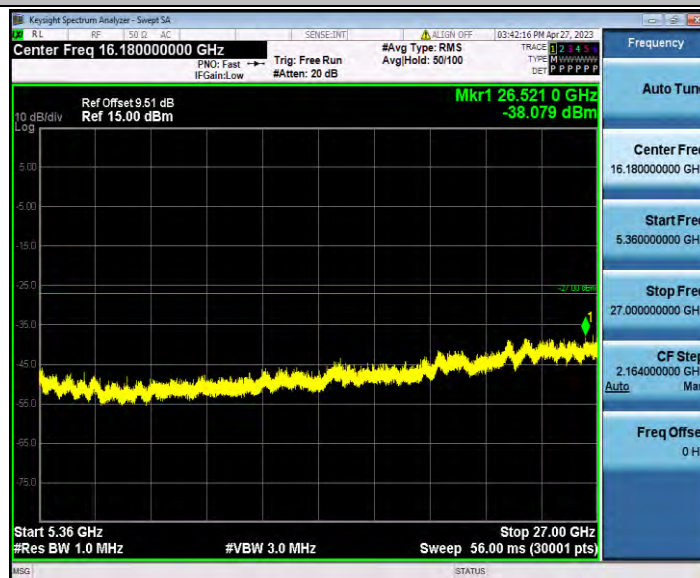
11AC40SISO_Ant2_5230_30~5140



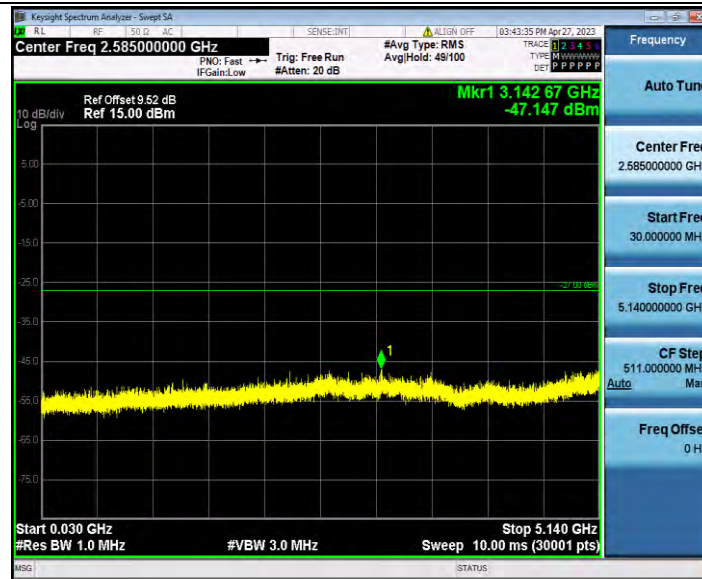
11AC80SISO_Ant2_5210_5360~40000



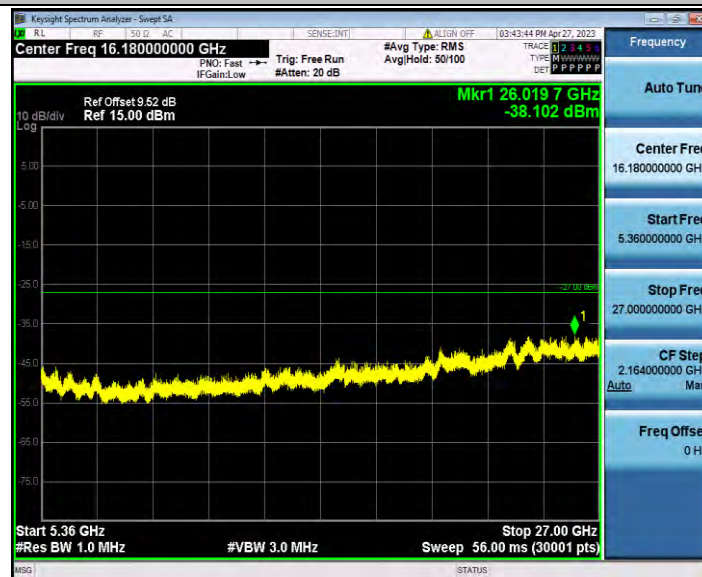
11AX20SISO_Ant2_5180_30~5140



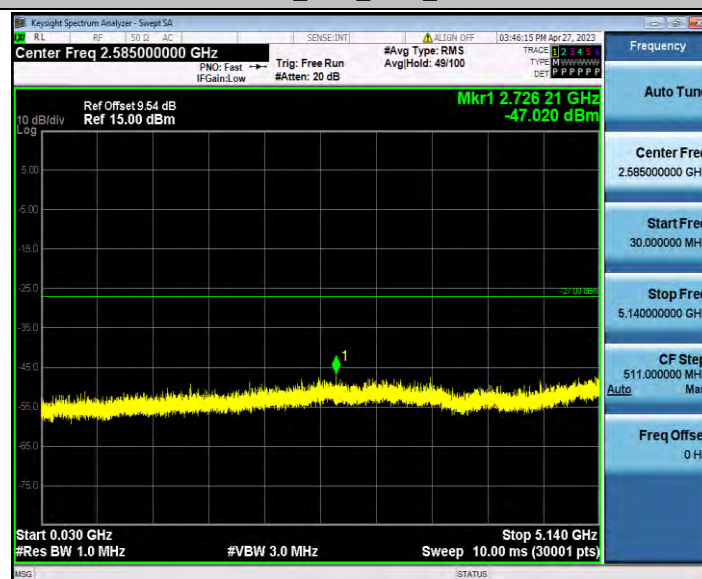
11AX20SISO_Ant2_5180_5360~40000



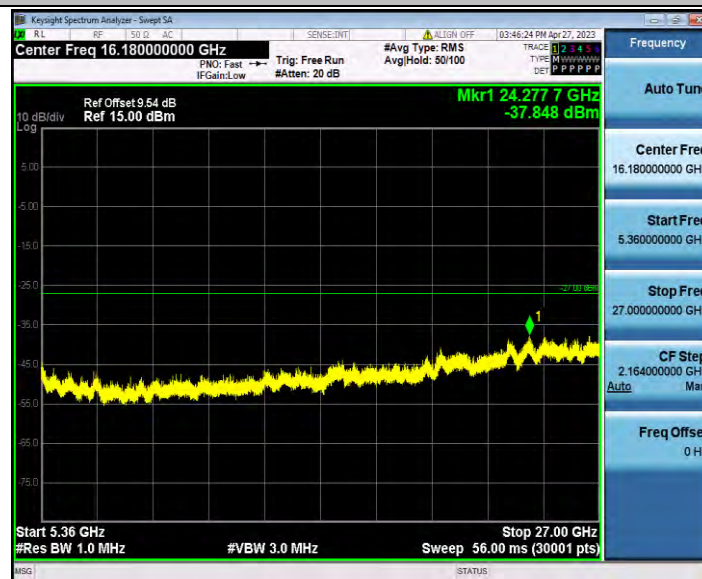
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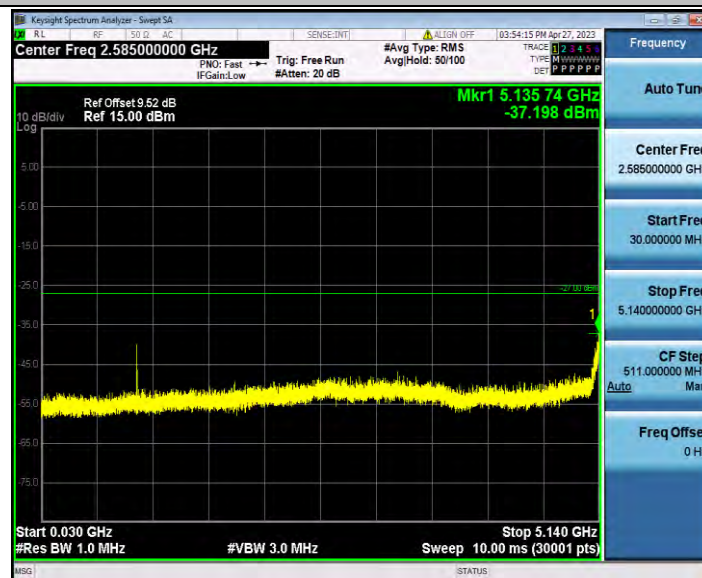
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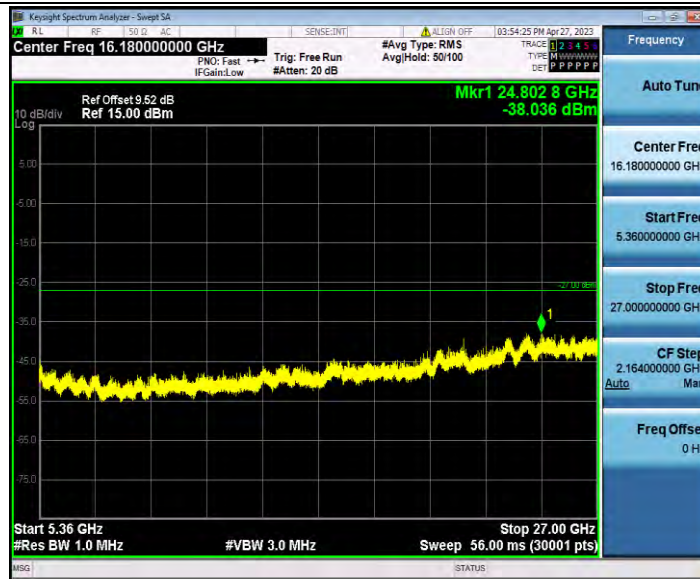
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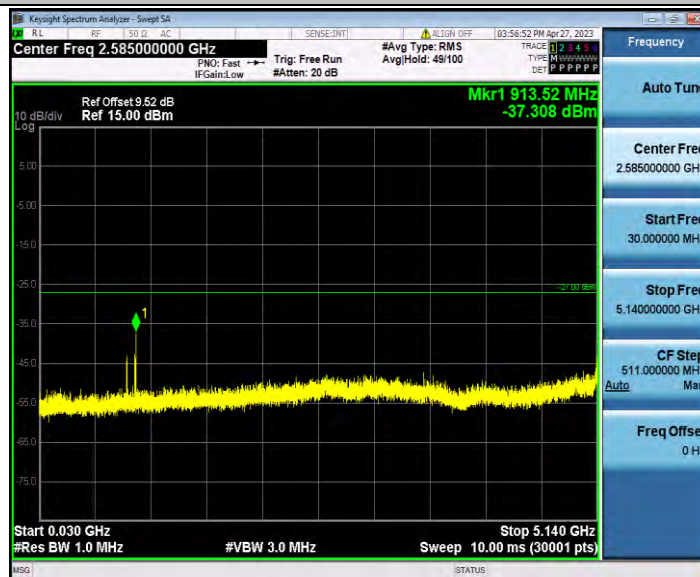
11AX20SISO_Ant2_5240_5360~40000



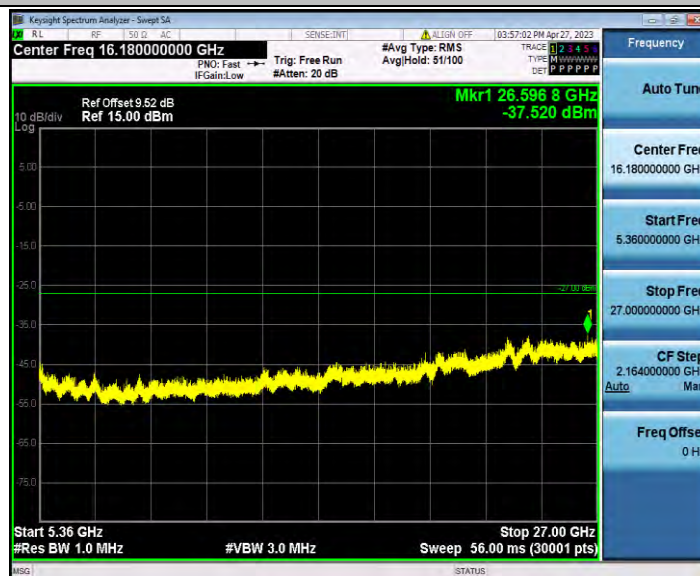
11AX40SISO_Ant2_5190_30~5140



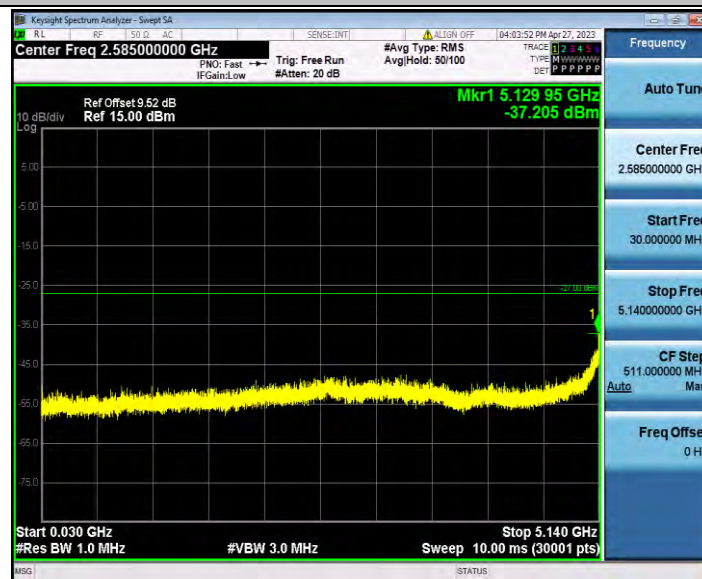
11AX40SISO_Ant2_5190_5360~40000



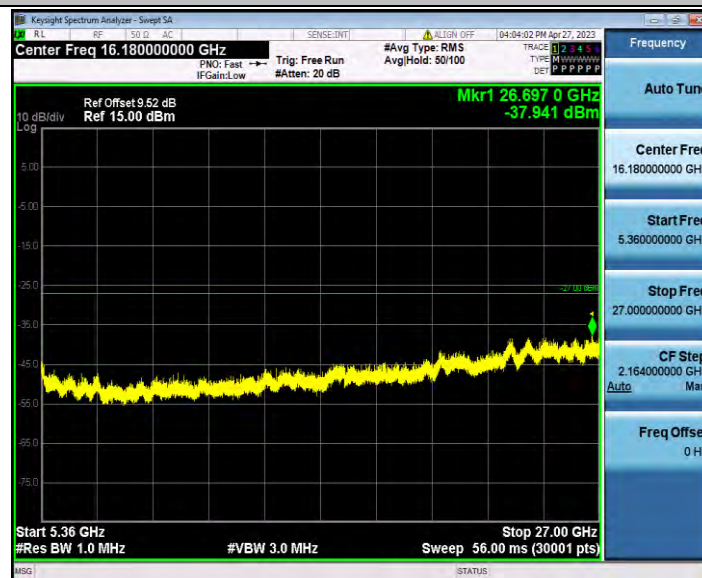
11AX40SISO_Ant2_5230_30~5140



11AX40SISO_Ant2_5230_5360~40000



11AX80SISO_Ant2_5210_30~5140



11AX80SISO_Ant2_5210_5360~40000