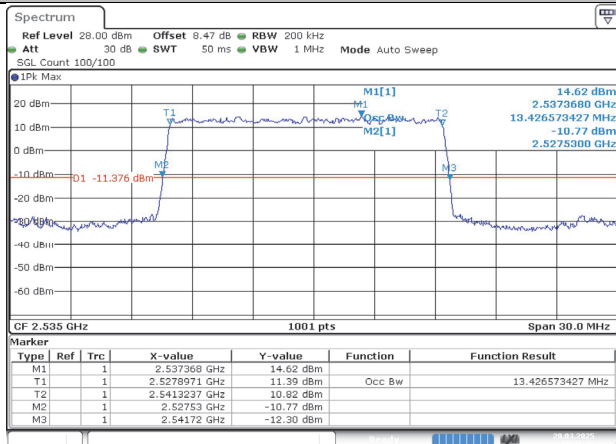


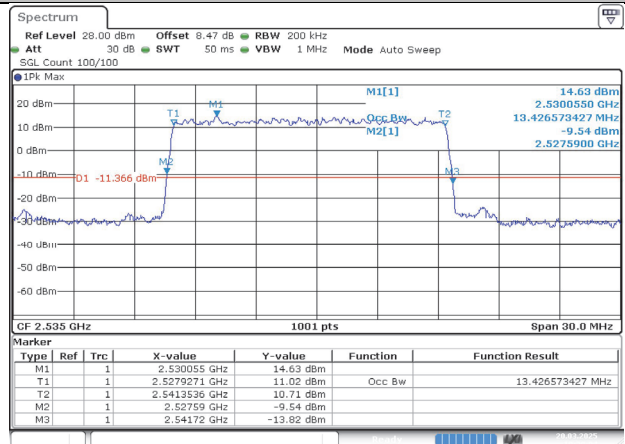
n7A / 15KHz / 15MHz

MCH / DFT-Pi2BPSK / Outer_Full



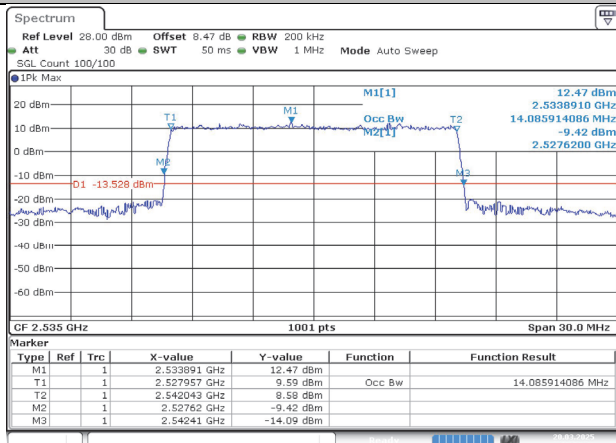
Date: 20 MAR 2025 10:37:47

MCH / DFT-QPSK / Outer_Full



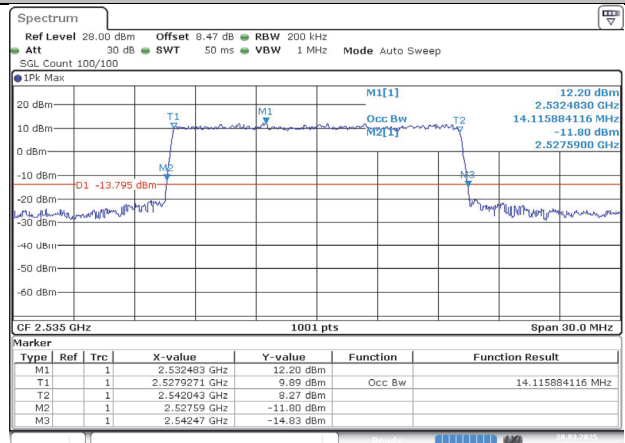
Date: 20 MAR 2025 10:38:04

MCH / CP-QPSK / Outer_Full



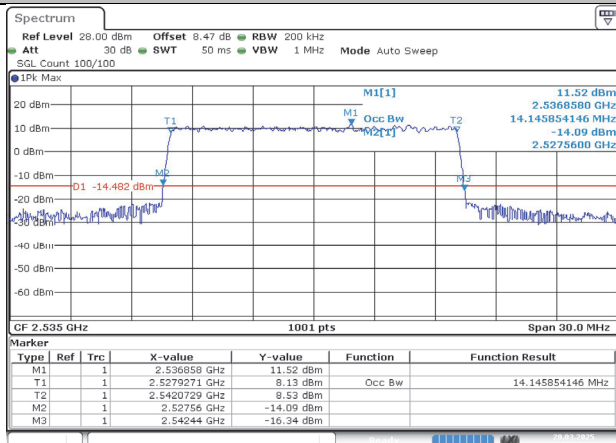
Date: 20 MAR 2025 10:38:42

MCH / CP-16QAM / Outer_Full



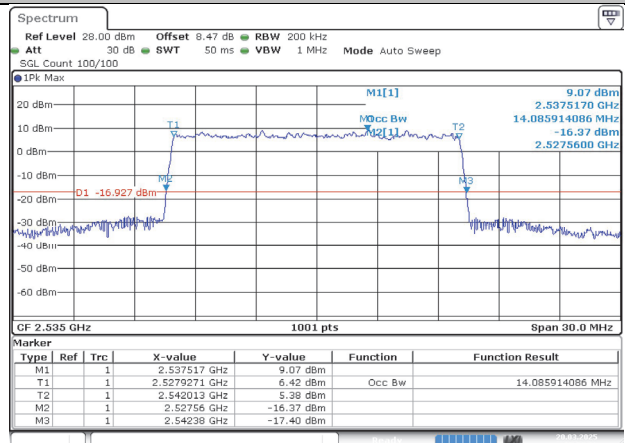
Date: 20 MAR 2025 10:38:51

MCH / CP-64QAM / Outer_Full



Date: 20 MAR 2025 10:38:59

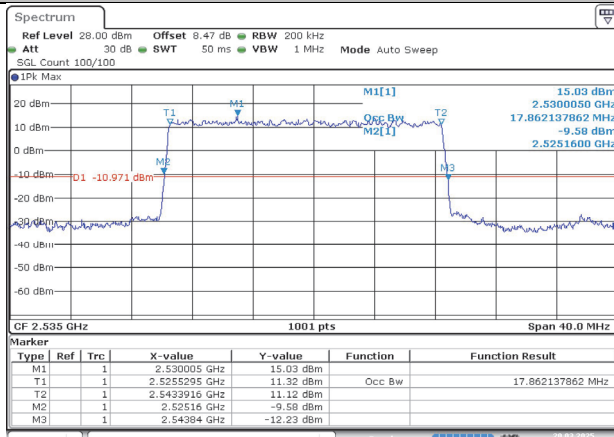
MCH / CP-256QAM / Outer_Full



Date: 20 MAR 2025 10:39:09

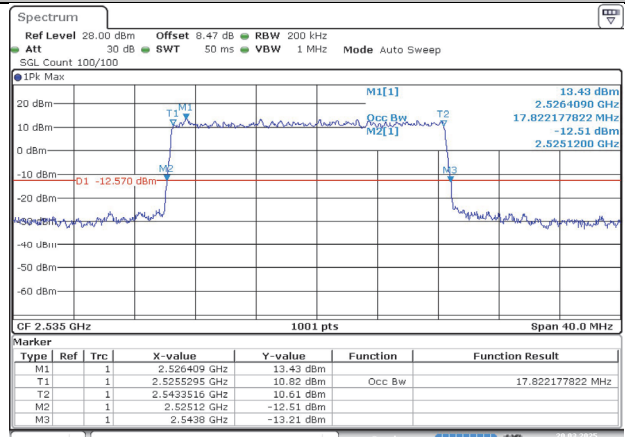
n7A / 15KHz / 20MHz

MCH / DFT-Pi2BPSK / Outer_Full



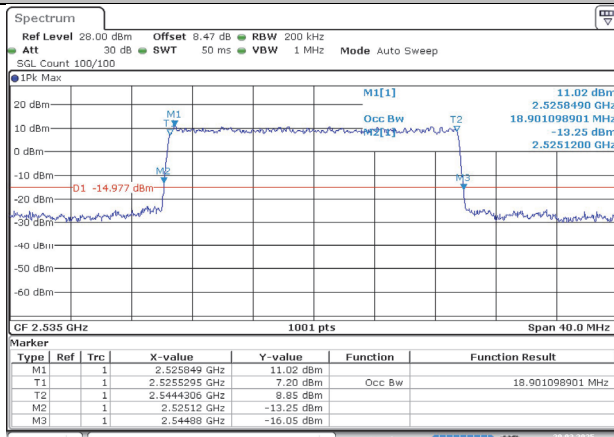
Date: 20 MAR 2025 10:39:30

MCH / DFT-QPSK / Outer_Full



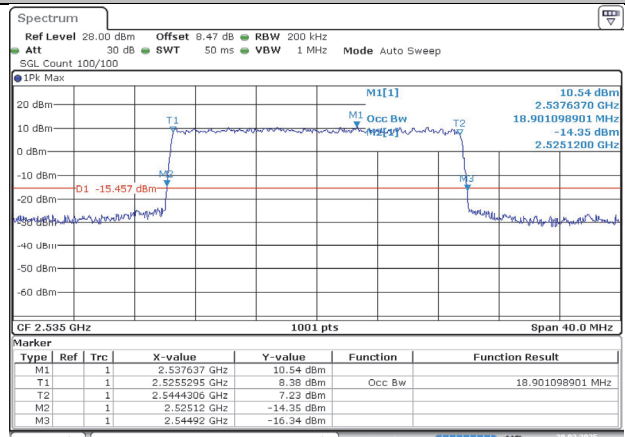
Date: 20 MAR 2025 10:39:52

MCH / CP-QPSK / Outer_Full



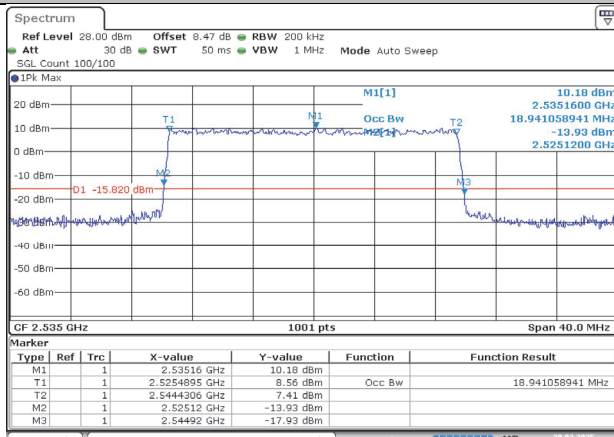
Date: 20 MAR 2025 10:40:32

MCH / CP-16QAM / Outer_Full



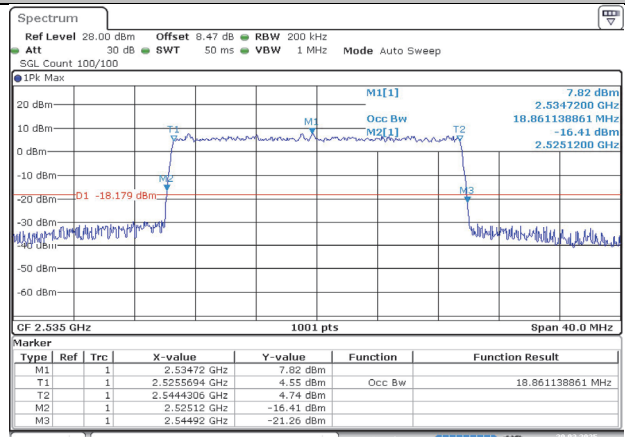
Date: 20 MAR 2025 10:40:40

MCH / CP-64QAM / Outer_Full



Date: 20 MAR 2025 10:40:49

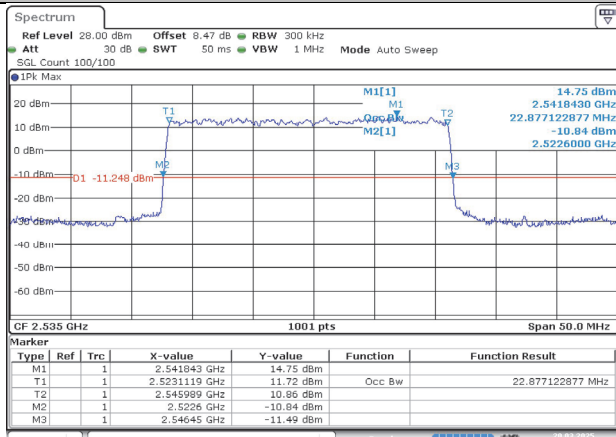
MCH / CP-256QAM / Outer_Full



Date: 20 MAR 2025 10:40:59

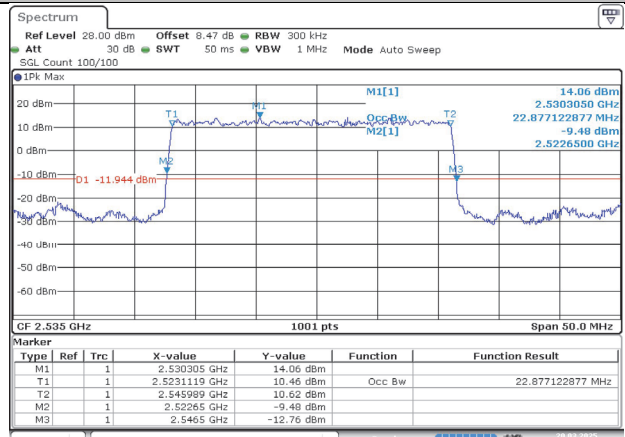
n7A / 15KHz / 25MHz

MCH / DFT-Pi2BPSK / Outer_Full



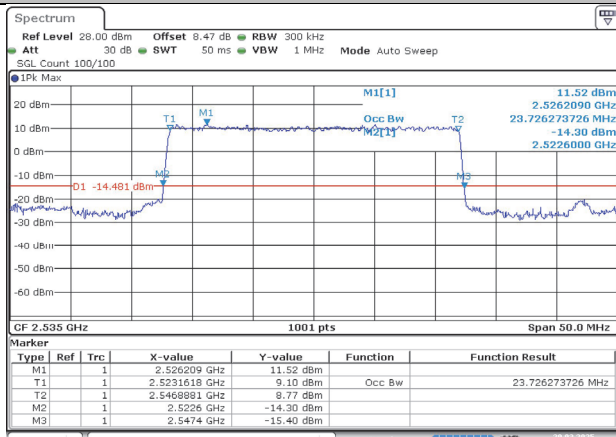
Date: 20 MAR 2025 10:41:30

MCH / DFT-QPSK / Outer_Full



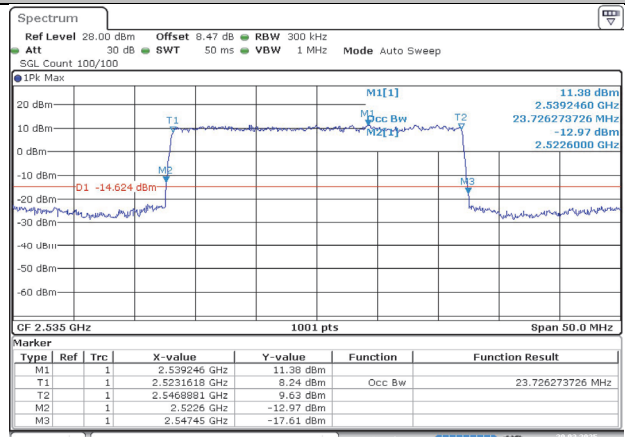
Date: 20 MAR 2025 10:41:45

MCH / CP-QPSK / Outer_Full



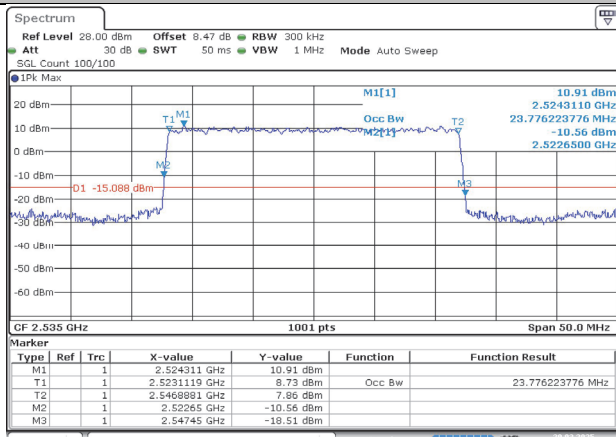
Date: 20 MAR 2025 10:42:21

MCH / CP-16QAM / Outer_Full



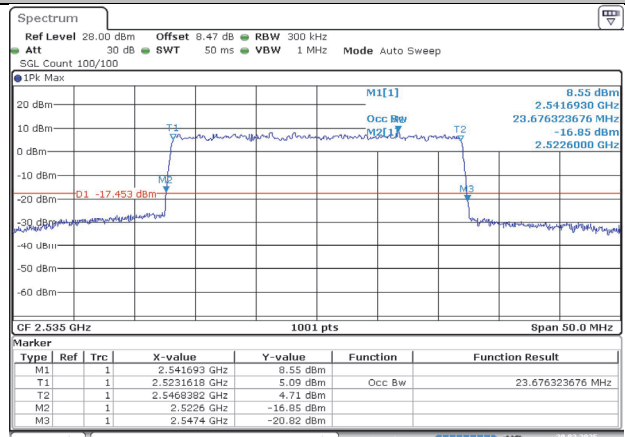
Date: 20 MAR 2025 10:42:29

MCH / CP-64QAM / Outer_Full



Date: 20 MAR 2025 10:42:36

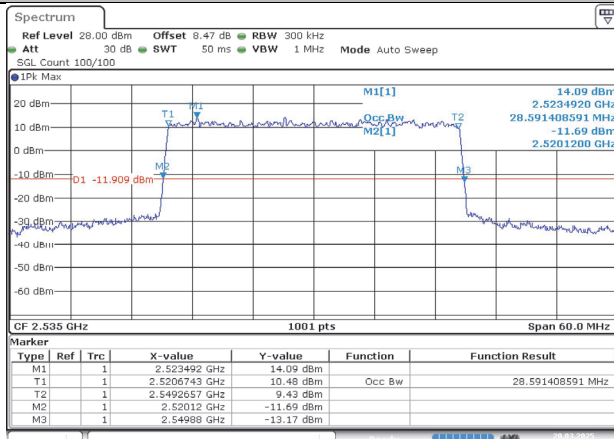
MCH / CP-256QAM / Outer_Full



Date: 20 MAR 2025 10:42:46

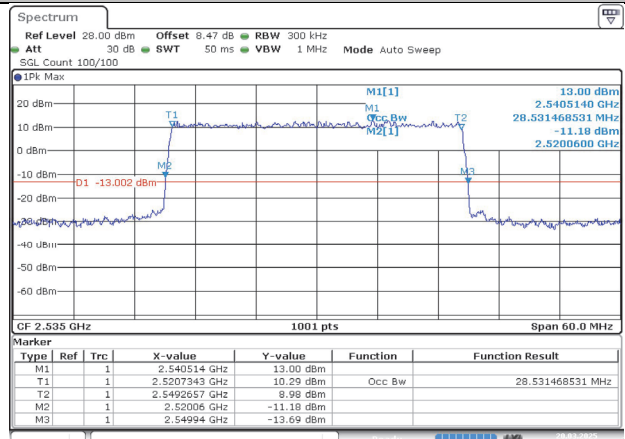
n7A / 15KHz / 30MHz

MCH / DFT-Pi2BPSK / Outer_Full



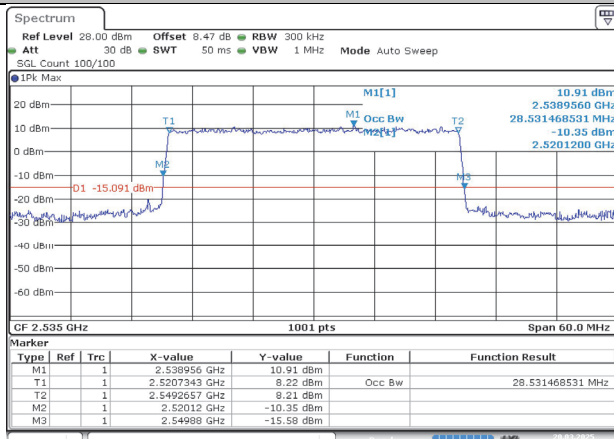
Date: 20 MAR 2025 10:43:06

MCH / DFT-QPSK / Outer_Full



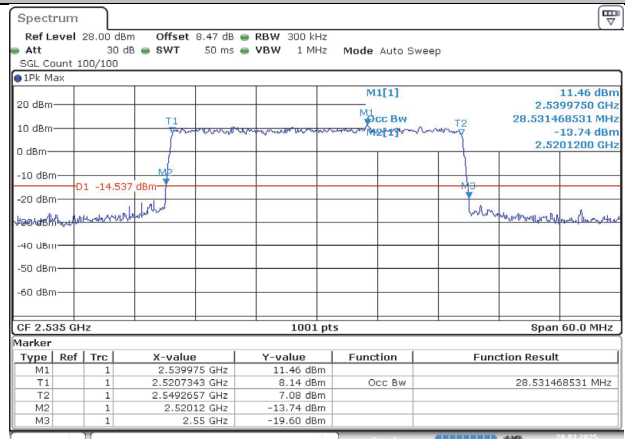
Date: 20 MAR 2025 10:43:22

MCH / CP-QPSK / Outer_Full



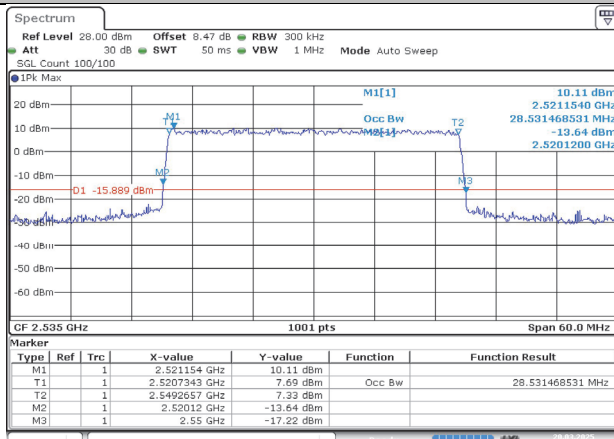
Date: 20 MAR 2025 10:43:57

MCH / CP-16QAM / Outer_Full



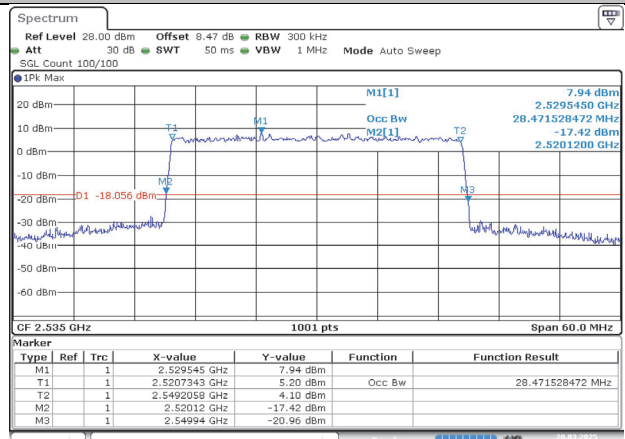
Date: 20 MAR 2025 10:44:04

MCH / CP-64QAM / Outer_Full



Date: 20 MAR 2025 10:44:12

MCH / CP-256QAM / Outer_Full



Date: 20 MAR 2025 10:44:21