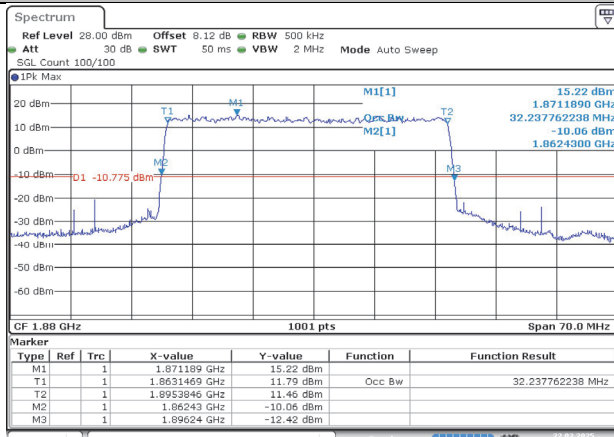


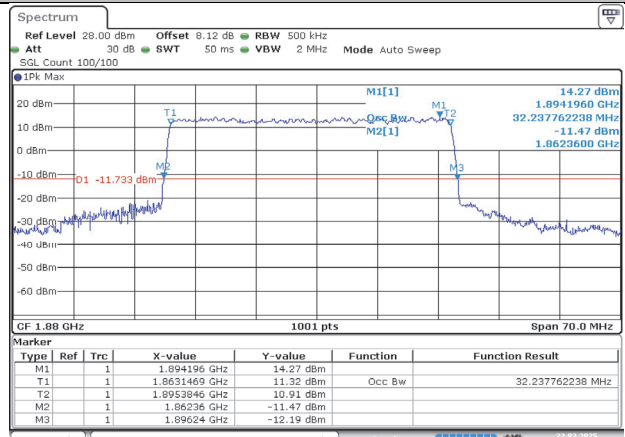
DC_4A_n2A / 15KHz / 35MHz

MCH / DFT-Pi2BPSK / Outer_Full



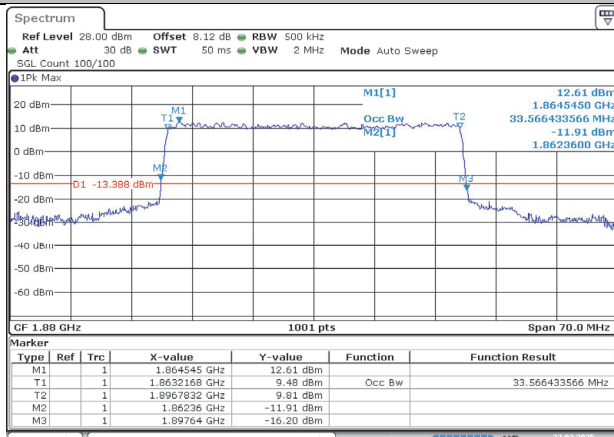
Date: 22 MAR 2025 01:18:19

MCH / DFT-QPSK / Outer_Full



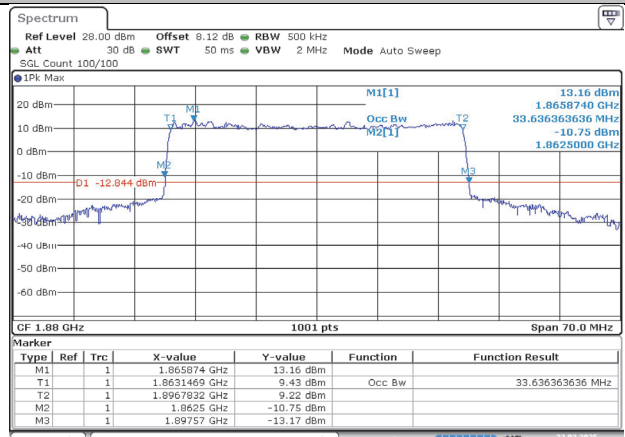
Date: 22 MAR 2025 01:18:28

MCH / CP-QPSK / Outer_Full



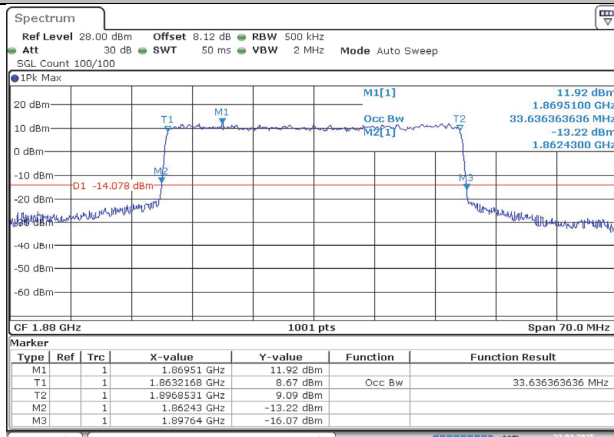
Date: 22 MAR 2025 01:19:02

MCH / CP-16QAM / Outer_Full



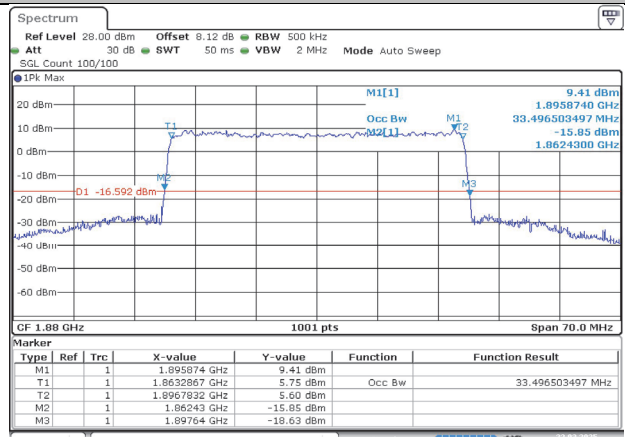
Date: 22 MAR 2025 01:19:09

MCH / CP-64QAM / Outer_Full



Date: 22 MAR 2025 01:19:17

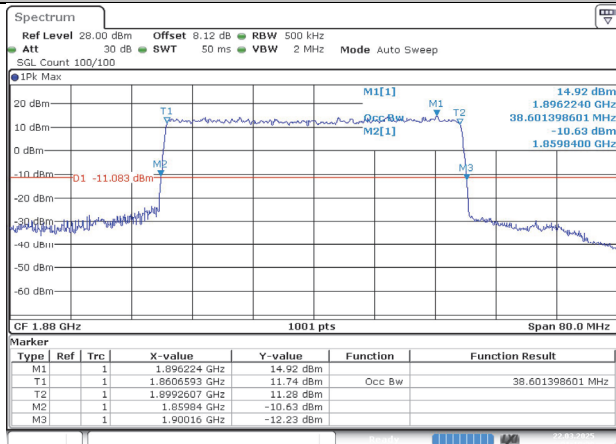
MCH / CP-256QAM / Outer_Full



Date: 22 MAR 2025 01:19:26

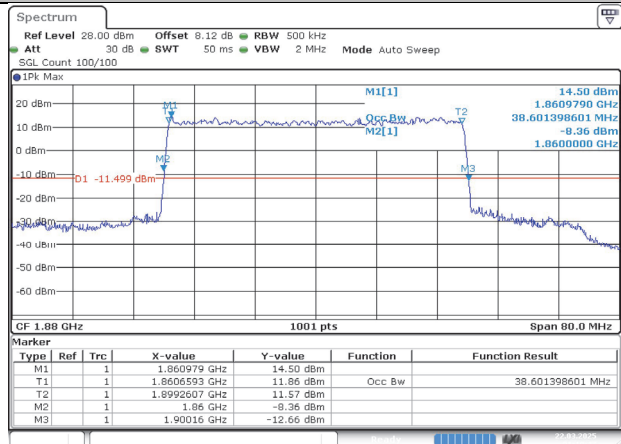
DC_4A_n2A / 15KHz / 40MHz

MCH / DFT-Pi2BPSK / Outer_Full



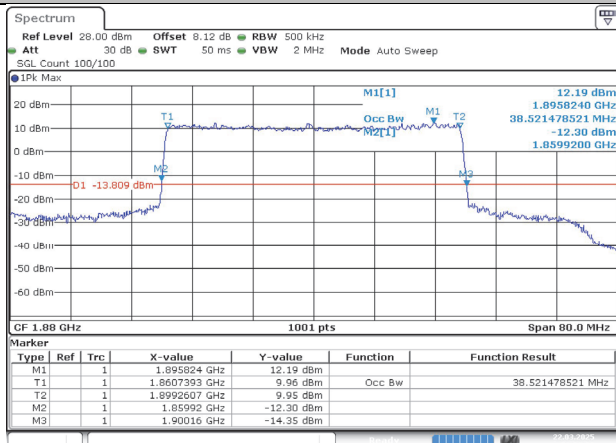
Date: 22 MAR 2025 01:19:38

MCH / DFT-QPSK / Outer_Full



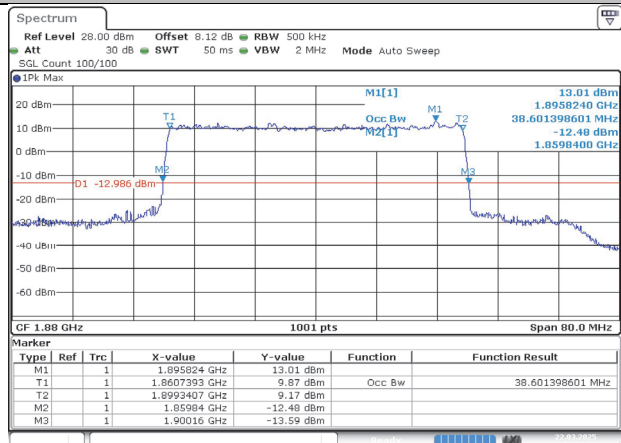
Date: 22 MAR 2025 01:19:47

MCH / CP-QPSK / Outer_Full



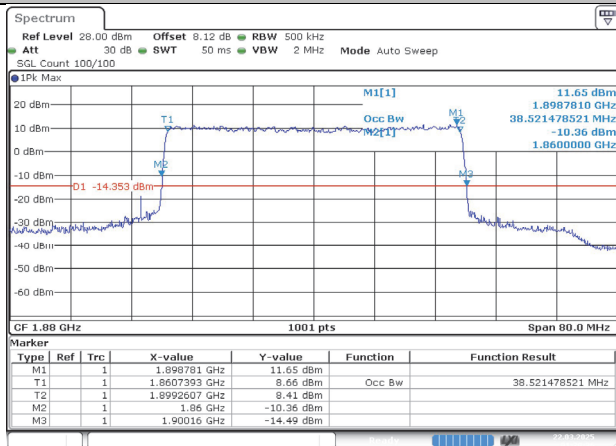
Date: 22 MAR 2025 01:20:20

MCH / CP-16QAM / Outer_Full



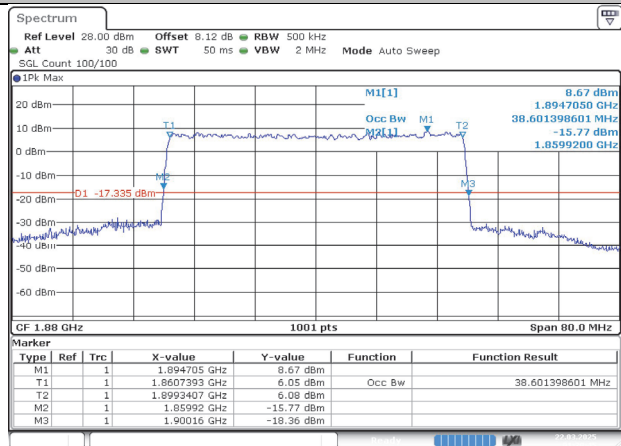
Date: 22 MAR 2025 01:20:28

MCH / CP-64QAM / Outer_Full



Date: 22 MAR 2025 01:20:36

MCH / CP-256QAM / Outer_Full



Date: 22 MAR 2025 01:20:45

5. Conducted Band Edges

5.1. Test Plots for SCS=15KHz

