

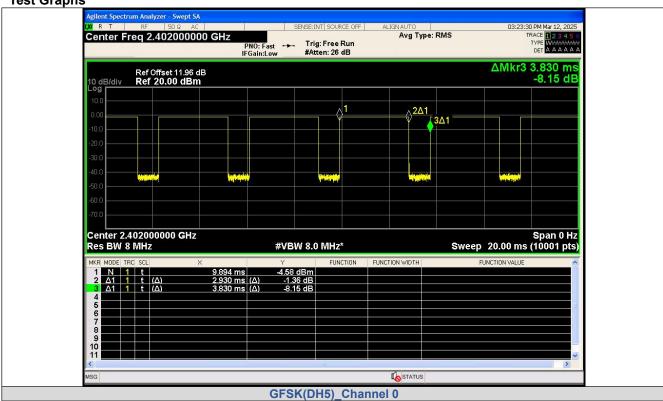
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APPENDIX VI. Duty Cycle

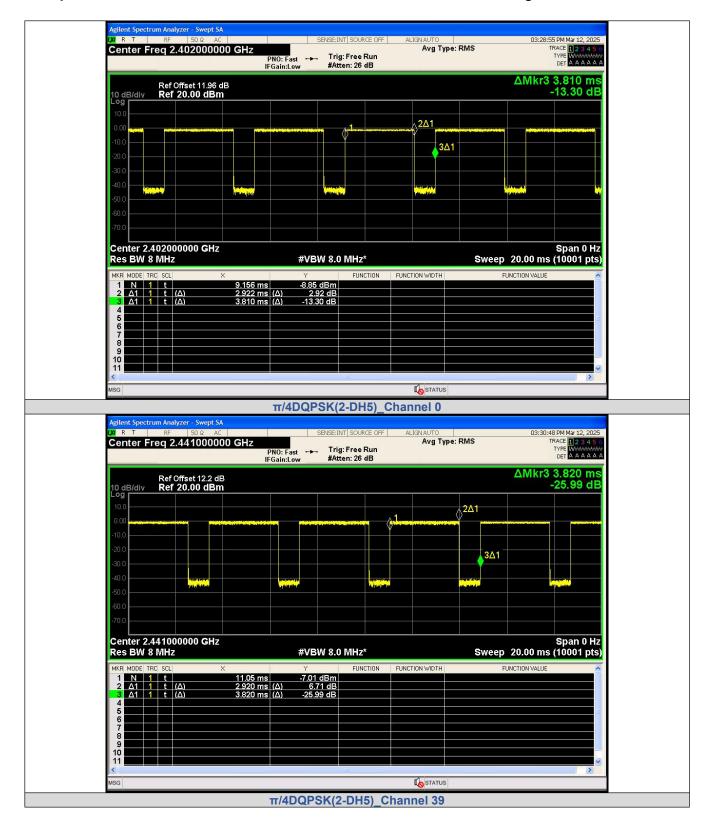
Test Result

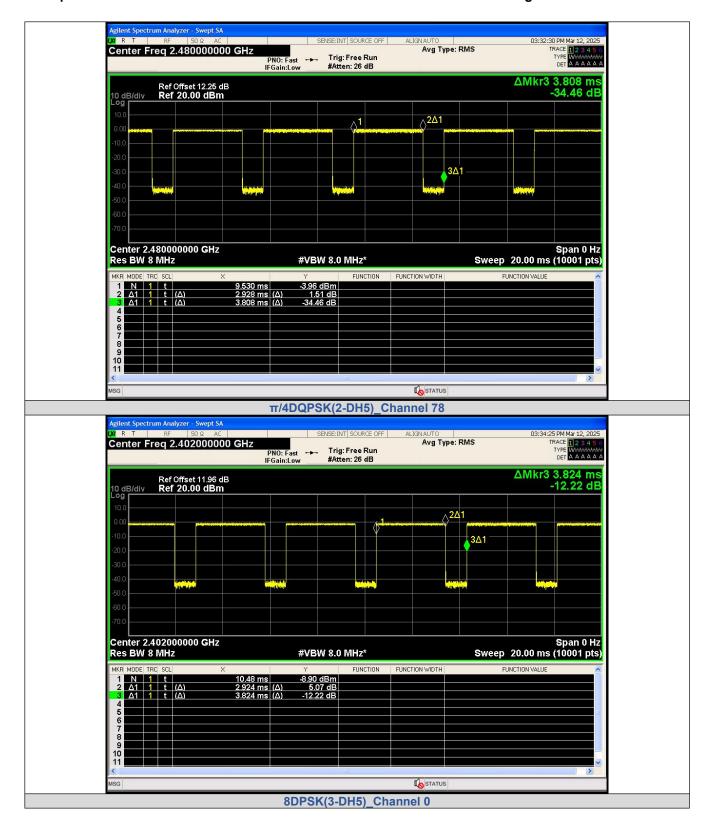
Modulation	Packets	Channel	On Time (ms)	Period (ms)	Duty Cycle (%)	Duty Cycle (linear)	Duty Cycle Factor (dB)
GFSK	DH5	0	2.930	3.830	76.50	0.7650	1.1634
		39	2.920	3.802	76.80	0.7680	1.1464
		78	2.922	3.810	76.69	0.7669	1.1526
π/4DQPSK	2-DH5	0	2.922	3.810	76.69	0.7669	1.1526
		39	2.920	3.820	76.44	0.7644	1.1668
		78	2.928	3.808	76.89	0.7689	1.1413
8DPSK	3-DH5	0	2.924	3.824	76.46	0.7646	1.1657
		39	2.912	3.812	76.39	0.7639	1.1696
		78	2.930	3.812	76.86	0.7686	1.143

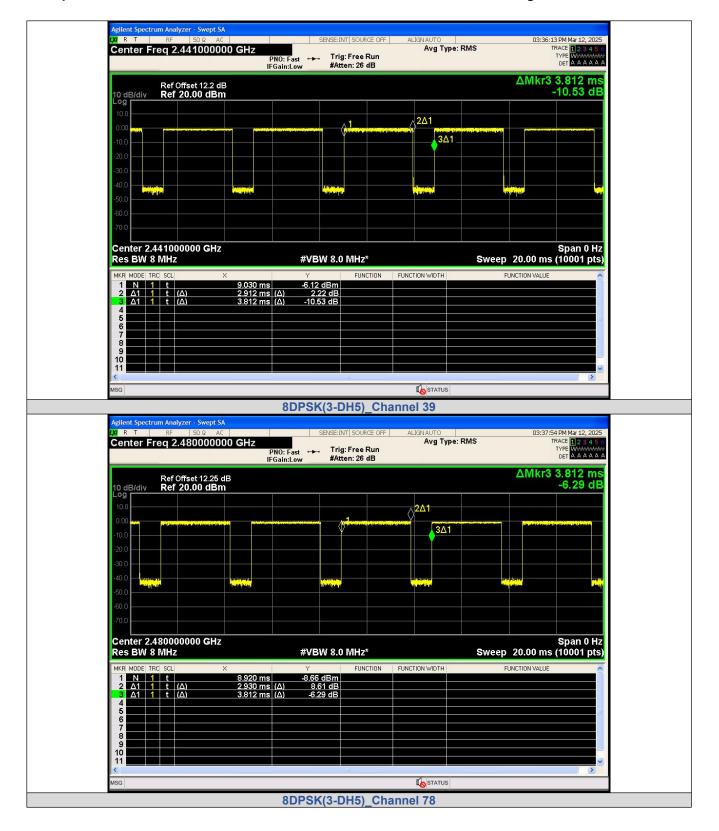












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APPENDIX VII. Dwell Time

Test Result

Modulation	Packet	Channel	Pulse Width (ms)	Number of Pulses in 31.6 seconds	Dwell Time (ms)	Limit (ms)	Result
GFSK	DH1	CH39 (2441MHz)	0.3840	309	118.66	< 400	PASS
	DH3		1.680	156	262.08		PASS
	DH5		2.944	104	306.18		PASS
π/4DQPSK	2-DH1		0.3744	311	116.44		PASS
	2-DH3		1.656	155	256.68		PASS
	2-DH5		2.928	104	304.51		PASS
8DPSK	3-DH1		0.3816	310	118.3		PASS
	3-DH3		1.632	155	252.96		PASS
	3-DH5		2.928	104	304.51		PASS

