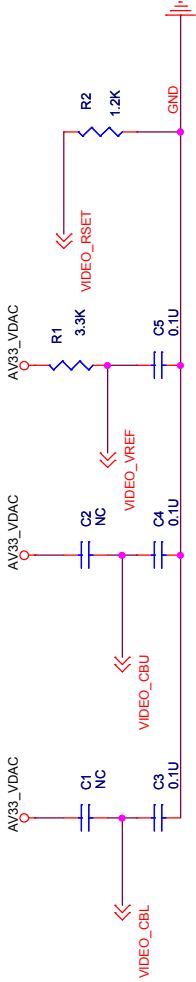
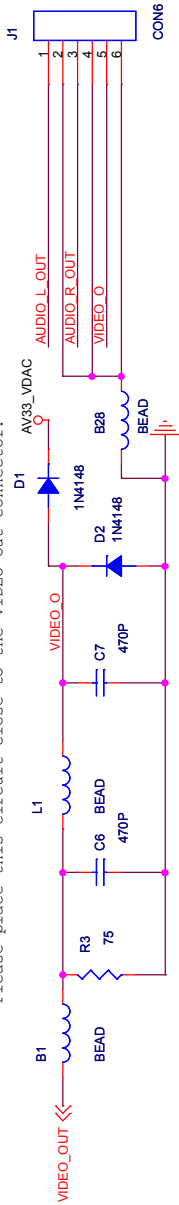


TV OUT

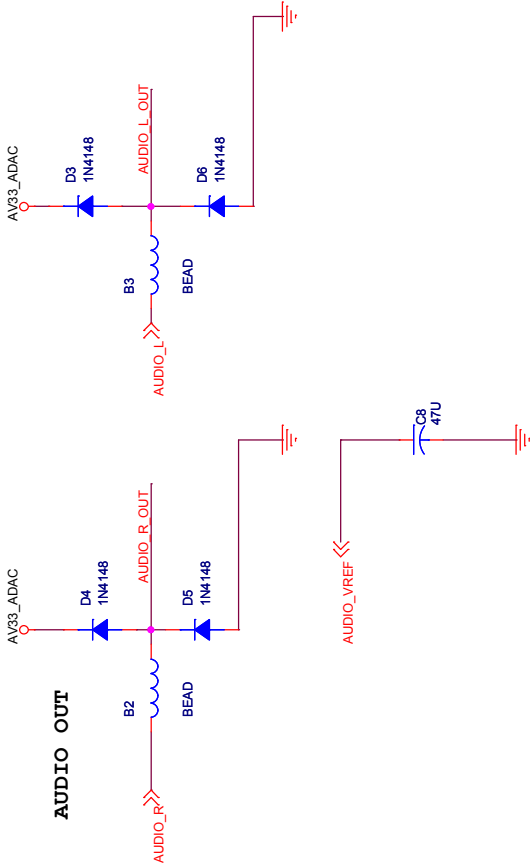
Please place this circuit close to the SPG290 chip.



Please place this circuit close to the VIDEO out connector.



AUDIO OUT



Matei Inc Design by Daniel Stelung

Title TV OUT

Size B Document Number SPG290 main board Rev 0H

Date: Tuesday, July 25, 2006

Sheet 1 of 7

DRAM_A[0..12] DRAM_A[0..12]
DRAM_D[0..31] DRAM_D[0..31]

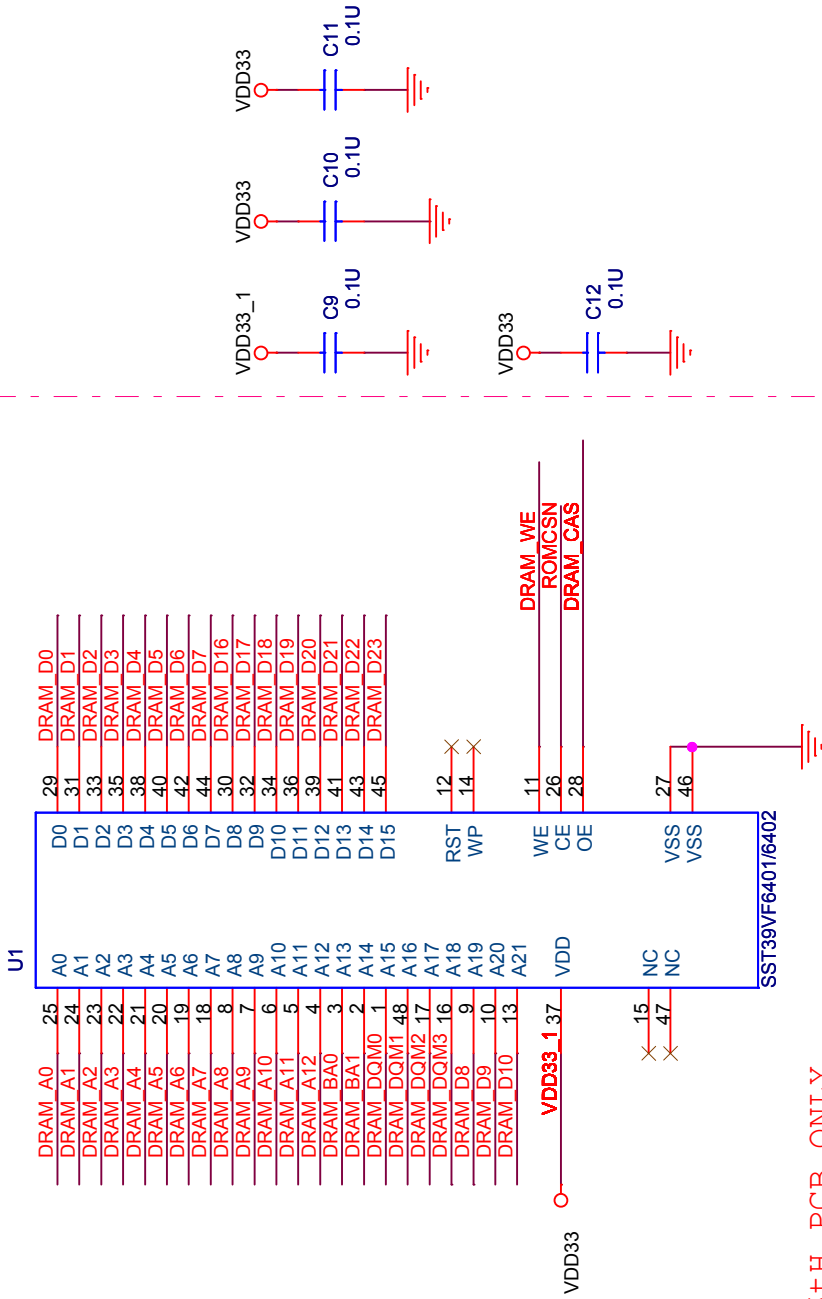
DRAM_BA0 << DRAM_BA0
DRAM_BA1 << DRAM_BA1
DRAM_DQM0 << DRAM_DQM0
DRAM_DQM1 << DRAM_DQM1
DRAM_DQM2 << DRAM_DQM2
DRAM_DQM3 << DRAM_DQM3

DRAM_CAS << DRAM_CAS

ROMCSN << ROMCSN

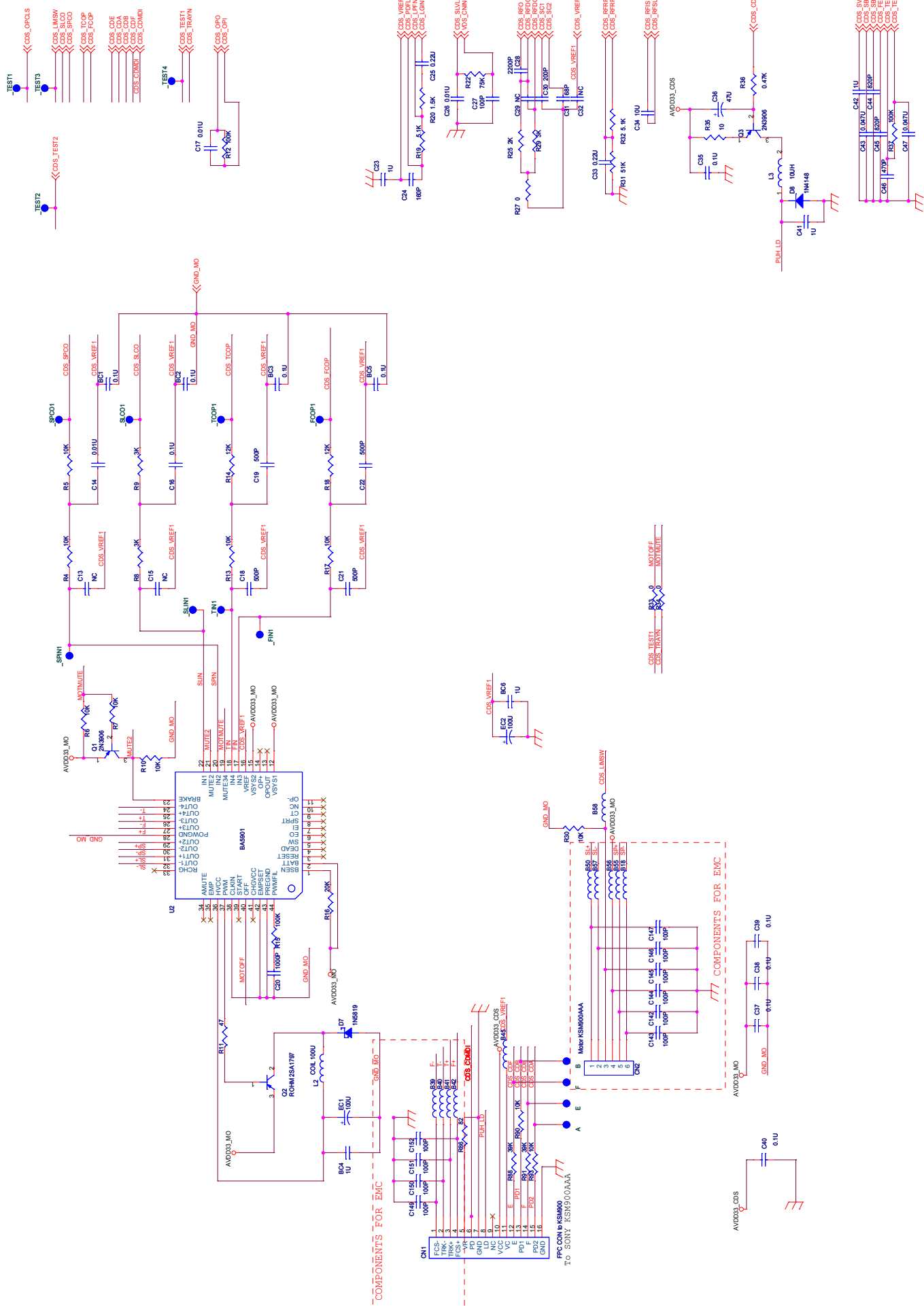
DRAM_WE << DRAM_WE

The Part use the flash/maskrom compatible layer
And,this part have change to 64M bit
Make sure the "small flash PCB" layer is the same

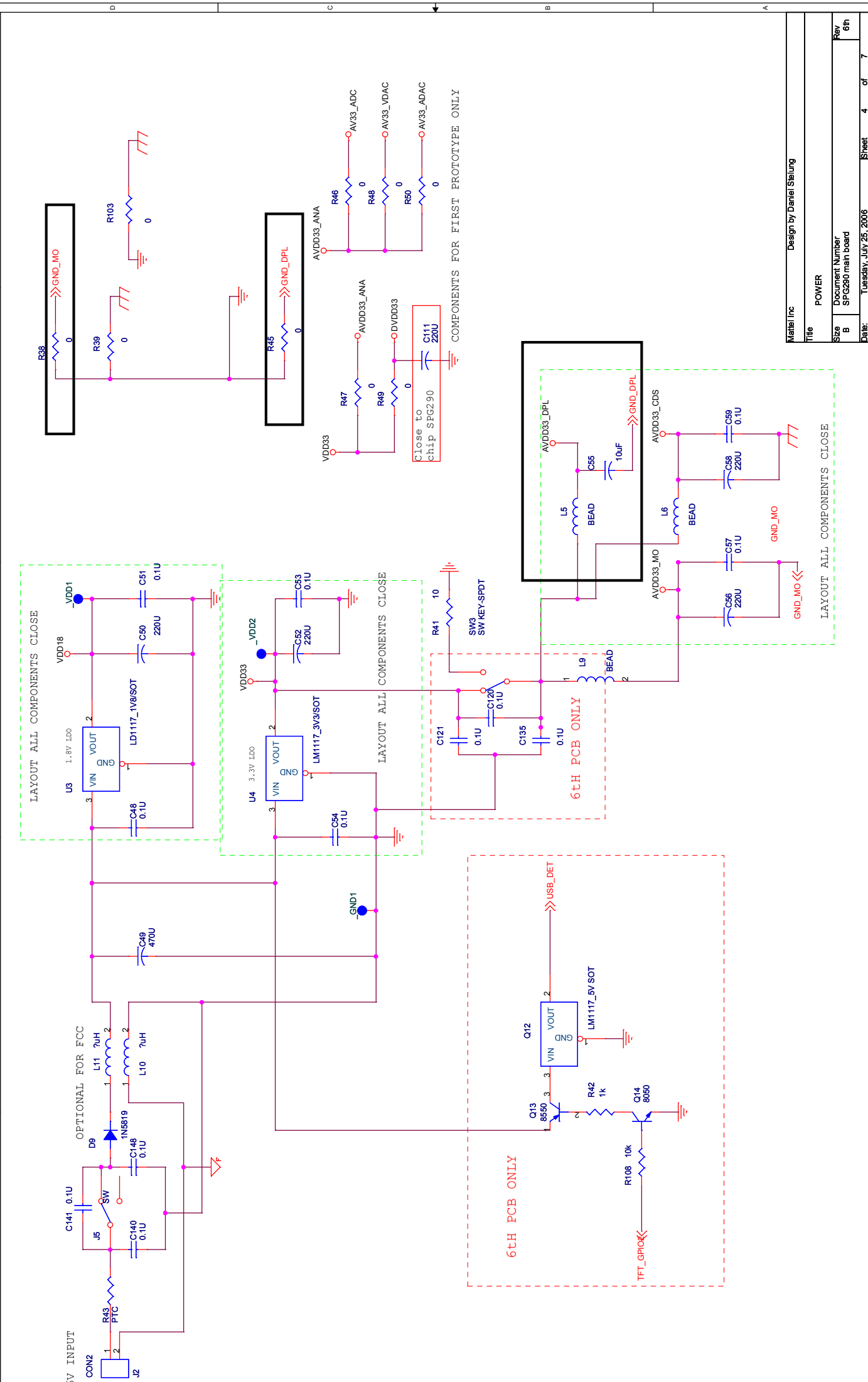


5th PCB the
DRAM_WE ADD B4
VDD33 ADD B27
ROMCSN ADD B5
DRAM_CAS ADD B6

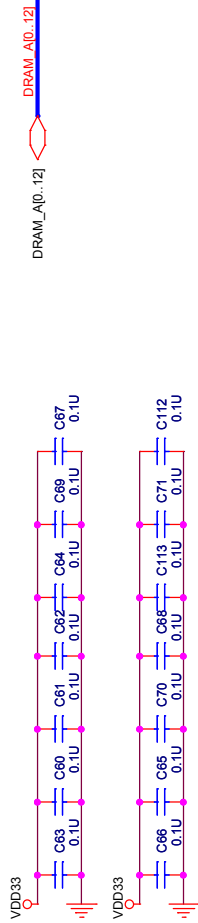
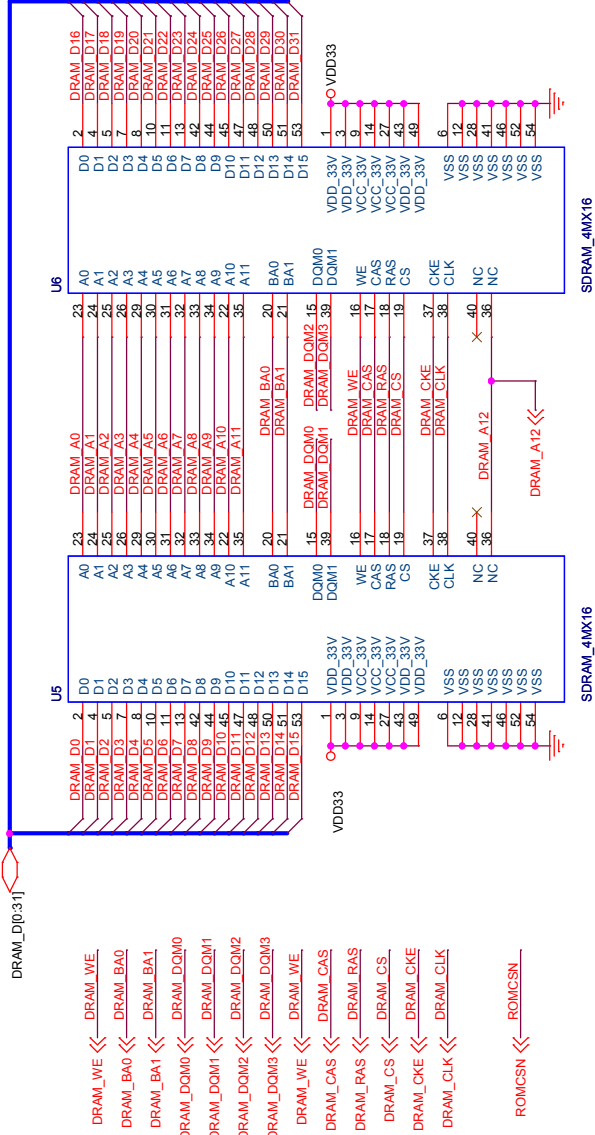
Mattel Inc		Design by Daniel Stelung	
Title		Boot ROM 1Mx16	
Size	Document Number	Rev	
A	SPG290 main board	6th	
Date:	Tuesday, July 25, 2006	Sheet	2 of 7



File	<Title>
Doc	Document Number
C	<Doc>
Rev	Rev
6th	6th
Sheet	3 of 7
DATE	2024/03/23/2025



Mater Inc		Design by Daniel Stieling	
Title		POWER	
Size	Document Number	Rev	
B	SFG290 main board	6th	
Date:	Tuesday, July 25, 2006	Sheet	4 of 7

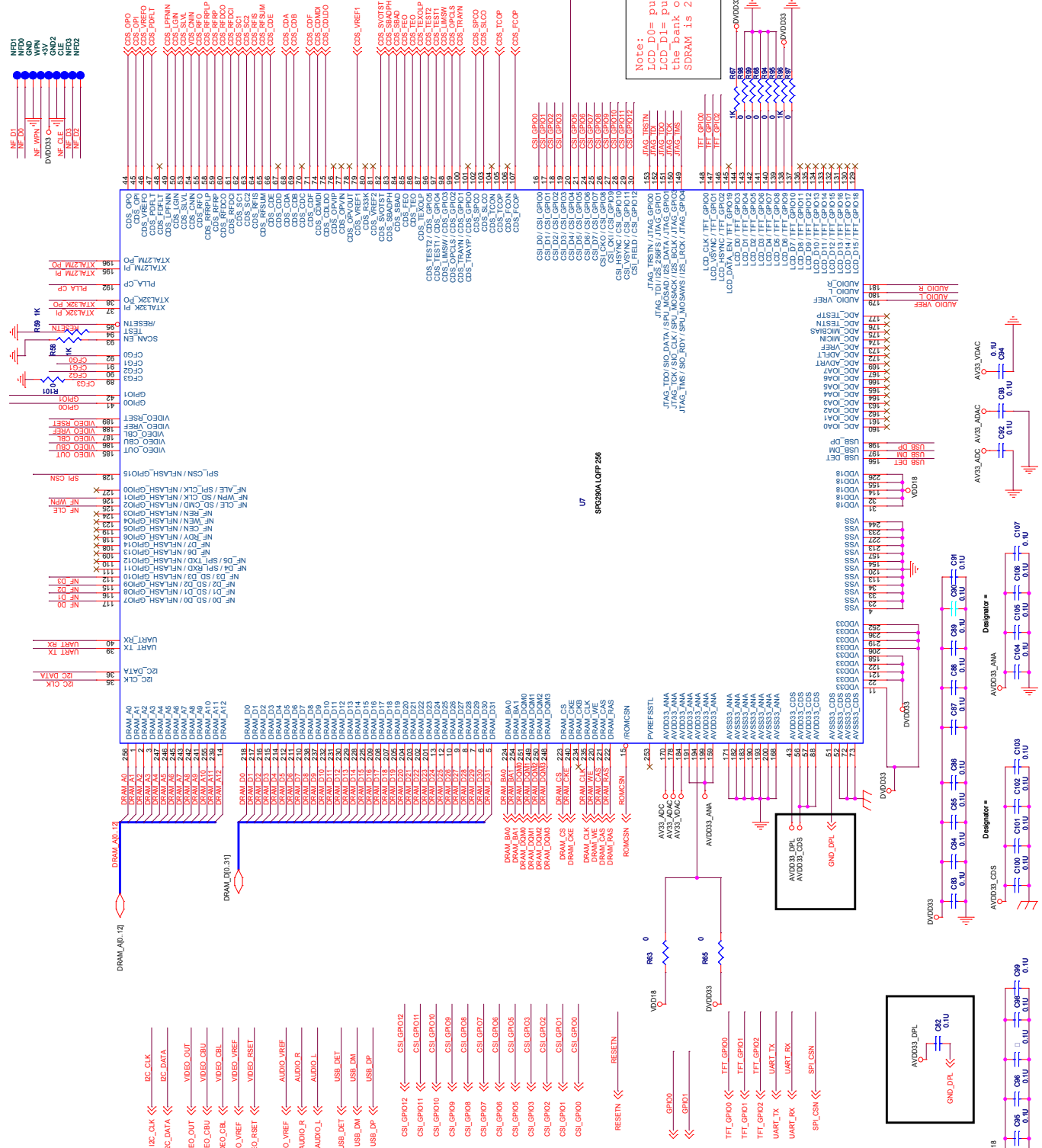


Matei Inc Design by Daniel Stelung

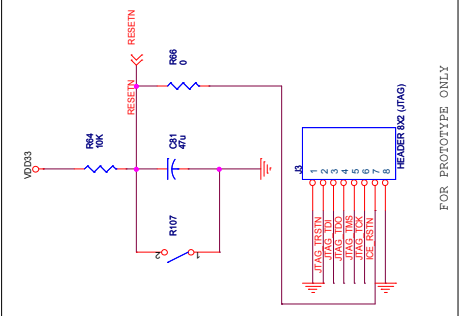
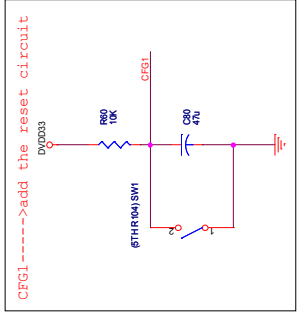
Title DRAM

Size B Document Number SP6230 main board Rev 6th

Date: Tuesday, July 25, 2006 Sheet 5 of 7

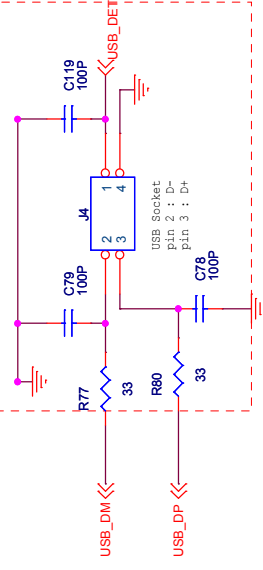


Note:
LCD_D0= pull high,
LCD_D1= pull low,
the_bank of
SDRAM is 2048.



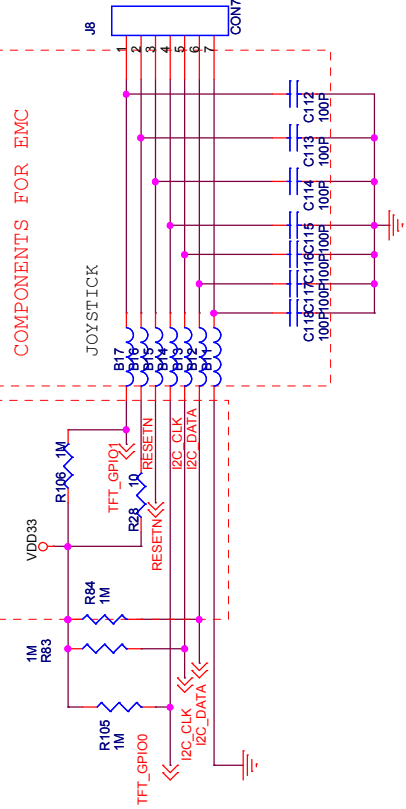
FOR PROTOTYPE ONLY

6th PCB ONLY



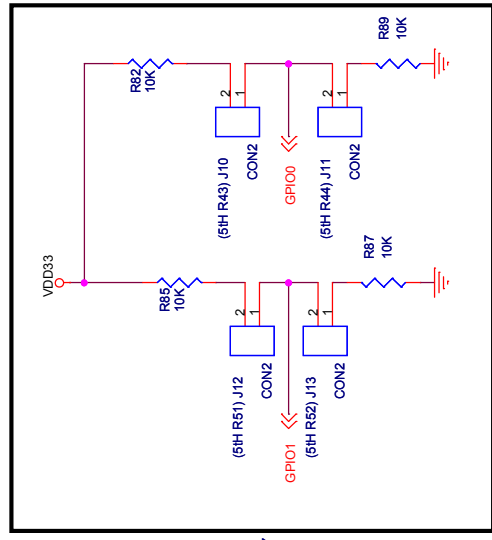
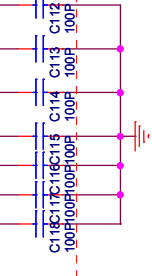
About GPIO0&GPIO1:
In this part, I have add 4 jumper
J10, J11, J12, J13, user can select the frequency
according to change the jumper setting.
chang the IO Setting can
select different frequency.
GPIO Setting Mode:
00--> 108MHz/108MHz
GPIO0/GPIO1
01--> 162MHz/81MHz
GPIO0/GPIO1
10--> 81MHz/81MHz
GPIO0/GPIO1
11--> 40MHz/40MHz

R28 6th PCB ONLY



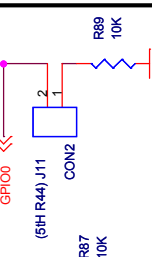
COMPONENTS FOR EMC

JOYSTICK



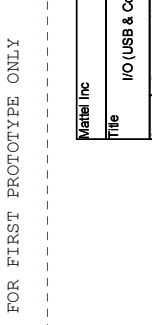
COMPONENTS FOR EMC

JOYSTICK



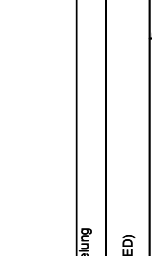
COMPONENTS FOR EMC

JOYSTICK



COMPONENTS FOR EMC

JOYSTICK



Metal Inc		Design by Daniel Stelling	
Title		I/O (USB & Controllers & RFID & LED)	
Size	Document Number	Rev	
B	SPQ290 main board	6th	
Date:	Tuesday, July 25, 2006	Sheet	7 of 7