EXHIBIT 2-1   CFS8DL5804BDMX

CIRCUIT DESCRIPTION

The Transmitter is a amplitude keyed oscillator (Q4,. etc.,) who's FDE is a saw (XL1), it is keyed on and off by Q3. The messages are generated by MICROPROCESSOR (U1). The receiver is a single chip receiver (U3) The Antenna ckt. (C38, ANT, C34) couples though a band-pass filter (C42, L15, XL2, L16, C47) into U3. U4 and U7 process the signal before it is applied to U1 for decoding.