

FCC PART 15.247

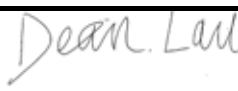
TEST REPORT

For

DT Research Inc.

6F., NO.1, Ning-Po E. Street, Taipei 100, Taiwan

FCC ID: YE3800D
Model: DT311

Report Type: Original Report	Product Type: Mobile Tablet
Test Engineer:	Dean Liu 
Report Number:	RDG150615001-00A
Report Date:	2015-07-01
Reviewed By:	Sula Huang RF Leader 
Test Laboratory:	Bay Area Compliance Laboratories Corp. (Dongguan) No.69 Pulongcun, Puxinhu Industrial Zone, Tangxia, Dongguan, Guangdong, China Tel: +86-769-86858888 Fax: +86-769-86858891 www.baclcorp.com.cn

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GENERAL INFORMATION

Product Description for Equipment under Test (EUT)

The *DT Research Inc.*'s product, model number: *DT311 (FCC ID: YE3800D)* (the "EUT") in this report was a *Mobile Tablet*, which was measured approximately: 31.5 cm (L) x 21.2 cm (W) x 4.2 cm (H), rated input voltage: DC 7.2V rechargeable Li-ion battery or DC19V charging from adapter. The device used Intel® Dual Band Wireless-AC 7265 module, FCC ID:PD97265NG, which support Bluetooth 4.0 standard include BLE and 802.11a/b/g/n/ac.

Adapter information:

Model: A11-065N1A

Input: 100-240V~50/60Hz, 1.7A

Output: 19V, 3.42A

All measurement and test data in this report was gathered from production sample serial number: 150615001 (Assigned by BACL, Dongguan). The EUT was received on 2015-06-15.

Objective

This report is prepared on behalf of *DT Research Inc.* in accordance with Part 2, Subpart J, Part 15, Subparts A, B and C of the Federal Communications Commission's rules

The tests were performed in order to determine the Bluetooth BDR and EDR mode of EUT compliance with FCC Part 15, Subpart C, and section 15.203, 15.205, 15.207, 15.209 and 15.247 rules.

Related Submittal(s)/Grant(s)

FCC Part 15B JBC, 15C DTS, 15E NII and Part 22H, 24E, 27 PCB submissions with FCC ID: YE3800D.

Test Methodology

All measurements contained in this report were conducted with ANSI C63.4-2009, American National Standard for Methods of Measurement of Radio-Noise Emissions from Low-Voltage Electrical and Electronic Equipment in the range of 9 kHz to 40 GHz.

All emissions measurement was performed and Bay Area Compliance Laboratories Corp. (Dongguan).

Test Facility

The Test site used by Bay Area Compliance Laboratories Corp. (Dongguan) to collect test data is located on the No.69 Pulongcun, Puxinhu Industrial Zone, Tangxia, Dongguan, Guangdong, China

Test site at Bay Area Compliance Laboratories Corp. (Dongguan) has been fully described in reports submitted to the Federal Communications Commission (FCC). The details of these reports have been found to be in compliance with the requirements of Section 2.948 of the FCC Rules on February 06, 2015. The facility also complies with the radiated and AC line conducted test site criteria set forth in ANSI C63.4-2009.

The Federal Communications Commission has the reports on file and is listed under FCC Registration No.: 273710. The test site has been approved by the FCC for public use and is listed in the FCC Public Access Link (PAL) database.

SYSTEM TEST CONFIGURATION

Description of Test Configuration

The system was configured for testing in an engineering mode. The Engineering mode was configured by the software:DRTU V1.7.6.,which was used to configure the test channel, the power level was configured as default value by the system.

EUT Exercise Software

The test software: DRTU V1.7.6 was used in the test, which was provided by the manufacturer.

Equipment Modifications

No modification was made to the EUT.

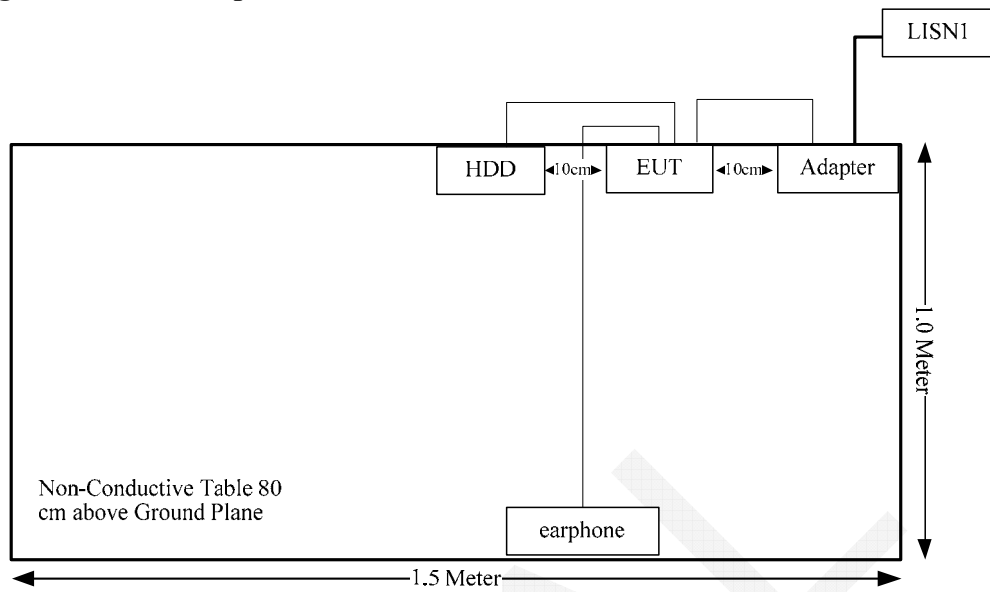
Local Support Equipment List and Details

Manufacturer	Description	Model	Serial Number
TOSHIBA	HDD	V63700-A 500GB	7283TCUTSJ2
/	Earphone	/	/

Support Cable List and Details

Cable Description	Shielding Type	Ferrite Core	Length (m)	From Port	To
Adapter cable	yes	No	1.18	Adapter 1	EUT
Adapter cable	yes	No	1.71	Adapter 2	EUT
Audio Cable	No	No	1.5	EUT	Earphone
USB Cable	yes	No	0.8	EUT	USB-HDD

Block Diagram of Test Setup



SUMMARY OF TEST RESULTS

FCC Rules	Description of Test	Result
FCC §15.247 (i) & §1.1310 & §2.1093	RF Exposure	Compliance
§15.203	Antenna Requirement	Compliance
§15.207 (a)	Conducted Emissions	Compliance
§15.205, §15.209, §15.247(d)	Spurious Emissions	Compliance
§15.247 (a)(1)	20 dB Bandwidth	Compliance
§15.247(a)(1)	Channel Separation Test	Compliance
§15.247(a)(1)(iii)	Time of Occupancy (Dwell Time)	Compliance
§15.247(a)(1)(iii)	Quantity of hopping channel Test	Compliance
§15.247(b)(1)	Peak Output Power Measurement	Compliance
§15.247(d)	Band Edges	Compliance

FCC §15.247 (i) & §1.1310 & §2.1093- RF EXPOSURE

Applicable Standard

According to §15.247(i) and §1.1310, systems operating under the provisions of this section shall be operated in a manner that ensure that the public is not exposed to radio frequency energy level in excess of the Commission's guideline.

According to KDB447498 D01 General RF Exposure Guidance v05r02:

The 1-g and 10-g SAR test exclusion thresholds for 100 MHz to 6 GHz at test separation distances ≤ 50 mm are determined by:

$[(\text{max. power of channel, including tune-up tolerance, mW})/(\text{min. test separation distance, mm})] \cdot [\sqrt{f(\text{GHz})}] \leq 3.0$ for 1-g SAR and ≤ 7.5 for 10-g extremity SAR, where

- $f(\text{GHz})$ is the RF channel transmit frequency in GHz
- Power and distance are rounded to the nearest mW and mm before calculation
- The result is rounded to one decimal place for comparison
- 3.0 and 7.5 are referred to as the numeric thresholds in the step 2 below

The test exclusions are applicable only when the minimum test separation distance is ≤ 50 mm and for transmission frequencies between 100 MHz and 6 GHz. When the minimum test separation distance is < 5 mm, a distance of 5 mm according to 5) in section 4.1 is applied to determine SAR test exclusion.

Measurement Result

The maximum output power = 4.81 dBm (3.03mW) at 2402 MHz

$[(\text{max. power of channel, mW})/(\text{min. test separation distance, mm})][\sqrt{f(\text{GHz})}]$
 $= 3.03/5 \cdot (\sqrt{2.402}) = 0.94 < 3.0$

So the stand-alone SAR evaluation is not necessary.

FCC §15.203 - ANTENNA REQUIREMENT

Applicable Standard

According to FCC § 15.203, an intentional radiator shall be designed to ensure that no antenna other than that furnished by the responsible party shall be used with the device. The use of a permanently attached antenna or of an antenna that uses a unique coupling to the intentional radiator shall be considered sufficient to comply with the provisions of this Section. The manufacturer may design the unit so that a broken antenna can be replaced by the user, but the use of a standard antenna jack or electrical connector is prohibited.

Antenna Connector Construction

The EUT has one internal PIFA antenna arrangement for BT, which the maximum antenna gain is 1.08 dBi, fulfill the requirement of this section. Please refer to the EUT photos.

Result: Compliance.

FCC §15.207 (a) – AC LINE CONDUCTED EMISSIONS

Applicable Standard

FCC§15.207

Measurement Uncertainty

Compliance or non-compliance with a disturbance limit shall be determined in the following manner :

If U_{lab} is less than or equal to U_{cispr} of Table 1, then:

- compliance is deemed to occur if no measured disturbance level exceeds the disturbance limit;
- non-compliance is deemed to occur if any measured disturbance level exceeds the disturbance limit.

If U_{lab} is greater than U_{cispr} of Table 1, then:

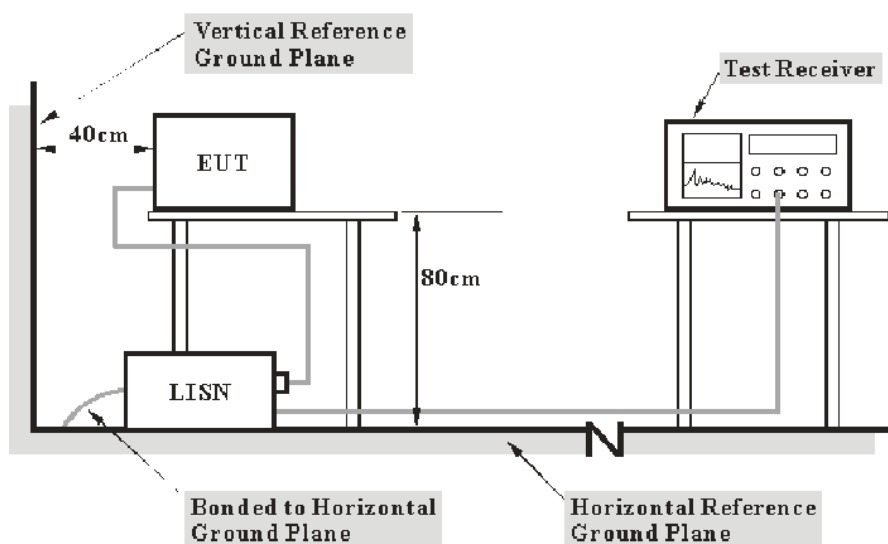
- compliance is deemed to occur if no measured disturbance level, increased by $(U_{lab} - U_{cispr})$, exceeds the disturbance limit;
- non-compliance is deemed to occur if any measured disturbance level, increased by $(U_{lab} - U_{cispr})$, exceeds the disturbance limit.

Based on CISPR 16-4-2: 2011, measurement uncertainty of conducted disturbance at mains port using AMN at Bay Area Compliance Laboratories Corp. (Dongguan) is 3.46 dB (150 kHz to 30 MHz).

Table 1 – Values of U_{cispr}

Measurement	U_{cispr}
Conducted disturbance at mains port using AMN (150 kHz to 30 MHz)	3.4 dB

EUT Setup



Note: 1. Support units were connected to second LISN.
 2. Both of LISNs (AMN) 80 cm from EUT and at the least 80 cm from other units and other metal planes support units.

The setup of EUT is according with per ANSI C63.4-2009 measurement procedure. The specification used was with the FCC Part 15.207 limits.

The spacing between the peripherals was 10 cm.

The adapter was connected to a 120 VAC/60 Hz power source.

EMI Test Receiver Setup

The EMI test receiver was set to investigate the spectrum from 150 kHz to 30 MHz.

During the conducted emission test, the EMI test receiver was set with the following configurations:

Frequency Range	IF B/W
150 kHz – 30 MHz	9 kHz

Test Procedure

During the conducted emission test, the adapter was connected to the outlet of the first LISN.

Maximizing procedure was performed on the six (6) highest emissions of the EUT.

All data was recorded in the Quasi-peak and average detection mode.

Corrected Amplitude & Margin Calculation

The basic equation is as follows:

$$V_C = V_R + A_C + VDF$$

Herein,

V_C : corrected voltage amplitude

V_R : reading voltage amplitude

A_C : attenuation caused by cable loss

VDF: voltage division factor of AMN or ISN

The “**Margin**” column of the following data tables indicates the degree of compliance within the applicable limit. For example, a margin of 7dB means the emission is 7dB below the maximum limit. The equation for margin calculation is as follows:

$$\text{Margin} = \text{Limit} - \text{Corrected Amplitude}$$

Test Equipment List and Details

Manufacturer	Description	Model	Serial Number	Calibration Date	Calibration Due Date
R&S	EMI Test Receiver	ESCS 30	830245/006	2014-10-20	2015-10-20
R&S	L.I.S.N	ESH2-Z5	892107/021	2015-06-09	2016-06-09
R&S	Two-line V-network	ENV 216	3560.6550.12	2014-12-11	2015-12-11
R&S	Test Software	EMC32	Version8.53.0	N/A	N/A

* **Statement of Traceability:** Bay Area Compliance Laboratories Corp. (Dongguan) attests that all calibrations have been performed, traceable to National Primary Standards and International System of Units (SI).

Test Results Summary

According to the recorded data in following table, the EUT complied with the FCC Part 15.207, with the worst margin reading of:

5.0 dB at 0.187494 MHz in the Line conducted mode

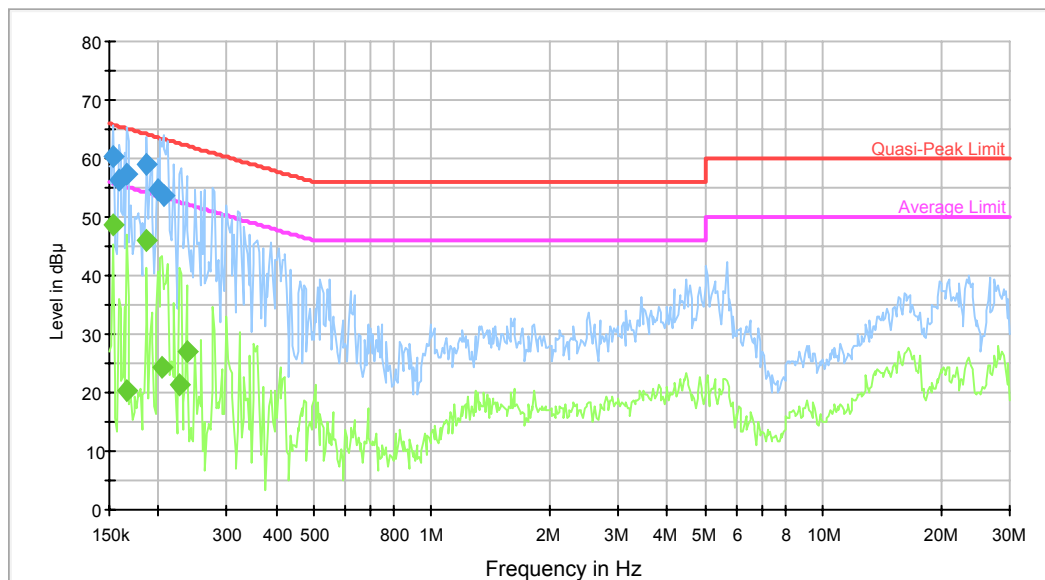
Test Data**Environmental Conditions**

Temperature:	27.6°C
Relative Humidity:	53 %
ATM Pressure:	100kPa

The testing was performed by Dean Liu on 2015-06-19.

Test Mode: Transmitting

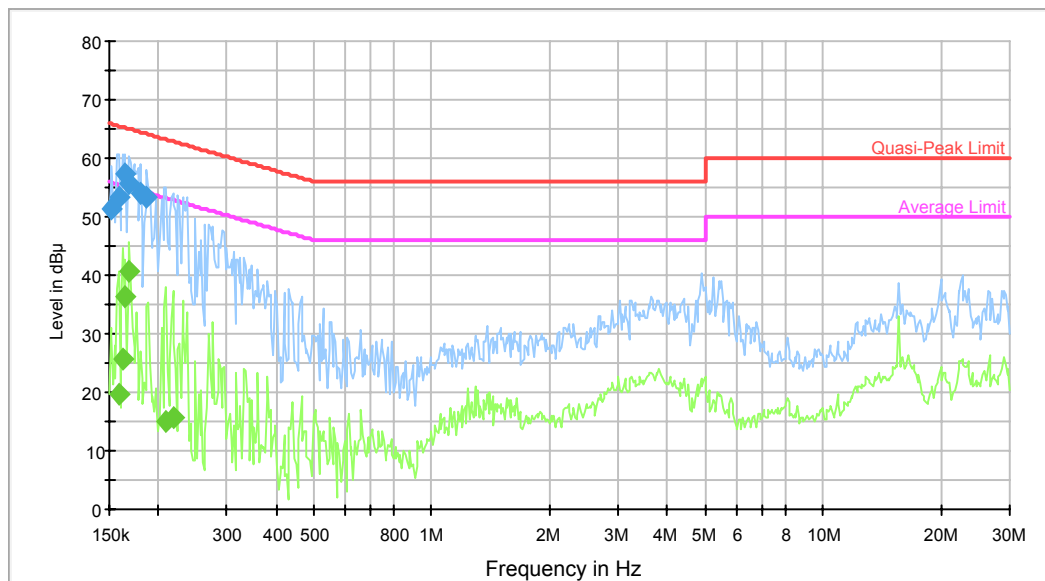
AC120 V, 60 Hz, Line:



Frequency (MHz)	QuasiPeak (dBμV)	Bandwidth (kHz)	Line	Corr. (dB)	Margin (dB)	Limit (dBμV)	Comment
0.153629	60.4	9.000	L1	10.2	5.4	65.8	Compliance
0.158604	56.2	9.000	L1	10.2	9.3	65.5	Compliance
0.166371	57.2	9.000	L1	10.2	7.9	65.1	Compliance
0.187494	59.1	9.000	L1	10.2	5.0	64.1	Compliance
0.199835	54.7	9.000	L1	10.2	8.9	63.6	Compliance
0.206306	53.7	9.000	L1	10.2	9.7	63.4	Compliance

Frequency (MHz)	Average (dBμV)	Bandwidth (kHz)	Line	Corr. (dB)	Margin (dB)	Limit (dBμV)	Comment
0.153629	48.6	9.000	L1	10.2	7.2	55.8	Compliance
0.166371	20.3	9.000	L1	10.2	34.8	55.1	Compliance
0.187494	45.9	9.000	L1	10.2	8.2	54.1	Compliance
0.204669	24.2	9.000	L1	10.2	29.2	53.4	Compliance
0.227007	21.2	9.000	L1	10.2	31.4	52.6	Compliance
0.236234	27.0	9.000	L1	10.2	25.2	52.2	Compliance

*within measurement uncertainty!

AC120 V, 60 Hz, Neutral:

Frequency (MHz)	QuasiPeak (dBμV)	Bandwidth (kHz)	Line	Corr. (dB)	Margin (dB)	Limit (dBμV)	Comment
0.152410	51.2	9.000	N	10.2	14.7	65.9	Compliance
0.158604	53.3	9.000	N	10.2	12.2	65.5	Compliance
0.165051	57.5	9.000	N	10.2	7.7	65.2	Compliance
0.169044	55.8	9.000	N	10.2	9.2	65.0	Compliance
0.180171	54.0	9.000	N	10.2	10.5	64.5	Compliance
0.187494	53.2	9.000	N	10.2	10.9	64.1	Compliance

Frequency (MHz)	Average (dBμV)	Bandwidth (kHz)	Line	Corr. (dB)	Margin (dB)	Limit (dBμV)	Comment
0.158604	19.6	9.000	N	10.2	35.9	55.5	Compliance
0.162441	25.6	9.000	N	10.2	29.7	55.3	Compliance
0.165051	36.4	9.000	N	10.2	18.8	55.2	Compliance
0.169044	40.5	9.000	N	10.2	14.5	55.0	Compliance
0.209621	15.0	9.000	N	10.2	38.2	53.2	Compliance
0.218141	15.7	9.000	N	10.2	37.2	52.9	Compliance

**within measurement uncertainty!*

FCC §15.209, §15.205 & §15.247(d) - SPURIOUS EMISSIONS

Applicable Standard

FCC §15.247 (d); §15.209; §15.205;

Measurement Uncertainty

Compliance or non-compliance with a disturbance limit shall be determined in the following manner :

If U_{lab} is less than or equal to U_{cisp} of Table 1, then:

- compliance is deemed to occur if no measured disturbance level exceeds the disturbance limit;
- non-compliance is deemed to occur if any measured disturbance level exceeds the disturbance limit.

If U_{lab} is greater than U_{cisp} of Table 1, then:

- compliance is deemed to occur if no measured disturbance level, increased by $(U_{lab} - U_{cisp})$, exceeds the disturbance limit;
- non-compliance is deemed to occur if any measured disturbance level, increased by $(U_{lab} - U_{cisp})$, exceeds the disturbance limit.

Based on CISPR 16-4-2: 2011, measurement uncertainty of radiated emission at a distance of 3m at Bay Area Compliance Laboratories Corp. (Dongguan) is:

30M~200MHz: 5.0 dB

200M~1GHz: 6.2 dB

1G~6GHz: 4.45 dB

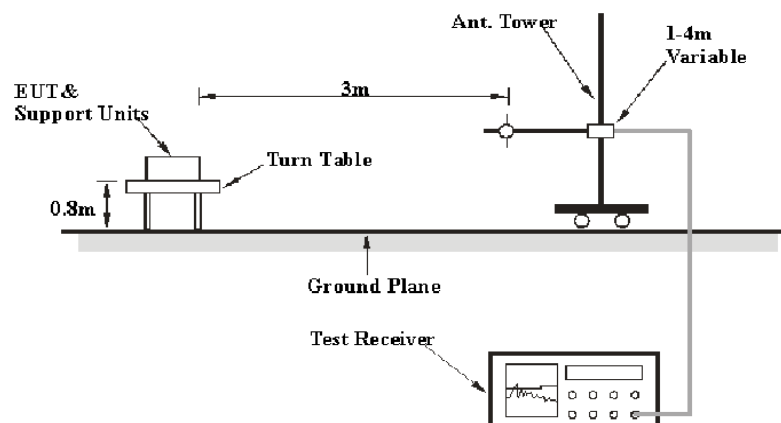
6G~18GHz: 5.23 dB

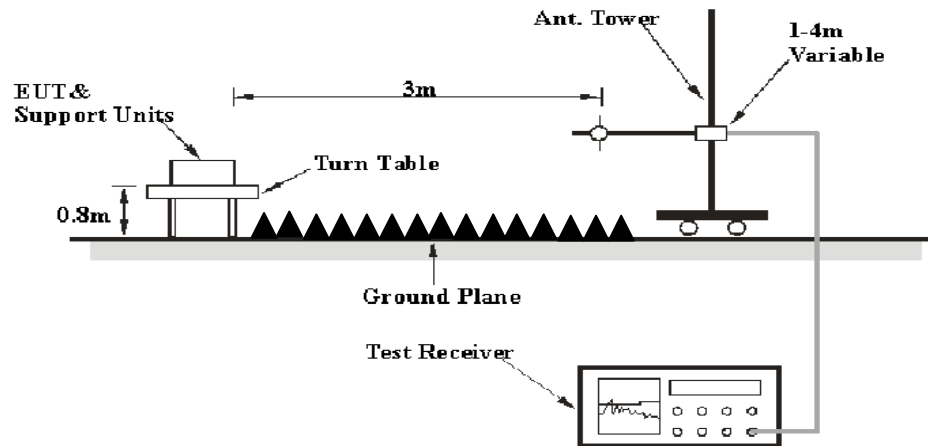
Table 1 – Values of U_{cisp}

Measurement	U_{cisp}
Radiated disturbance (electric field strength at an OATS or in a SAC) (30 MHz to 1000 MHz)	6.3 dB
Radiated disturbance (electric field strength in a FAR) (1 GHz to 6 GHz)	5.2 dB
Radiated disturbance (electric field strength in a FAR) (6 GHz to 18 GHz)	5.5 dB

EUT Setup

Below 1GHz:



Above 1GHz:

The radiated emission tests were performed in the 3 meters test site, using the setup accordance with the ANSI C63.4-2009. The specification used was the FCC 15.209, and FCC 15.247 limits.

The external I/O cables were draped along the test table and formed a bundle 30 to 40 cm long in the middle.

The spacing between the peripherals was 10 cm.

EMI Test Receiver & Spectrum Analyzer Setup

The system was investigated from 30 MHz to 25 GHz.

During the radiated emission test, the EMI test receiver & Spectrum Analyzer Setup were set with the following configurations:

Frequency Range	RBW	Video B/W	IF B/W	Detector
30 MHz – 1000 MHz	120 kHz	300 kHz	120 kHz	QP
Above 1 GHz	1MHz	3 MHz	/	PK
	1MHz	10 Hz	/	Ave.

Test Procedure

Maximizing procedure was performed on the highest emissions to ensure that the EUT complied with all installation combinations.

Data was recorded in Quasi-peak detection mode for frequency range of 30 MHz - 1 GHz, peak and average detection modes for frequencies above 1 GHz.

Test Equipment List and Details

Manufacturer	Description	Model	Serial Number	Calibration Date	Calibration Due Date
R&S	EMI Test Receiver	ESCI	100224	2015-05-09	2016-05-09
Sunol Sciences	Antenna	JB3	A060611-3	2014-07-28	2017-07-27
HP	Amplifier	8447E	2434A02181	2014-09-01	2015-09-01
R&S	Spectrum Analyzer	E4440A	SG43360054	2014-12-04	2015-12-04
ETS LINDGREN	Horn Antenna	3115	000 527 35	2012-09-06	2015-09-06
Mini-Circuit	Amplifier	ZVA-213-S+	054201245	2015-02-19	2016-02-19
R&S	Spectrum Analyzer	FSP 38	100478	2015-05-09	2016-05-09
Ducommun Technologies	Horn Antenna	ARH-4223-02	1007726-01 1304	2014-06-16	2017-06-15
Quinstar	Amplifier	QLW-18405536-JO	15964001001	2014-09-06	2015-09-06

* **Statement of Traceability:** Bay Area Compliance Laboratories Corp. (Dongguan) attests that all calibrations have been performed, traceable to National Primary Standards and International System of Units (SI).

Corrected Amplitude & Margin Calculation

The Corrected Amplitude is calculated by adding the Antenna Factor and Cable Loss, and subtracting the Amplifier Gain from the Meter Reading. The basic equation is as follows:

$$\text{Corrected Amplitude} = \text{Meter Reading} + \text{Antenna Factor} + \text{Cable Loss} - \text{Amplifier Gain}$$

The “**Margin**” column of the following data tables indicates the degree of compliance with the applicable limit. For example, a margin of 7dB means the emission is 7dB below the limit. The equation for margin calculation is as follows:

$$\text{Margin} = \text{Limit} - \text{Corrected Amplitude}$$

Test Results Summary

According to the recorded data in following table, the EUT complied with the FCC Title 47, Part 15, Subpart C, and section 15.205, 15.209 and 15.247, with the worst margin reading of:

9.41 dB at 139.61 MHz in the Horizontal polarization of EDR Mode (8DPSK)

Test Data**Environmental Conditions**

Temperature:	25.7 °C
Relative Humidity:	57 %
ATM Pressure:	99.9 kPa

* The testing was performed by Dean Liu on 2015-06-18.

Test Mode: Transmitting

BDR Mode (GFSK):

DDR Mode (GFSK)									
Frequency	Receiver		Rx Antenna		Cable loss (dB)	Amplifier Gain (dB)	Corrected Amplitude (dBμV/m)	FCC 15.247	
(MHz)	Reading (dBμV)	Detector (PK/QP/AV)	Polar (H/V)	Factor (dB)				Limit (dBμV/m)	Margin (dB)
Low Channel: 2402 MHz									
2402	68.54	PK	H	25.65	3.66	0.00	97.85	N/A	N/A
2402	49.3	AV	H	25.65	3.66	0.00	78.61	N/A	N/A
2402	70.43	PK	V	25.65	3.66	0.00	99.74	N/A	N/A
2402	51.31	AV	V	25.65	3.66	0.00	80.62	N/A	N/A
2390	25.87	PK	V	25.61	3.63	0.00	55.11	74.00	18.89
2390	14.32	AV	V	25.61	3.63	0.00	43.56	54.00	10.44
4804	32.28	PK	V	30.59	5.06	27.41	40.52	74.00	33.48
4804	19.9	AV	V	30.59	5.06	27.41	28.14	54.00	25.86
7206	32.44	PK	V	34.09	6.61	25.91	47.23	74.00	26.77
7206	20.07	AV	V	34.09	6.61	25.91	34.86	54.00	19.14
9608	30.56	PK	V	35.96	8.53	27.55	47.50	74.00	26.50
9608	18.73	AV	V	35.96	8.53	27.55	35.67	54.00	18.33
4365	33.48	PK	V	29.83	5.00	26.92	41.39	74.00	32.61
4365	21.28	AV	V	29.83	5.00	26.92	29.19	54.00	24.81
345.25	37.8	QP	H	14.98	2.22	21.63	33.37	46.00	12.63
139.61	40.1	QP	H	13.27	1.44	21.42	33.39	43.50	10.11
Middle Channel: 2441 MHz									
2441	67.78	PK	H	25.75	3.76	0.00	97.29	N/A	N/A
2441	57.46	AV	H	25.75	3.76	0.00	86.97	N/A	N/A
2441	71.45	PK	V	25.75	3.76	0.00	100.96	N/A	N/A
2441	61.1	AV	V	25.75	3.76	0.00	90.61	N/A	N/A
4882	32.73	PK	V	30.79	5.19	27.42	41.29	74.00	32.71
4882	20.31	AV	V	30.79	5.19	27.42	28.87	54.00	25.13
7323	32.77	PK	V	34.38	6.75	25.88	48.02	74.00	25.98
7323	20.39	AV	V	34.38	6.75	25.88	35.64	54.00	18.36
9764	31.04	PK	V	36.33	8.62	27.20	48.79	74.00	25.21
9764	19.11	AV	V	36.33	8.62	27.20	36.86	54.00	17.14
4365	33.84	PK	V	29.83	5.00	26.92	41.75	74.00	32.25
4365	21.59	AV	V	29.83	5.00	26.92	29.50	54.00	24.50
3095	34.63	PK	V	27.50	6.82	27.45	41.50	74.00	32.50
3095	33.47	AV	V	27.50	6.82	27.45	40.34	54.00	13.66
345.25	37.5	QP	H	14.98	2.22	21.63	33.07	46.00	12.93
139.61	40.3	QP	H	13.27	1.44	21.42	33.59	43.50	9.91
High Channel: 2480 MHz									
2480	65.76	PK	H	25.85	3.68	0.00	95.29	N/A	N/A
2480	55.41	AV	H	25.85	3.68	0.00	84.94	N/A	N/A
2480	66.63	PK	V	25.85	3.68	0.00	96.16	N/A	N/A
2480	56.33	AV	V	25.85	3.68	0.00	85.86	N/A	N/A
2483.5	26.31	PK	V	25.86	3.67	0.00	55.84	74.00	18.16
2483.5	14.46	AV	V	25.86	3.67	0.00	43.99	54.00	10.01
4960	31.67	PK	V	31.00	5.34	27.43	40.58	74.00	33.42
4960	19.3	AV	V	31.00	5.34	27.43	28.21	54.00	25.79
7440	31.76	PK	V	34.66	6.89	25.97	47.34	74.00	26.66
7440	19.3	AV	V	34.66	6.89	25.97	34.88	54.00	19.12
9920	29.93	PK	V	36.71	8.71	26.66	48.69	74.00	25.31
9920	18	AV	V	36.71	8.71	26.66	36.76	54.00	17.24
4365	32.7	PK	V	29.83	5.00	26.92	40.61	74.00	33.39
4365	20.5	AV	V	29.83	5.00	26.92	28.41	54.00	25.59
345.25	37.1	QP	H	14.98	2.22	21.63	32.67	46.00	13.33
139.61	40.2	QP	H	13.27	1.44	21.42	33.49	43.50	10.01

EDR Mode ($\pi/4$ -DQPSK):

Frequency	Receiver		Rx Antenna		Cable loss (dB)	Amplifier Gain (dB)	Corrected Amplitude (dBμV/m)	FCC 15.247	
(MHz)	Reading (dBμV)	Detector (PK/QP/AV)	Polar (H/V)	Factor (dB)				Limit (dBμV/m)	Margin (dB)
Low Channel: 2402 MHz									
2402	66.39	PK	H	25.65	3.66	0.00	95.70	N/A	N/A
2402	54.58	AV	H	25.65	3.66	0.00	83.89	N/A	N/A
2402	68.07	PK	V	25.65	3.66	0.00	97.38	N/A	N/A
2402	56.2	AV	V	25.65	3.66	0.00	85.51	N/A	N/A
2390	25.3	PK	V	25.61	3.63	0.00	54.54	74.00	19.46
2390	13.96	AV	V	25.61	3.63	0.00	43.20	54.00	10.80
4804	32.03	PK	V	30.59	5.06	27.41	40.27	74.00	33.73
4804	19.7	AV	V	30.59	5.06	27.41	27.94	54.00	26.06
7206	32.6	PK	V	34.09	6.61	25.91	47.39	74.00	26.61
7206	20.46	AV	V	34.09	6.61	25.91	35.25	54.00	18.75
9608	30.21	PK	V	35.96	8.53	27.55	47.15	74.00	26.85
9608	18.42	AV	V	35.96	8.53	27.55	35.36	54.00	18.64
2995	33.52	PK	V	27.19	6.77	27.53	39.95	74.00	34.05
2995	21.36	AV	V	27.19	6.77	27.53	27.79	54.00	26.21
345.25	38.1	QP	H	14.98	2.22	21.63	33.67	46.00	12.33
139.61	40.6	QP	H	13.27	1.44	21.42	33.89	43.50	9.61
Middle Channel: 2441 MHz									
2441	66.52	PK	H	25.75	3.76	0.00	96.03	N/A	N/A
2441	54.85	AV	H	25.75	3.76	0.00	84.36	N/A	N/A
2441	69.3	PK	V	25.75	3.76	0.00	98.81	N/A	N/A
2441	57.98	AV	V	25.75	3.76	0.00	87.49	N/A	N/A
4882	32.35	PK	V	30.79	5.19	27.42	40.91	74.00	33.09
4882	20.13	AV	V	30.79	5.19	27.42	28.69	54.00	25.31
7323	33.04	PK	V	34.38	6.75	25.88	48.29	74.00	25.71
7323	20.85	AV	V	34.38	6.75	25.88	36.10	54.00	17.90
9764	30.63	PK	V	36.33	8.62	27.20	48.38	74.00	25.62
9764	18.72	AV	V	36.33	8.62	27.20	36.47	54.00	17.53
2995	33.88	PK	V	27.19	6.77	27.53	40.31	74.00	33.69
2995	21.69	AV	V	27.19	6.77	27.53	28.12	54.00	25.88
2065	35.35	PK	V	24.77	3.32	27.42	36.02	74.00	37.98
2065	23.16	AV	V	24.77	3.32	27.42	23.83	54.00	30.17
345.25	37.9	QP	H	14.98	2.22	21.63	33.47	46.00	12.53
139.61	40.5	QP	H	13.27	1.44	21.42	33.79	43.50	9.71
High Channel: 2480 MHz									
2480	63.51	PK	H	25.85	3.68	0.00	93.04	N/A	N/A
2480	51.46	AV	H	25.85	3.68	0.00	80.99	N/A	N/A
2480	64.4	PK	V	25.85	3.68	0.00	93.93	N/A	N/A
2480	52.43	AV	V	25.85	3.68	0.00	81.96	N/A	N/A
2483.5	25.97	PK	V	25.86	3.67	0.00	55.50	74.00	18.50
2483.5	14.49	AV	V	25.86	3.67	0.00	44.02	54.00	9.98
4960	31.15	PK	V	31.00	5.34	27.43	40.06	74.00	33.94
4960	18.98	AV	V	31.00	5.34	27.43	27.89	54.00	26.11
7440	31.93	PK	V	34.66	6.89	25.97	47.51	74.00	26.49
7440	19.83	AV	V	34.66	6.89	25.97	35.41	54.00	18.59
9920	29.55	PK	V	36.71	8.71	26.66	48.31	74.00	25.69
9920	17.55	AV	V	36.71	8.71	26.66	36.31	54.00	17.69
2995	32.84	PK	V	27.19	6.77	27.53	39.27	74.00	34.73
2995	20.49	AV	V	27.19	6.77	27.53	26.92	54.00	27.08
345.25	37.8	QP	H	14.98	2.22	21.63	33.37	46.00	12.63
139.61	40.4	QP	H	13.27	1.44	21.42	33.69	43.50	9.81

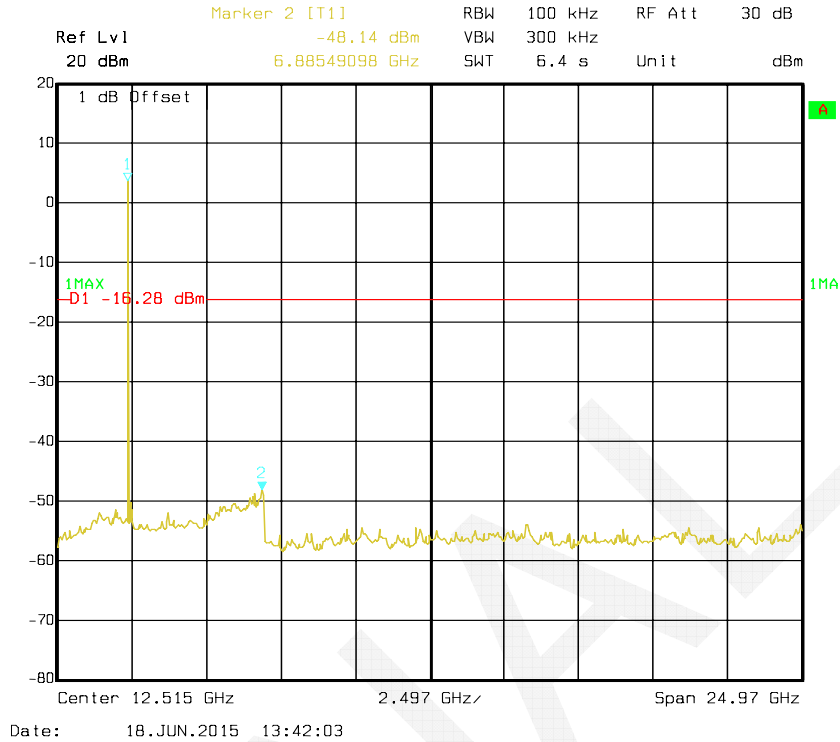
EDR Mode (8-DPSK):

Frequency	Receiver		Rx Antenna		Cable loss (dB)	Amplifier Gain (dB)	Corrected Amplitude (dBμV/m)	FCC 15.247	
(MHz)	Reading (dBμV)	Detector (PK/QP/AV)	Polar (H/V)	Factor (dB)				Limit (dBμV/m)	Margin (dB)
Low Channel: 2402 MHz									
2402	65.77	PK	H	25.65	3.66	0.00	95.08	N/A	N/A
2402	53.51	AV	H	25.65	3.66	0.00	82.82	N/A	N/A
2402	67.44	PK	V	25.65	3.66	0.00	96.75	N/A	N/A
2402	55.23	AV	V	25.65	3.66	0.00	84.54	N/A	N/A
2390	25.89	PK	V	25.61	3.63	0.00	55.13	74.00	18.87
2390	13.86	AV	V	25.61	3.63	0.00	43.10	54.00	10.90
4804	32.16	PK	V	30.59	5.06	27.41	40.40	74.00	33.60
4804	20.02	AV	V	30.59	5.06	27.41	28.26	54.00	25.74
7206	32.89	PK	V	34.09	6.61	25.91	47.68	74.00	26.32
7206	20.6	AV	V	34.09	6.61	25.91	35.39	54.00	18.61
9608	30.53	PK	V	35.96	8.53	27.55	47.47	74.00	26.53
9608	18.39	AV	V	35.96	8.53	27.55	35.33	54.00	18.67
3055	33.37	PK	V	27.38	6.66	27.49	39.92	74.00	34.08
3055	21.17	AV	V	27.38	6.66	27.49	27.72	54.00	26.28
345.25	38.7	QP	H	14.98	2.22	21.63	34.27	46.00	11.73
139.61	40.8	QP	H	13.27	1.44	21.42	34.09	43.50	9.41
Middle Channel: 2441 MHz									
2441	66.02	PK	H	25.75	3.76	0.00	95.53	N/A	N/A
2441	53.71	AV	H	25.75	3.76	0.00	83.22	N/A	N/A
2441	68.78	PK	V	25.75	3.76	0.00	98.29	N/A	N/A
2441	56.46	AV	V	25.75	3.76	0.00	85.97	N/A	N/A
4882	32.56	PK	V	30.79	5.19	27.42	41.12	74.00	32.88
4882	20.42	AV	V	30.79	5.19	27.42	28.98	54.00	25.02
7323	33.27	PK	V	34.38	6.75	25.88	48.52	74.00	25.48
7323	21.09	AV	V	34.38	6.75	25.88	36.34	54.00	17.66
9764	30.98	PK	V	36.33	8.62	27.20	48.73	74.00	25.27
9764	18.72	AV	V	36.33	8.62	27.20	36.47	54.00	17.53
3055	33.87	PK	V	27.38	6.66	27.49	40.42	74.00	33.58
3055	21.61	AV	V	27.38	6.66	27.49	28.16	54.00	25.84
2155	35.4	PK	V	25.00	3.18	27.33	36.25	74.00	37.75
2155	23.16	AV	V	25.00	3.18	27.33	24.01	54.00	29.99
345.25	38.4	QP	H	14.98	2.22	21.63	33.97	46.00	12.03
139.61	40.3	QP	H	13.27	1.44	21.42	33.59	43.50	9.91
High Channel: 2480 MHz									
2480	62.67	PK	H	25.85	3.68	0.00	92.20	N/A	N/A
2480	50.1	AV	H	25.85	3.68	0.00	79.63	N/A	N/A
2480	63.72	PK	V	25.85	3.68	0.00	93.25	N/A	N/A
2480	51.09	AV	V	25.85	3.68	0.00	80.62	N/A	N/A
2483.5	26.1	PK	V	25.86	3.67	0.00	55.63	74.00	18.37
2483.5	14.47	AV	V	25.86	3.67	0.00	44.00	54.00	10.00
4960	31.41	PK	V	31.00	5.34	27.43	40.32	74.00	33.68
4960	19.37	AV	V	31.00	5.34	27.43	28.28	54.00	25.72
7440	32.17	PK	V	34.66	6.89	25.97	47.75	74.00	26.25
7440	19.89	AV	V	34.66	6.89	25.97	35.47	54.00	18.53
9920	29.96	PK	V	36.71	8.71	26.66	48.72	74.00	25.28
9920	17.6	AV	V	36.71	8.71	26.66	36.36	54.00	17.64
3055	32.84	PK	V	27.38	6.66	27.49	39.39	74.00	34.61
3055	20.6	AV	V	27.38	6.66	27.49	27.15	54.00	26.85
345.25	38.5	QP	H	14.98	2.22	21.63	34.07	46.00	11.93
139.61	40.4	QP	H	13.27	1.44	21.42	33.69	43.50	9.81

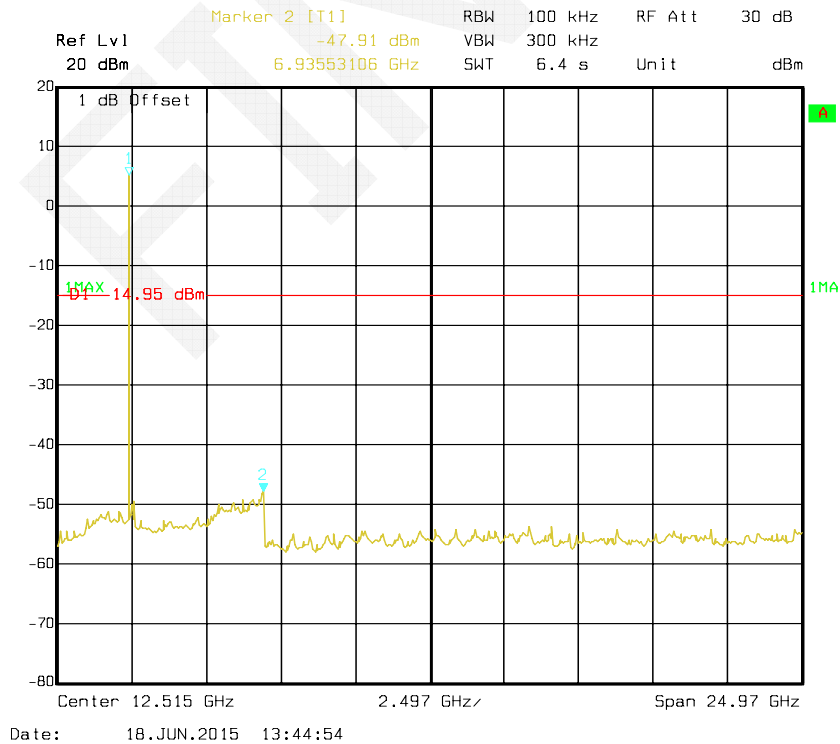
Conducted Spurious Emissions at Antenna Port

BDR Mode (GFSK):

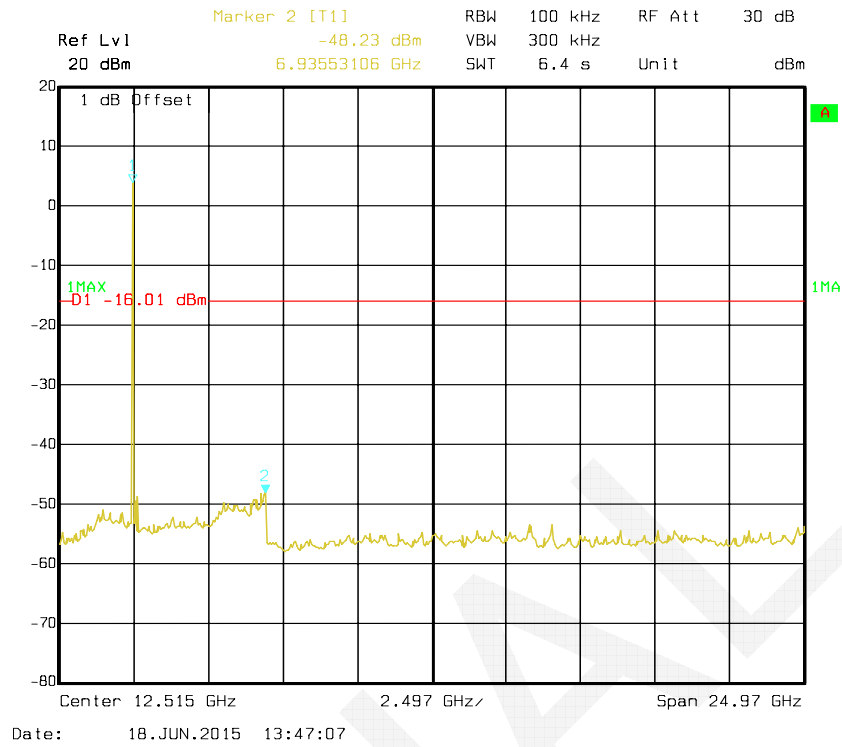
Low Channel



Middle Channel

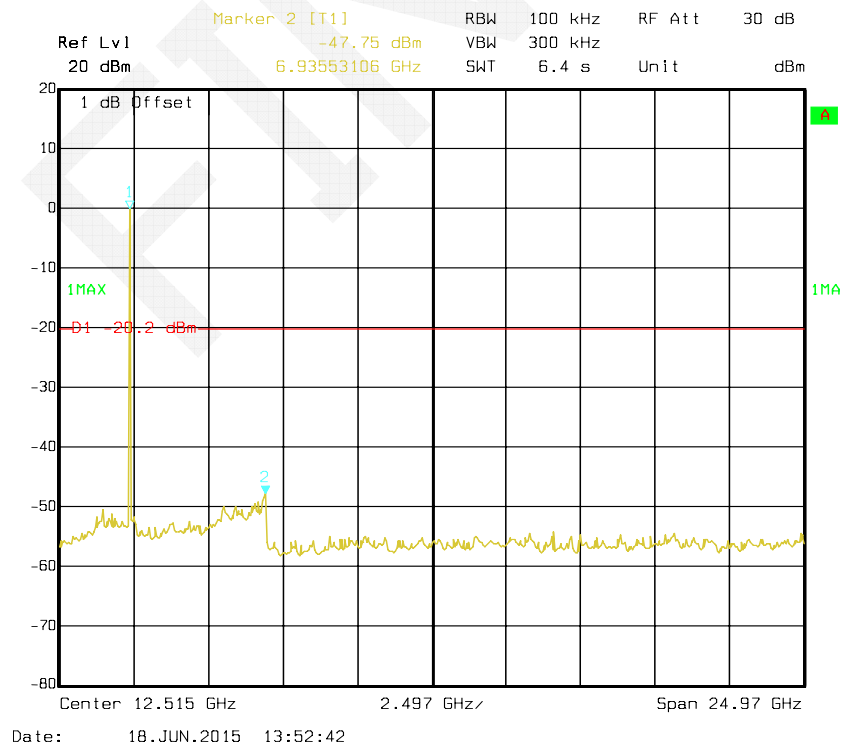


High Channel

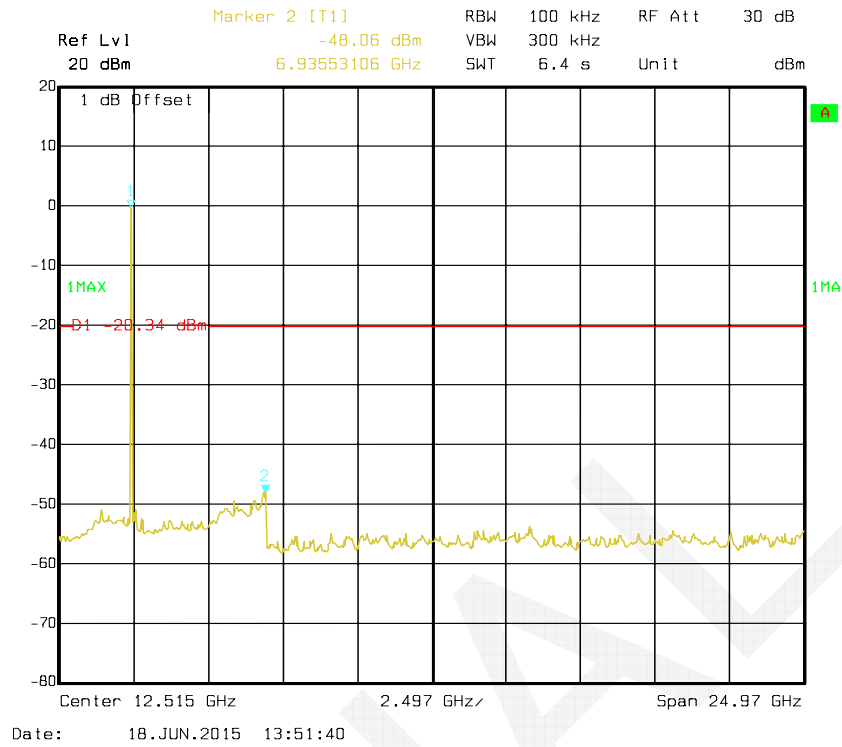


EDR Mode ($\pi/4$ -DQPSK):

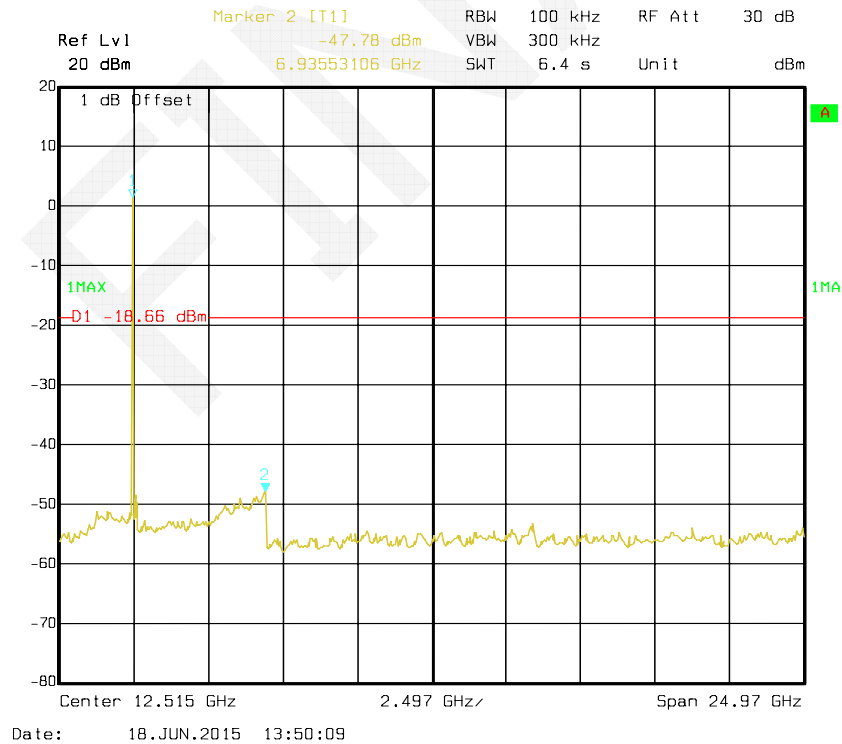
Low Channel



Middle Channel

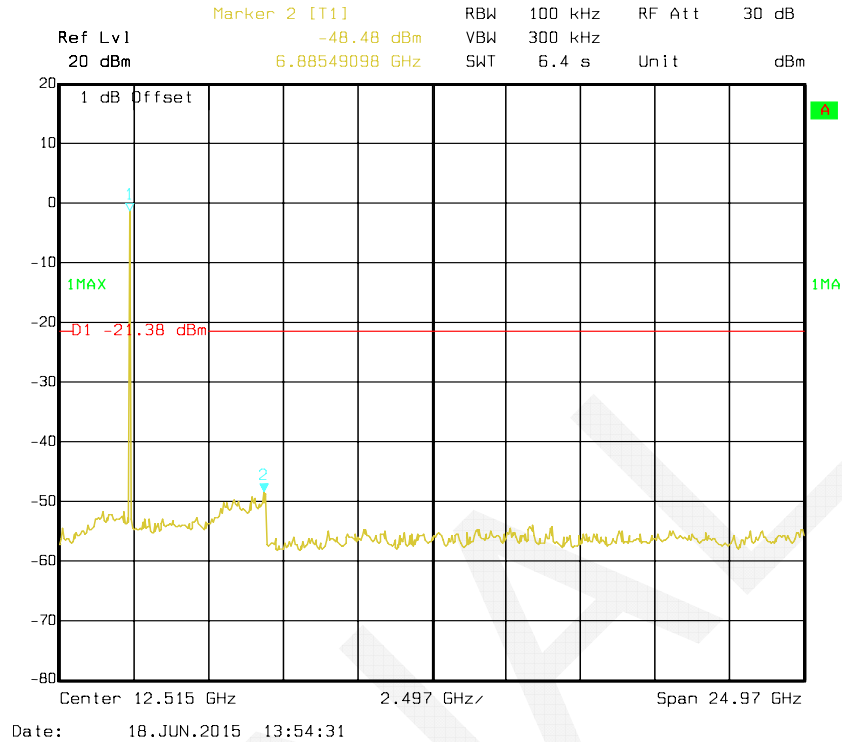


High Channel

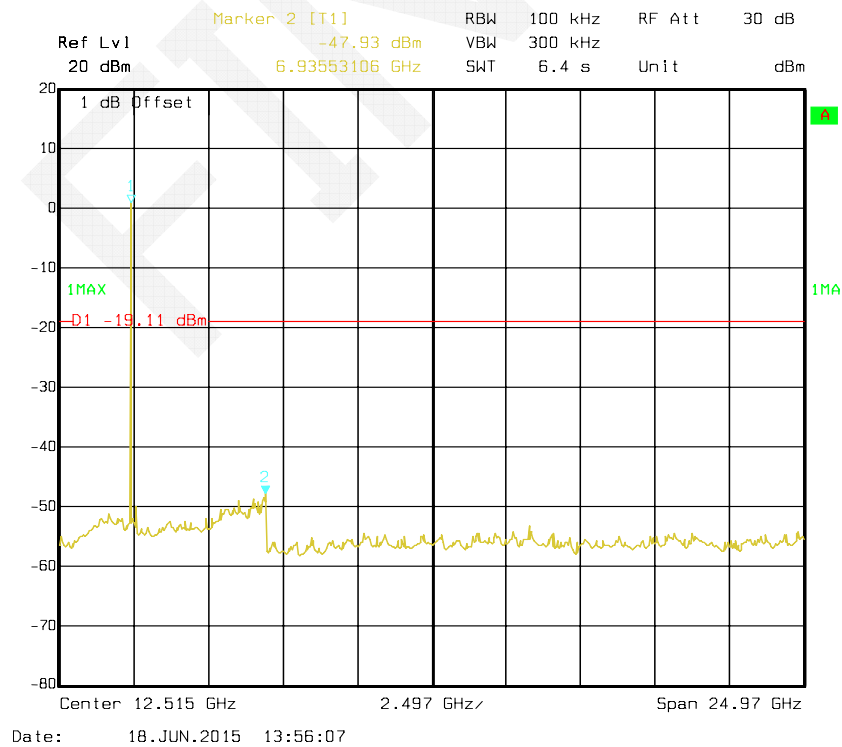


EDR Mode (8-DPSK):

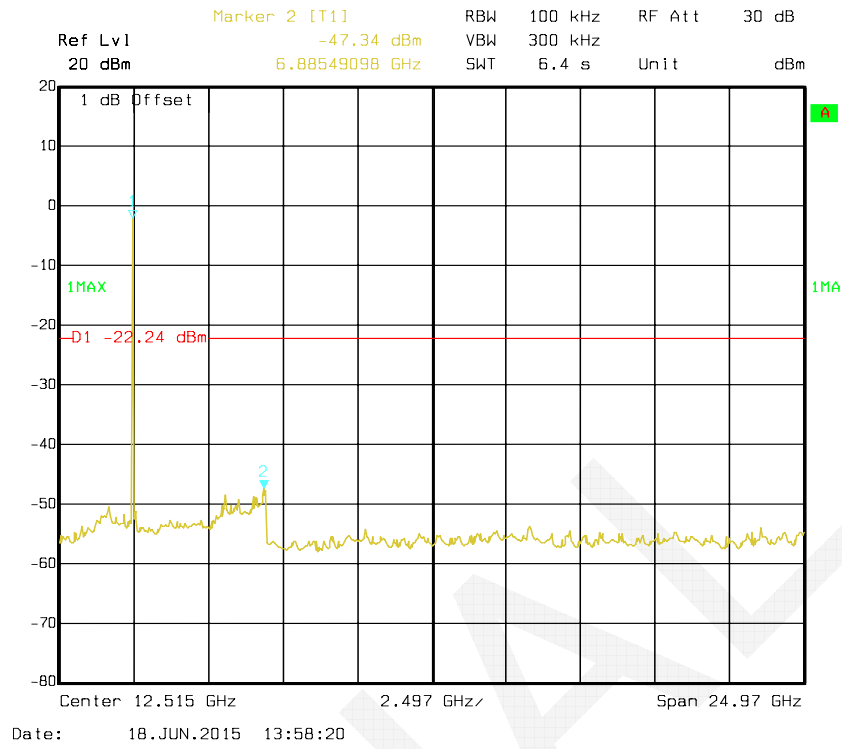
Low Channel



Middle Channel



High Channel



FCC §15.247(a) (1) - CHANNEL SEPARATION TEST

Applicable Standard

Frequency hopping systems shall have hopping channel carrier frequencies separated by a minimum of 25 kHz or the 20dB bandwidth of the hopping channel, whichever is greater. Alternatively, frequency hopping systems operating in the 2400-2483.50 MHz band may have hopping channel carrier frequencies that are separated by 25 kHz or two-thirds of the 20dB bandwidth of the hopping channel, whichever is greater provided the systems operate with an output power no greater than 125 mW.

Test Equipment List and Details

Manufacturer	Description	Model	Serial Number	Calibration Date	Calibration Due Date
R&S	Spectrum Analyzer	FSP 38	100478	2015-05-09	2016-05-09

* **Statement of Traceability:** Bay Area Compliance Laboratories Corp. (Dongguan) attests that all calibrations have been performed, traceable to National Primary Standards and International System of Units (SI).

Test Procedure

1. Set the EUT in transmitting mode, spectrum Bandwidth was set at 30 kHz, maxhold the channel.
2. Set the adjacent channel of the EUT maxhold another trace
3. Measure the channel separation.

Test Data

Environmental Conditions

Temperature:	25.8°C
Relative Humidity:	56 %
ATM Pressure:	100 kPa

* The testing was performed by Dean Liu on 2015-06-26.

Test Result: Compliance.

Please refer to following tables and plots

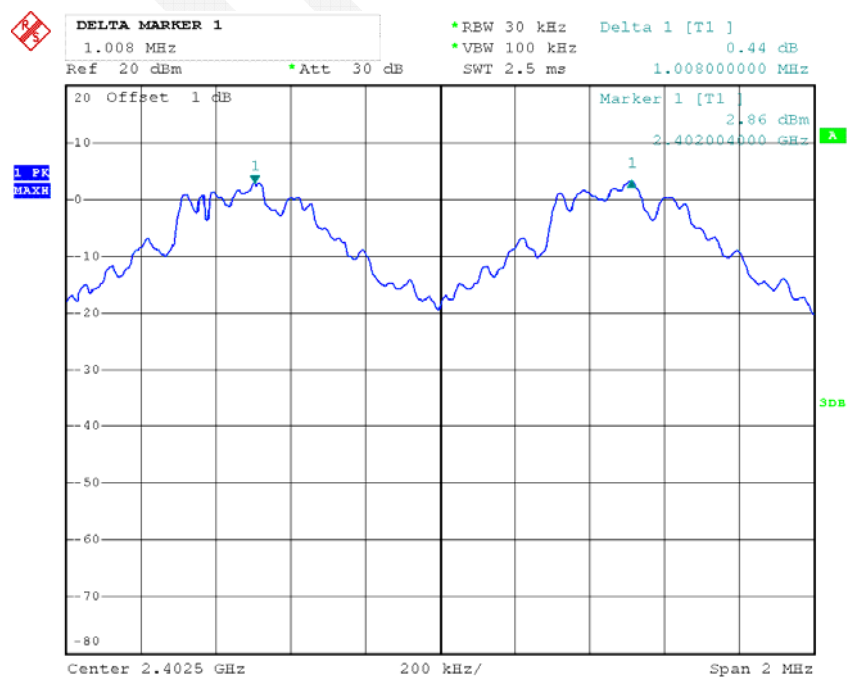
Test Mode: Transmitting

Mode	Channel	Frequency	Channel Seperation	Limit	Result
		MHz	MHz	MHz	
BDR (GFSK)	Low	2402	1.008	0.663	Pass
	Adjacent	2403			
	Middle	2441	1.008	0.663	
	Adjacent	2442			
	High	2480	1.000	0.663	
	Adjacent	2479			
EDR ($\pi/4$ -DQPSK)	Low	2402	1.012	0.976	Pass
	Adjacent	2403			
	Middle	2441	1.004	0.979	
	Adjacent	2442			
	High	2480	1.008	0.979	
	Adjacent	2479			
EDR (8DPSK)	Low	2402	1.000	0.981	Pass
	Adjacent	2403			
	Middle	2441	1.004	0.987	
	Adjacent	2442			
	High	2480	1.012	0.984	
	Adjacent	2479			

Note: Limit= (2/3)*20dB bandwidth

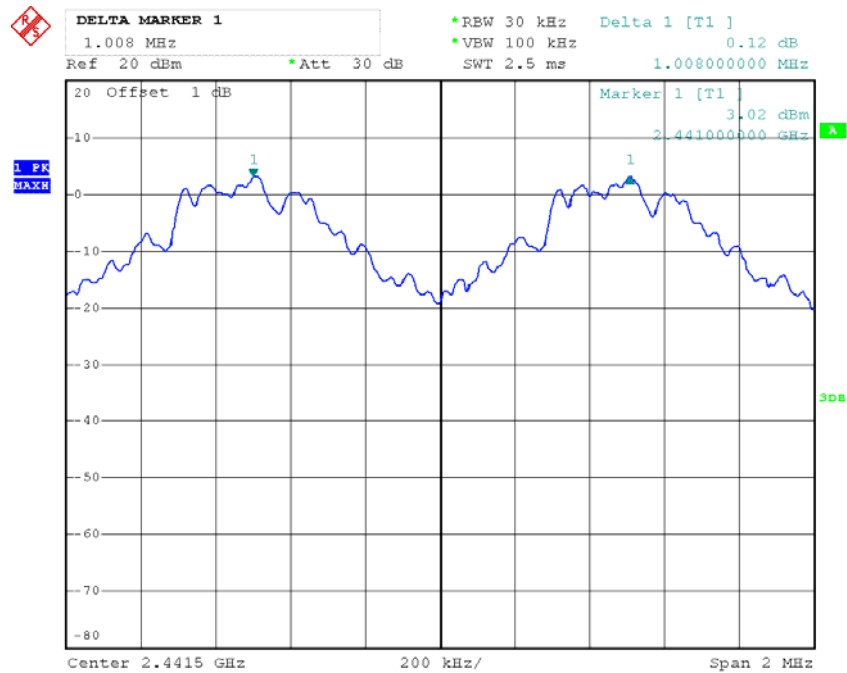
BDR Mode (GFSK):

Low Channel



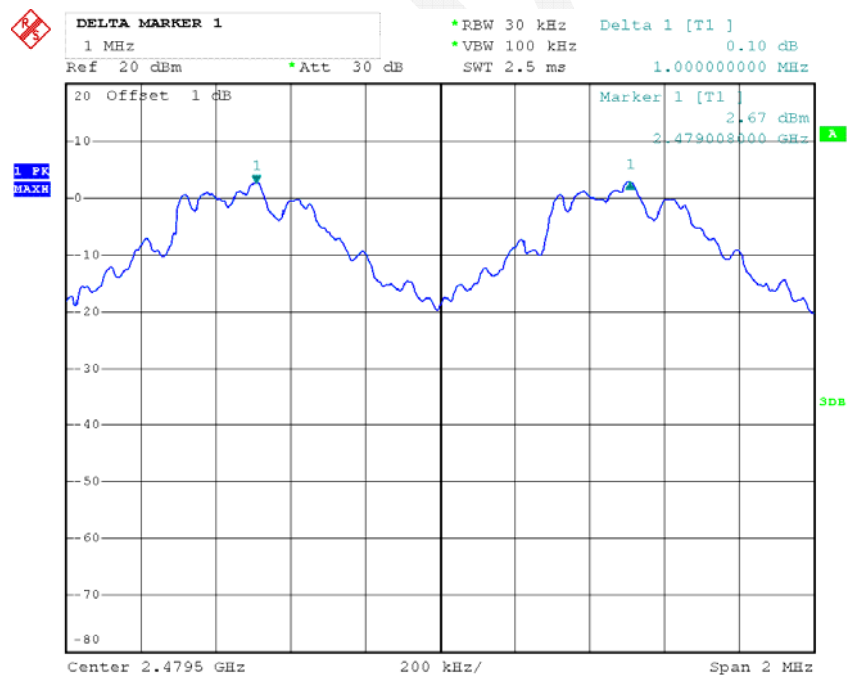
Date: 26.JUN.2015 16:12:38

Middle Channel



Date: 26.JUN.2015 16:13:31

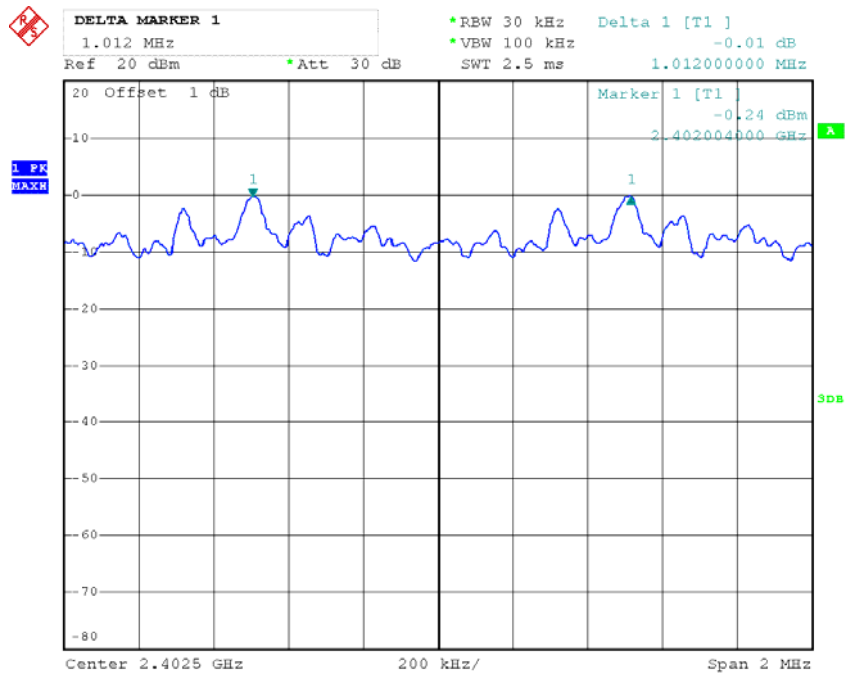
High Channel



Date: 26.JUN.2015 16:14:31

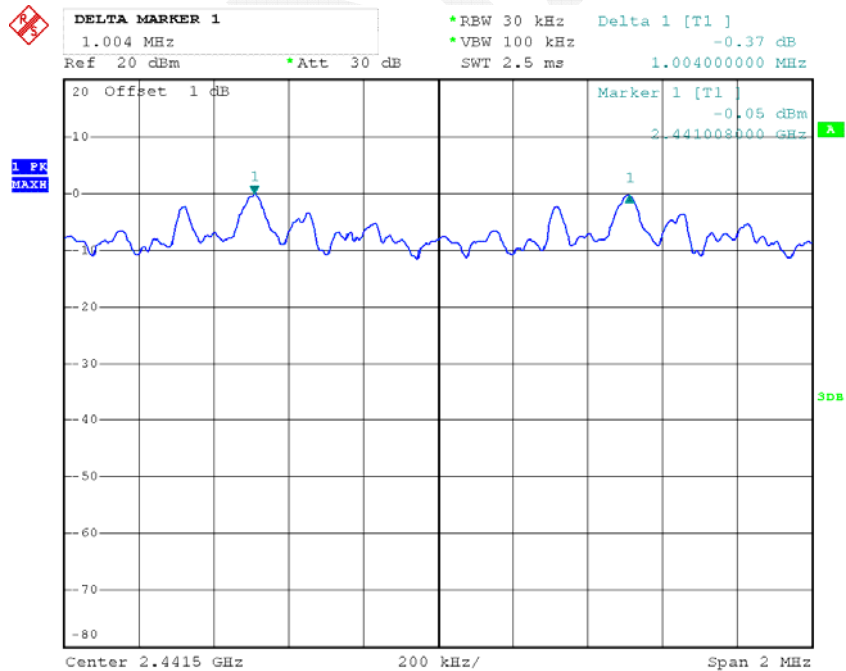
EDR Mode ($\pi/4$ -DQPSK):

Low Channel



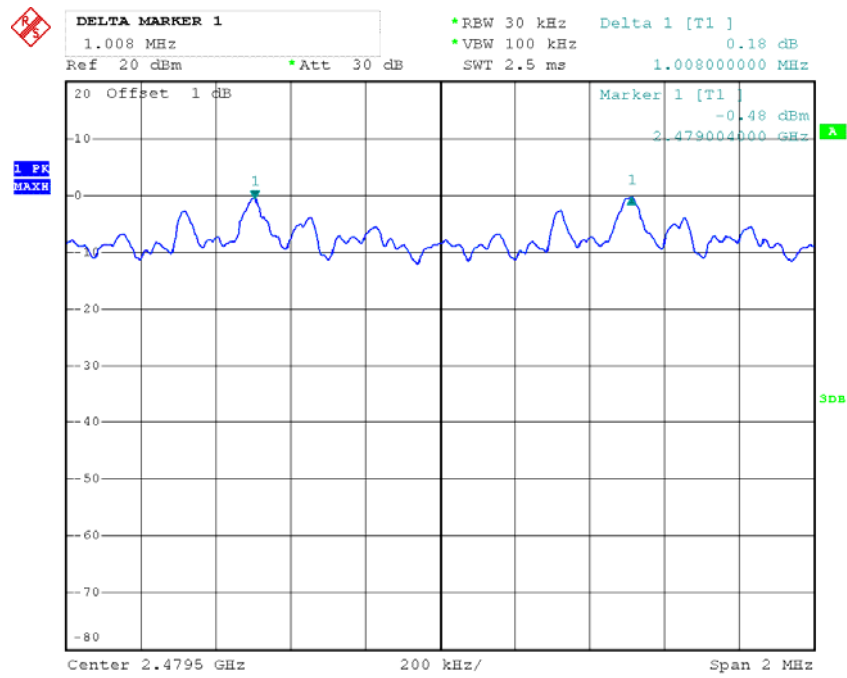
Date: 26.JUN.2015 16:17:04

Middle Channel



Date: 26.JUN.2015 16:16:04

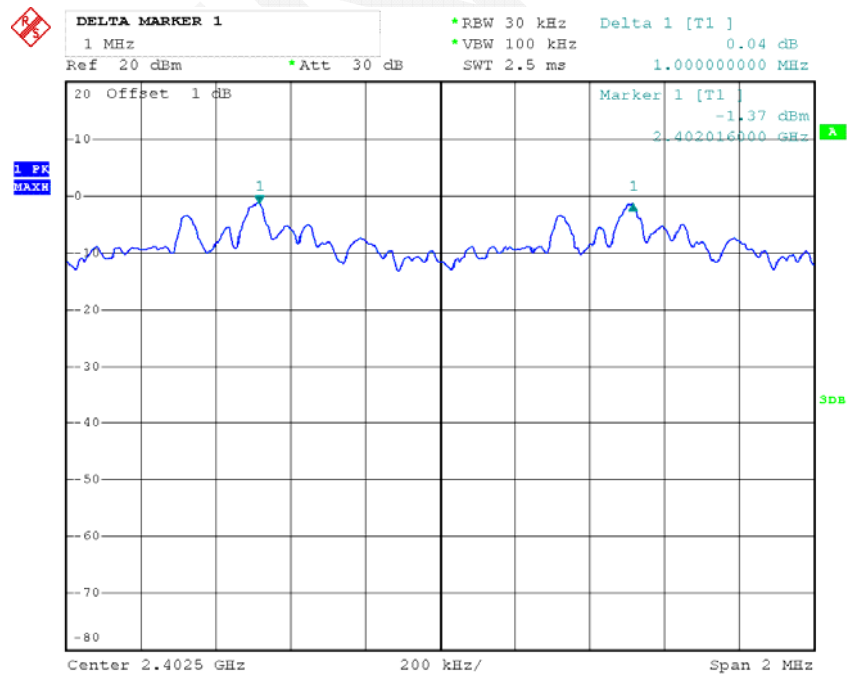
High Channel



Date: 26.JUN.2015 16:15:20

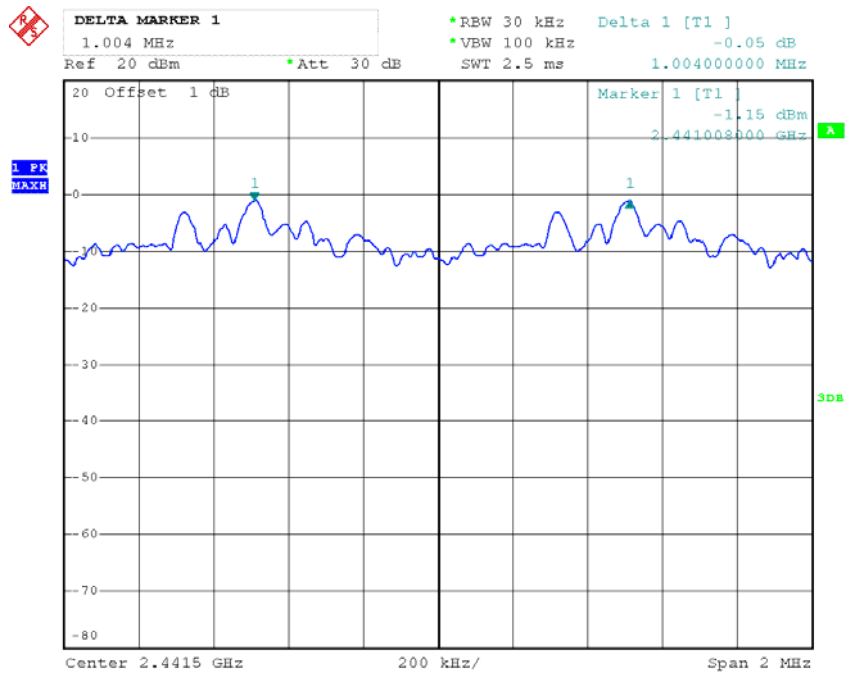
EDR Mode (8-DPSK):

Low Channel



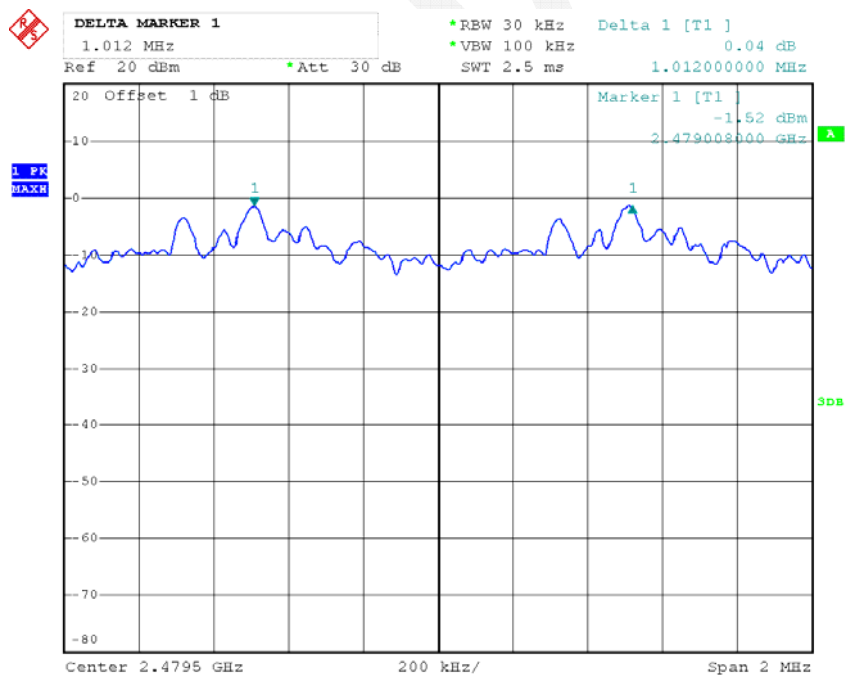
Date: 26.JUN.2015 16:18:04

Middle Channel



Date: 26.JUN.2015 16:19:08

High Channel



Date: 26.JUN.2015 16:19:59

FCC §15.247(a) (1) – 20 dB BANDWIDTH TESTING

Applicable Standard

Alternatively, frequency hopping systems operating in the 2400–2483.5 MHz band may have hopping channel carrier frequencies that are separated by 25 kHz or two-thirds of the 20 dB bandwidth of the hopping channel, whichever is greater, provided the systems operate with an output power no greater than 125 mW.

Test Procedure

1. Check the calibration of the measuring instrument using either an internal calibrator or a known signal from an external generator.
2. Position the EUT on the test table without connection to measurement instrument. Turn on the EUT. Then set it to any one convenient frequency within its operating range. Set a reference level on the measuring instrument equal to the highest peak value.
3. Measure the frequency difference of two frequencies that were attenuated 20 dB from the reference level. Record the frequency difference as the emission bandwidth.
4. Repeat above procedures until all frequencies measured were complete.

Test Equipment List and Details

Manufacturer	Description	Model	Serial Number	Calibration Date	Calibration Due Date
R&S	Spectrum Analyzer	FSP 38	100478	2015-05-09	2016-05-09

* **Statement of Traceability:** Bay Area Compliance Laboratories Corp. (Dongguan) attests that all calibrations have been performed, traceable to National Primary Standards and International System of Units (SI).

Test Data

Environmental Conditions

Temperature:	25.4 °C
Relative Humidity:	53 %
ATM Pressure:	99.7 kPa

* The testing was performed by Dean Liu on 2015-06-23.

Test Result: Compliance.

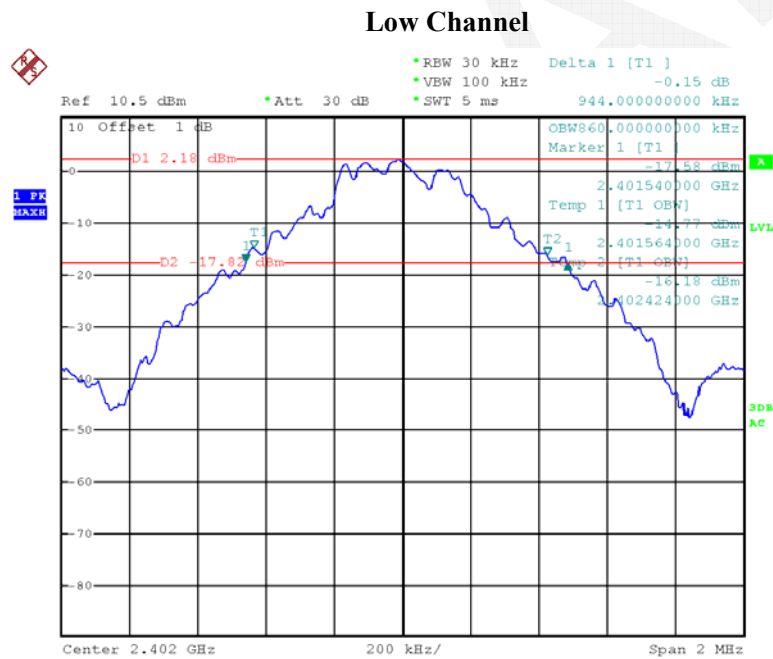
Please refer to following tables and plots

Test Mode: Transmitting

Mode	Channel	Frequency (MHz)	20 dB Bandwidth (MHz)
BDR Mode (GFSK)	Low	2402	0.994
	Middle	2441	0.994
	High	2480	0.994
EDR Mode ($\pi/4$ -DQPSK):	Low	2402	1.464
	Middle	2441	1.468
	High	2480	1.468
EDR Mode (8-DPSK):	Low	2402	1.472
	Middle	2441	1.480
	High	2480	1.476

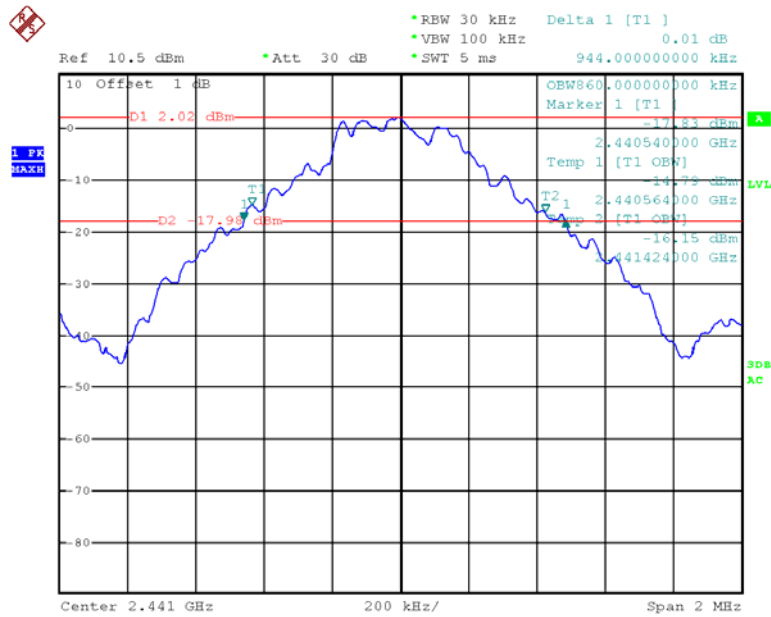
Please refer to the following plots.

BDR Mode (GFSK):



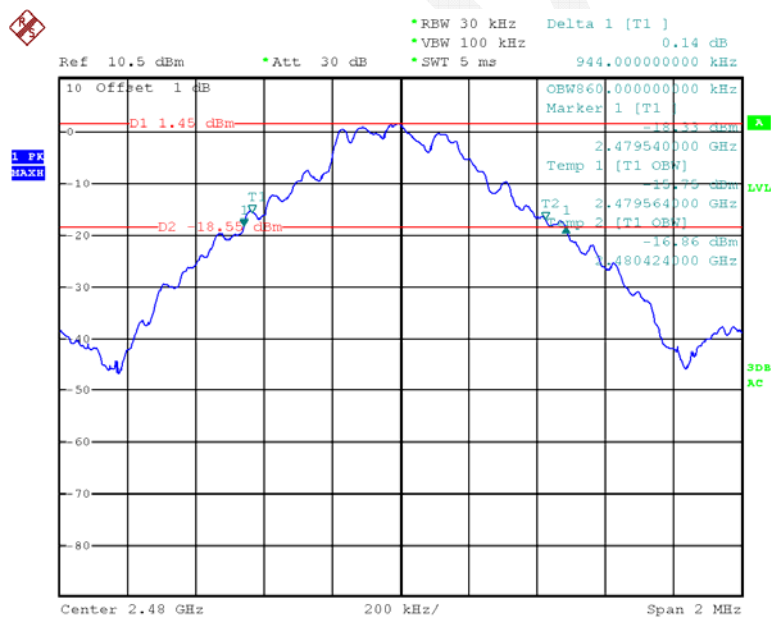
Date: 23.JUN.2015 18:52:03

Middle Channel

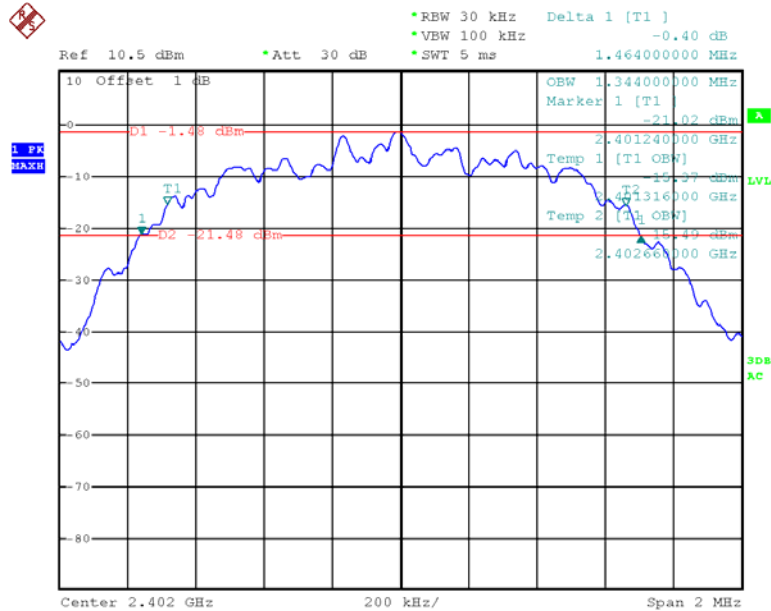


Date: 23.JUN.2015 18:54:55

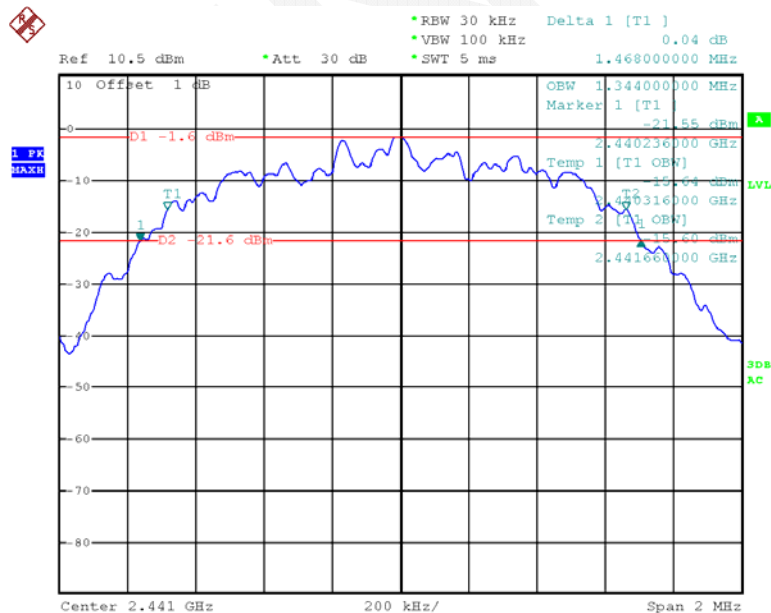
High Channel



Date: 23.JUN.2015 18:57:55

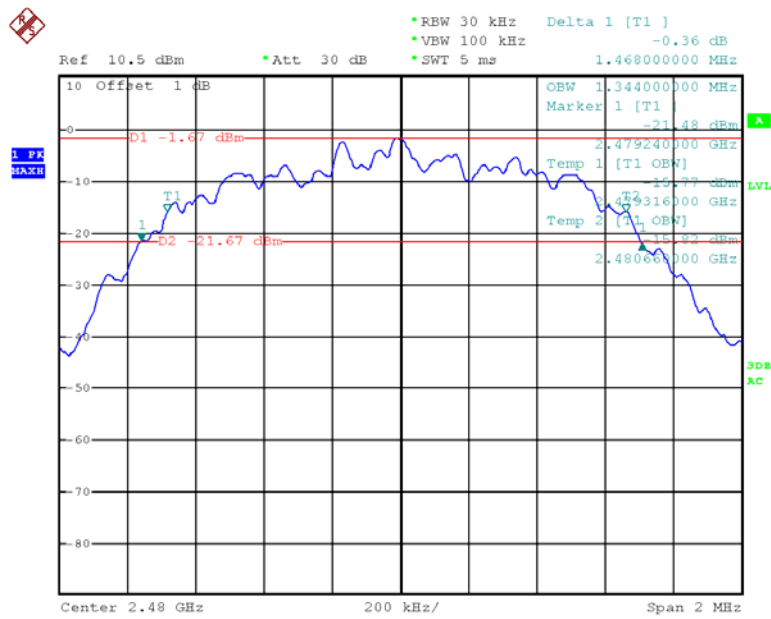
*EDR Mode ($\pi/4$ -DQPSK):***Low Channel**

Date: 23.JUN.2015 19:33:36

Middle Channel

Date: 23.JUN.2015 19:36:11

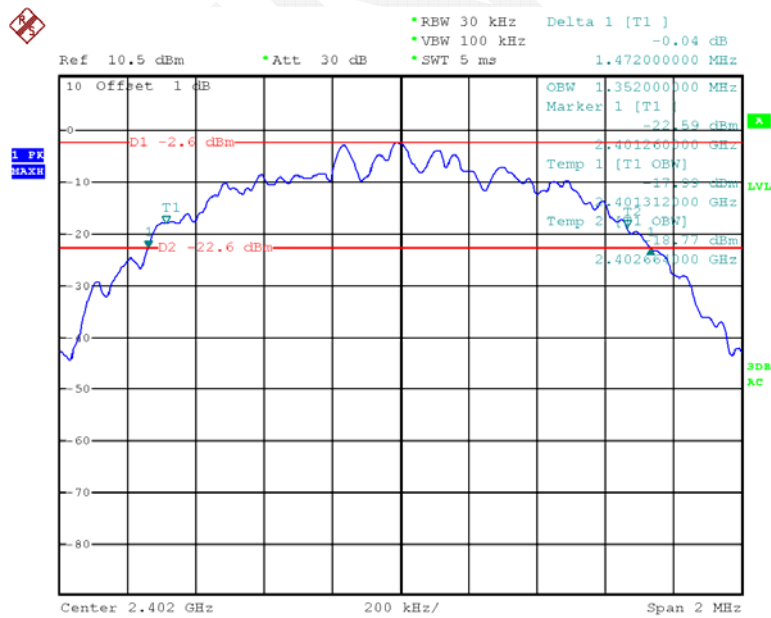
High Channel



Date: 23.JUN.2015 19:38:43

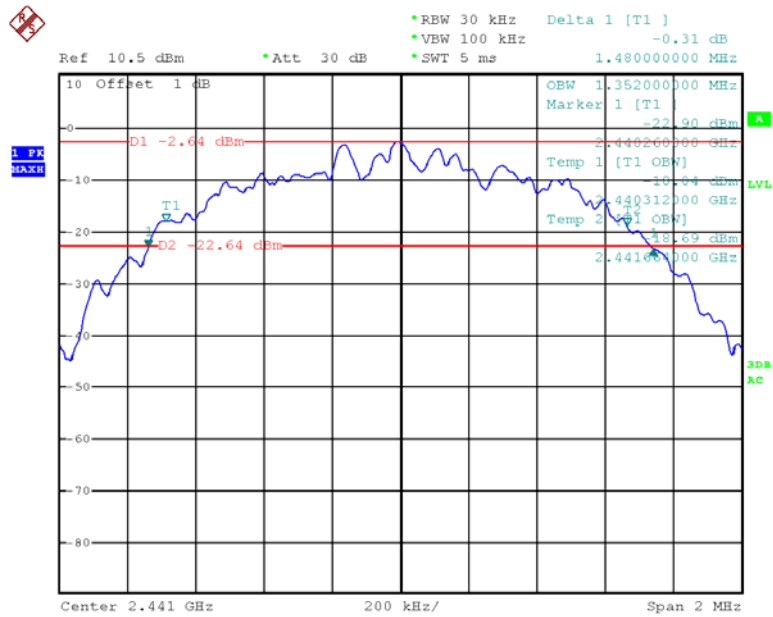
EDR Mode (8-DPSK):

Low Channel



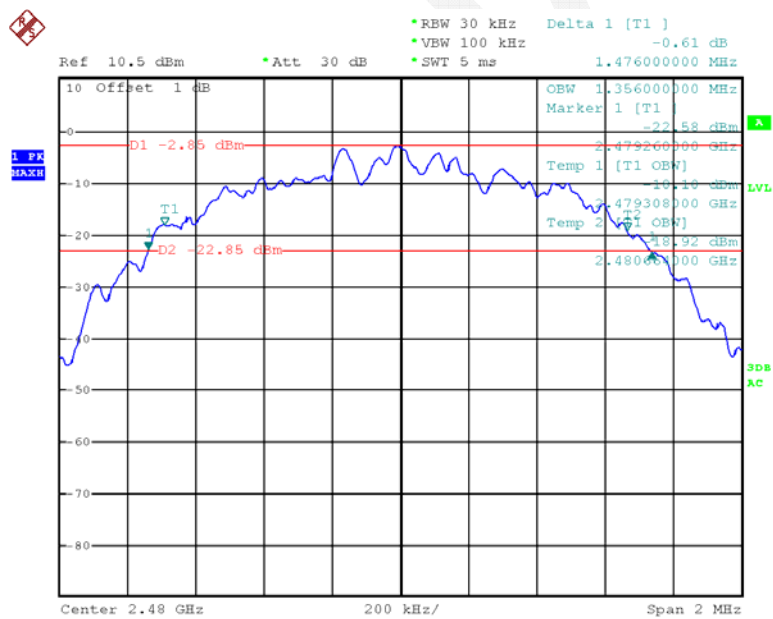
Date: 23.JUN.2015 19:43:40

Middle Channel



Date: 23.JUN.2015 19:46:18

High Channel



Date: 23.JUN.2015 19:48:41

FCC §15.247(a) (1) (iii) - QUANTITY OF HOPPING CHANNEL TEST

Applicable Standard

Frequency hopping systems in the 2400–2483.5 MHz band shall use at least 15 channels. The average time of occupancy on any channel shall not be greater than 0.4 seconds within a period of 0.4 seconds multiplied by the number of hopping channels employed. Frequency hopping systems may avoid or suppress transmissions on a particular hopping frequency provided that a minimum of 15 channels are used.

Test Procedure

1. Check the calibration of the measuring instrument (SA) using either an internal calibrator or a known signal from an external generator.
2. Set the EUT in hopping mode from first channel to last.
3. By using the Max-Hold function record the Quantity of the channel.

Test Equipment List and Details

Manufacturer	Description	Model	Serial Number	Calibration Date	Calibration Due Date
R&S	Spectrum Analyzer	FSP 38	100478	2015-05-09	2016-05-09

* **Statement of Traceability:** Bay Area Compliance Laboratories Corp. (Dongguan) attests that all calibrations have been performed, traceable to National Primary Standards and International System of Units (SI).

Test Data

Environmental Conditions

Temperature:	25.6 °C
Relative Humidity:	57 %
ATM Pressure:	99.9 kPa

* The testing was performed by Dean Liu on 2015-06-24.

Test Result: Compliance.

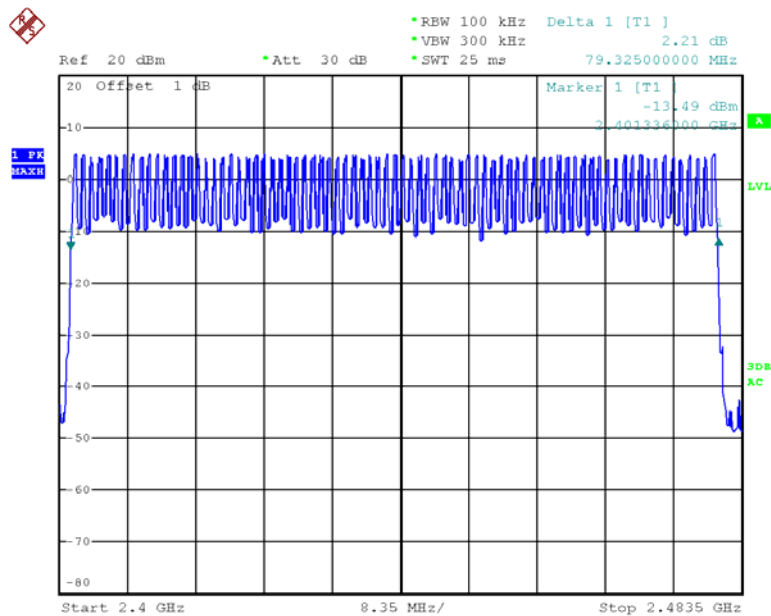
Please refer to following tables and plots

Test Mode: Transmitting

BDR Mode (GFSK):

Frequency Range (MHz)	Number of Hopping Channel	Limit
2400-2483.5	79	≥ 15

Number of Hopping Channels

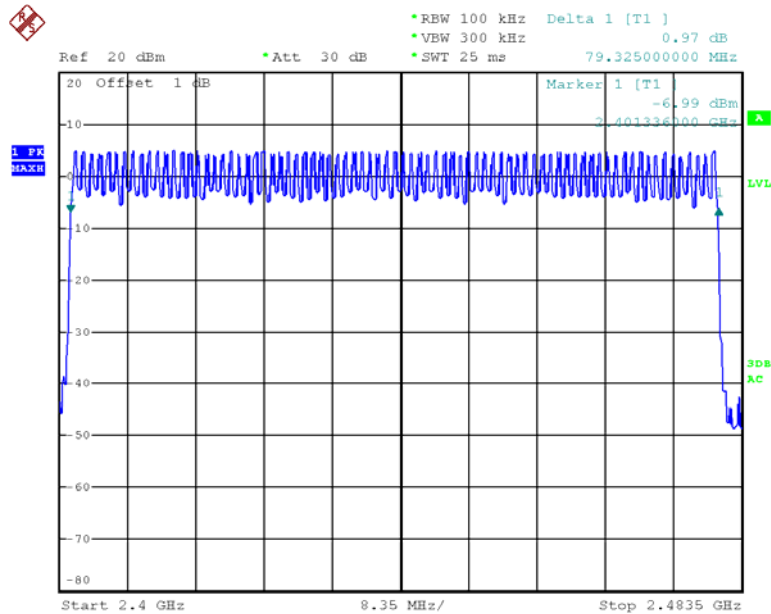


Date: 24.JUN.2015 20:42:53

EDR Mode ($\pi/4$ -DQPSK):

Frequency Range (MHz)	Number of Hopping Channel	Limit
2400-2483.5	79	≥ 15

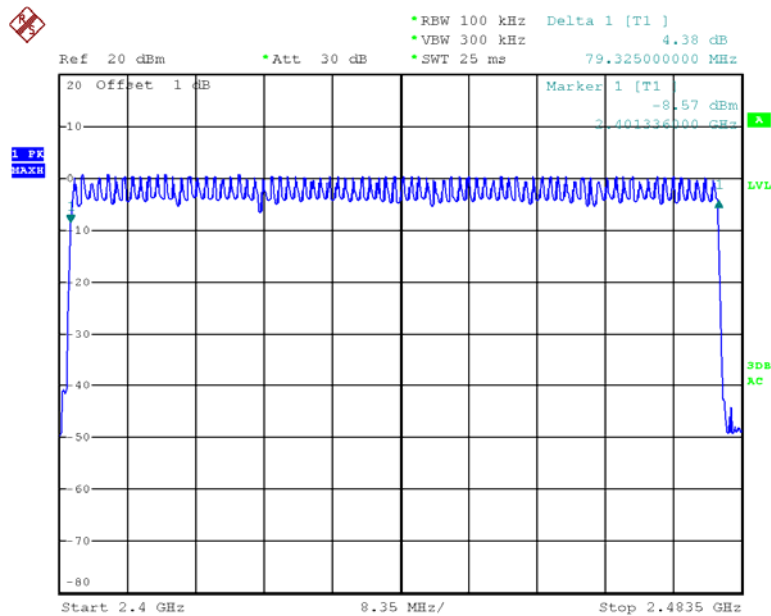
Number of Hopping Channels



Date: 24.JUN.2015 20:44:45

EDR Mode (8-DPSK):

Frequency Range (MHz)	Number of Hopping Channel	Limit
2400-2483.5	79	≥15

Number of Hopping Channels

Date: 24.JUN.2015 20:48:26

FCC §15.247(a) (1) (iii) - TIME OF OCCUPANCY (DWELL TIME)

Applicable Standard

Frequency hopping systems in the 2400-2483.5 MHz shall use at least 15 channels. The average time of occupancy on any channel shall not be greater than 0.4 seconds within a period of 0.4 seconds multiplied by the number of hopping channels employed. Frequency hopping systems may avoid or suppress transmissions on a particular hopping frequency provided that a minimum of 15 channels are used.

Test Procedure

The EUT was worked in channel hopping; Spectrum SPAN was set as 0. Sweep was set as 0.4 * channel no. (s), the quantity of pulse was get from single sweep. In addition, the time of single pulses was tested.

Dwell Time= time slot length * hope rate/ number of hopping channels * 31.6s
Hop rate=1600/s

Test Equipment List and Details

Manufacturer	Description	Model	Serial Number	Calibration Date	Calibration Due Date
R&S	Spectrum Analyzer	FSP 38	100478	2015-05-09	2016-05-09

* **Statement of Traceability:** Bay Area Compliance Laboratories Corp. (Dongguan) attests that all calibrations have been performed, traceable to National Primary Standards and International System of Units (SI).

Test Data

Environmental Conditions

Temperature:	25.8°C
Relative Humidity:	56 %
ATM Pressure:	100 kPa

* The testing was performed by Dean Liu on 2015-06-26.

Test Result: Compliance.

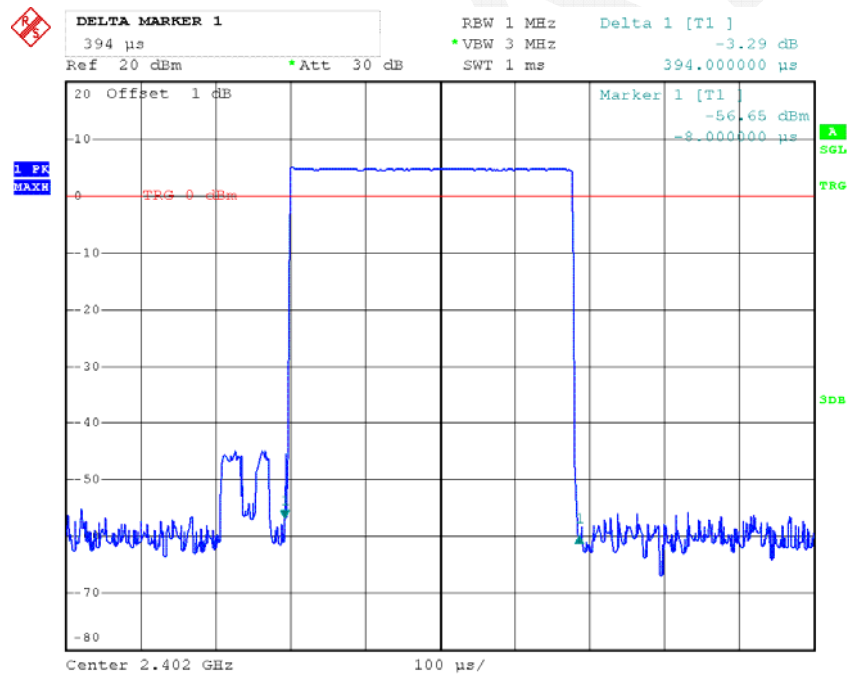
Please refer to following tables and plots

Test Mode: Transmitting

BDR Mode (GFSK):

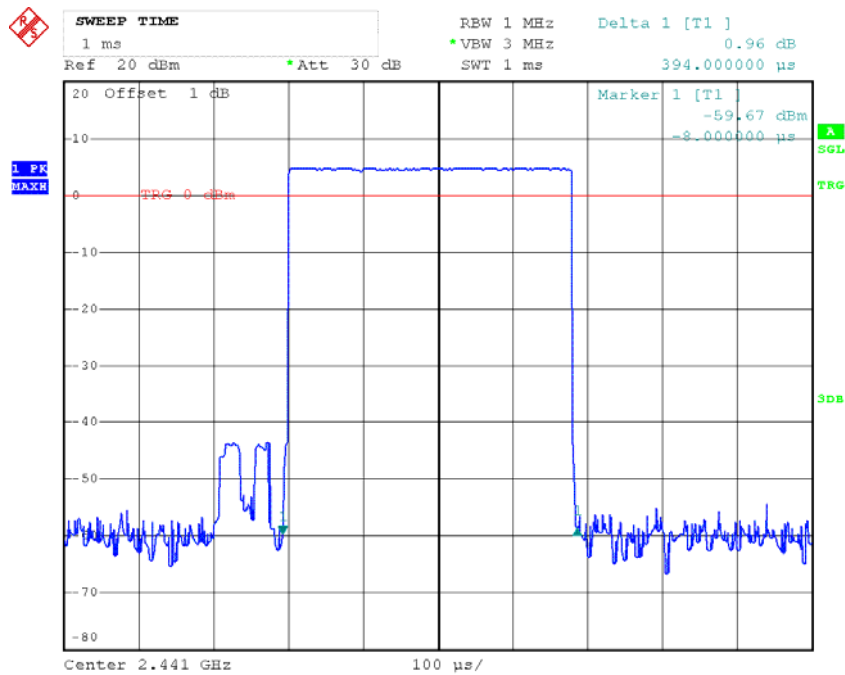
Mode	Channel	Pulse Width (ms)	Dwell Time (s)	Limit (s)	Result
DH1	Low	0.394	0.126	0.4	Pass
	Middle	0.394	0.126	0.4	Pass
	High	0.394	0.126	0.4	Pass
	Note: Dwell time=Pulse time (ms) $\times$ (1600/2/79) $\times$ 31.6 s				
DH3	Low	1.672	0.268	0.4	Pass
	Middle	1.672	0.268	0.4	Pass
	High	1.672	0.268	0.4	Pass
	Note: Dwell time=Pulse time (ms) $\times$ (1600/4/79) $\times$ 31.6 s				
DH5	Low	2.922	0.312	0.4	Pass
	Middle	2.922	0.312	0.4	Pass
	High	2.922	0.312	0.4	Pass
	Note: Dwell time=Pulse time (ms) $\times$ (1600/6/79) $\times$ 31.6 s				

DH1: Low Channel



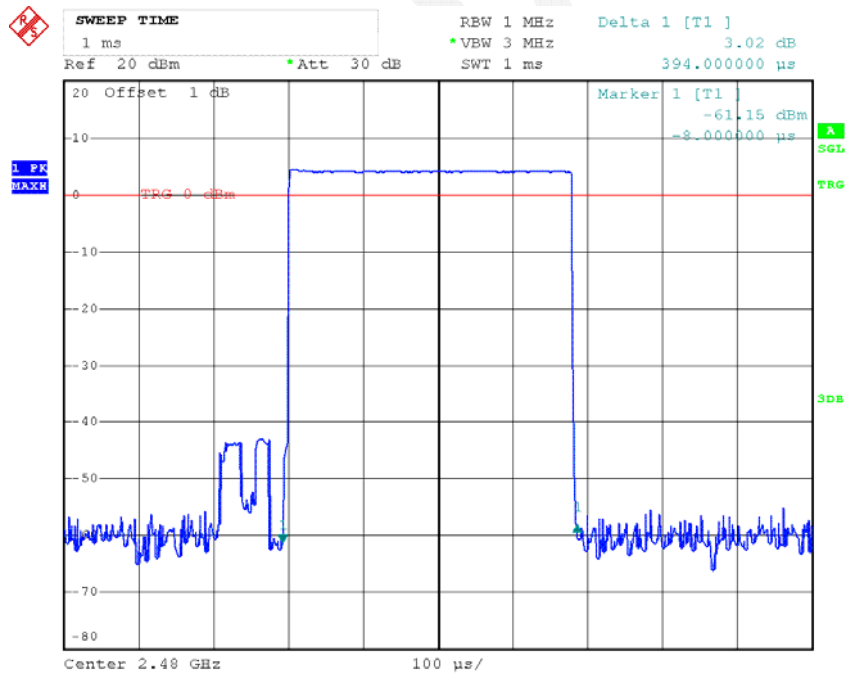
Date: 26.JUN.2015 16:24:05

DH1: Middle Channel



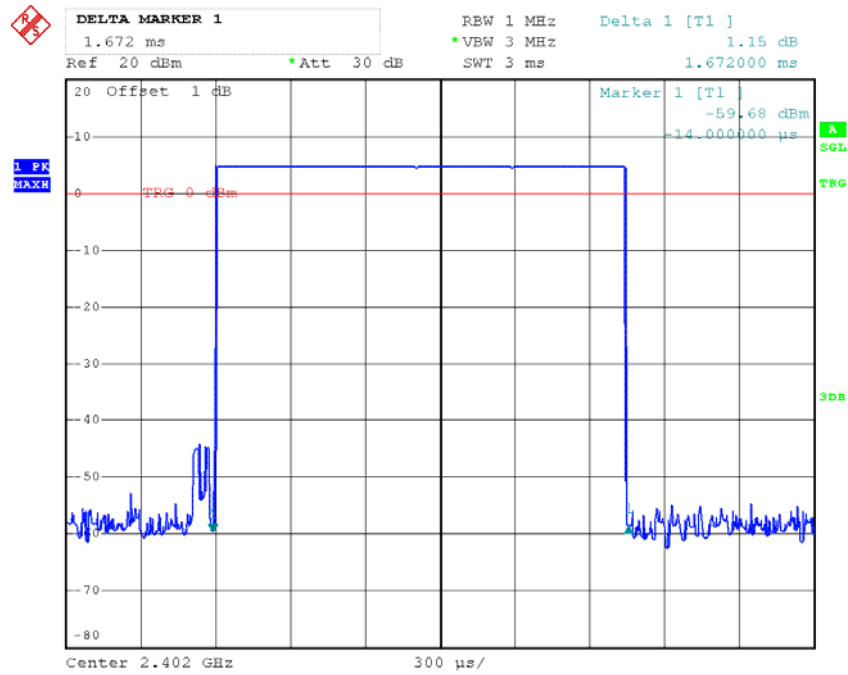
Date: 26.JUN.2015 16:24:17

DH1: High Channel



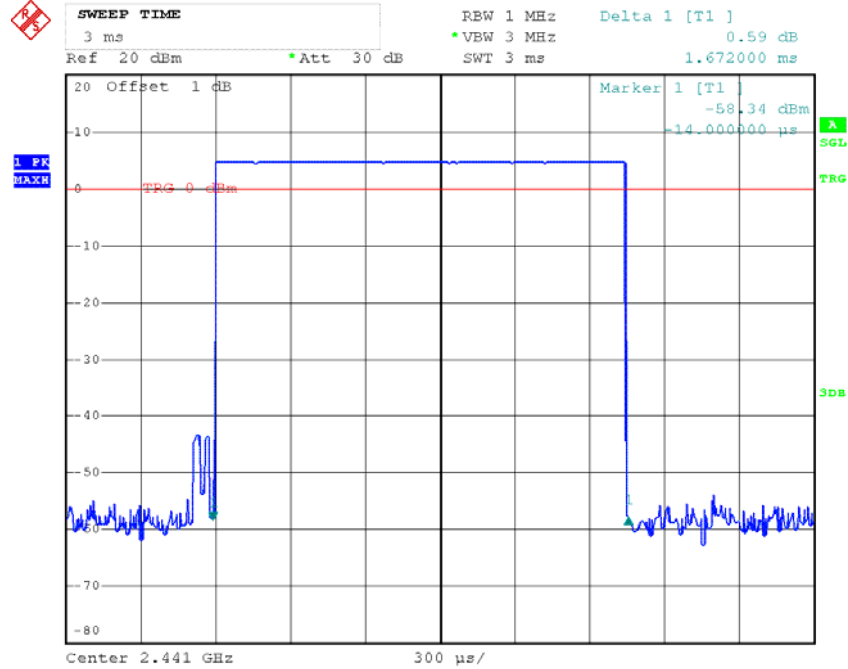
Date: 26.JUN.2015 16:24:28

DH3: Low Channel



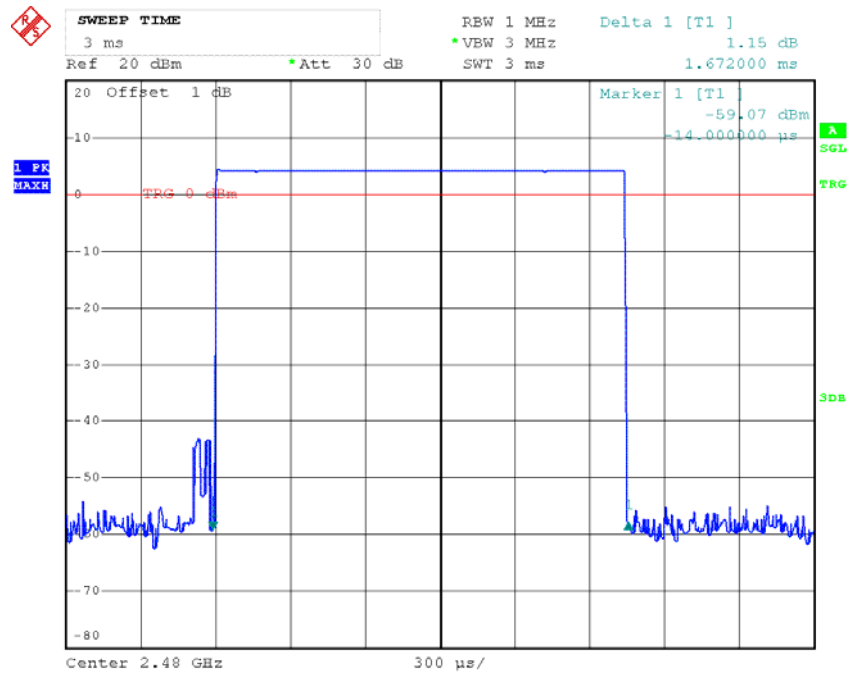
Date: 26.JUN.2015 16:26:03

DH3: Middle Channel



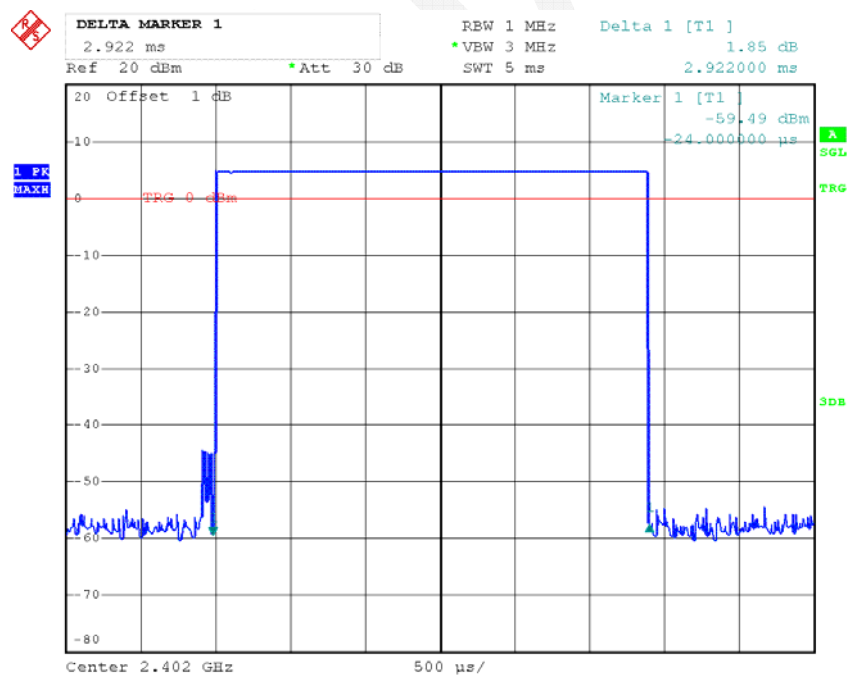
Date: 26.JUN.2015 16:26:13

DH3: High Channel



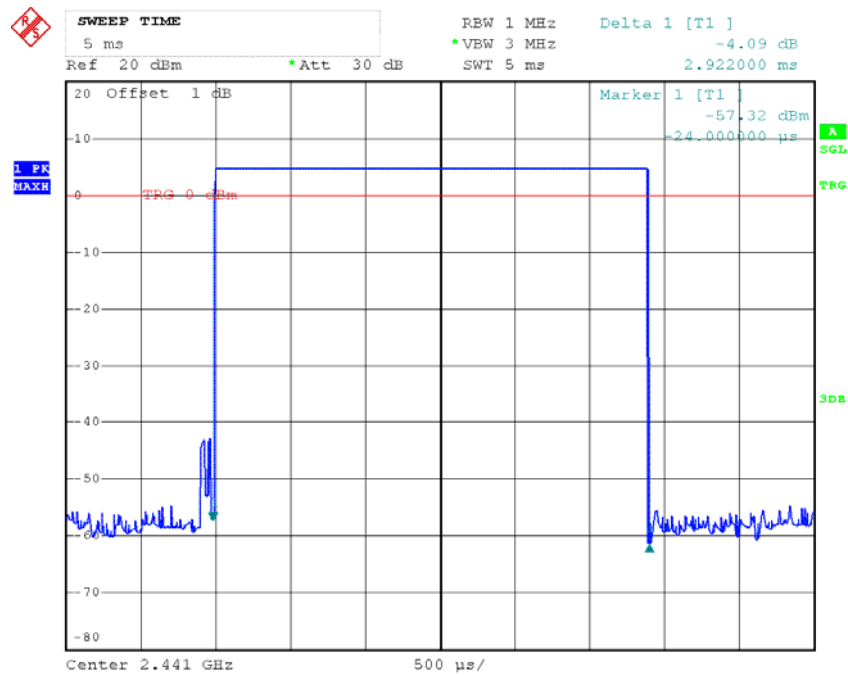
Date: 26.JUN.2015 16:26:26

DH5: Low Channel



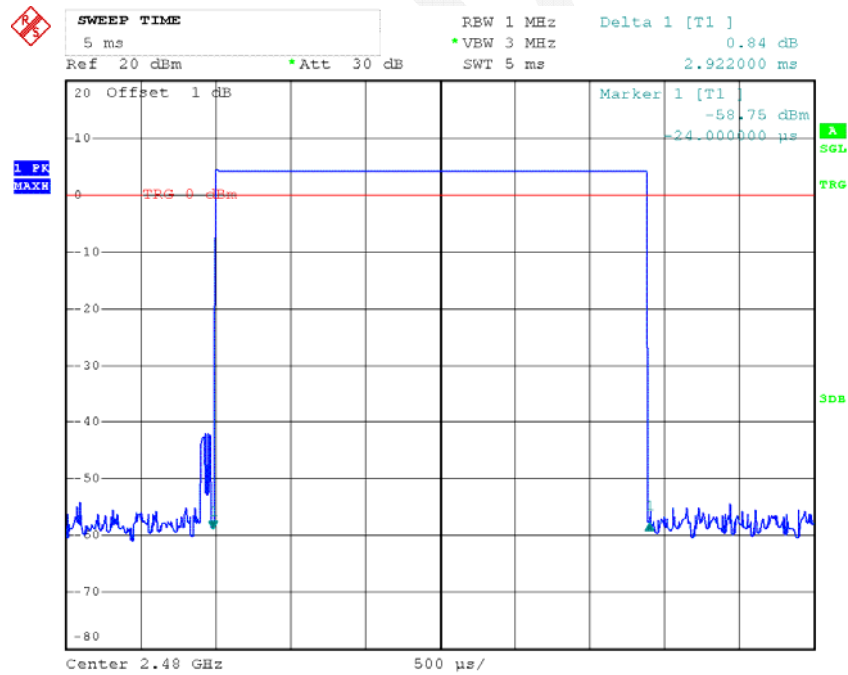
Date: 26.JUN.2015 16:27:30

DH5: Middle Channel



Date: 26.JUN.2015 16:27:43

DH5: High Channel

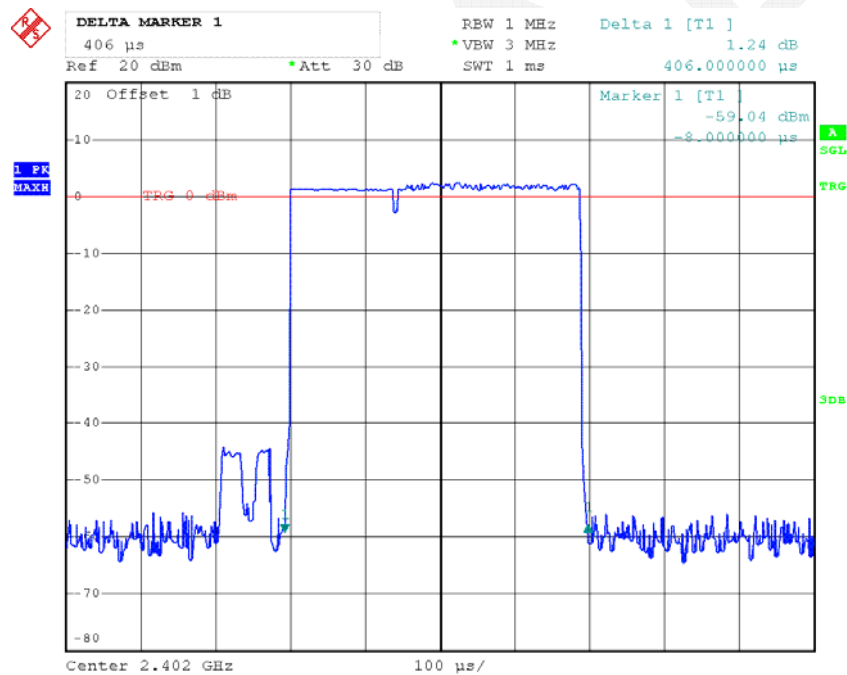


Date: 26.JUN.2015 16:28:01

EDR Mode ($\pi/4$ -DQPSK):

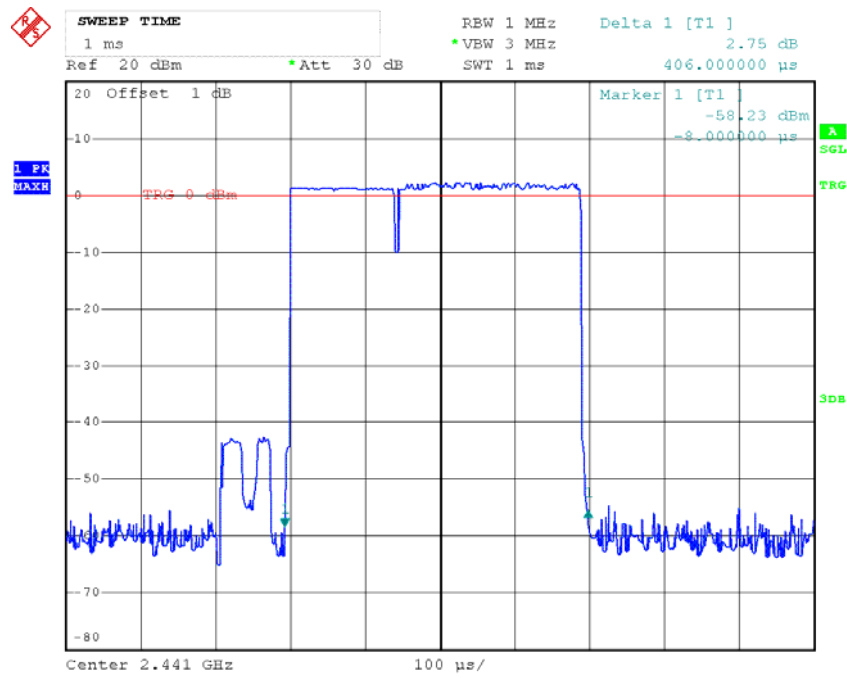
Mode	Channel	Pulse Width (ms)	Dwell Time (s)	Limit (s)	Result
DH1	Low	0.406	0.130	0.4	Pass
	Middle	0.406	0.130	0.4	Pass
	High	0.406	0.130	0.4	Pass
	Note: Dwell time=Pulse time (ms) $\times$ (1600/2/79) $\times$ 31.6 s				
DH3	Low	1.678	0.268	0.4	Pass
	Middle	1.678	0.268	0.4	Pass
	High	1.678	0.268	0.4	Pass
	Note: Dwell time=Pulse time (ms) $\times$ (1600/4/79) $\times$ 31.6 s				
DH5	Low	2.938	0.313	0.4	Pass
	Middle	2.938	0.313	0.4	Pass
	High	2.938	0.313	0.4	Pass
	Note: Dwell time=Pulse time (ms) $\times$ (1600/6/79) $\times$ 31.6 s				

DH1: Low Channel



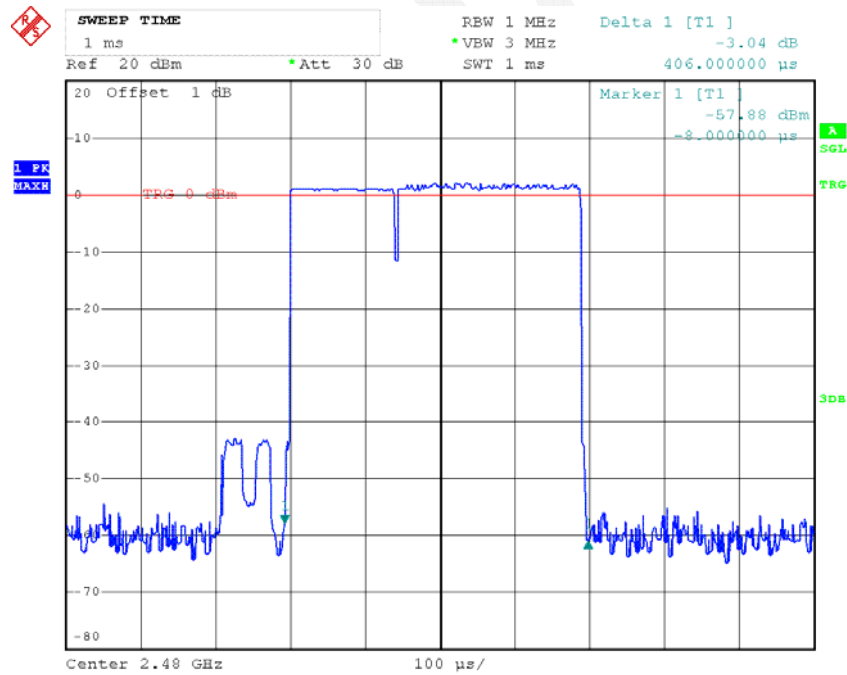
Date: 26.JUN.2015 16:31:07

DH1: Middle Channel



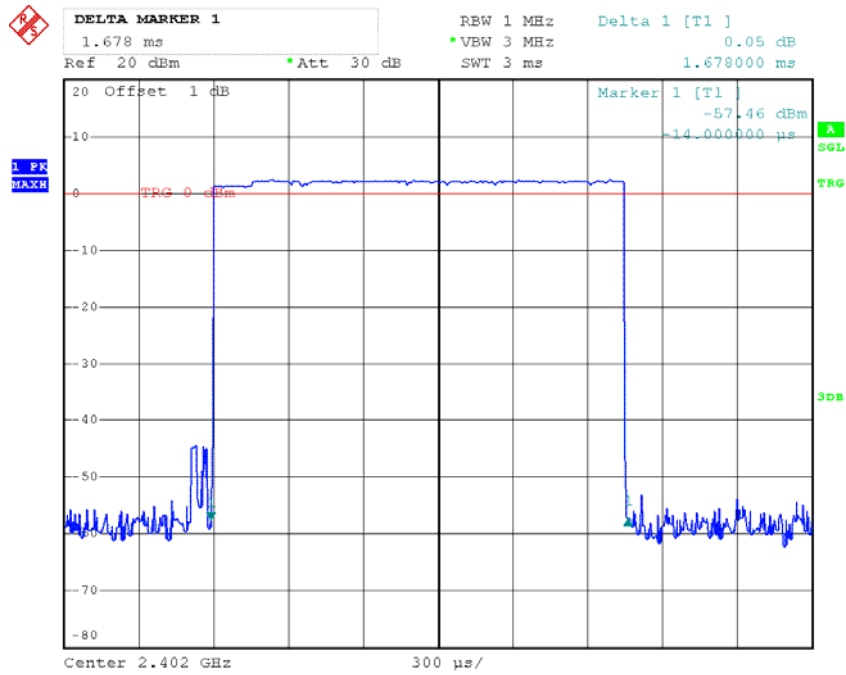
Date: 26.JUN.2015 16:31:23

DH1: High Channel



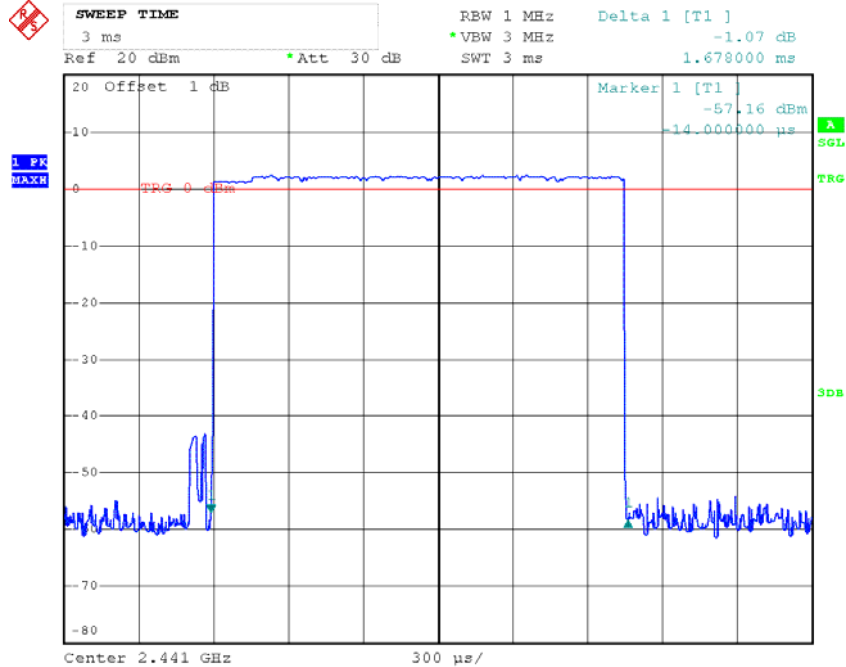
Date: 26.JUN.2015 16:31:36

DH3: Low Channel



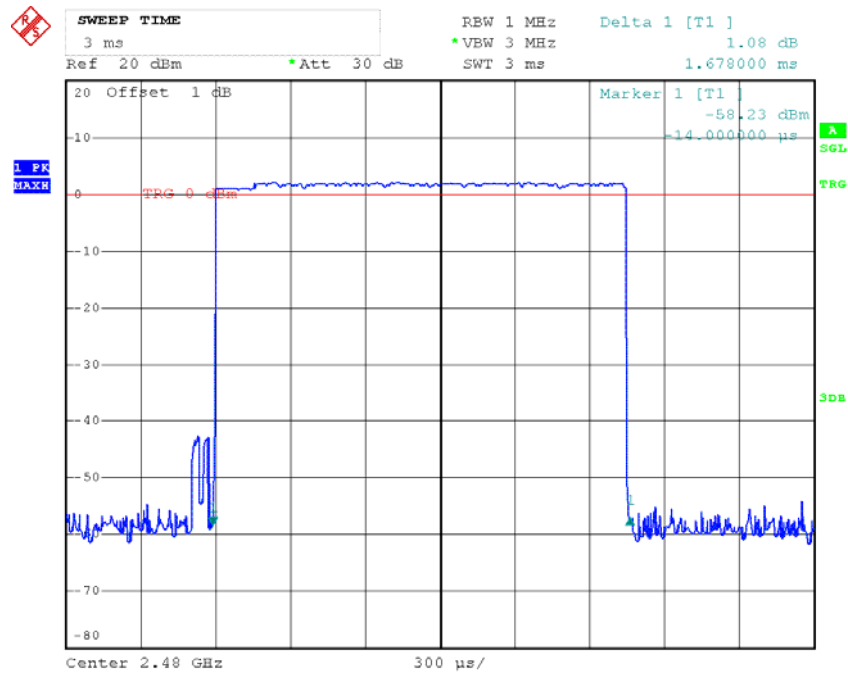
Date: 26.JUN.2015 16:32:34

DH3: Middle Channel



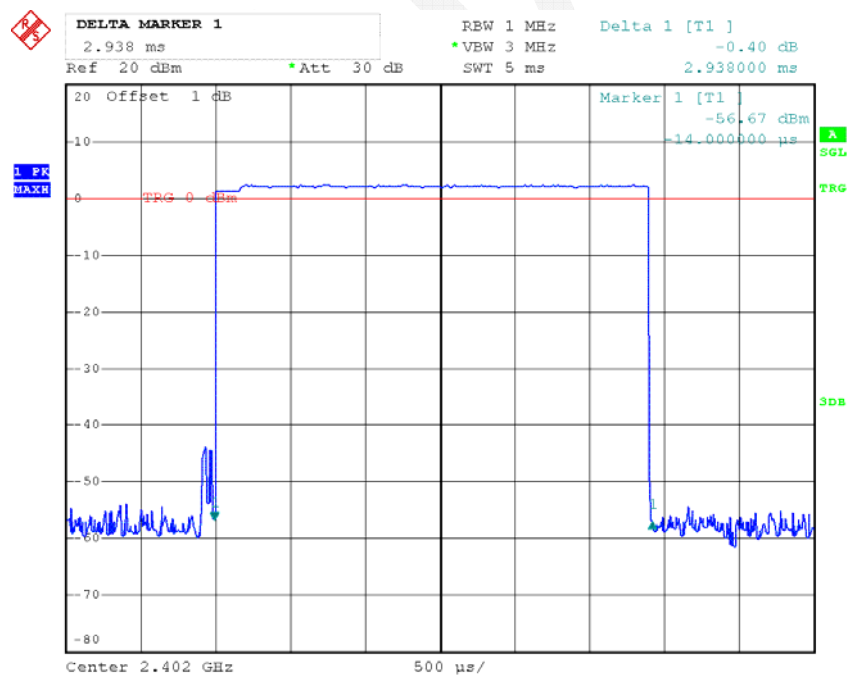
Date: 26.JUN.2015 16:32:42

DH3: High Channel



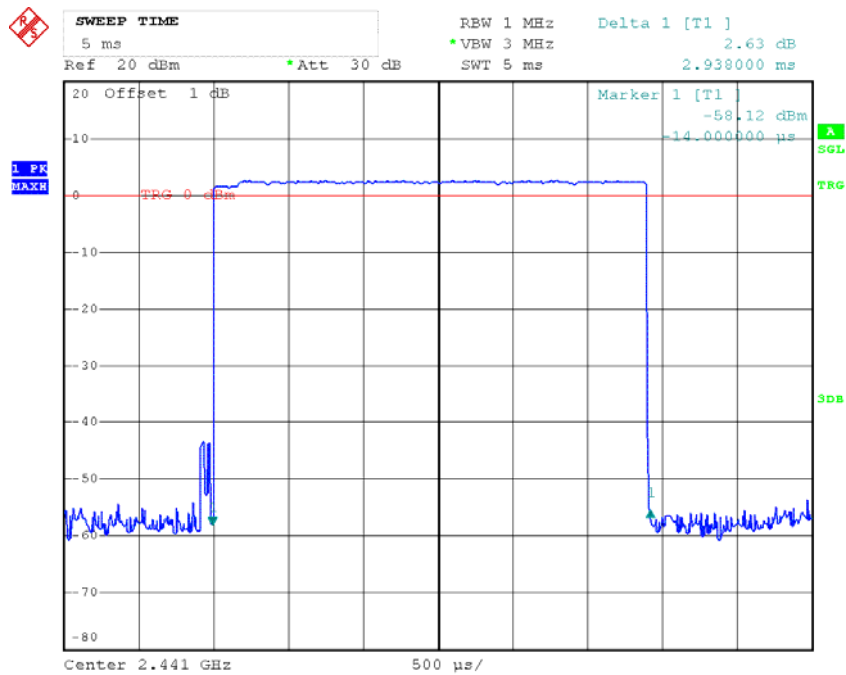
Date: 26.JUN.2015 16:32:51

DH5: Low Channel



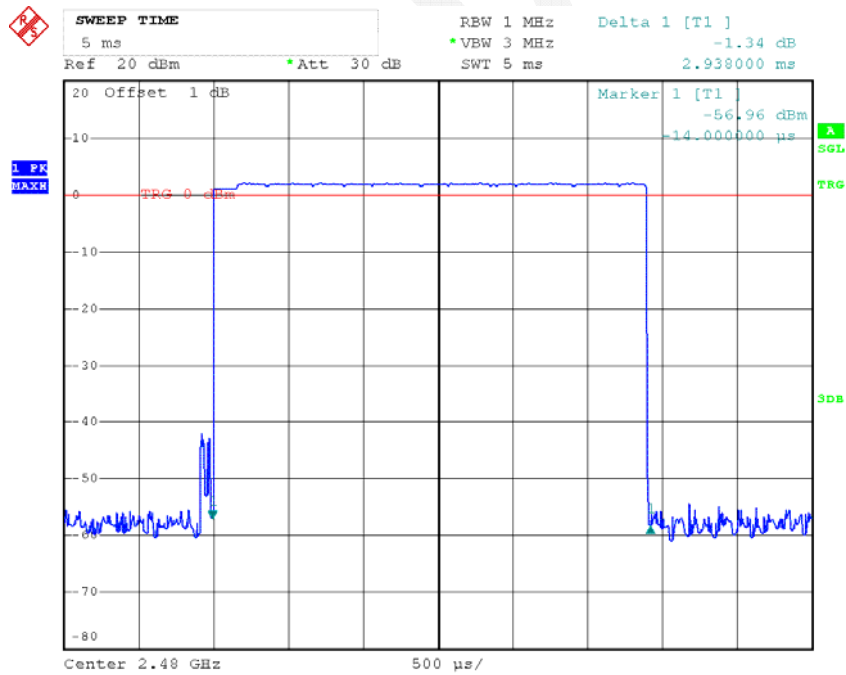
Date: 26.JUN.2015 16:34:01

DH5: Middle Channel



Date: 26.JUN.2015 16:34:15

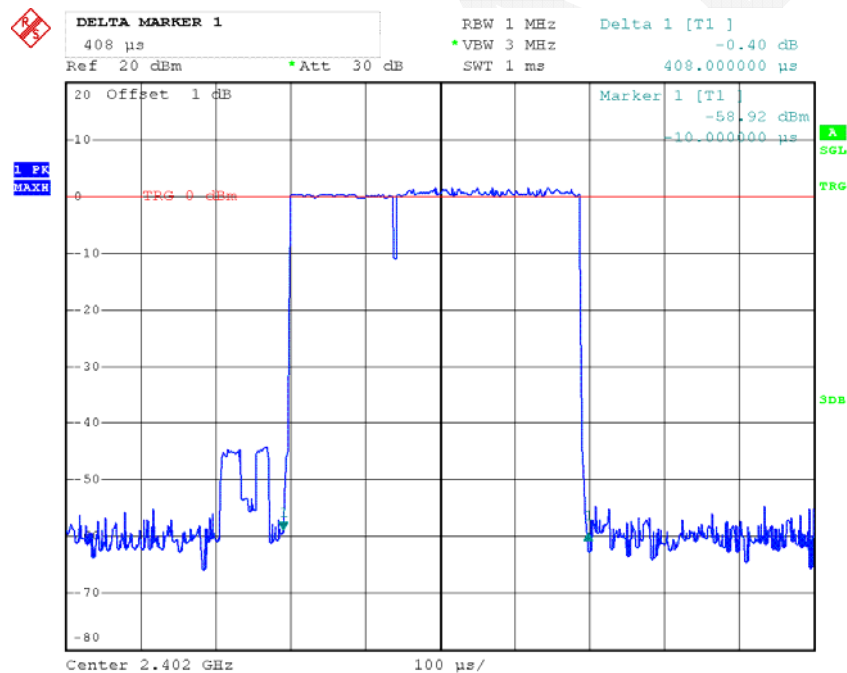
DH5: High Channel



Date: 26.JUN.2015 16:34:24

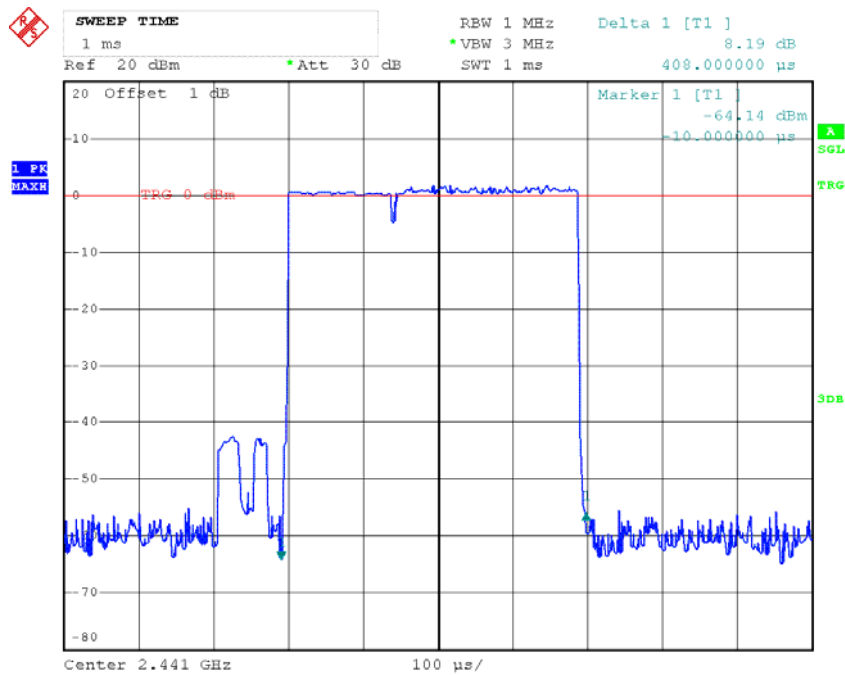
EDR Mode (8-DPSK):

Mode	Channel	Pulse Width (ms)	Dwell Time (s)	Limit (s)	Result
DH1	Low	0.408	0.131	0.4	Pass
	Middle	0.408	0.131	0.4	Pass
	High	0.408	0.131	0.4	Pass
	Note: Dwell time=Pulse time (ms) $\times$ (1600/2/79) $\times$ 31.6 s				
DH3	Low	1.674	0.268	0.4	Pass
	Middle	1.674	0.268	0.4	Pass
	High	1.674	0.268	0.4	Pass
	Note: Dwell time=Pulse time (ms) $\times$ (1600/4/79) $\times$ 31.6 s				
DH5	Low	2.934	0.313	0.4	Pass
	Middle	2.934	0.313	0.4	Pass
	High	2.944	0.314	0.4	Pass
	Note: Dwell time=Pulse time (ms) $\times$ (1600/6/79) $\times$ 31.6 s				

DH1: Low Channel

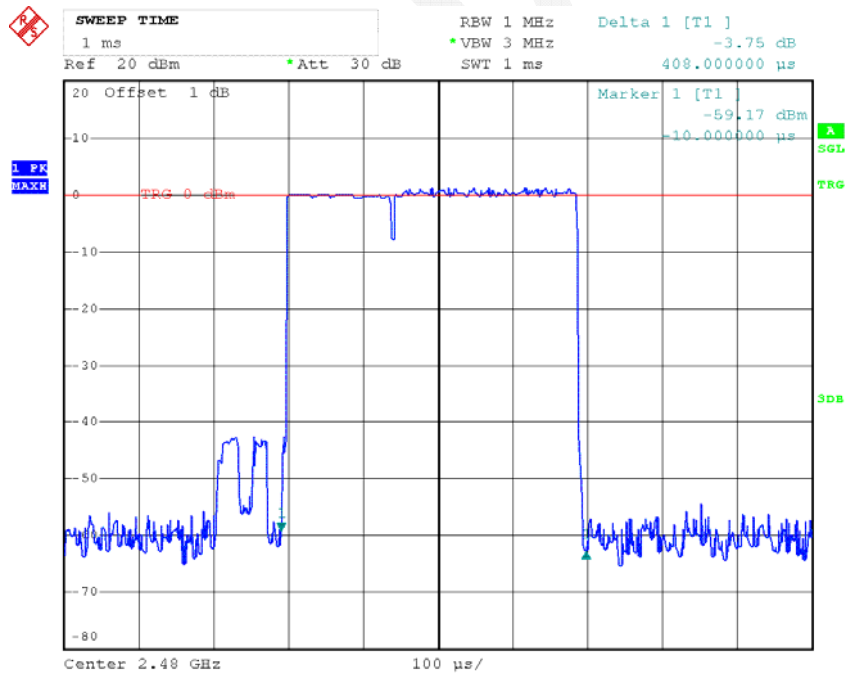
Date: 26.JUN.2015 16:35:53

DH1: Middle Channel



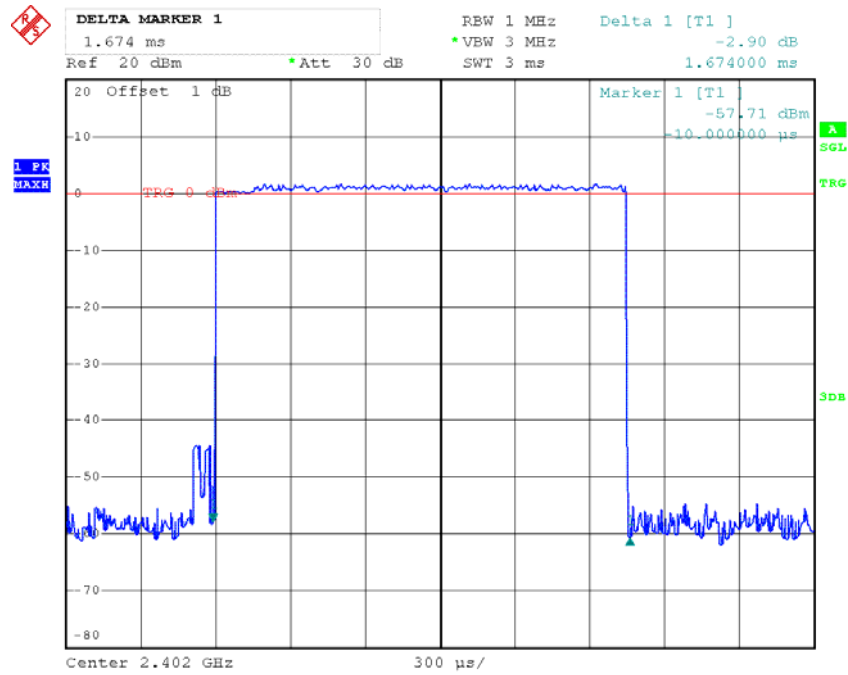
Date: 26.JUN.2015 16:36:16

DH1: High Channel



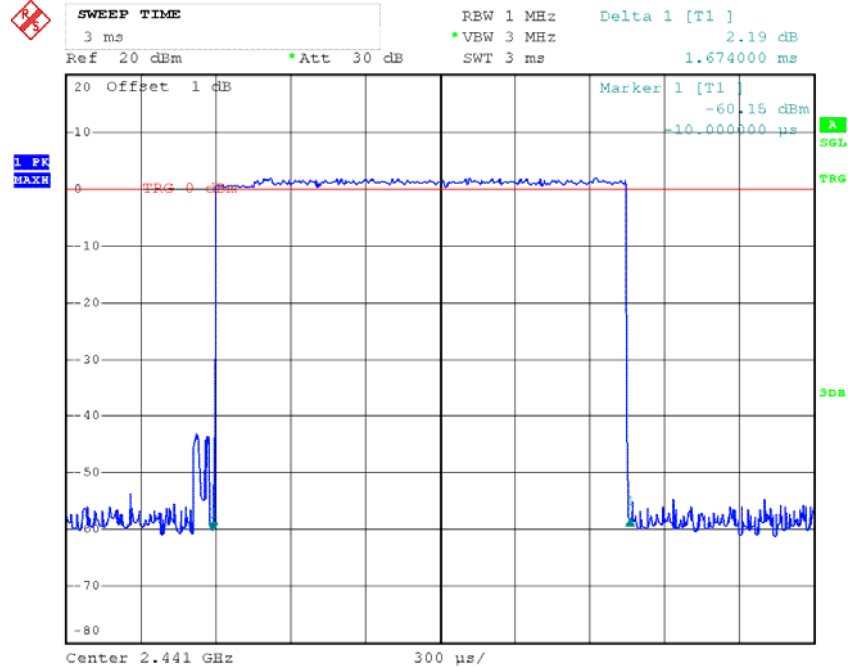
Date: 26.JUN.2015 16:36:40

DH3: Low Channel



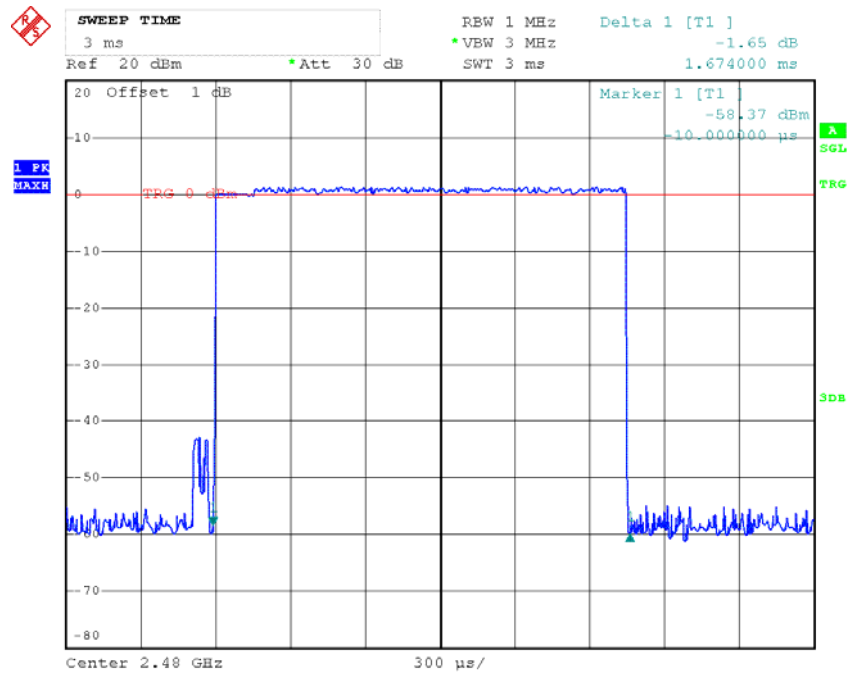
Date: 26.JUN.2015 16:38:03

DH3: Middle Channel



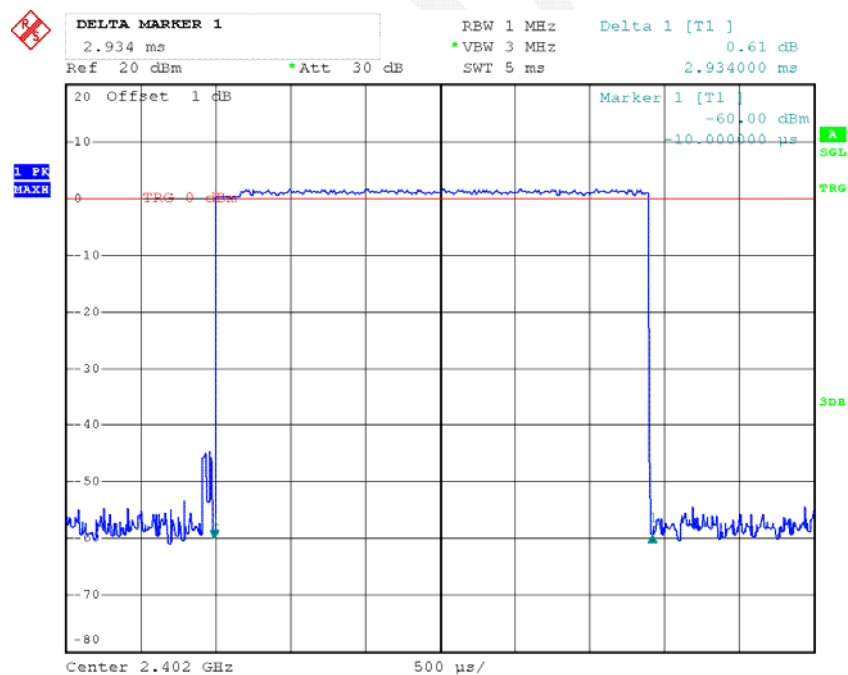
Date: 26.JUN.2015 16:38:14

DH3: High Channel



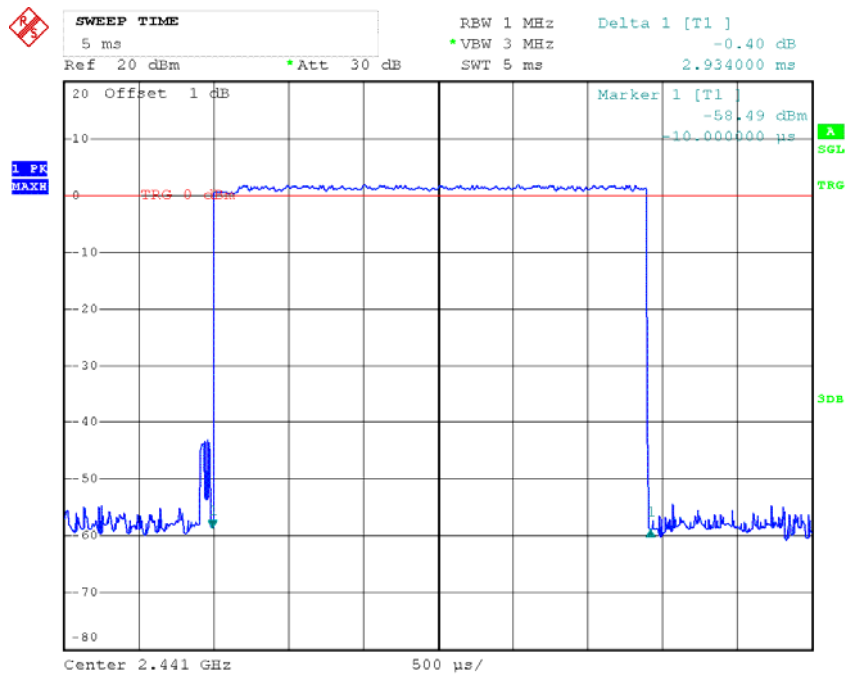
Date: 26.JUN.2015 16:38:25

DH5: Low Channel



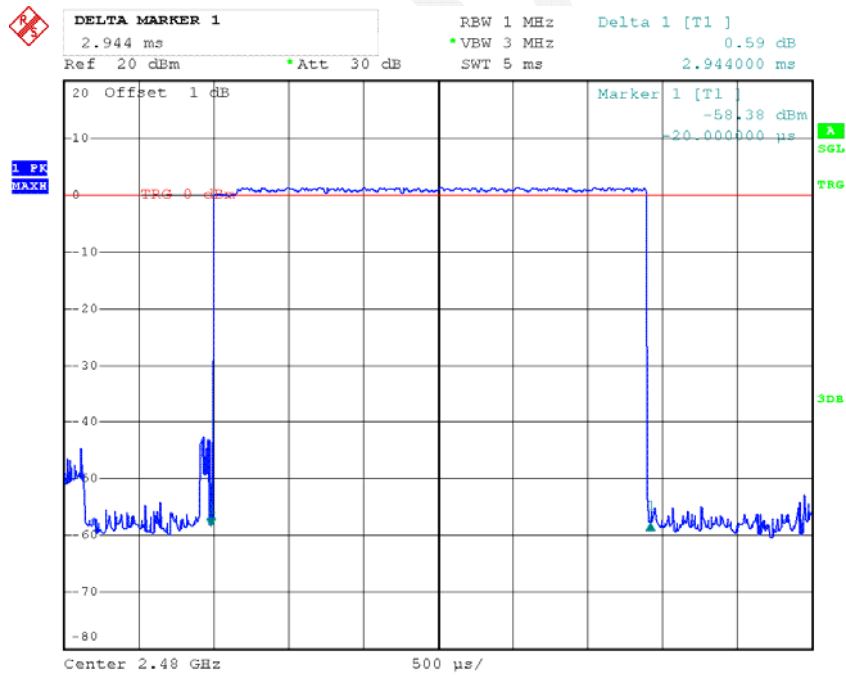
Date: 26.JUN.2015 16:39:09

DH5: Middle Channel



Date: 26.JUN.2015 16:39:20

DH5: High Channel



Date: 26.JUN.2015 16:40:03

FCC §15.247(b) (1) - PEAK OUTPUT POWER MEASUREMENT**Applicable Standard**

According to §15.247(b) (1), for frequency hopping systems operating in the 2400–2483.5 MHz band employing at least 75 non-overlapping hopping channels, and all frequency hopping systems in the 5725–5850 MHz band: 1 watt. For all other frequency hopping systems in the 2400–2483.5 MHz band: 0.125 watts.

Test Procedure

1. Place the EUT on a bench and set in transmitting mode.
2. Remove the antenna from the EUT and then connect a low loss RF cable from the antenna port to an test equipment.
3. Add a correction factor to the display.

Test Equipment List and Details

Manufacturer	Description	Model	Serial Number	Calibration Date	Calibration Due Date
R&S	Spectrum Analyzer	FSP 38	100478	2015-05-09	2016-05-09

* **Statement of Traceability:** Bay Area Compliance Laboratories Corp. (Dongguan) attests that all calibrations have been performed, traceable to National Primary Standards and International System of Units (SI).

Test Data**Environmental Conditions**

Temperature:	25.6 °C
Relative Humidity:	57 %
ATM Pressure:	99.9 kPa

* The testing was performed by Dean Liu on 2015-06-24.

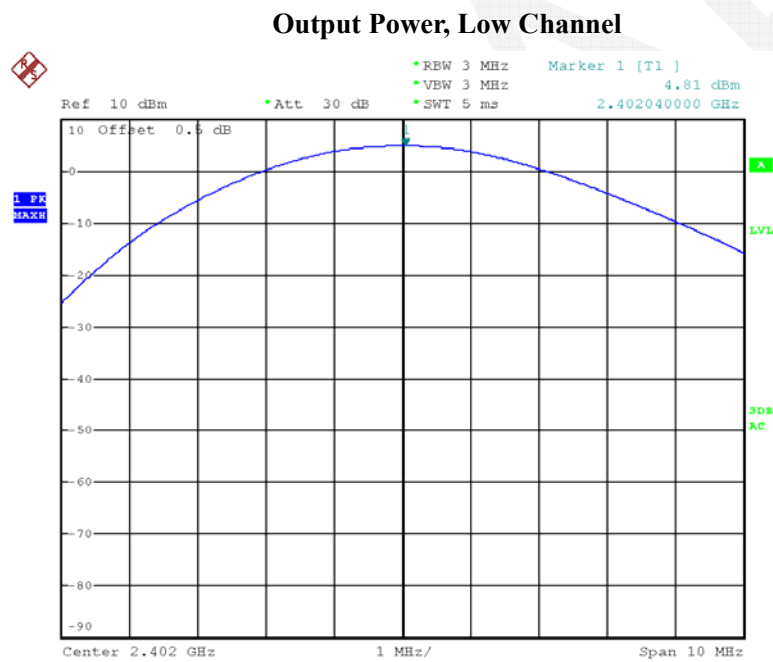
Test Result: Compliance.

Test Mode: Transmitting

Mode	Frequency (MHz)	Output power (dBm)	Limit (dBm)
BDR Mode (GFSK)	2402	4.81	30
	2441	4.71	30
	2480	4.18	30
EDR Mode ($\pi/4$ -DQPSK)	2402	3.64	30
	2441	3.53	30
	2480	2.71	30
EDR Mode (8-DPSK)	2402	2.74	30
	2441	2.62	30
	2480	1.77	30

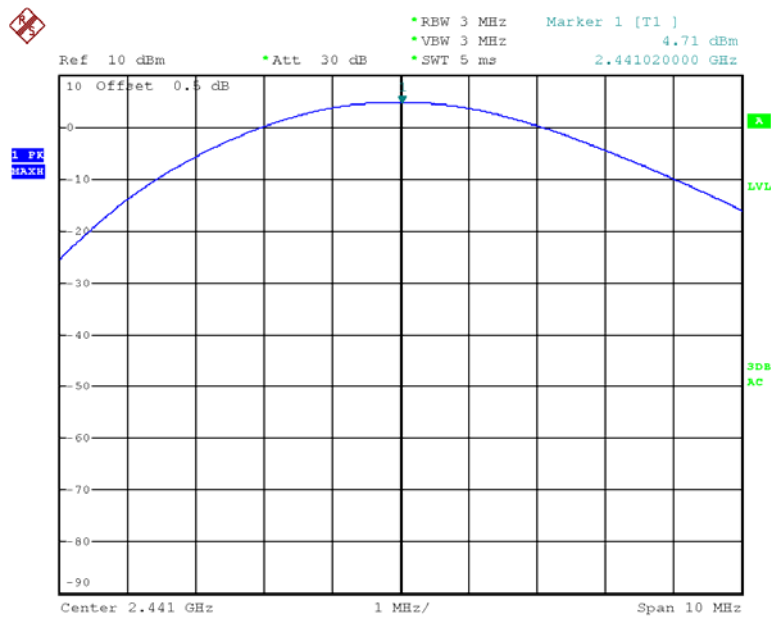
Note: The data above was tested in conducted mode.

BDR Mode (GFSK):



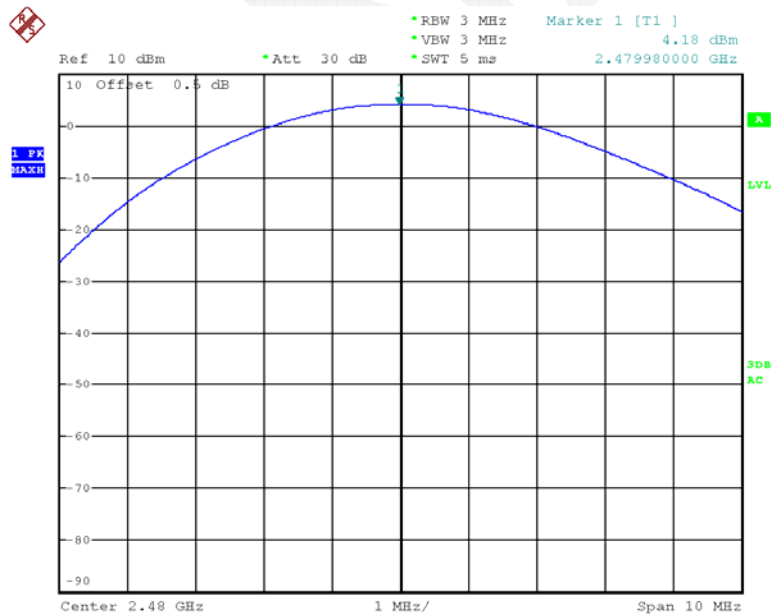
Date: 24.JUN.2015 18:31:48

Output Power, Middle Channel



Date: 24.JUN.2015 18:32:34

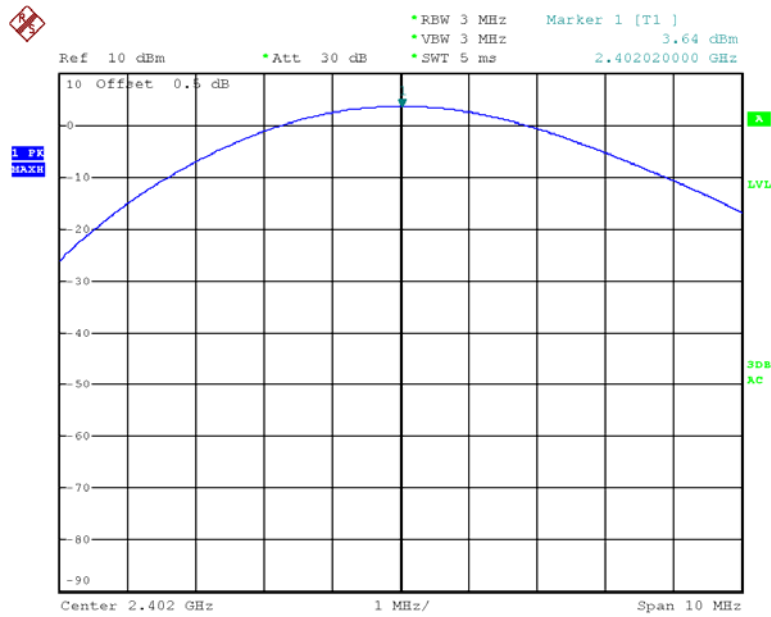
Output Power, High Channel



Date: 24.JUN.2015 18:33:02

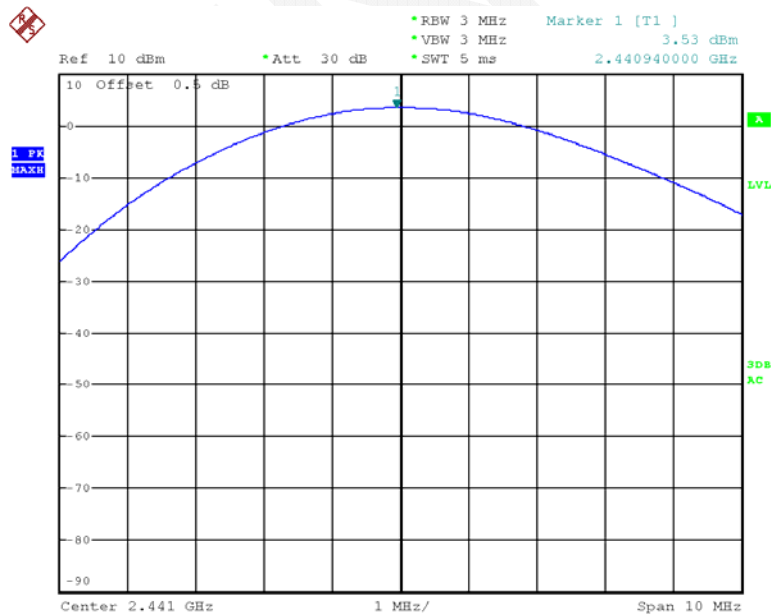
EDR Mode ($\pi/4$ -DQPSK):

Output Power, Low Channel



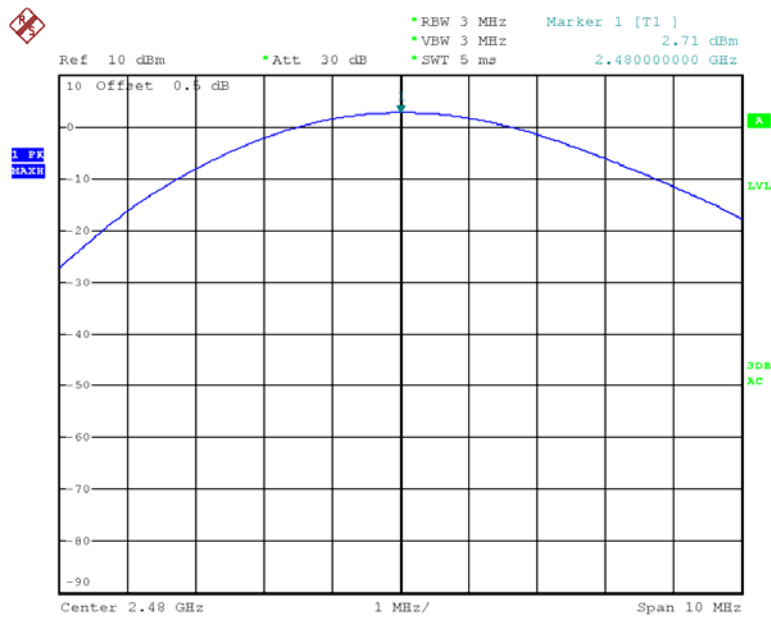
Date: 24.JUN.2015 18:34:09

Output Power, Middle Channel



Date: 24.JUN.2015 18:35:00

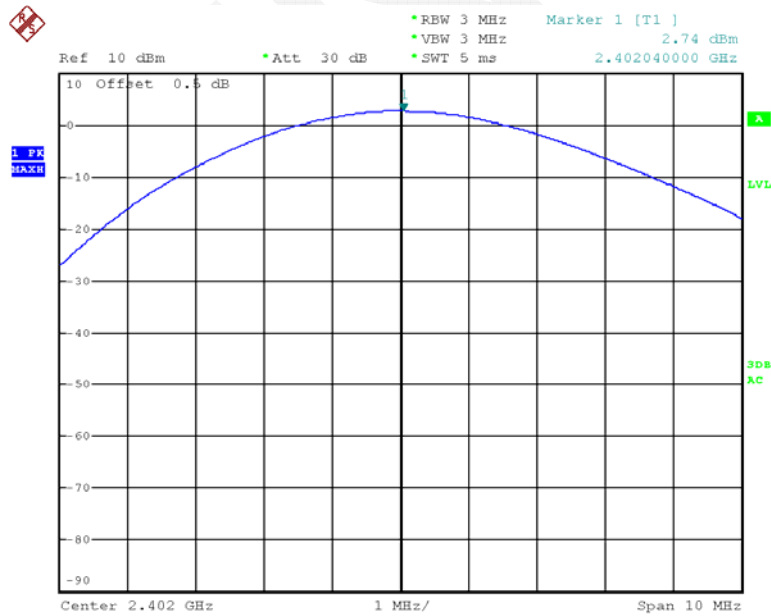
Output Power, High Channel



Date: 24.JUN.2015 18:36:31

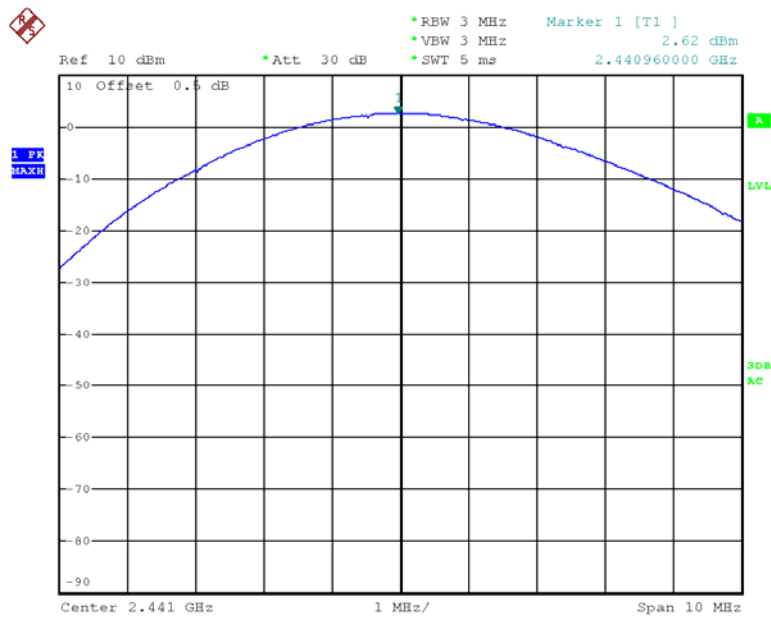
EDR Mode (8-DPSK):

Output Power, Low Channel



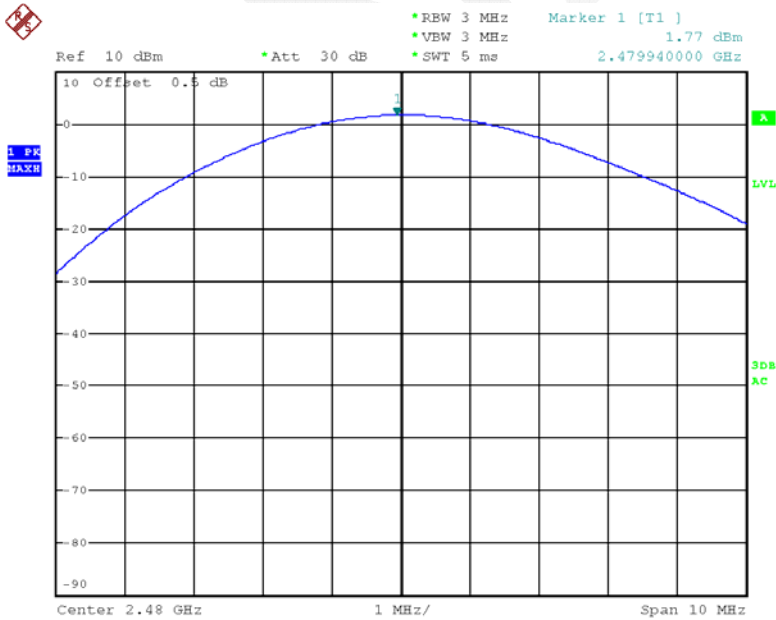
Date: 24.JUN.2015 18:38:49

Output Power, Middle Channel



Date: 24.JUN.2015 18:39:14

Output Power, High Channel



Date: 24.JUN.2015 18:39:36

FCC §15.247(d) - BAND EDGES TESTING

Applicable Standard

In any 100 kHz bandwidth outside the frequency band in which the spread spectrum or digitally modulated intentional radiator is operating, the radio frequency power that is produced by the intentional radiator shall be at least 20 dB below that in the 100 kHz bandwidth within the band that contains the highest level of the desired power, based on either an RF conducted or a radiated measurement, provided the transmitter demonstrates compliance with the peak conducted power limits. If the transmitter complies with the conducted power limits based on the use of RMS averaging over a time interval, as permitted under paragraph (b)(3) of this section, the attenuation required under this paragraph shall be 30 dB instead of 20 dB. Attenuation below the general limits specified in §15.209(a) is not required. In addition, radiated emissions which fall in the restricted bands, as defined in §15.205(a), must also comply with the radiated emission limits specified in §15.209(a) (see §15.205(c)).

Test Procedure

1. Check the calibration of the measuring instrument using either an internal calibrator or a known signal from an external generator.
2. Remove the antenna from the EUT and then connect to a low loss RF cable from the antenna port to a EMI test receiver, then turn on the EUT and make it operate in transmitting mode. Then set it to Low Channel and High Channel within its operating range, and make sure the instrument is operated in its linear range.
3. Set both RBW and VBW of spectrum analyzer to 100 kHz with a convenient frequency span including 100 kHz bandwidth from band edge.
4. Measure the highest amplitude appearing on spectral display and set it as a reference level. Plot the graph with marking the highest point and edge frequency.
5. Repeat above procedures until all measured frequencies were complete.

Test Equipment List and Details

Manufacturer	Description	Model	Serial Number	Calibration Date	Calibration Due Date
R&S	Spectrum Analyzer	FSP 38	100478	2015-05-09	2016-05-09

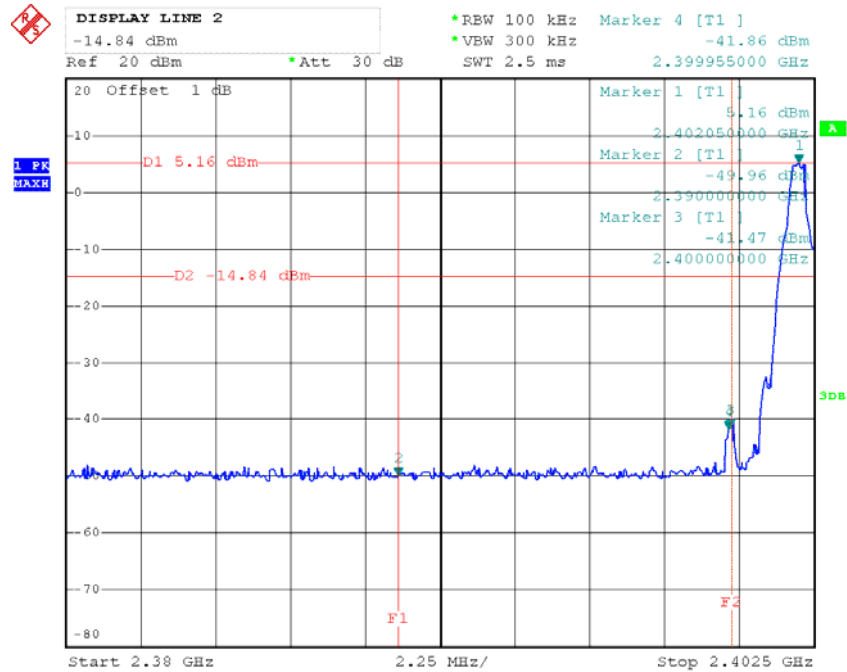
* **Statement of Traceability:** Bay Area Compliance Laboratories Corp. (Dongguan) attests that all calibrations have been performed, traceable to National Primary Standards and International System of Units (SI).

Test Data

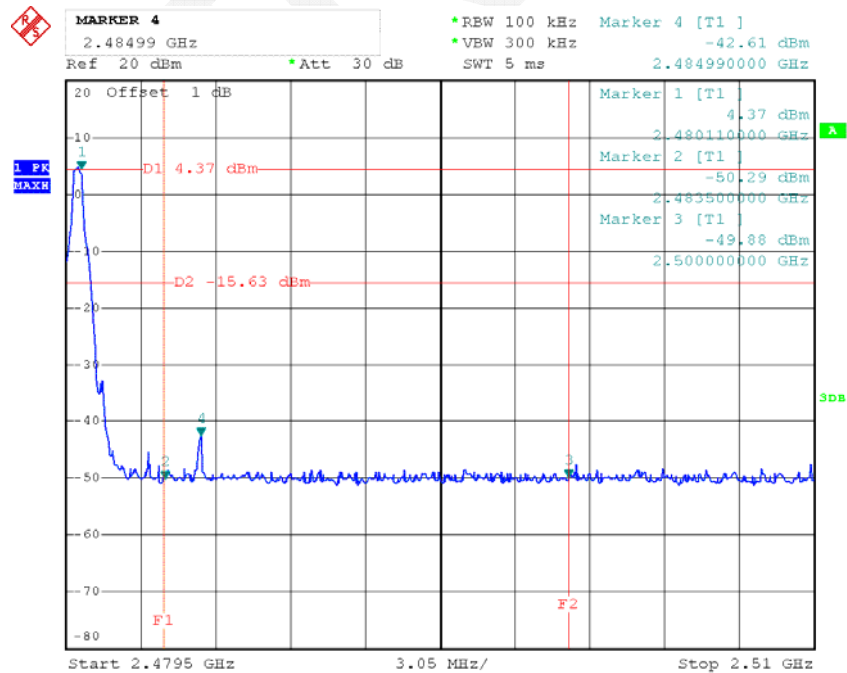
Environmental Conditions

Temperature:	25.8°C
Relative Humidity:	56 %
ATM Pressure:	100 kPa

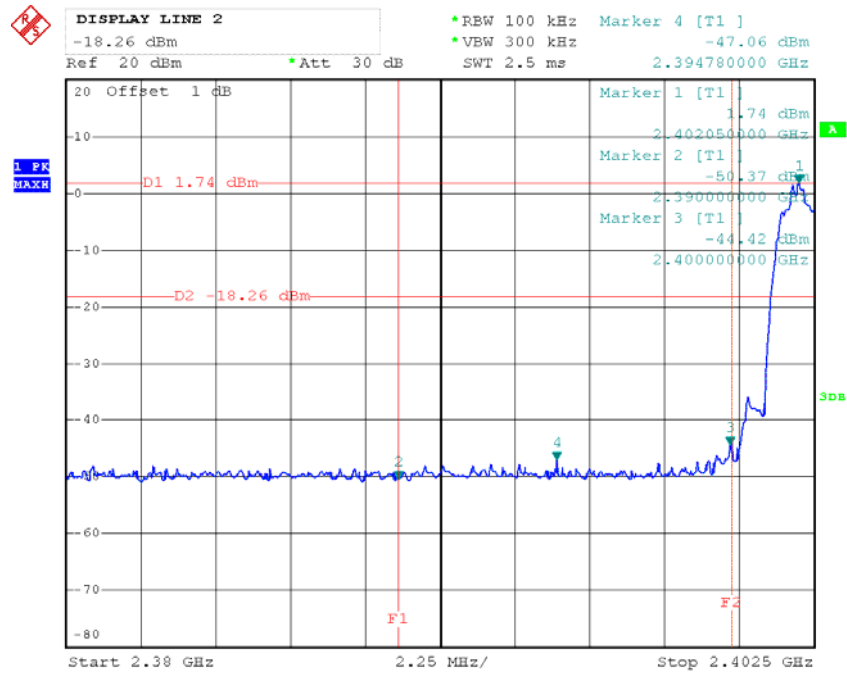
* The testing was performed by Dean Liu on 2015-06-26.

Test Result: Compliance*BDR Mode (GFSK):***Band Edge, Left Side**

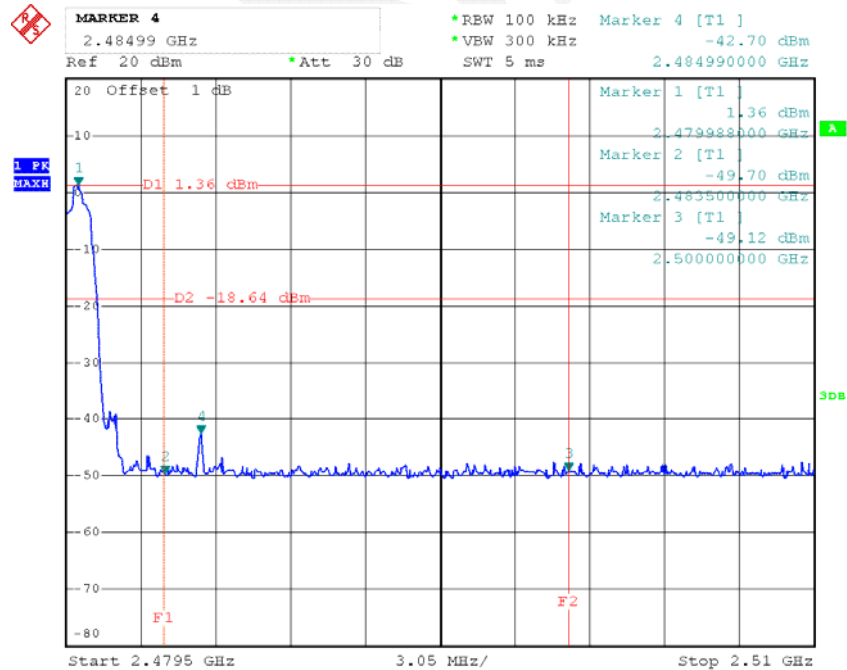
Date: 26.JUN.2015 15:59:10

Band Edge, Right Side

Date: 26.JUN.2015 16:10:01

*EDR Mode ($\pi/4$ -DQPSK):***Band Edge, Left Side**

Date: 26.JUN.2015 16:02:17

Band Edge, Right Side

Date: 26.JUN.2015 16:09:11

Band Edge, Left Side



DISPLAY LINE 2

- 19.71 dBm
- *RBW 100 kHz
- *VBW 300 kHz
- Ref 20 dBm
- *Att 30 dB
- SWT 5 ms
- 2.484990000 GHz

Marker Data:

Marker	Freq [GHz]	Power [dBm]
1 [T1]	2.479527000	-0.29
2 [T1]	2.483500000	-49.12
3 [T1]	2.500000000	-48.96

Plot Labels:

- Offset 1 dB
- D1 -0.29 dBm
- D2 -19.71 dBm
- F1
- F2
- Start 2.4795 GHz
- Stop 2.51 GHz
- 3.05 MHz/

Date: 26.JUN.2015 16:07:21

***** END OF REPORT *****