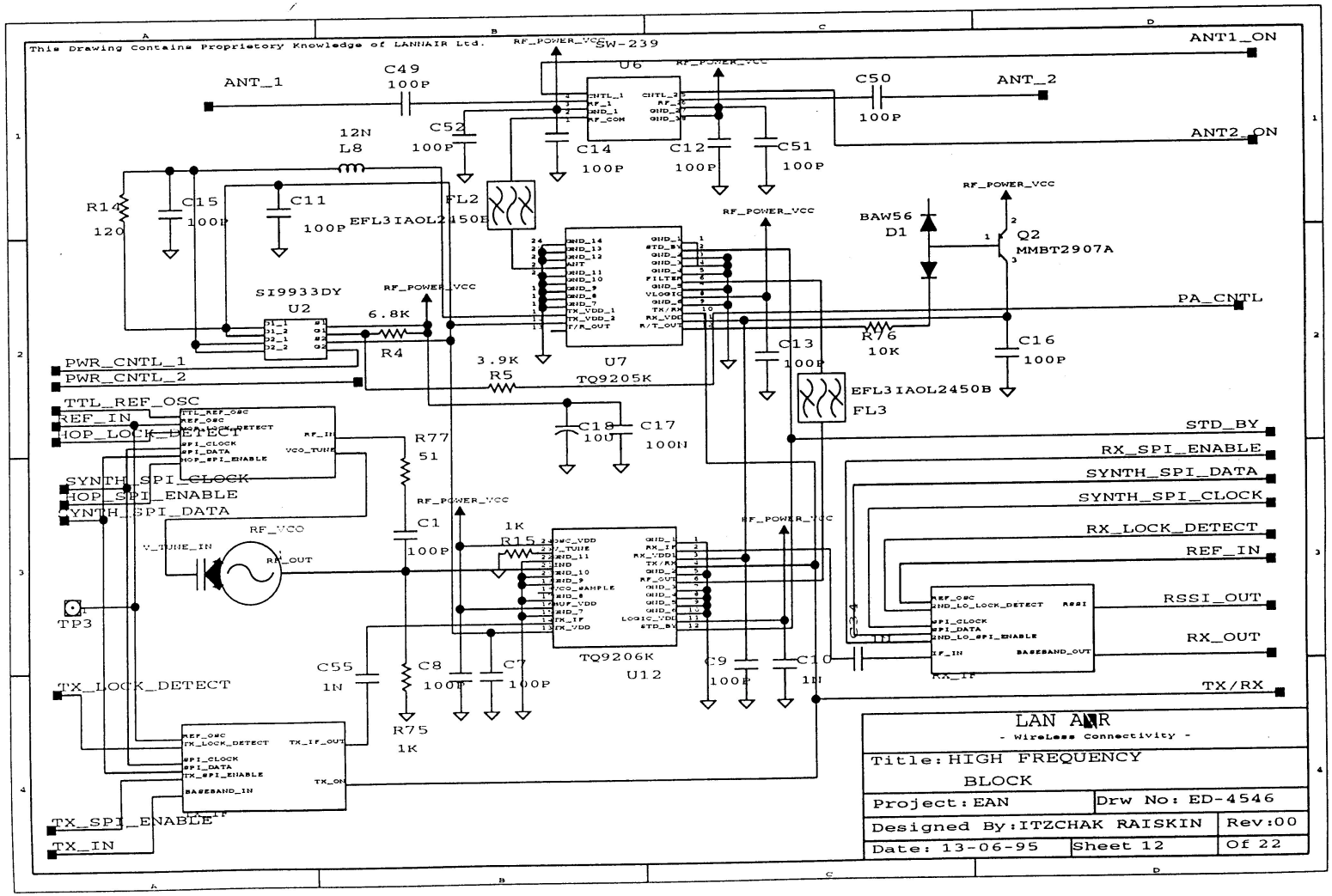
This Drawing Contains Proprietory Knowledge of LANNAIR Ltd.
ANT1_ON
ANT_1
ANT_2
ANT2_ON
SW-239
U6
C49 100P
C50 100P
C52 100P
C14 100P
C12 100P
C51 100P
12N L8
R14 120
C15 100P
C11 100P
EFL3IAOL2150E
FL2
SI9933DY U2
6.8K R4
3.9K R5
U7 TQ9205K
BAW56 D1
Q2 MMBT2907A
R76 10K
C16 100P
C13 100P
EFL3IAOL2450B FL3
C18 10U
C17 100N
PA_CNTL
PWR_CNTL_1
PWR_CNTL_2
TTL_REF_OSC
REF_IN
HOP_LOCK_DETECT
SYNTH_SPI_CLOCK
HOP_SPI_ENABLE
SYNTH_SPI_DATA
R77 51
C1 100P
R15 1K
RF_VCO
V_TUNE_IN
RF_OUT
TP3
TQ9206K U12
C55 1N
C8 100P
C7 100P
R75 1K
C9 100P
C10 1N
C4 100P
TX_LOCK_DETECT
TX_SPI_ENABLE
TX_IN
STD_BY
RX_SPI_ENABLE
SYNTH_SPI_DATA
SYNTH_SPI_CLOCK
RX_LOCK_DETECT
REF_IN
RSSI_OUT
RX_OUT
TX/RX
LAN AIR
- WireLess Connectivity -
Title: HIGH FREQUENCY BLOCK
Project: EAN
Drw No: ED-4546
Designed By: ITZCHAK RAISKIN
Rev:00
Date: 13-06-95
Sheet 12
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