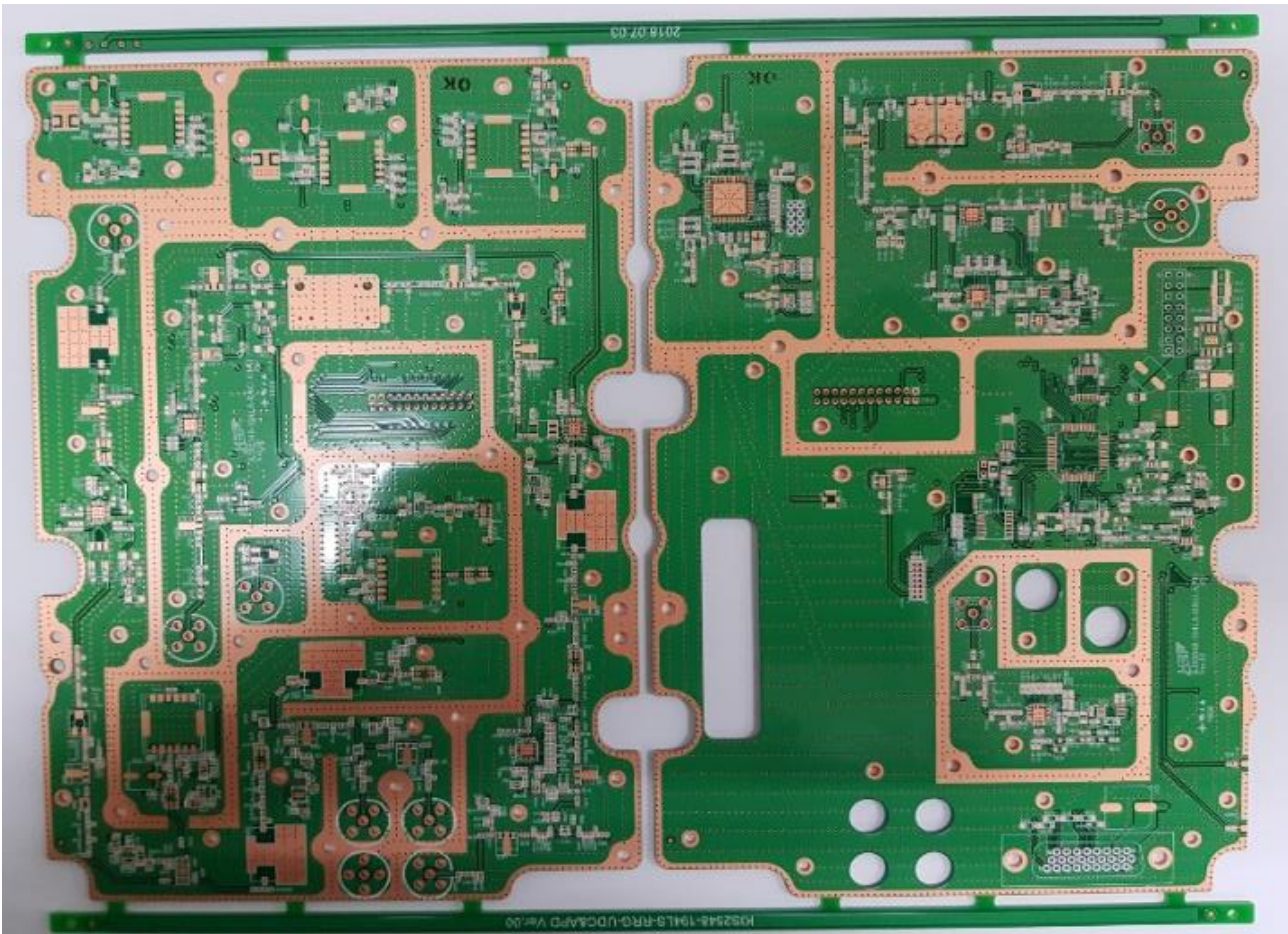
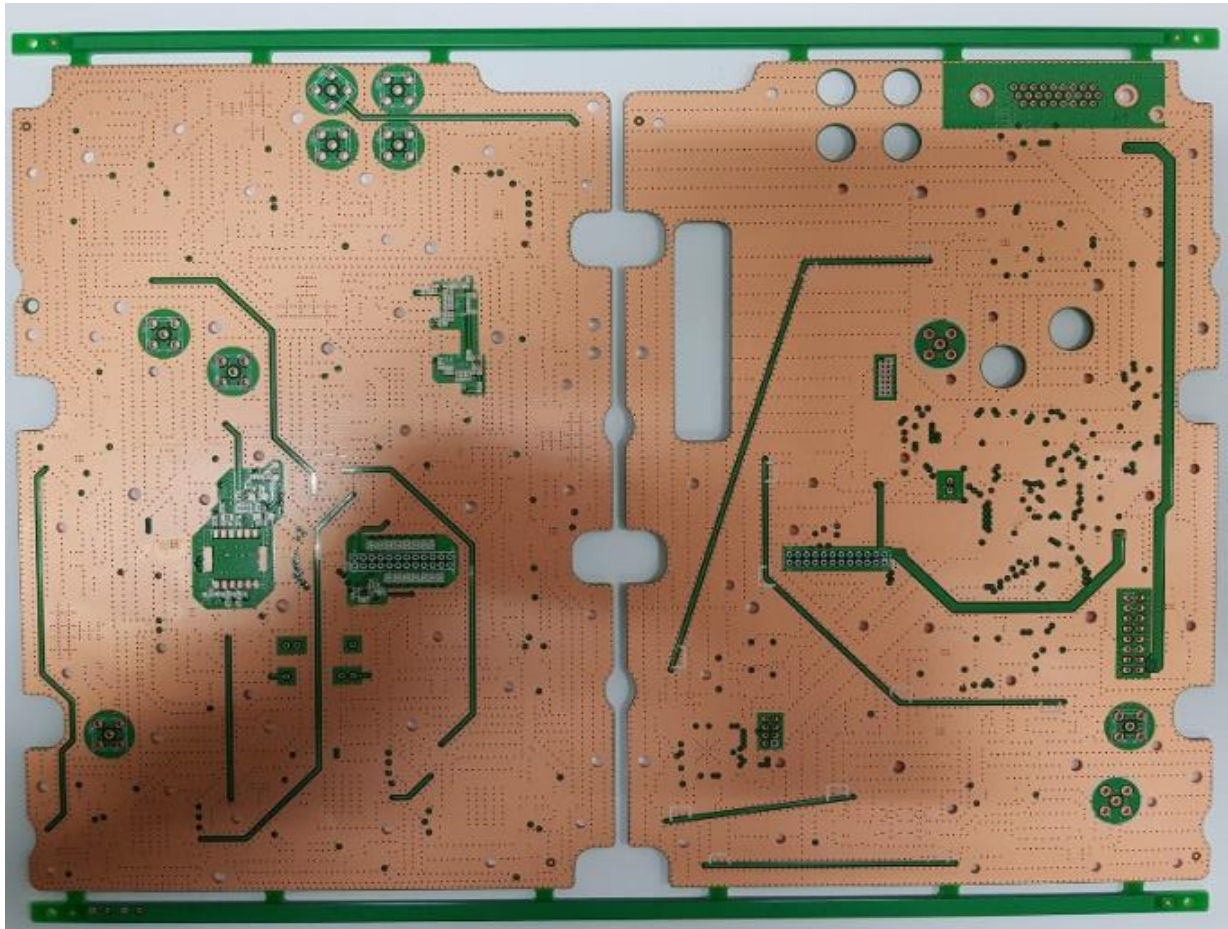


## INTERNAL PHOTOGRAPHS

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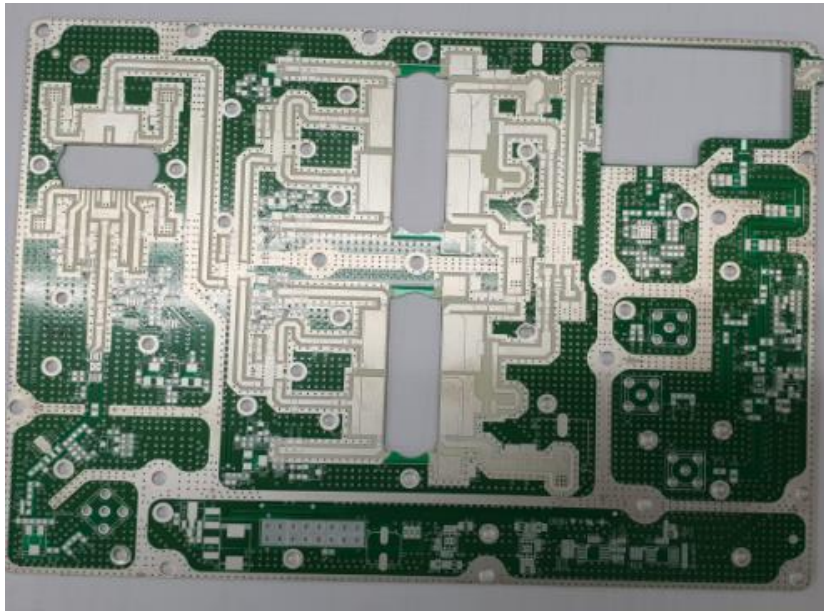


**[BARE PCB - UDC(Top)]**

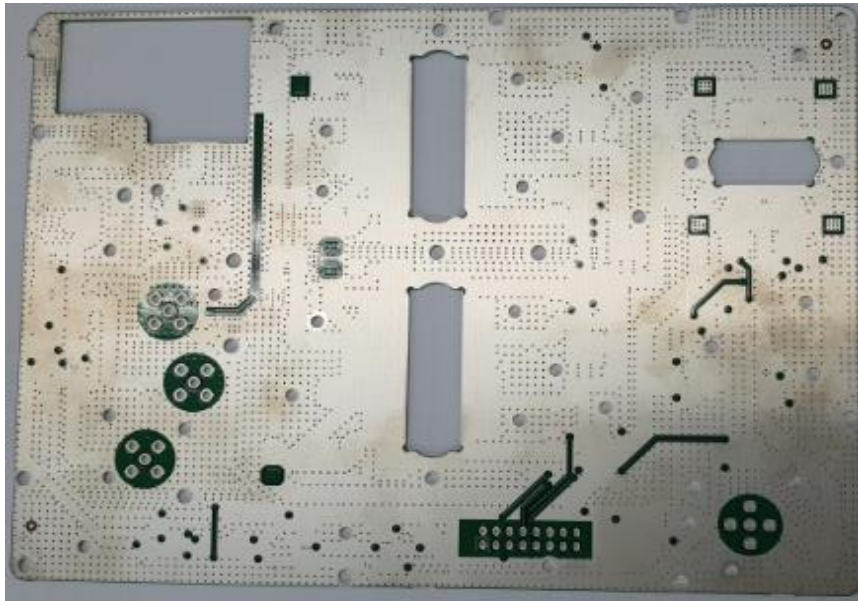


**[BARE PCB - UDC(Bottom)]**

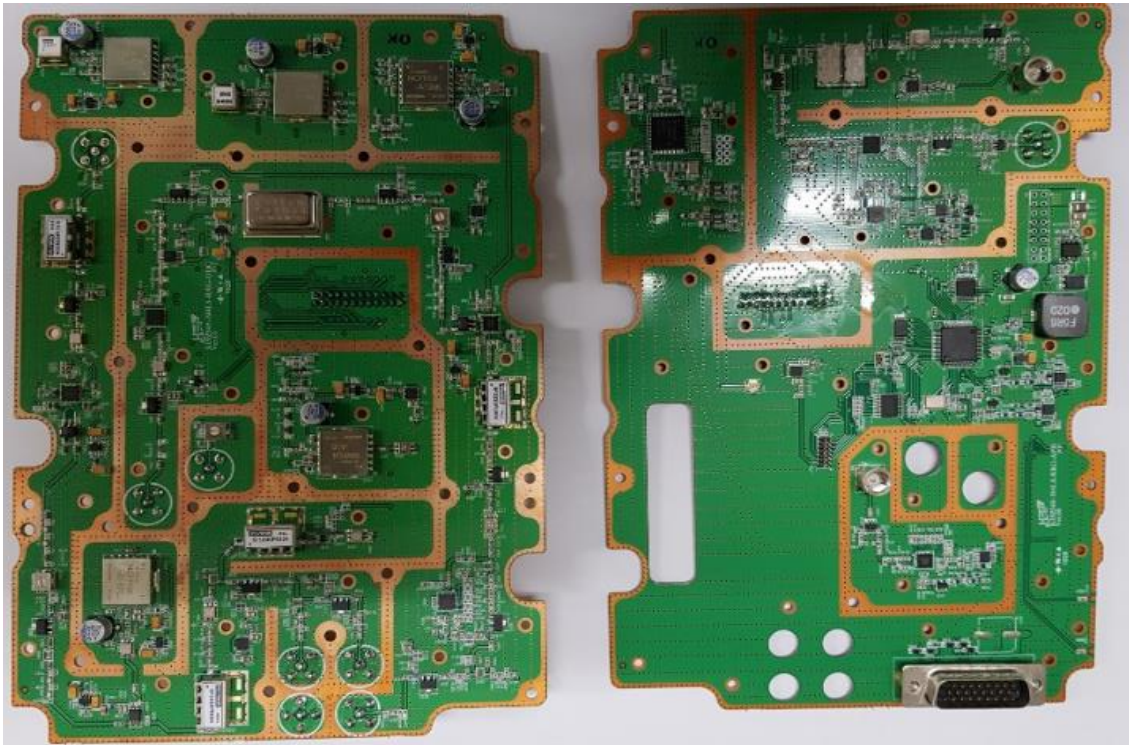




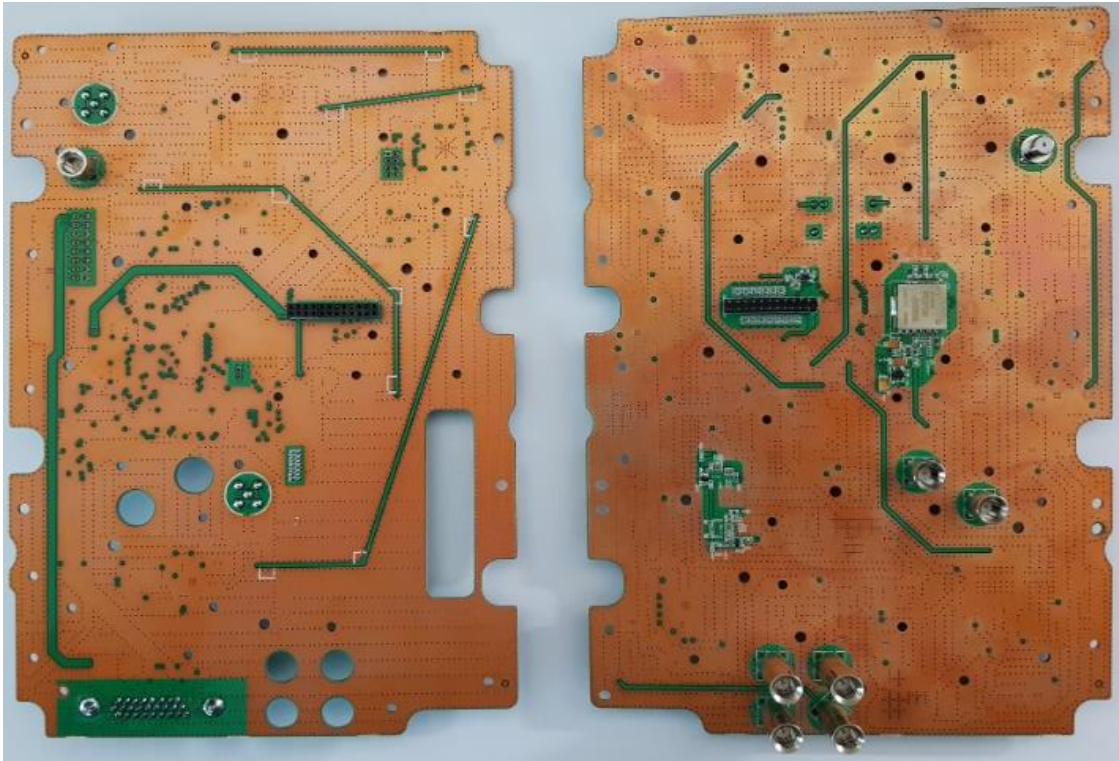
**[BARE PCB - PAU(Top)]**



**[BARE PCB - PAU(Bottom)]**

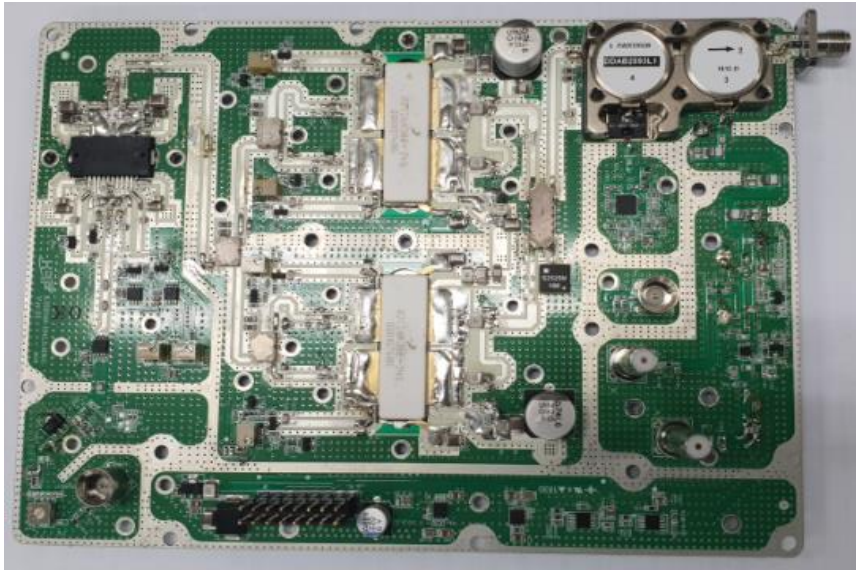


**[PBA - UDC(Top)]**



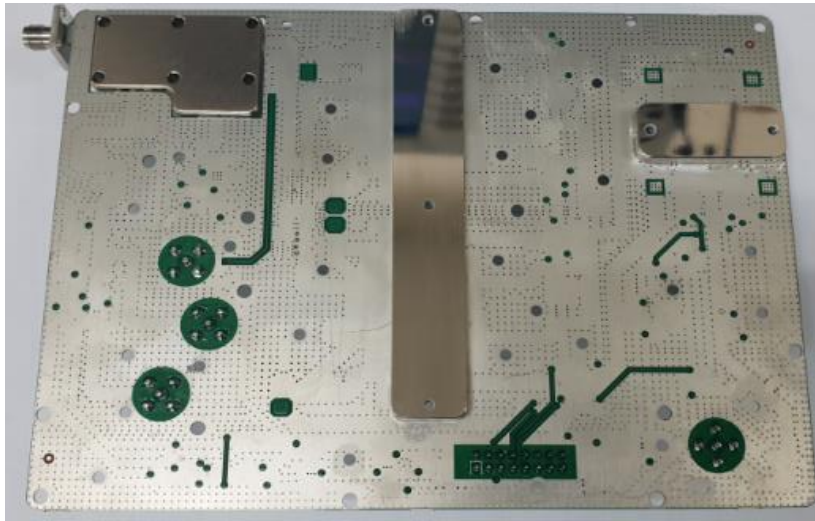
**[PBA - UDC(Bottom)]**





**[PBA - PAU(Top)]**





**[PBA - PAU(Bottom)]**