

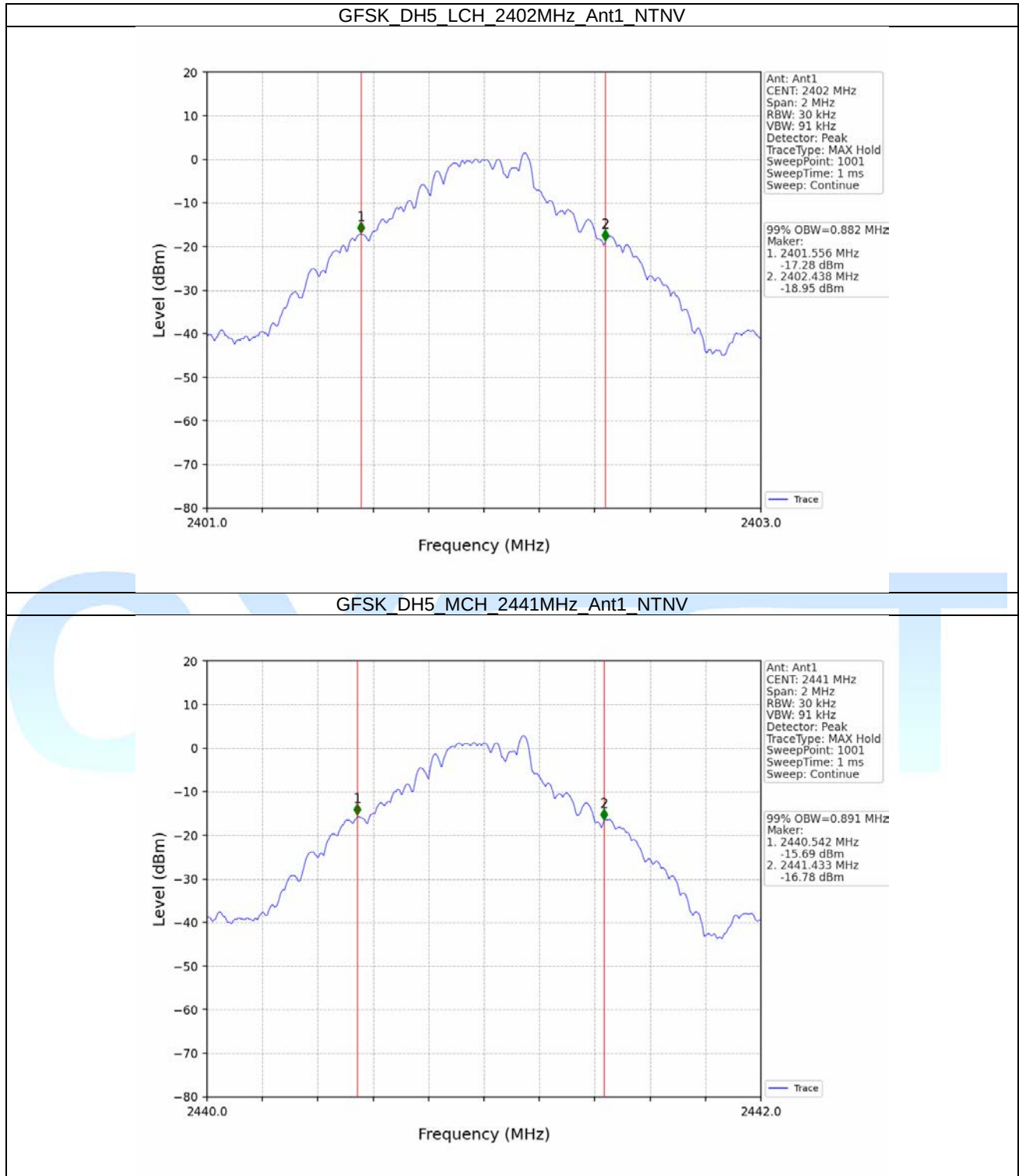
1. Bandwidth

1.1 OBW

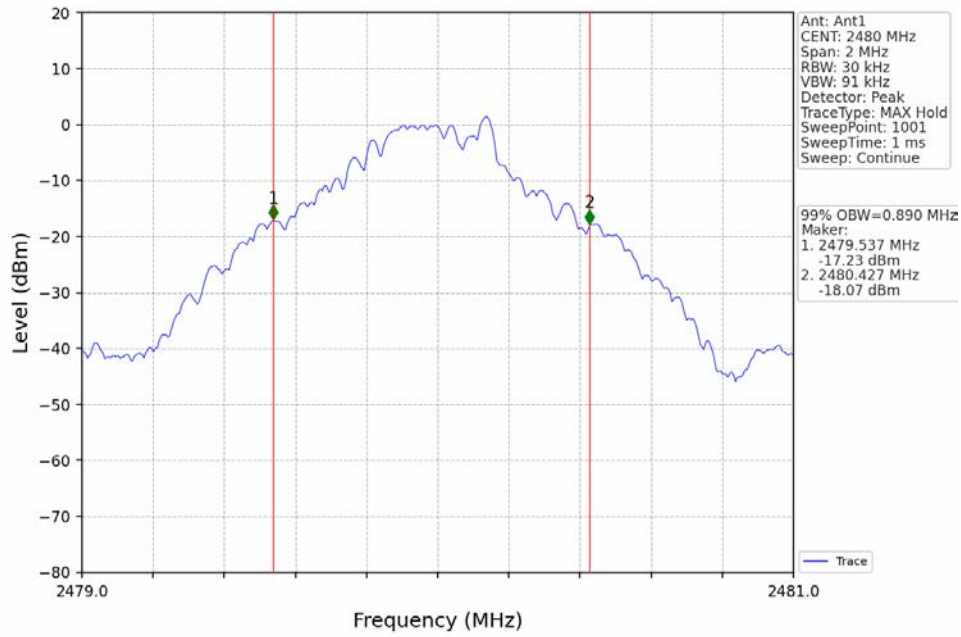
1.1.1 Test Result

Mode	TX Type	Frequency (MHz)	Packet Type	ANT	99% Occupied Bandwidth (MHz)		Verdict
					Result	Limit	
GFSK	SISO	2402	DH5	1	0.882	/	Pass
		2441	DH5	1	0.891	/	Pass
		2480	DH5	1	0.890	/	Pass
Pi/4DQPSK	SISO	2402	2DH5	1	1.193	/	Pass
		2441	2DH5	1	1.194	/	Pass
		2480	2DH5	1	1.194	/	Pass
8DPSK	SISO	2402	3DH5	1	1.191	/	Pass
		2441	3DH5	1	1.190	/	Pass
		2480	3DH5	1	1.190	/	Pass

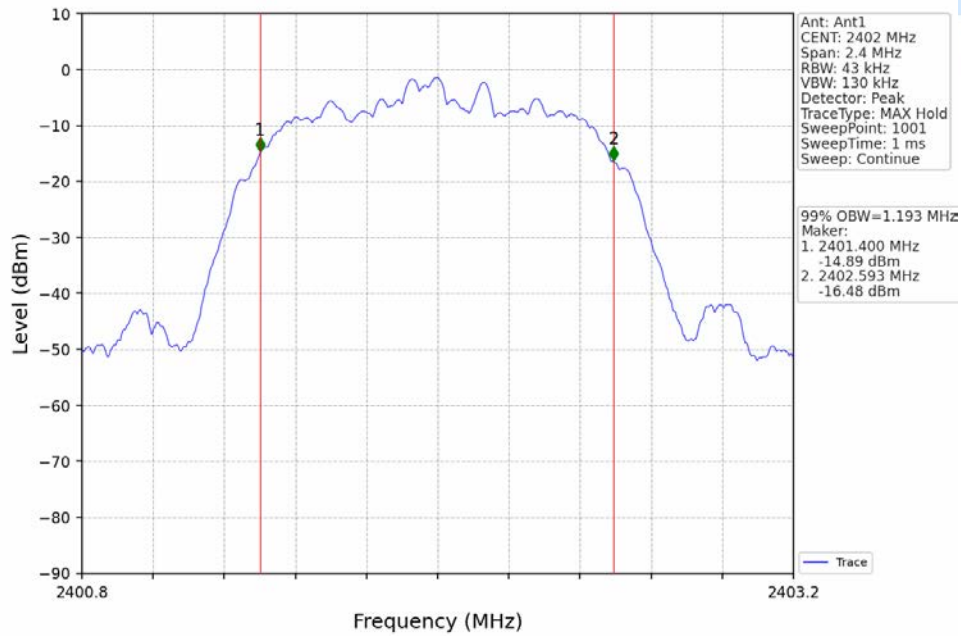
1.1.2 Test Graph



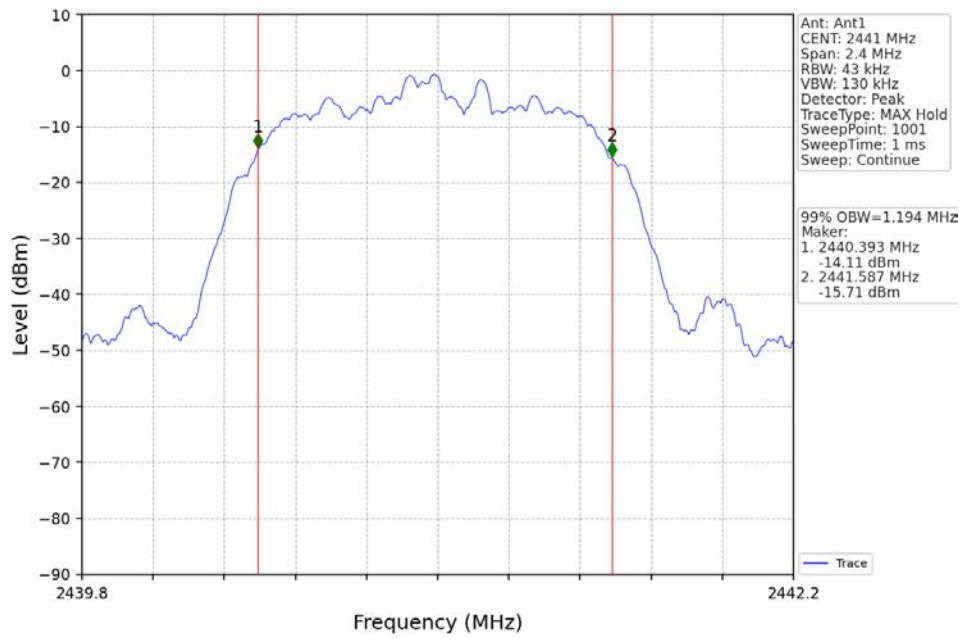
GFSK_DH5_HCH_2480MHz_Ant1_NTNV



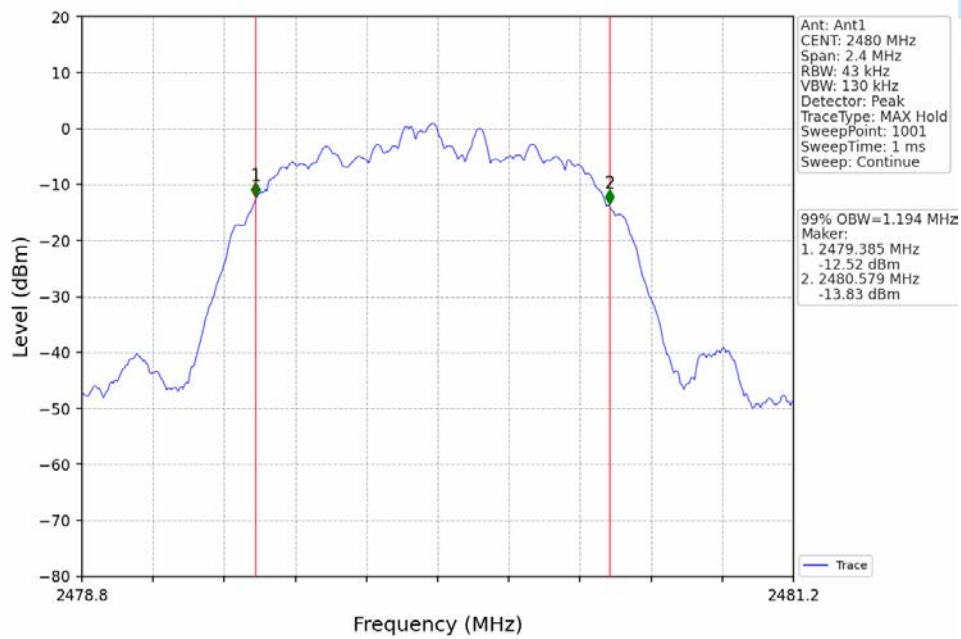
Pi/4DQPSK_2DH5_LCH_2402MHz_Ant1_NTNV



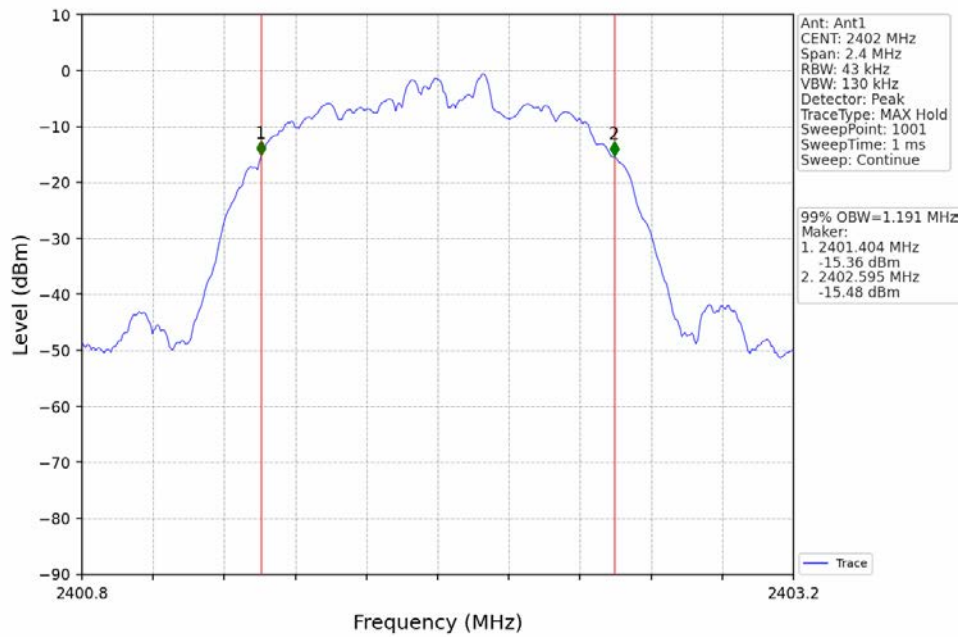
Pi/4DQPSK_2DH5_MCH_2441MHz_Ant1_NTNV



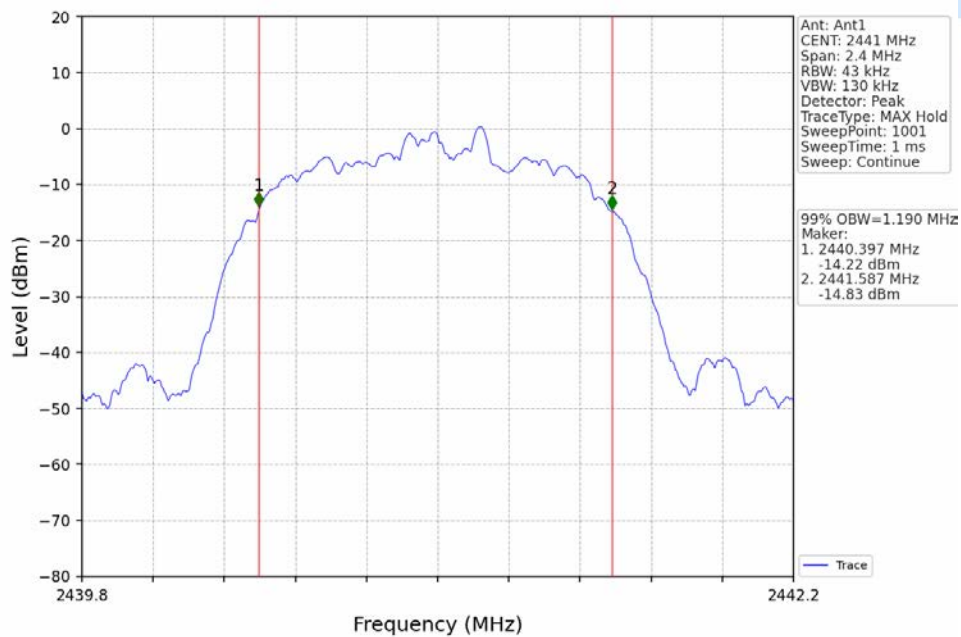
Pi/4DQPSK_2DH5_HCH_2480MHz_Ant1_NTNV

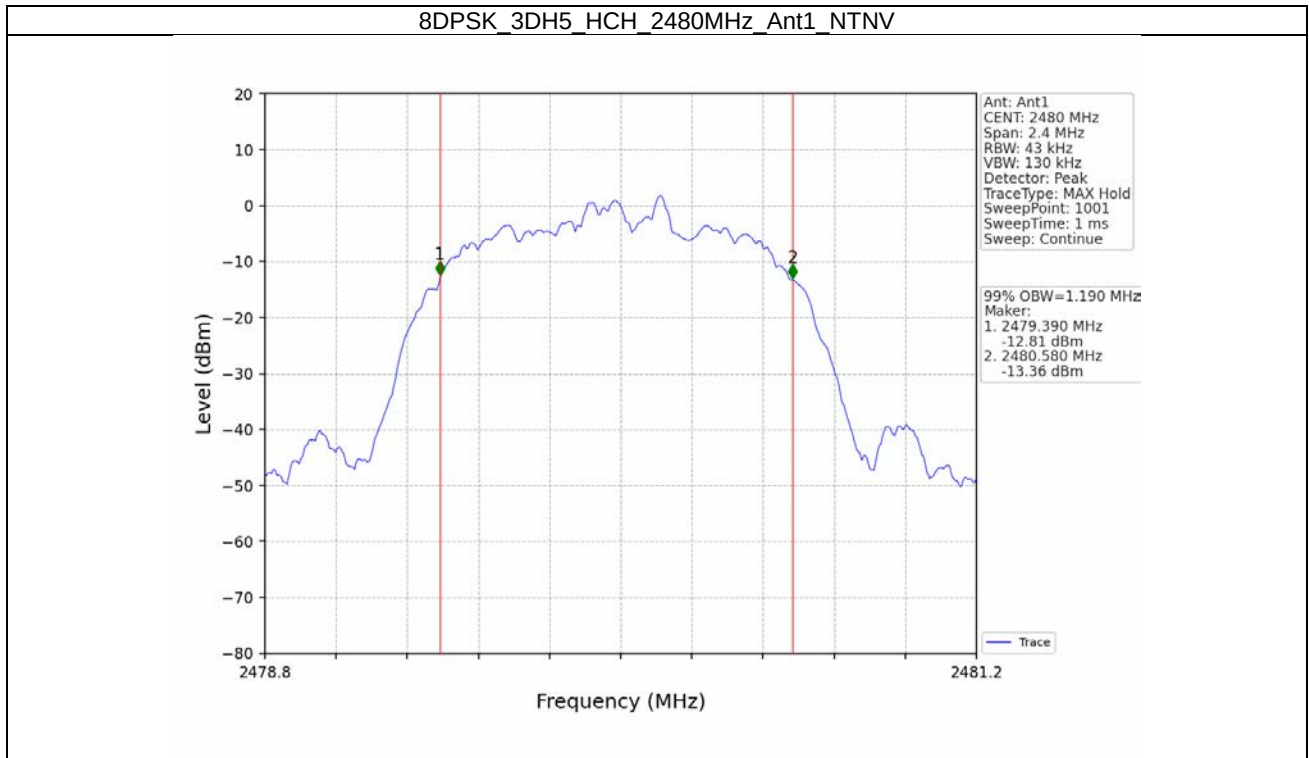


8DPSK_3DH5_LCH_2402MHz_Ant1_NTNV



8DPSK_3DH5_MCH_2441MHz_Ant1_NTNV



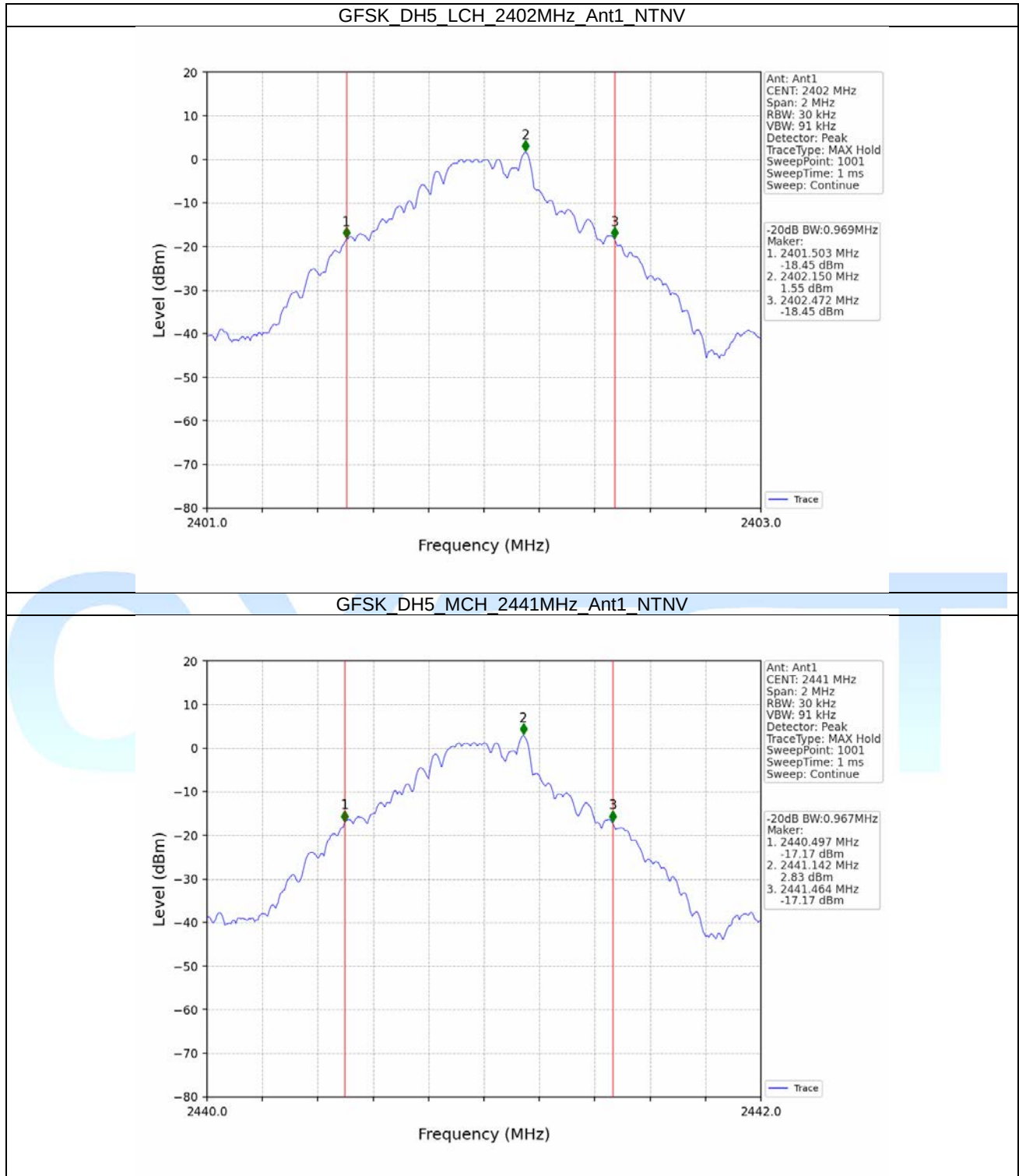


1.2 20dB BW

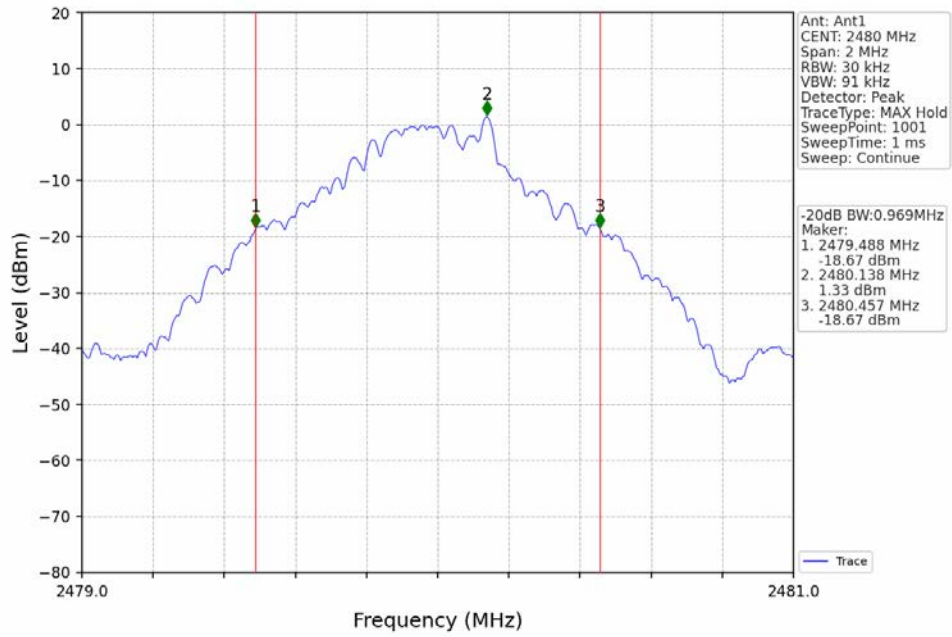
1.2.1 Test Result

Mode	TX Type	Frequency (MHz)	Packet Type	ANT	20dB Bandwidth (MHz)		Verdict
					Result	Limit	
GFSK	SISO	2402	DH5	1	0.969	/	Pass
		2441	DH5	1	0.967	/	Pass
		2480	DH5	1	0.969	/	Pass
Pi/4DQPSK	SISO	2402	2DH5	1	1.347	/	Pass
		2441	2DH5	1	1.347	/	Pass
		2480	2DH5	1	1.348	/	Pass
8DPSK	SISO	2402	3DH5	1	1.329	/	Pass
		2441	3DH5	1	1.315	/	Pass
		2480	3DH5	1	1.328	/	Pass

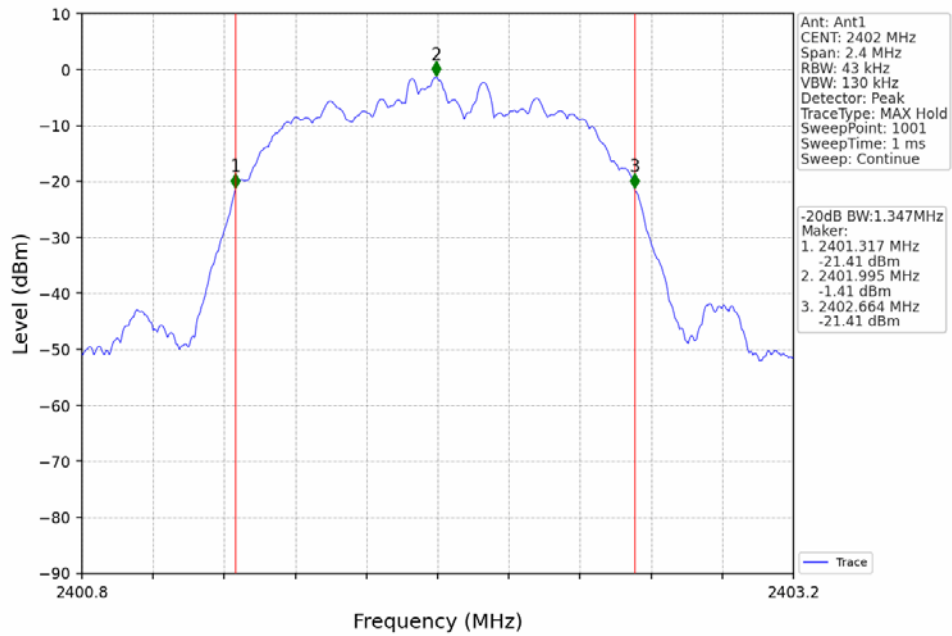
1.2.2 Test Graph



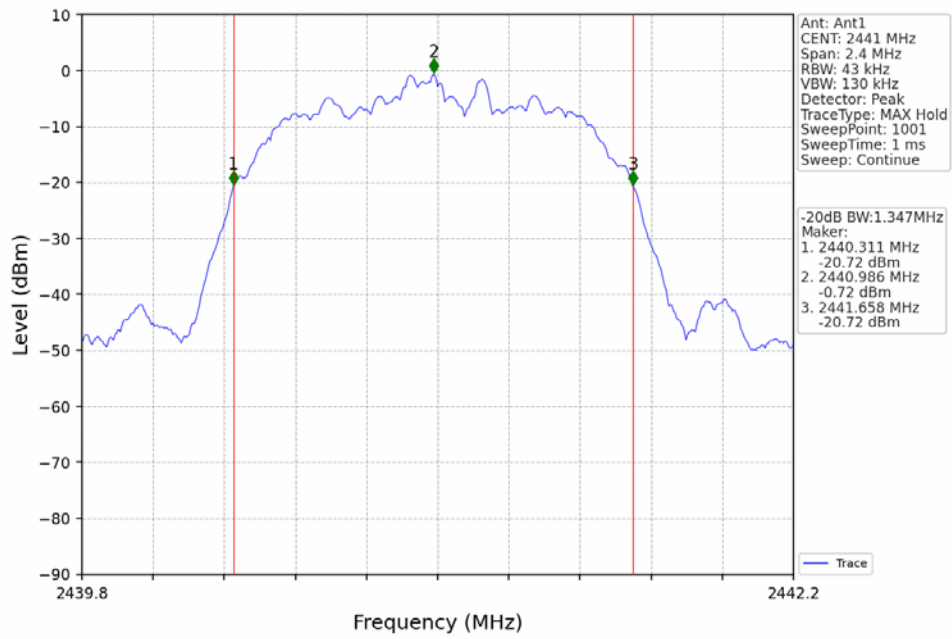
GFSK_DH5_HCH_2480MHz_Ant1_NTNV



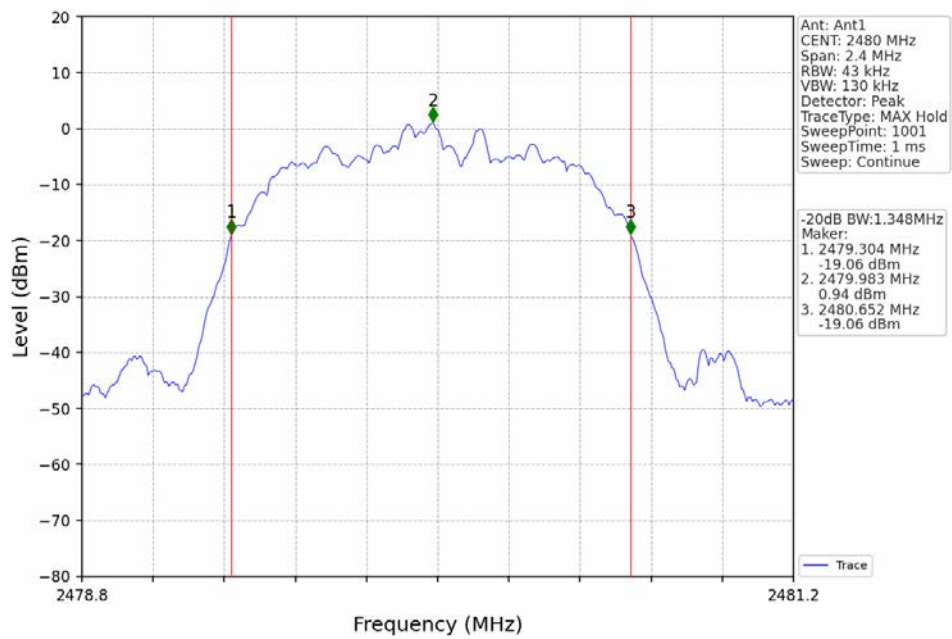
Pi/4DQPSK_2DH5_LCH_2402MHz_Ant1_NTNV



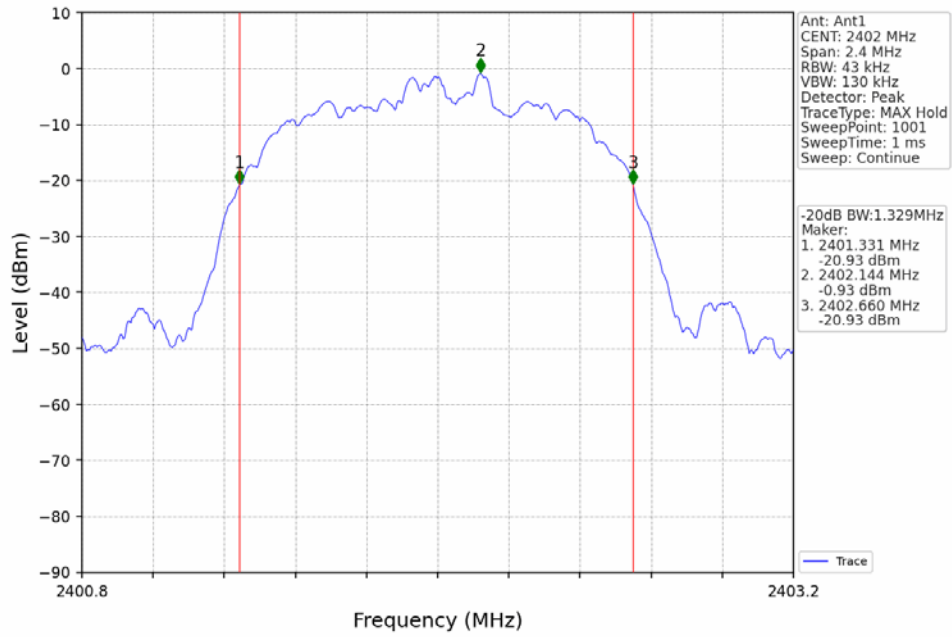
Pi/4DQPSK_2DH5_MCH_2441MHz_Ant1_NTNV



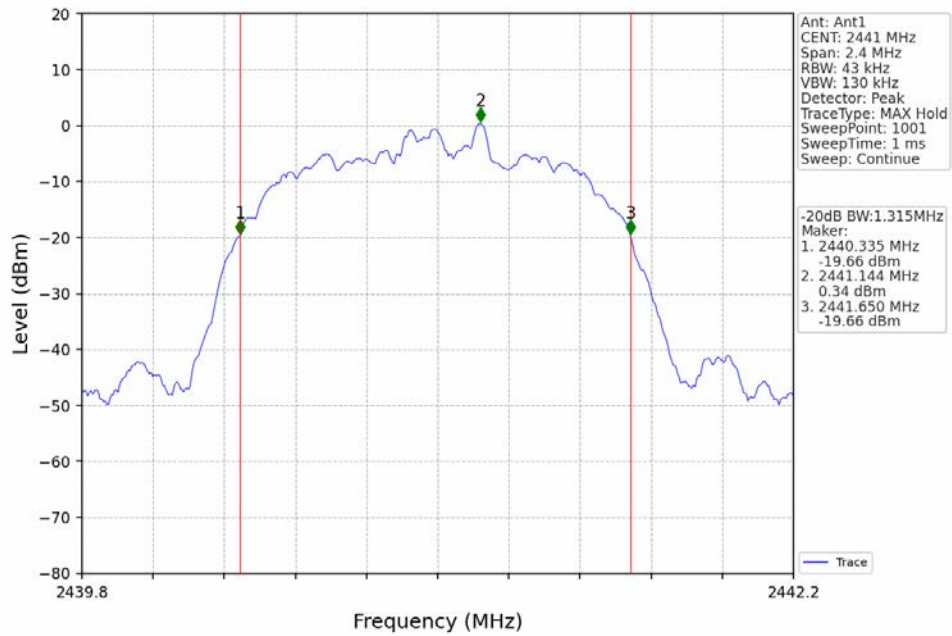
Pi/4DQPSK_2DH5_HCH_2480MHz_Ant1_NTNV

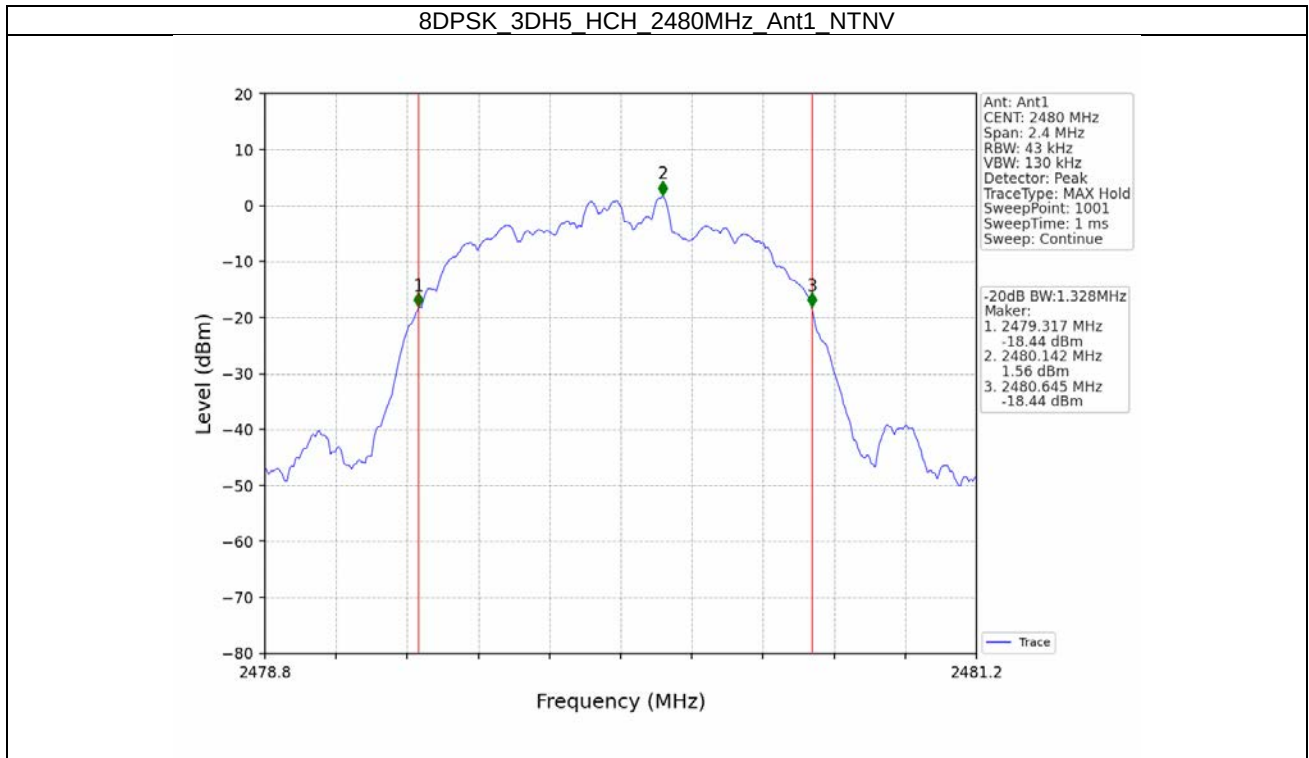


8DPSK_3DH5_LCH_2402MHz_Ant1_NTNV



8DPSK_3DH5_MCH_2441MHz_Ant1_NTNV





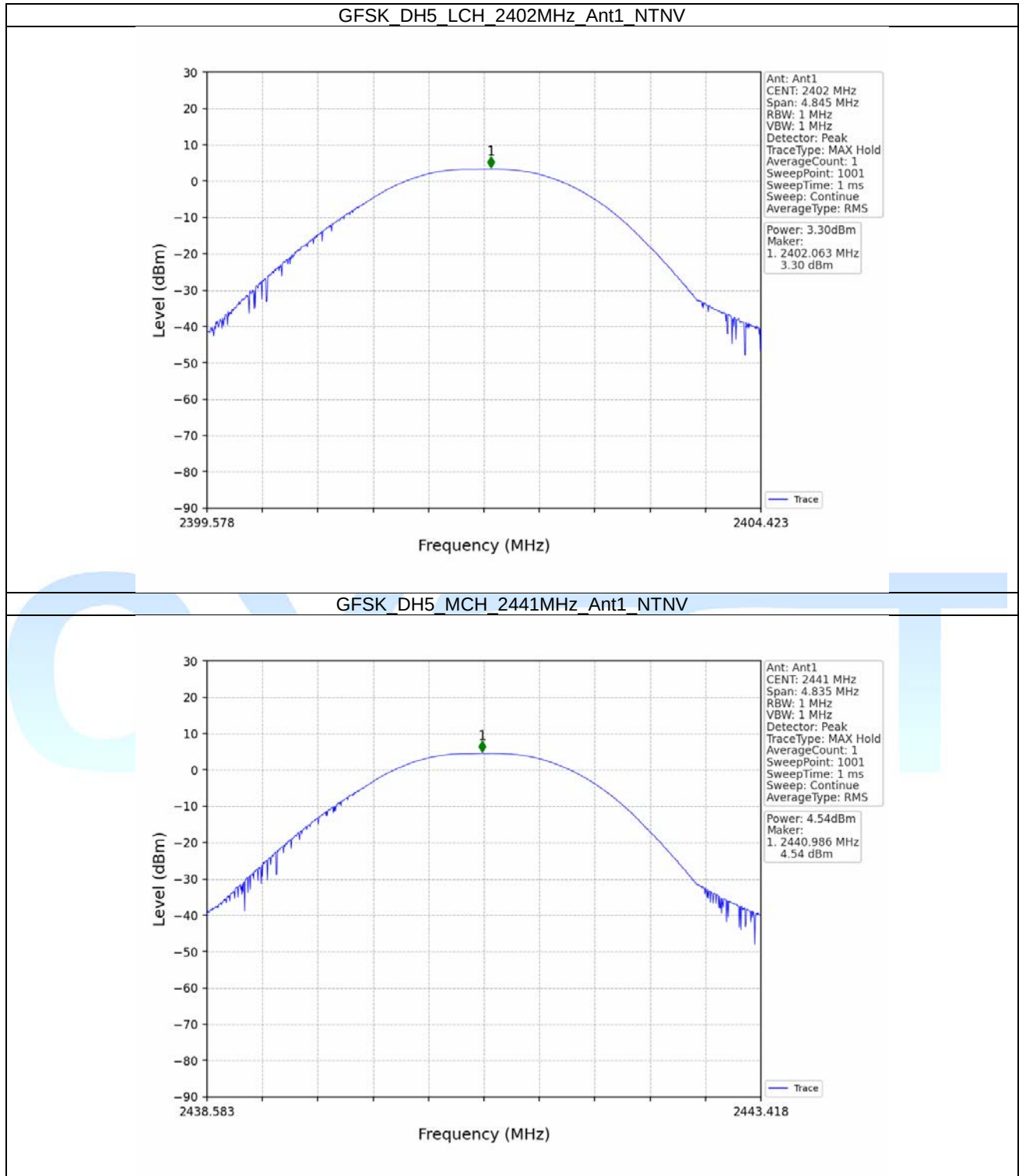
2. Maximum Conducted Output Power

2.1 Power

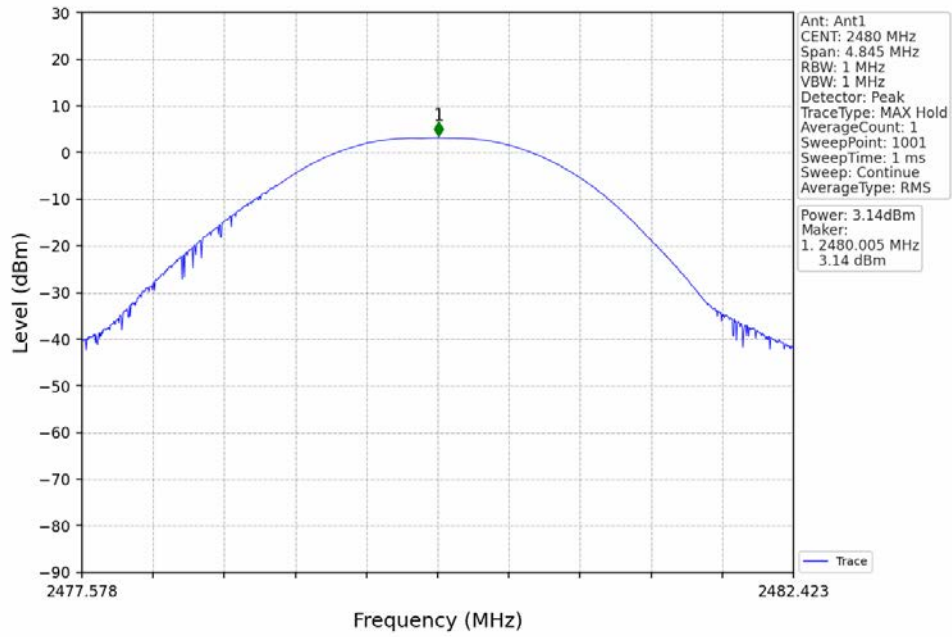
2.1.1 Test Result

Mode	TX Type	Frequency (MHz)	Packet Type	Maximum Peak Conducted Output Power (dBm)		Verdict
				ANT1	Limit	
GFSK	SISO	2402	DH5	3.30	<=20.97	Pass
		2441	DH5	4.54	<=20.97	Pass
		2480	DH5	3.14	<=20.97	Pass
Pi/4DQPSK	SISO	2402	2DH5	2.37	<=20.97	Pass
		2441	2DH5	3.09	<=20.97	Pass
		2480	2DH5	4.79	<=20.97	Pass
8DPSK	SISO	2402	3DH5	2.82	<=20.97	Pass
		2441	3DH5	3.58	<=20.97	Pass
		2480	3DH5	5.24	<=20.97	Pass
Note1: Antenna Gain: Ant1: 1.83dBi;						

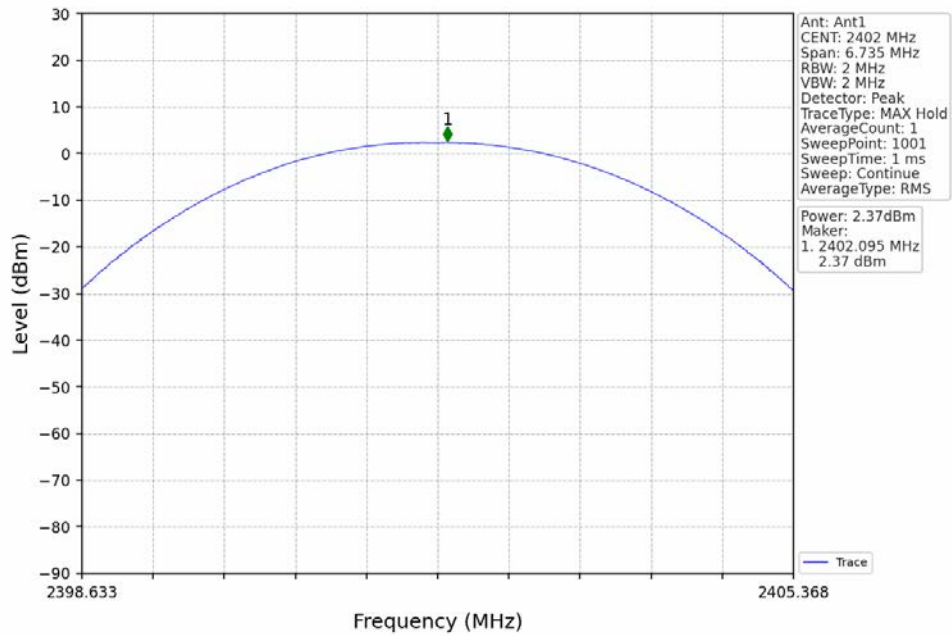
2.1.2 Test Graph



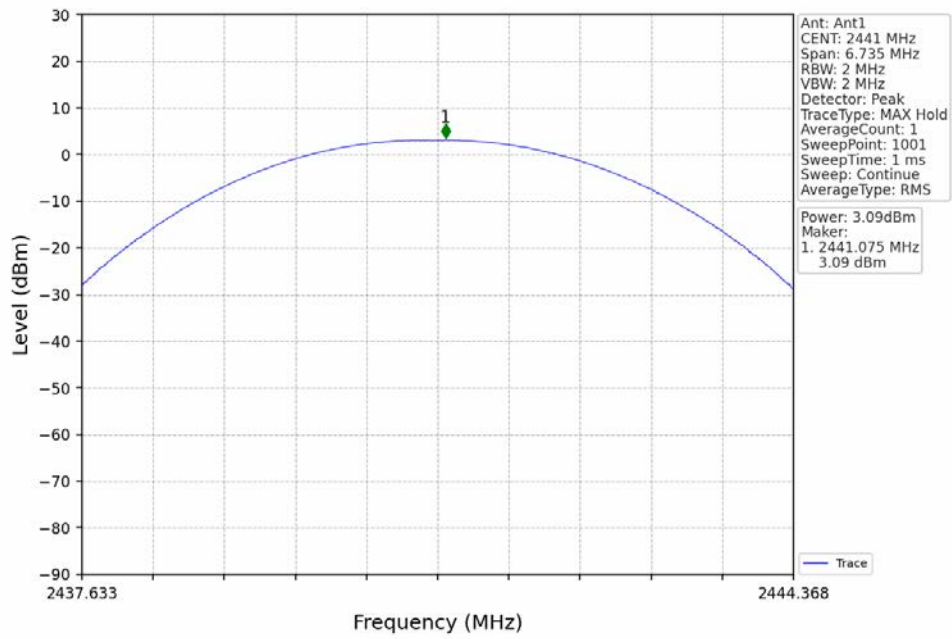
GFSK_DH5_HCH_2480MHz_Ant1_NTNV



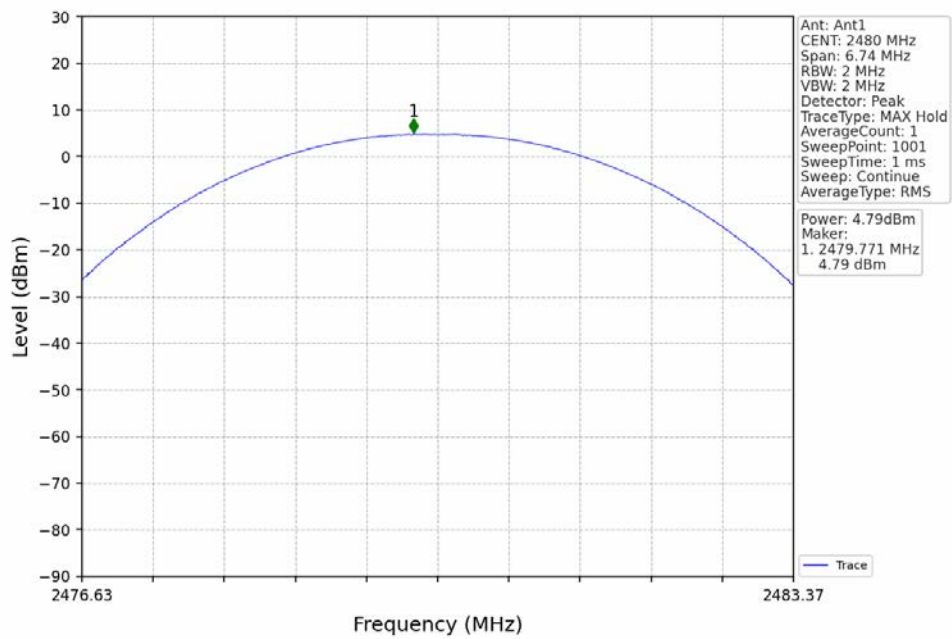
Pi/4DQPSK_2DH5_LCH_2402MHz_Ant1_NTNV



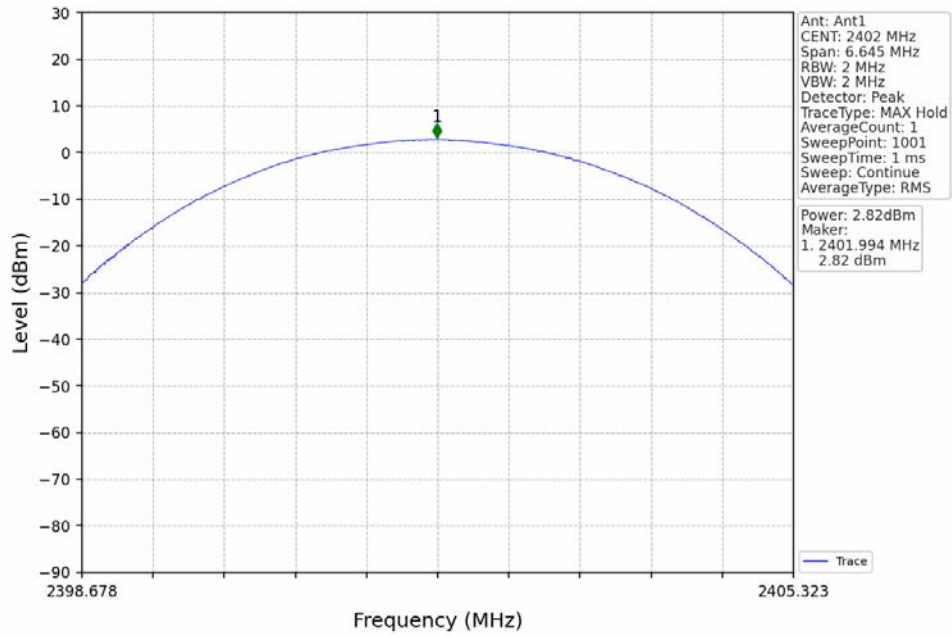
Pi/4DQPSK_2DH5_MCH_2441MHz_Ant1_NTNV



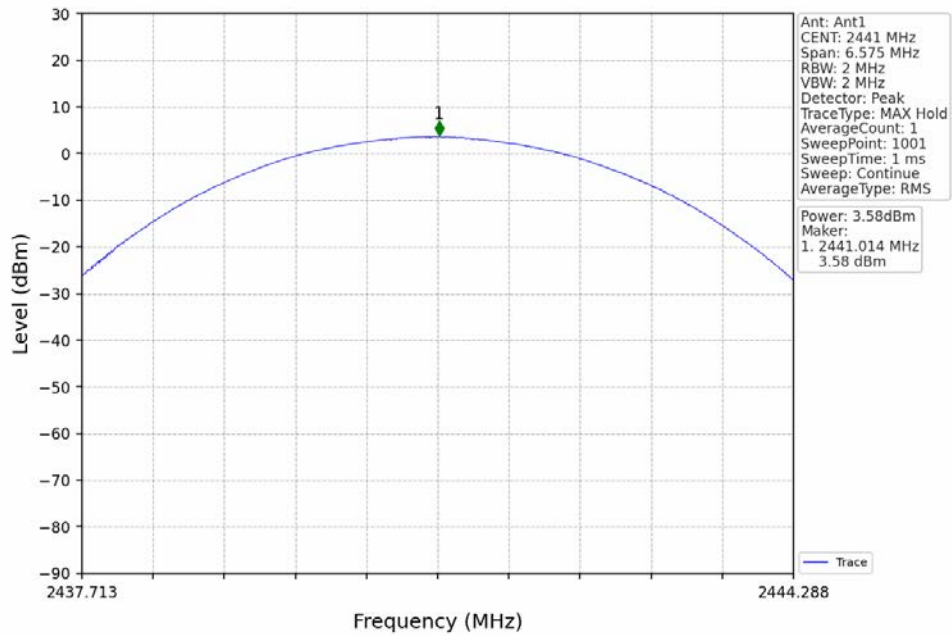
Pi/4DQPSK_2DH5_HCH_2480MHz_Ant1_NTNV

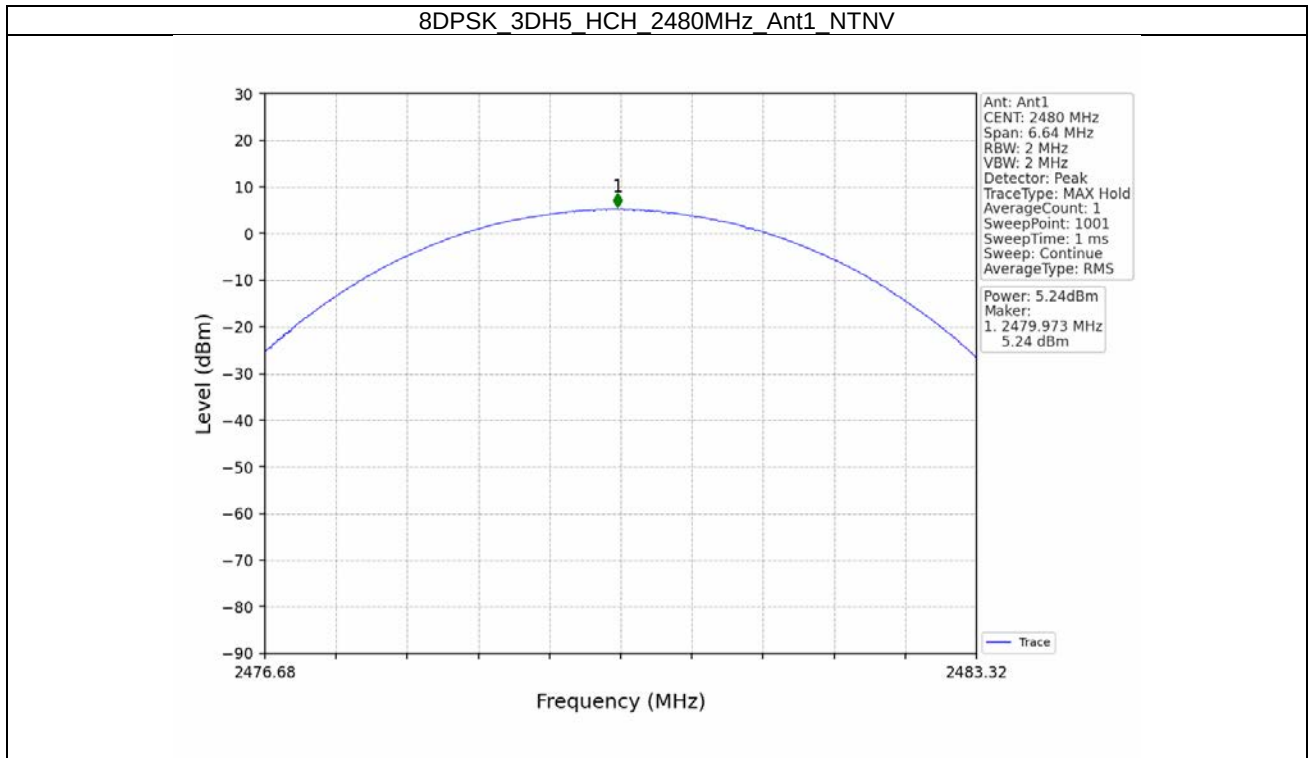


8DPSK_3DH5_LCH_2402MHz_Ant1_NTNV



8DPSK_3DH5_MCH_2441MHz_Ant1_NTNV





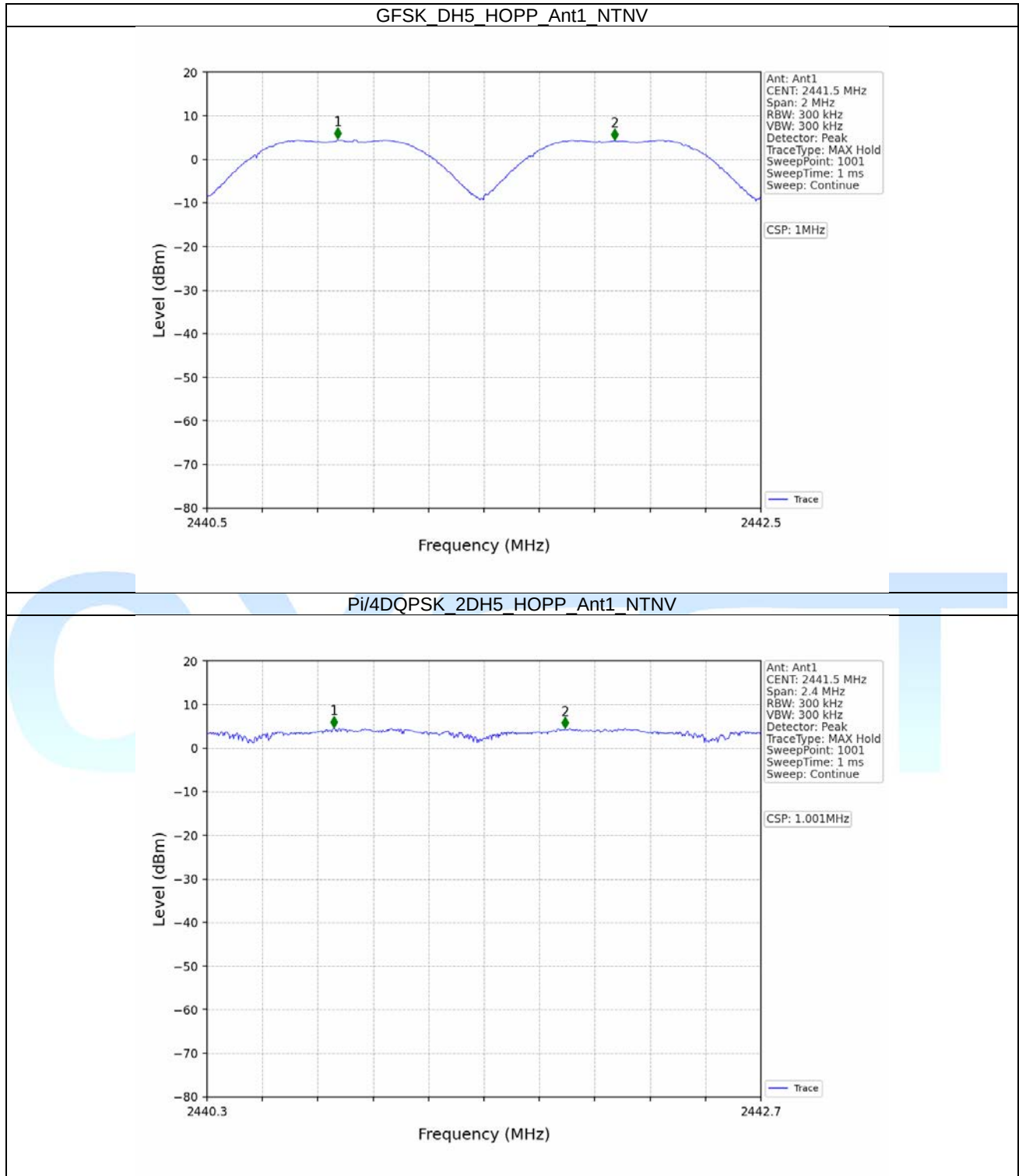
3. Carrier Frequency Separation

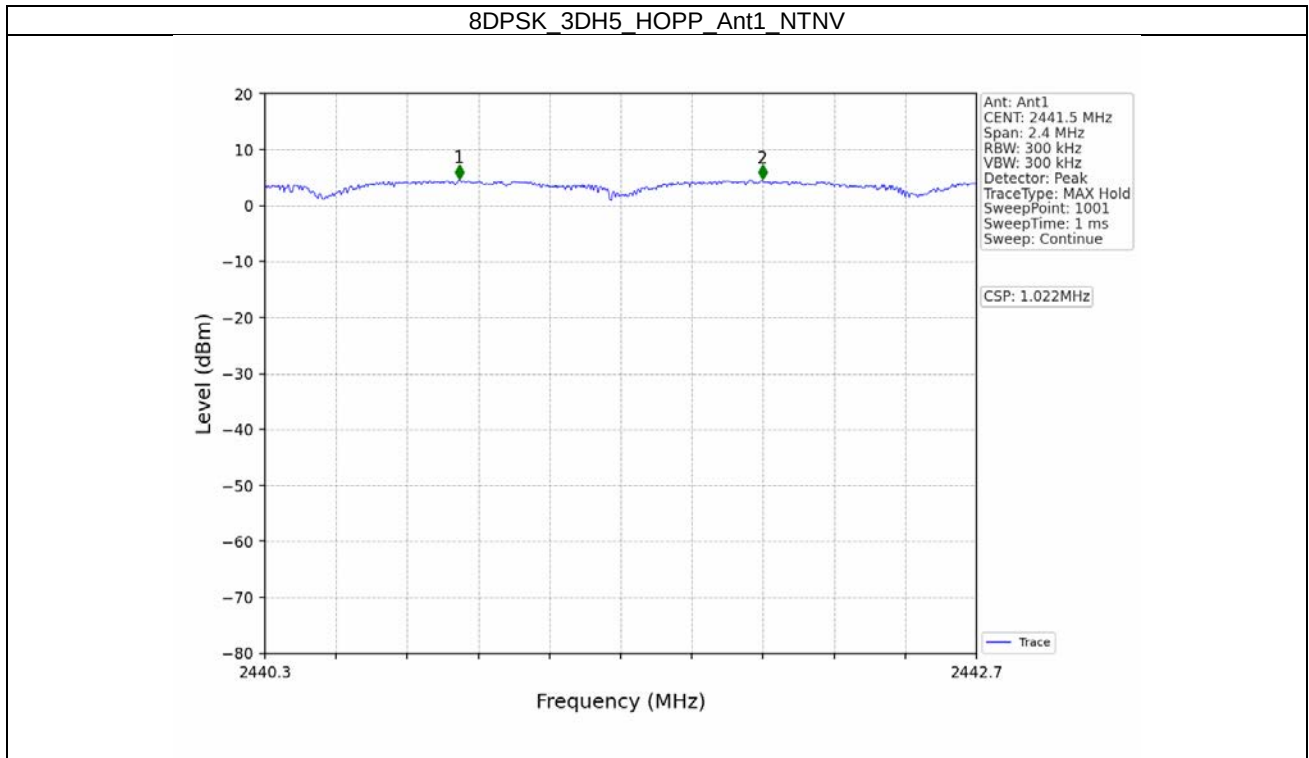
3.1 Ant1

3.1.1 Test Result

Ant1							
Mode	TX Type	Frequency (MHz)	Packet Type	Channel Separation (MHz)	20dB Bandwidth (MHz)	Limit (MHz)	Verdict
GFSK	SISO	HOPP	DH5	1.000	0.969	≥ 0.969	Pass
PSK/4DQPSK	SISO	HOPP	2DH5	1.001	1.348	≥ 0.899	Pass
8DPSK	SISO	HOPP	3DH5	1.022	1.329	≥ 0.886	Pass

3.1.2 Test Graph





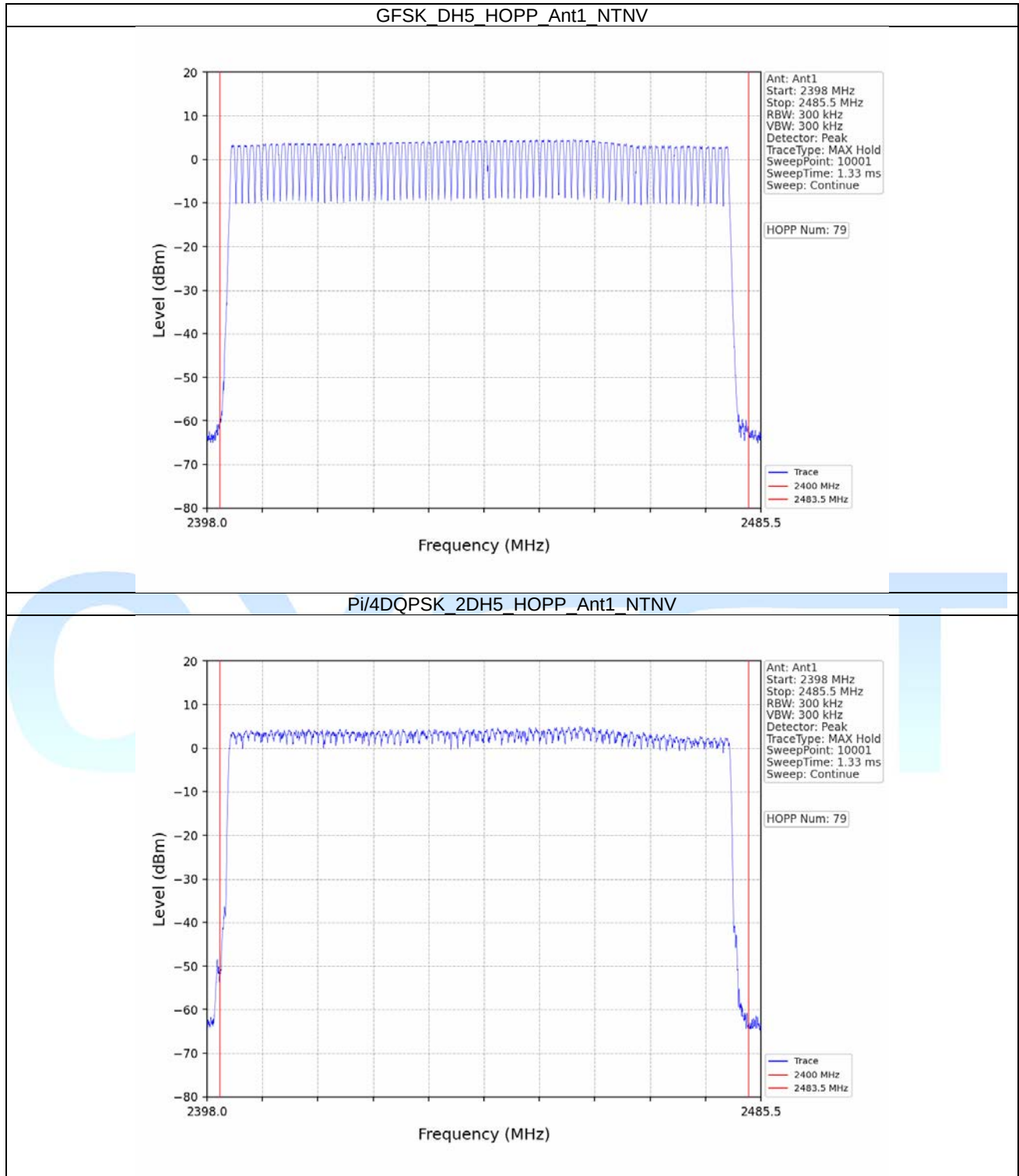
4. Number of Hopping Frequencies

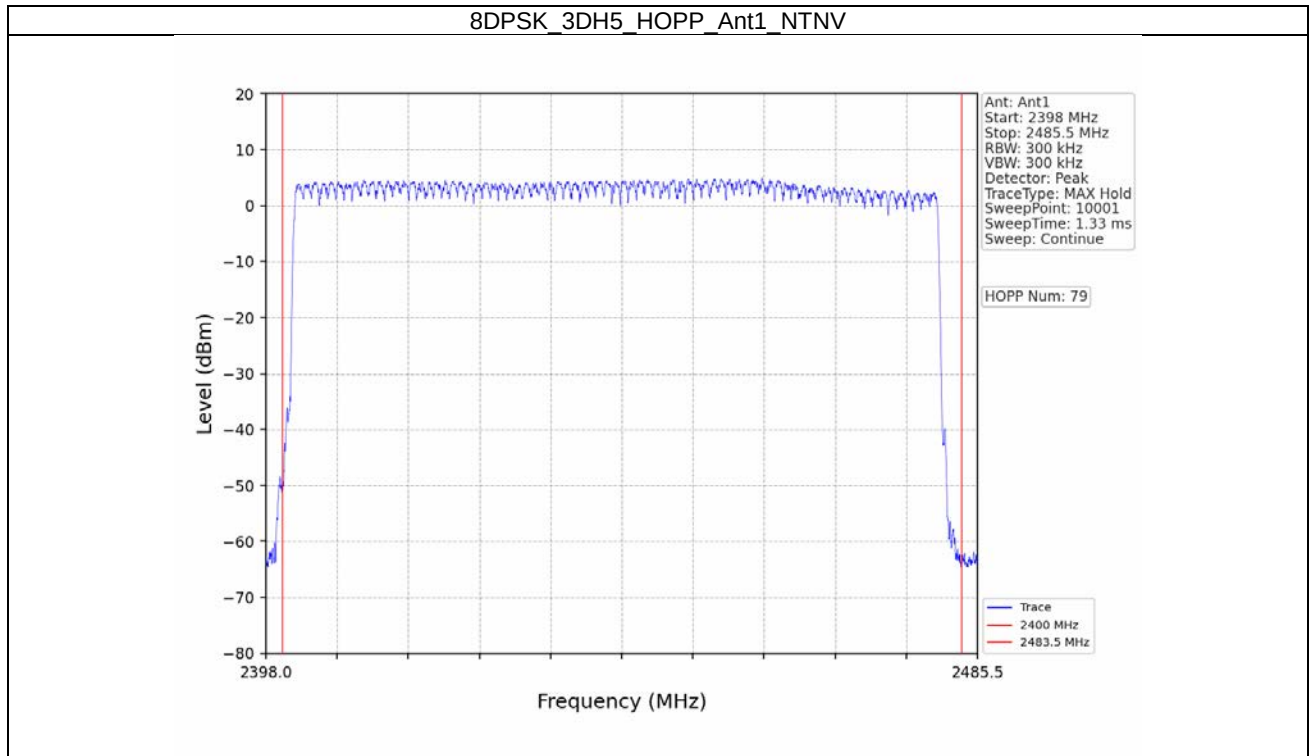
4.1 HoppNum

4.1.1 Test Result

Mode	TX Type	Frequency (MHz)	Packet Type	Num of Hopping Frequencies		Verdict
				ANT1	Limit	
GFSK	SISO	HOPP	DH5	79	≥ 15	Pass
Pi/4DQPSK	SISO	HOPP	2DH5	79	≥ 15	Pass
8DPSK	SISO	HOPP	3DH5	79	≥ 15	Pass

4.1.2 Test Graph





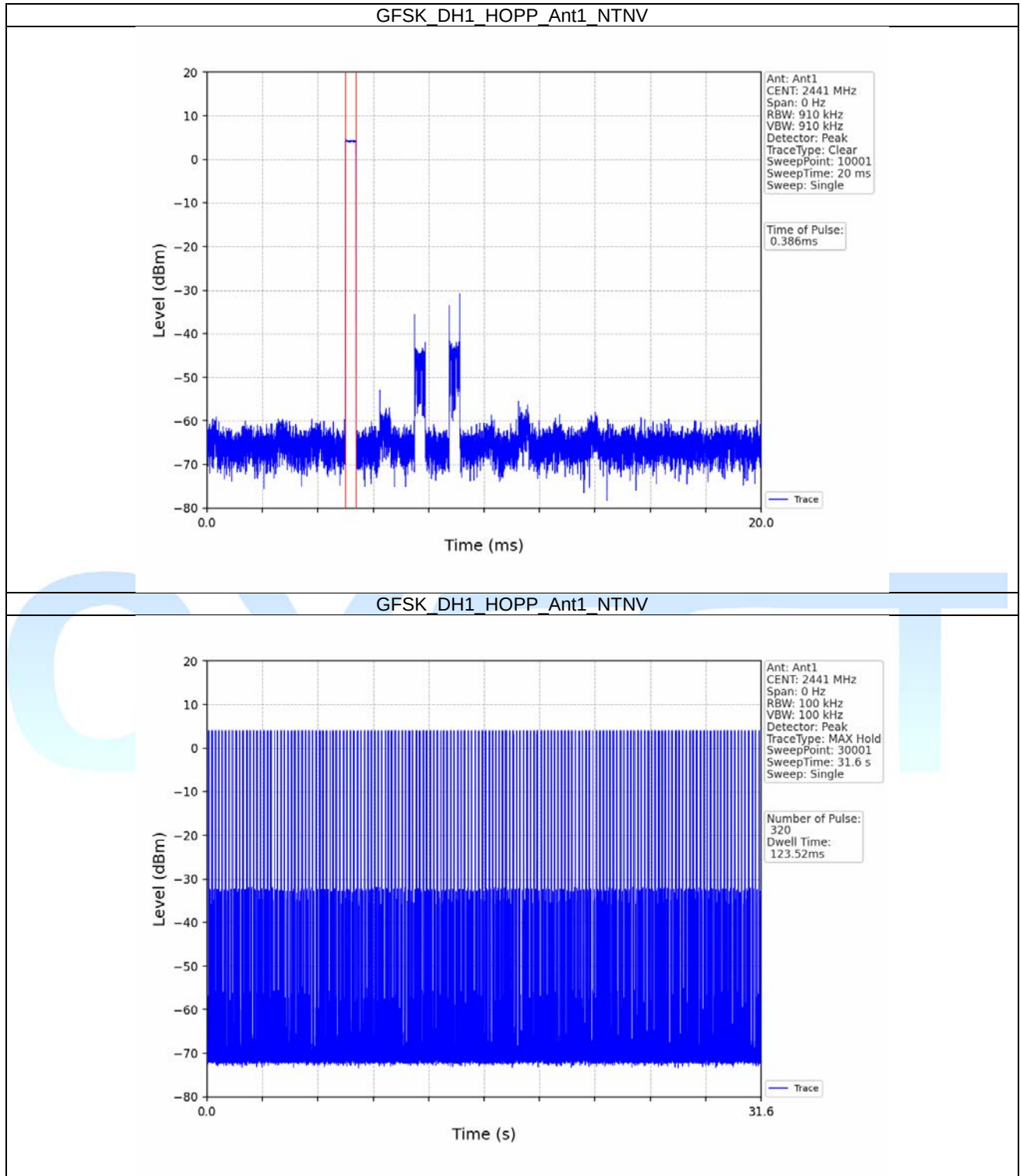
5. Time of Occupancy (Dwell Time)

5.1 Ant1

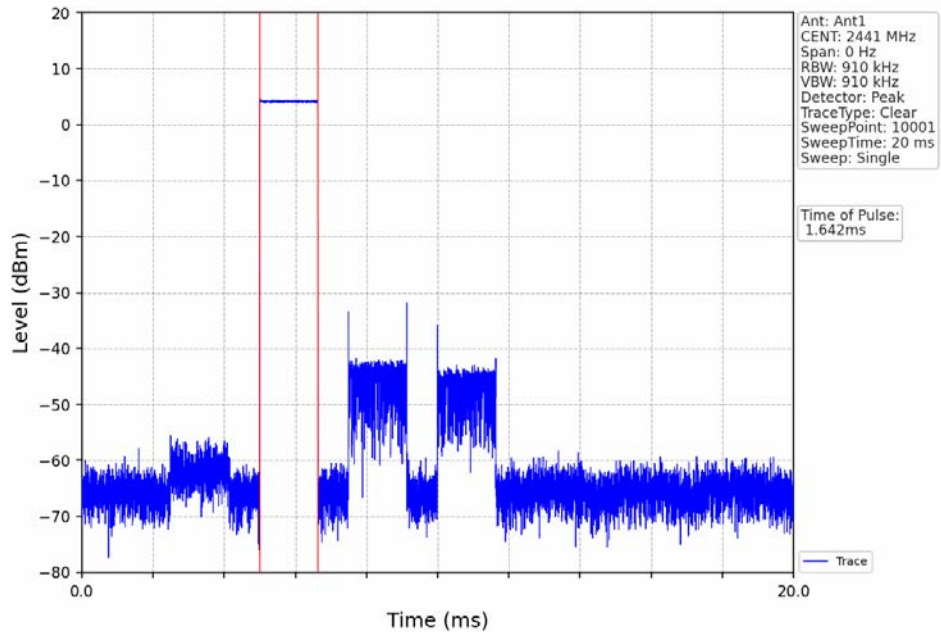
5.1.1 Test Result

Ant1									
Mode	TX Type	Frequency (MHz)	Packet Type	Duration of Single Pulse (ms)	Observation Period (s)	Num of Pulse in Observation Period	Dwell Time (ms)	Limit (ms)	Verdict
GFSK	SISO	HOPP	DH1	0.386	31.600	320	123.520	<=400	Pass
			DH3	1.642	31.600	164	269.288	<=400	Pass
			DH5	2.892	31.600	96	277.632	<=400	Pass
Pi/4DQPSK	SISO	HOPP	2DH1	0.392	31.600	320	125.440	<=400	Pass
			2DH3	1.644	31.600	161	264.684	<=400	Pass
			2DH5	2.896	31.600	93	269.328	<=400	Pass
8DPSK	SISO	HOPP	3DH1	0.392	31.600	320	125.440	<=400	Pass
			3DH3	1.648	31.600	150	247.200	<=400	Pass
			3DH5	2.894	31.600	104	300.976	<=400	Pass

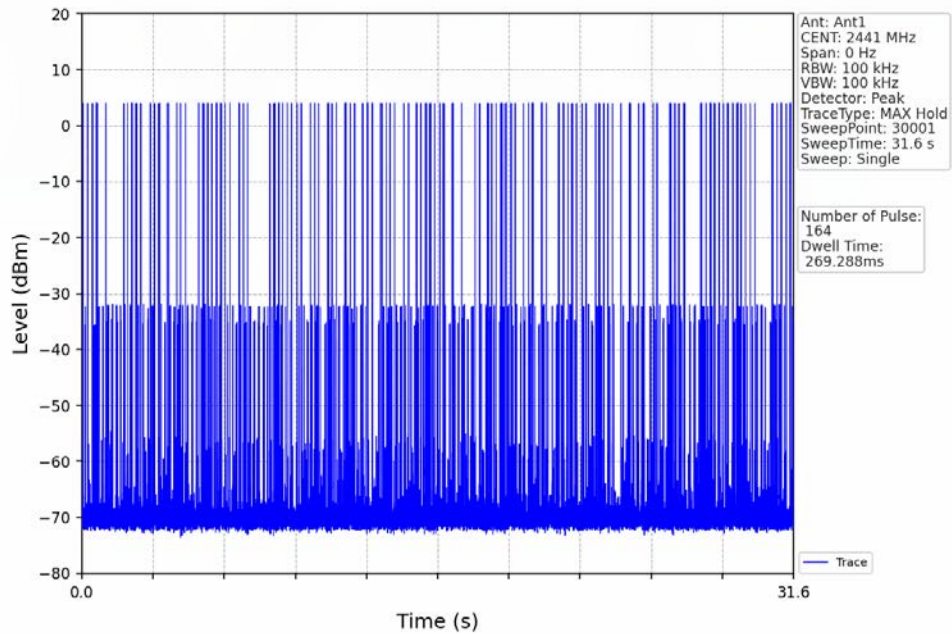
5.1.2 Test Graph

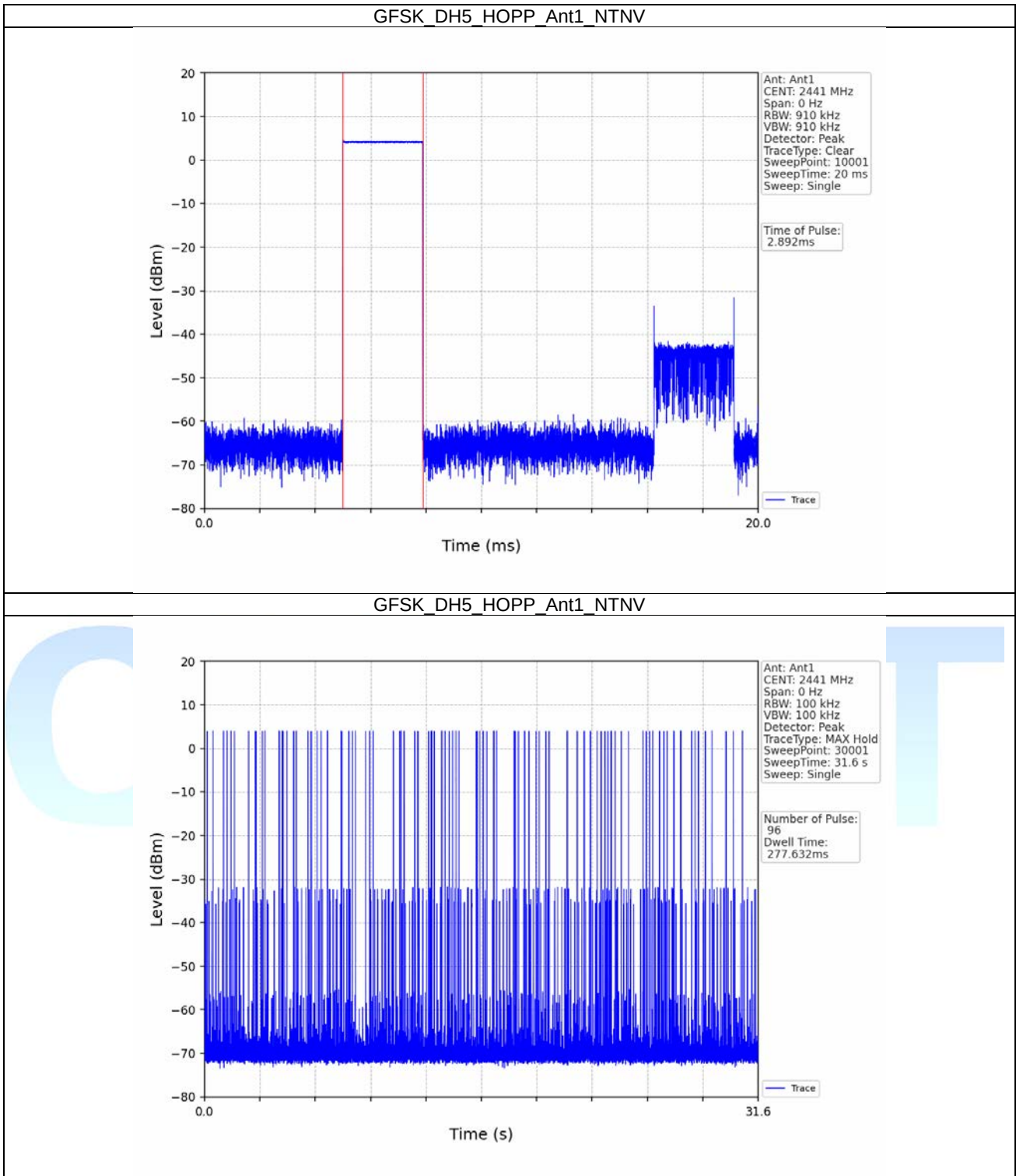


GFSK_DH3_HOPP_Ant1_NTNV

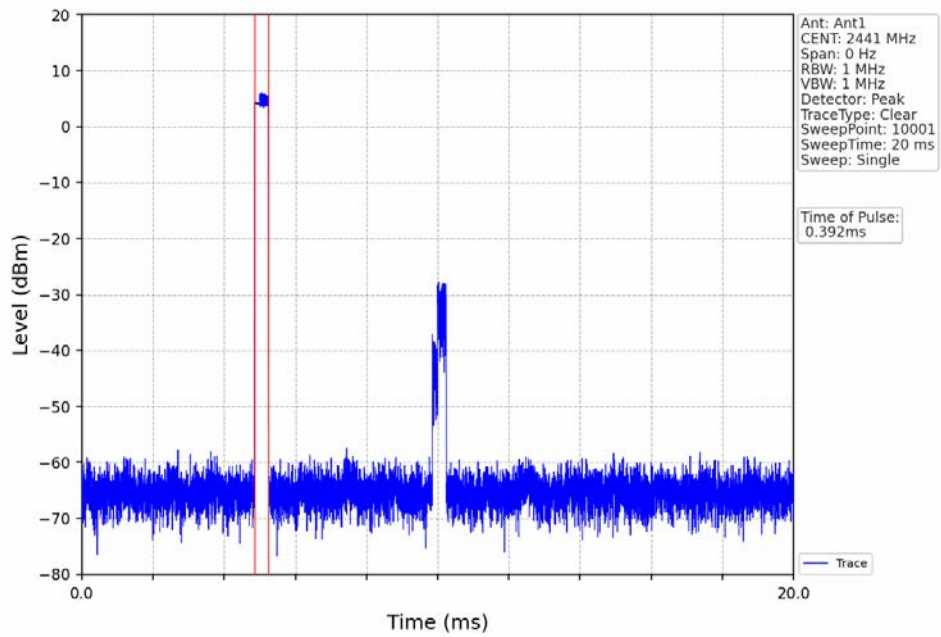


GFSK_DH3_HOPP_Ant1_NTNV

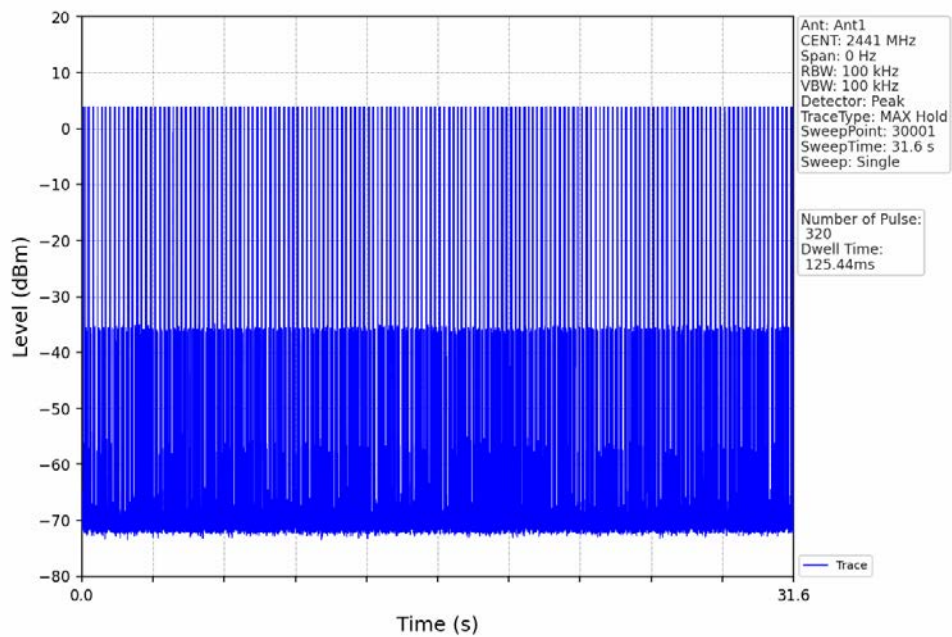




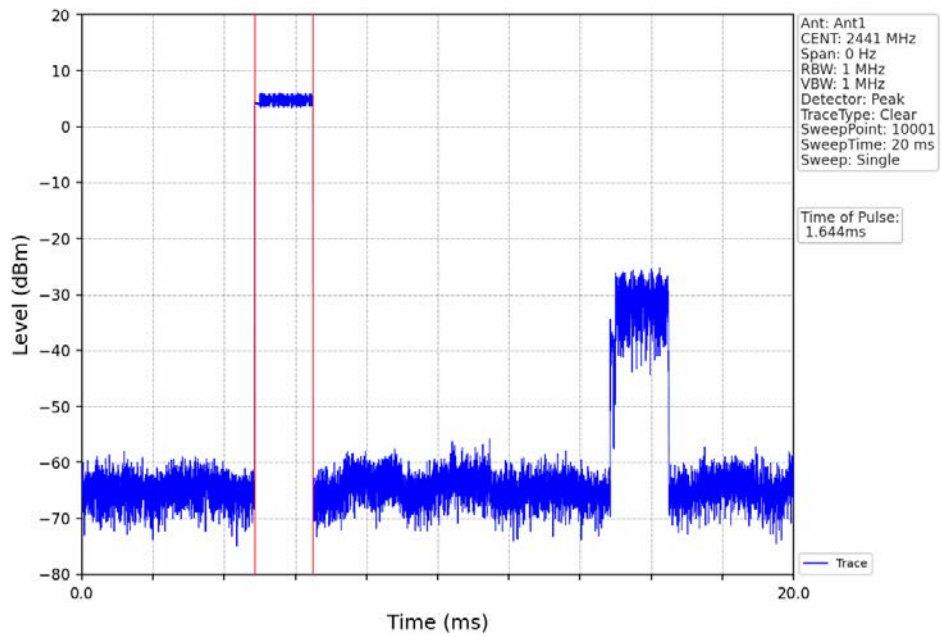
Pi/4DQPSK_2DH1_HOPP_Ant1_NTNV



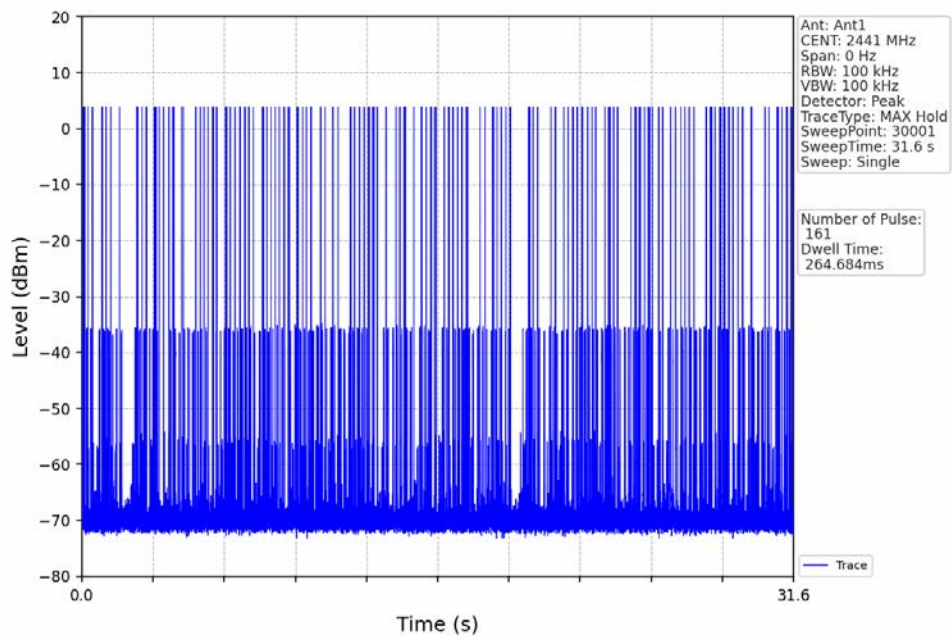
Pi/4DQPSK_2DH1_HOPP_Ant1_NTNV



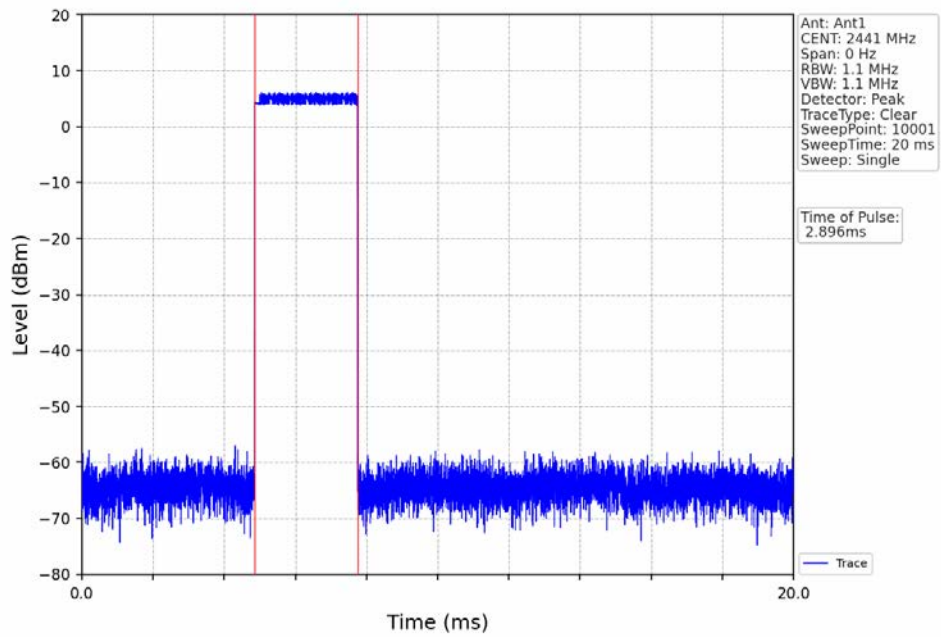
Pi/4DQPSK_2DH3_HOPP_Ant1_NTNV



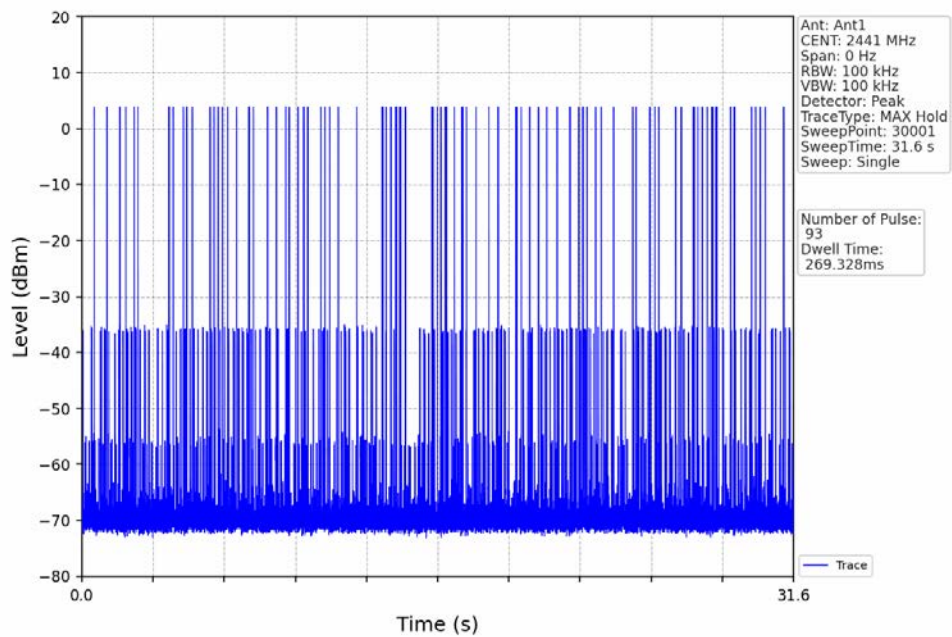
Pi/4DQPSK_2DH3_HOPP_Ant1_NTNV



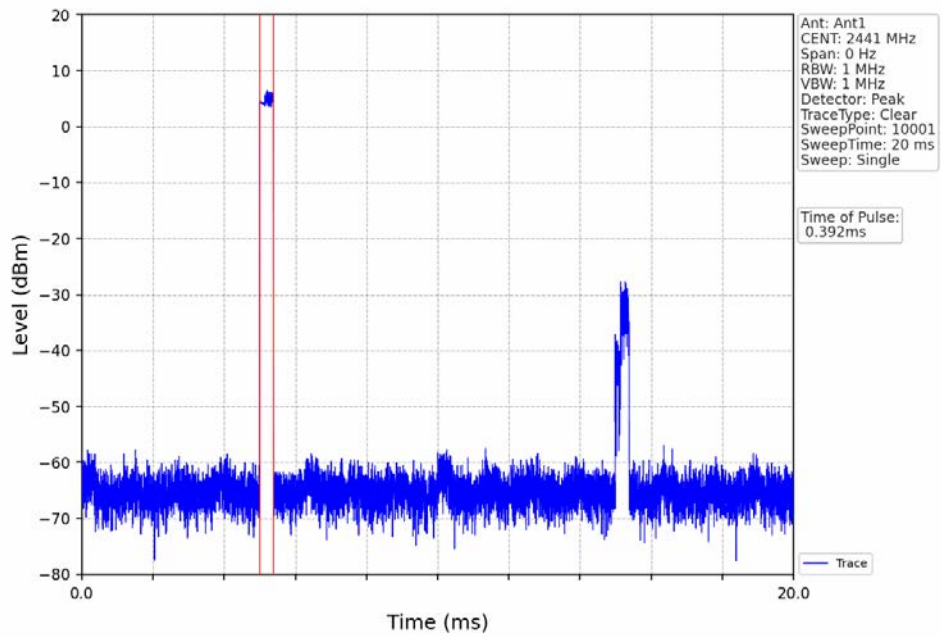
Pi/4DQPSK_2DH5_HOPP_Ant1_NTNV



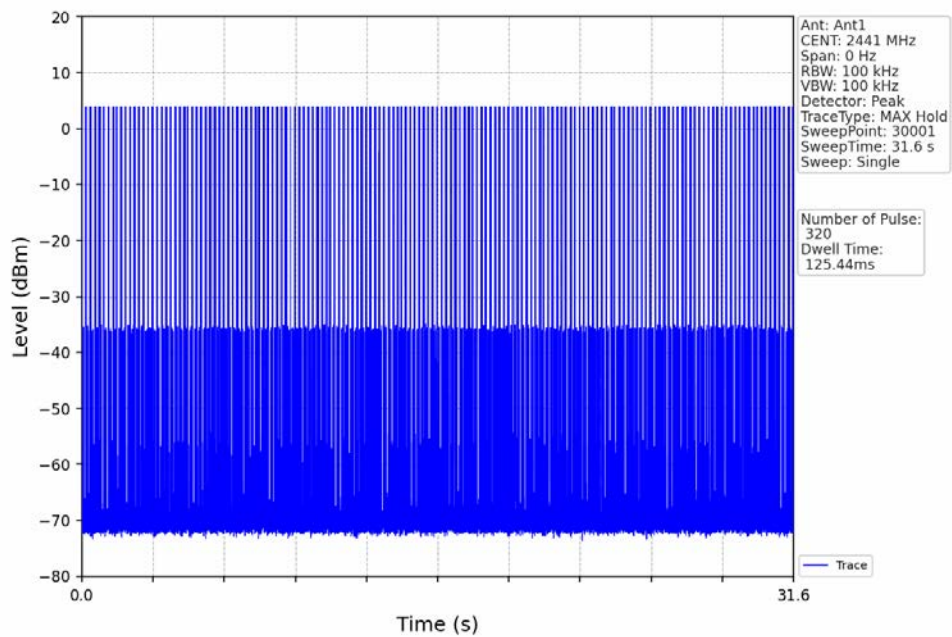
Pi/4DQPSK_2DH5_HOPP_Ant1_NTNV



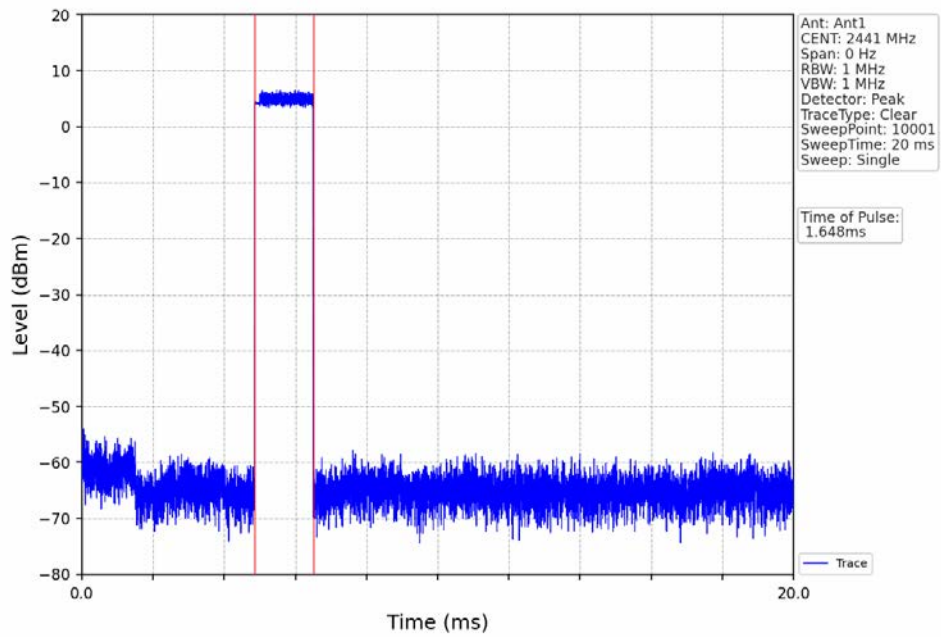
8DPSK_3DH1_HOPP_Ant1_NTNV



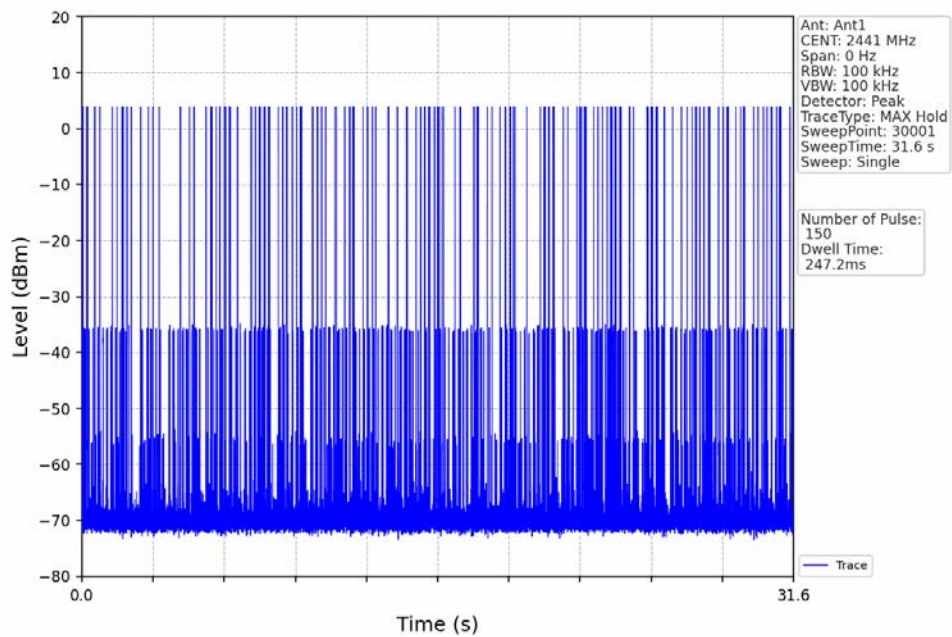
8DPSK_3DH1_HOPP_Ant1_NTNV



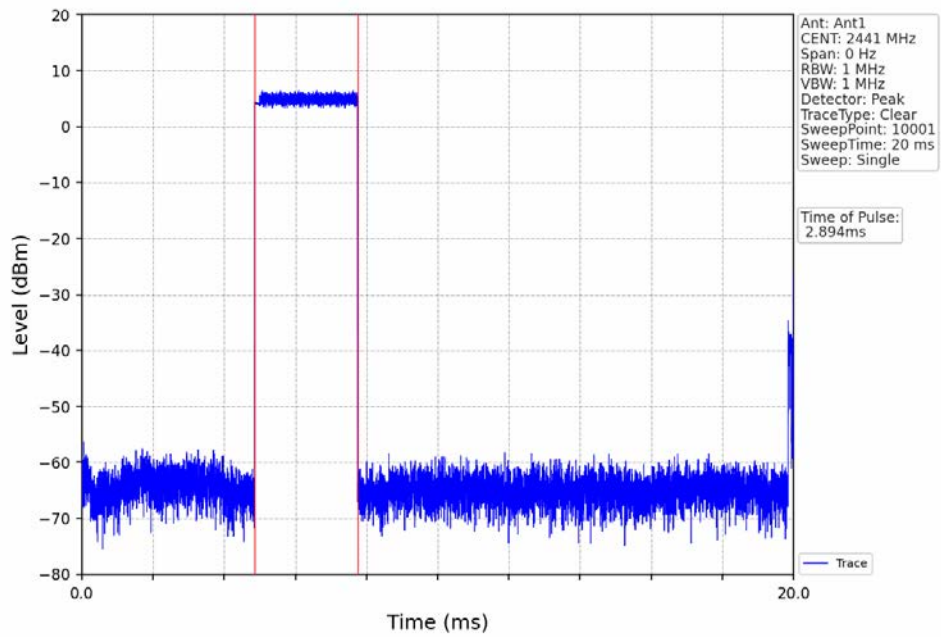
8DPSK_3DH3_HOPP_Ant1_NTNV



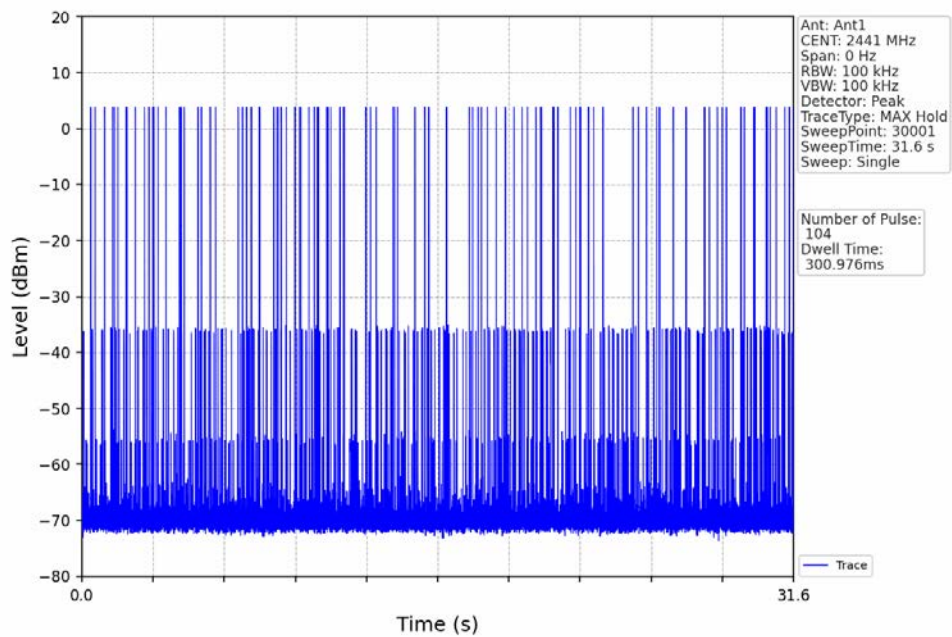
8DPSK_3DH3_HOPP_Ant1_NTNV



8DPSK_3DH5_HOPP_Ant1_NTNV



8DPSK_3DH5_HOPP_Ant1_NTNV



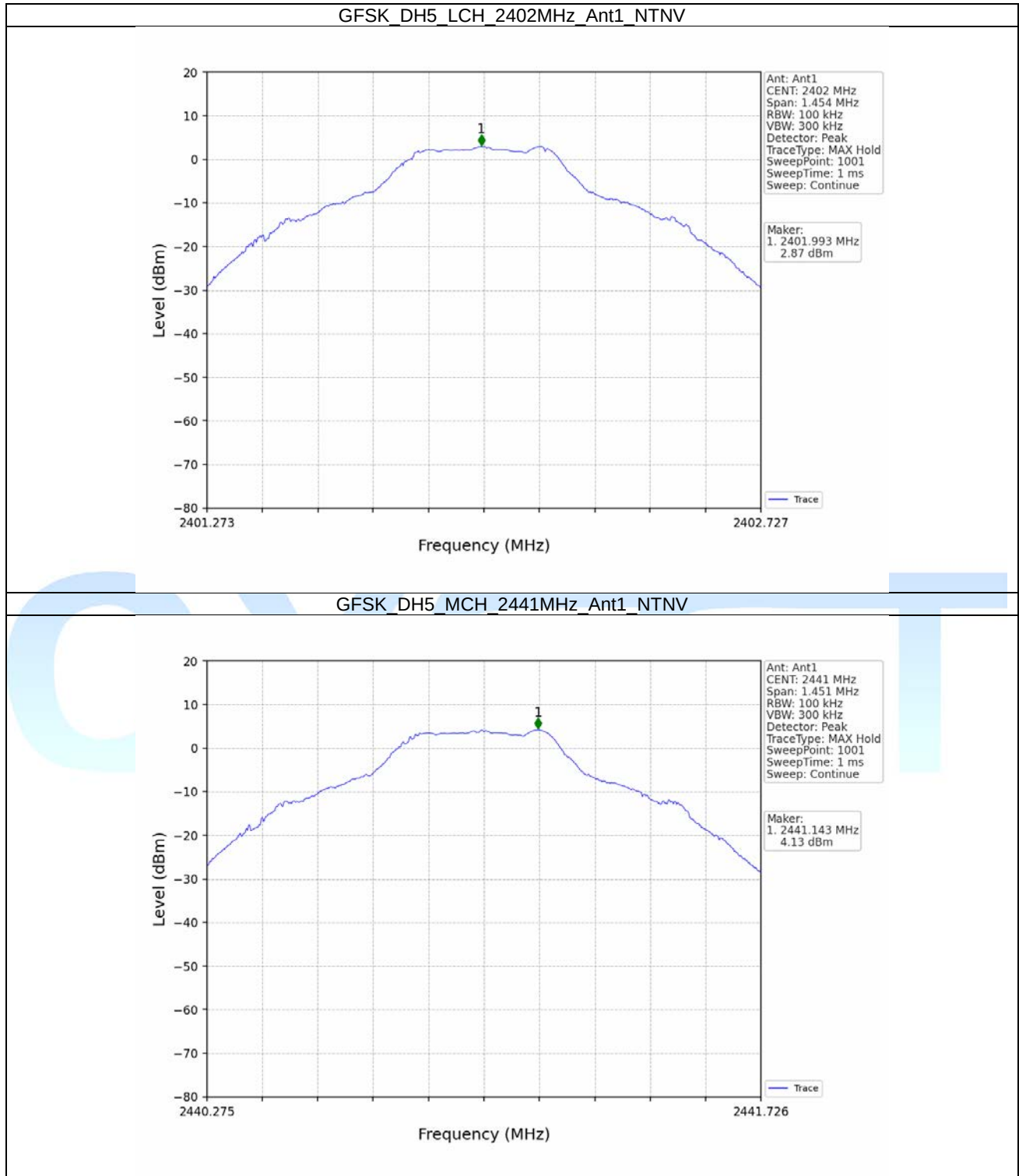
6. Unwanted Emissions In Non-restricted Frequency Bands

6.1 Ref

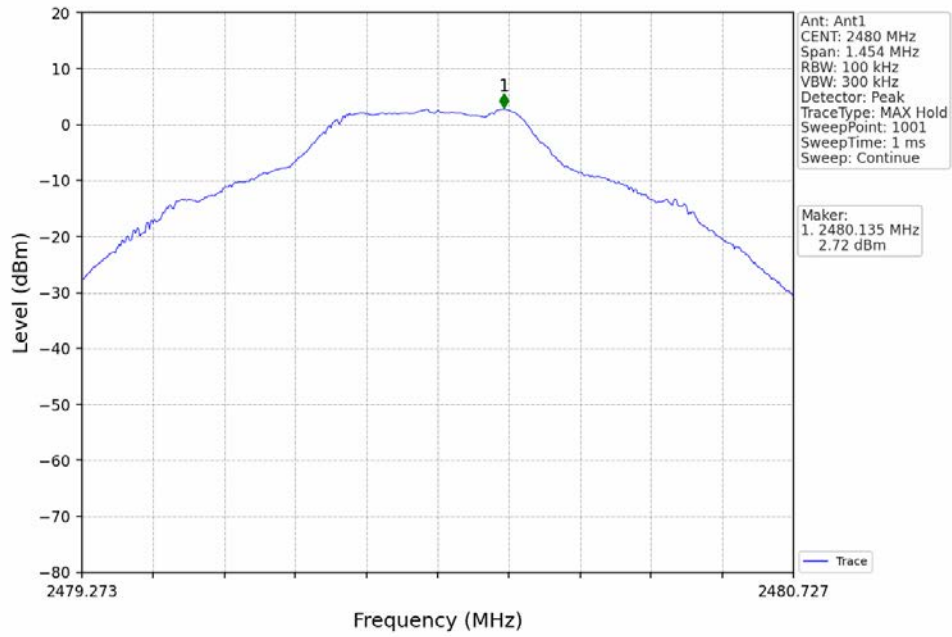
6.1.1 Test Result

Mode	TX Type	Frequency (MHz)	Packet Type	ANT	Level of Reference (dBm)
GFSK	SISO	2402	DH5	1	2.87
		2441	DH5	1	4.13
		2480	DH5	1	2.72
Pi/4DQPSK	SISO	2402	2DH5	1	-0.16
		2441	2DH5	1	0.59
		2480	2DH5	1	2.12
8DPSK	SISO	2402	3DH5	1	-0.06
		2441	3DH5	1	0.66
		2480	3DH5	1	1.98
Note1: Refer to FCC Part 15.247 (d) and ANSI C63.10-2013, the channel contains the maximum PSD level was used to establish the reference level.					

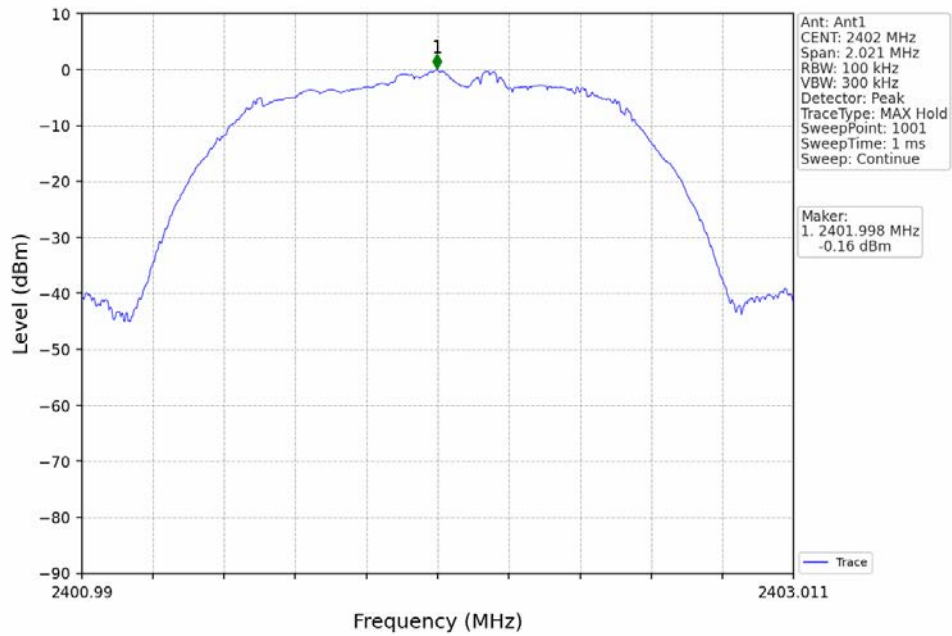
6.1.2 Test Graph



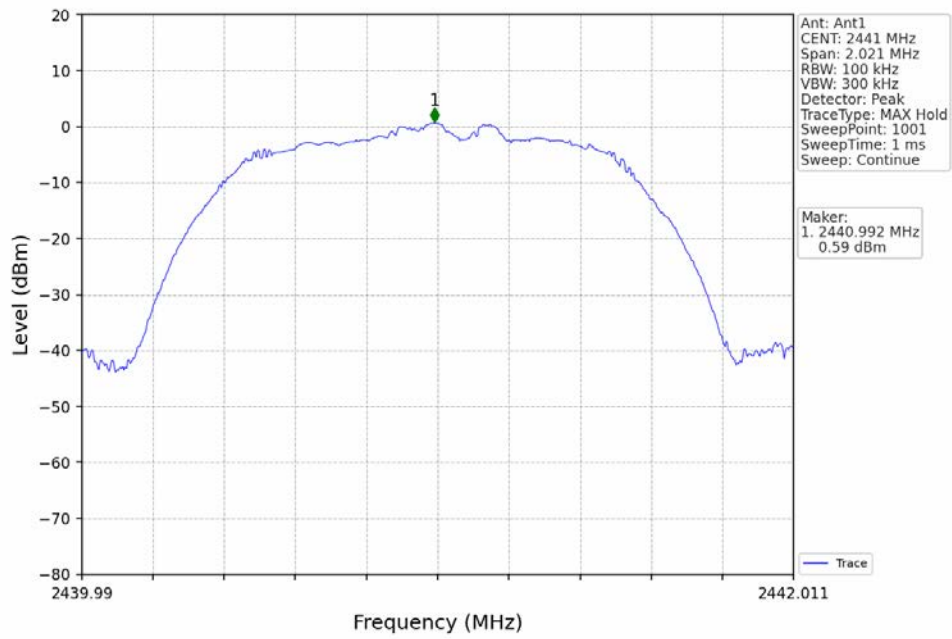
GFSK_DH5_HCH_2480MHz_Ant1_NTNV



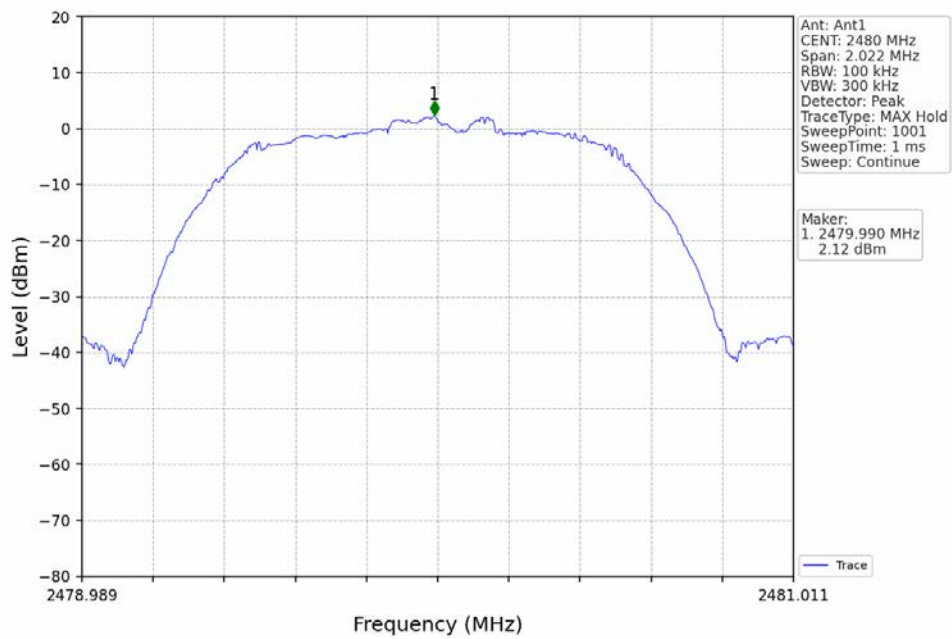
Pi/4DQPSK_2DH5_LCH_2402MHz_Ant1_NTNV



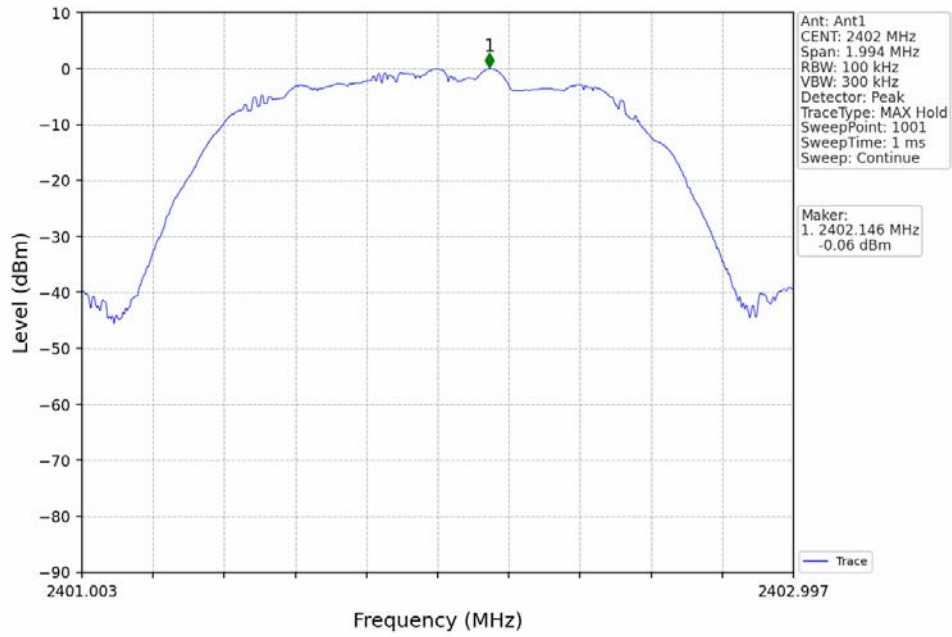
Pi/4DQPSK_2DH5_MCH_2441MHz_Ant1_NTNV



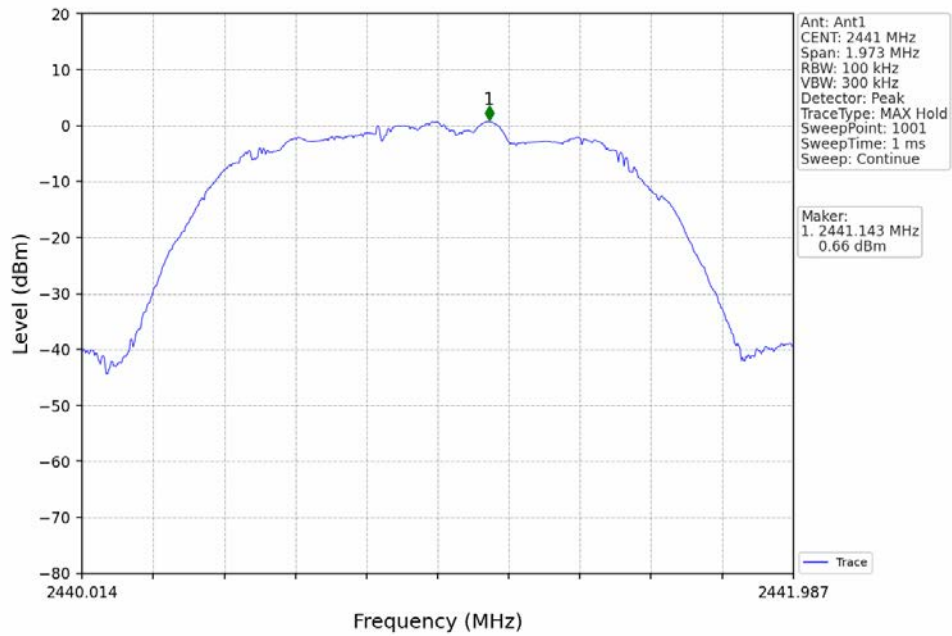
Pi/4DQPSK_2DH5_HCH_2480MHz_Ant1_NTNV

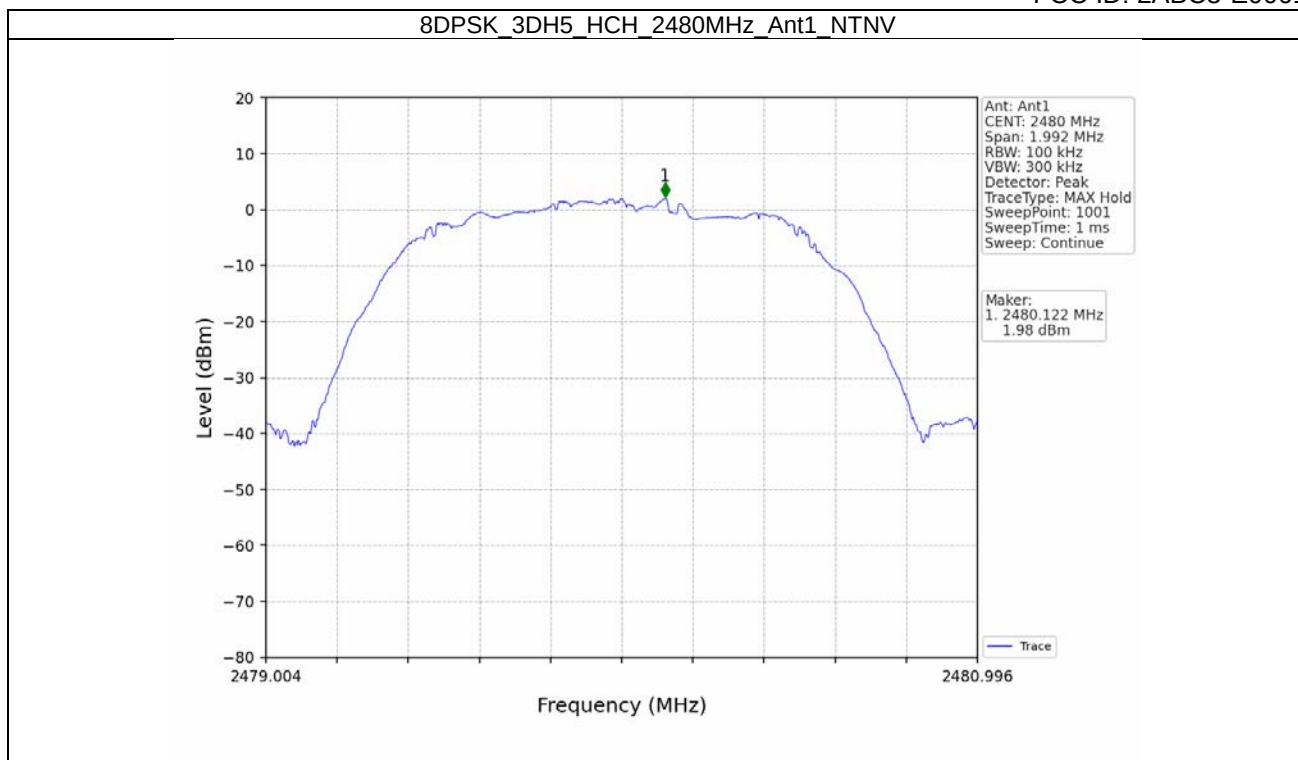


8DPSK_3DH5_LCH_2402MHz_Ant1_NTNV



8DPSK_3DH5_MCH_2441MHz_Ant1_NTNV





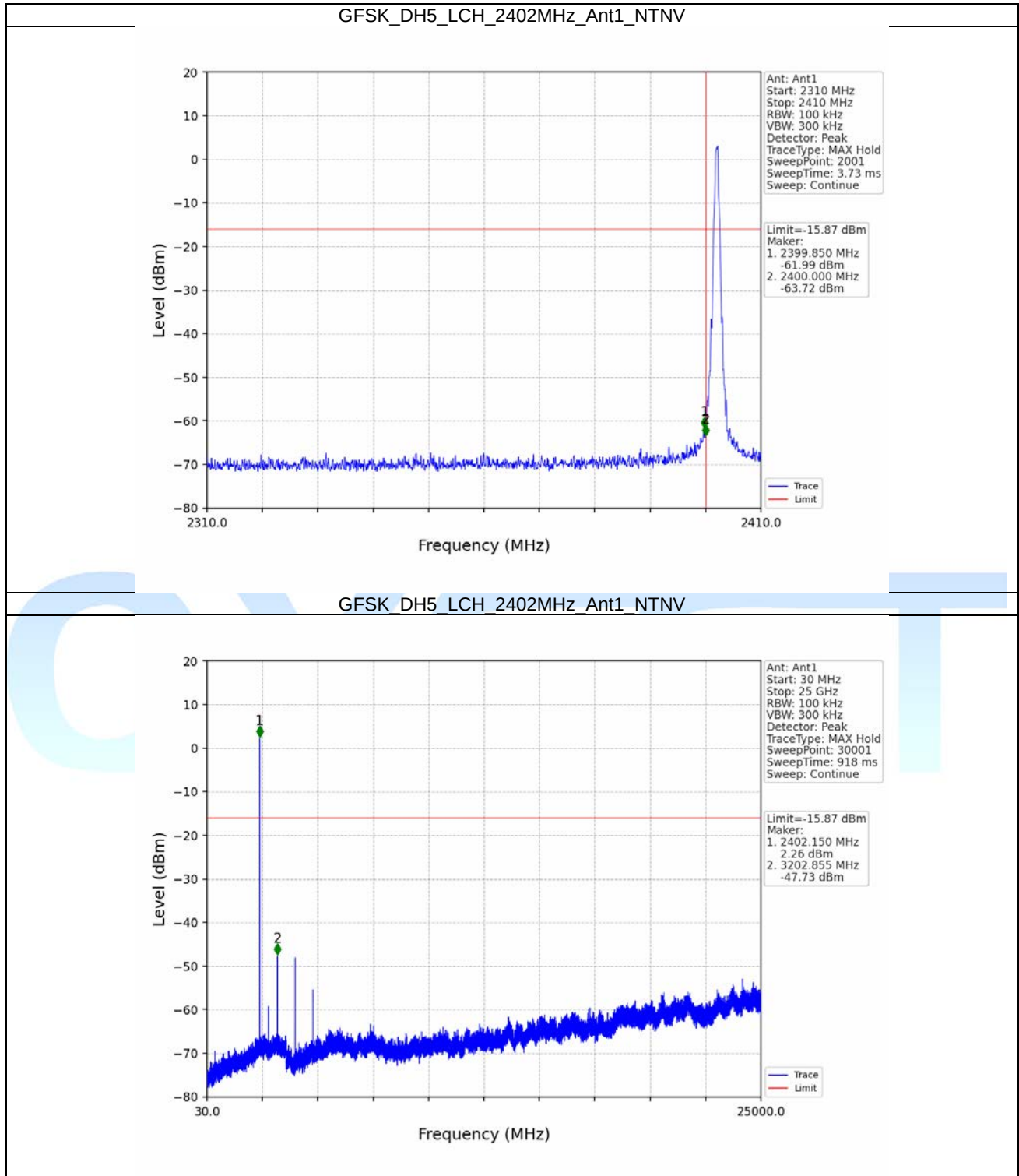
6.2 CSE

6.2.1 Test Result

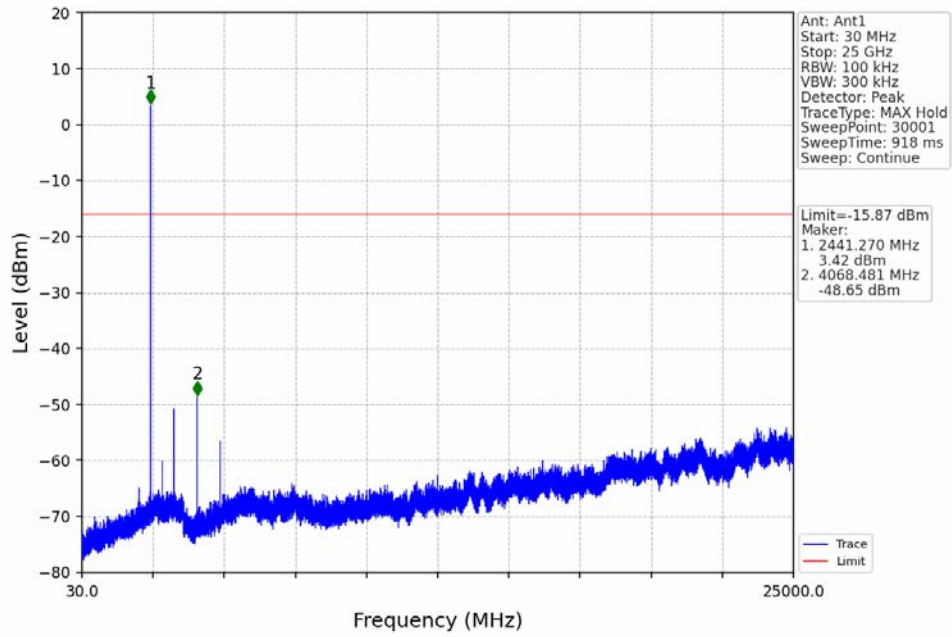
Mode	TX Type	Frequency (MHz)	Packet Type	ANT	Level of Reference (dBm)	Limit (dBm)	Verdict
GFSK	SISO	2402	DH5	1	4.13	-15.87	Pass
		2441	DH5	1	4.13	-15.87	Pass
		2480	DH5	1	4.13	-15.87	Pass
		HOPP	DH5	1	4.13	-15.87	Pass
					4.13	-15.87	Pass
Pi/4DQPSK	SISO	2402	2DH5	1	2.12	-17.88	Pass
		2441	2DH5	1	2.12	-17.88	Pass
		2480	2DH5	1	2.12	-17.88	Pass
		HOPP	2DH5	1	2.12	-17.88	Pass
					2.12	-17.88	Pass
8DPSK	SISO	2402	3DH5	1	1.98	-18.02	Pass
		2441	3DH5	1	1.98	-18.02	Pass
		2480	3DH5	1	1.98	-18.02	Pass
		HOPP	3DH5	1	1.98	-18.02	Pass
					1.98	-18.02	Pass

Note1: Refer to FCC Part 15.247 (d) and ANSI C63.10-2013, the channel contains the maximum PSD level was used to establish the reference level.

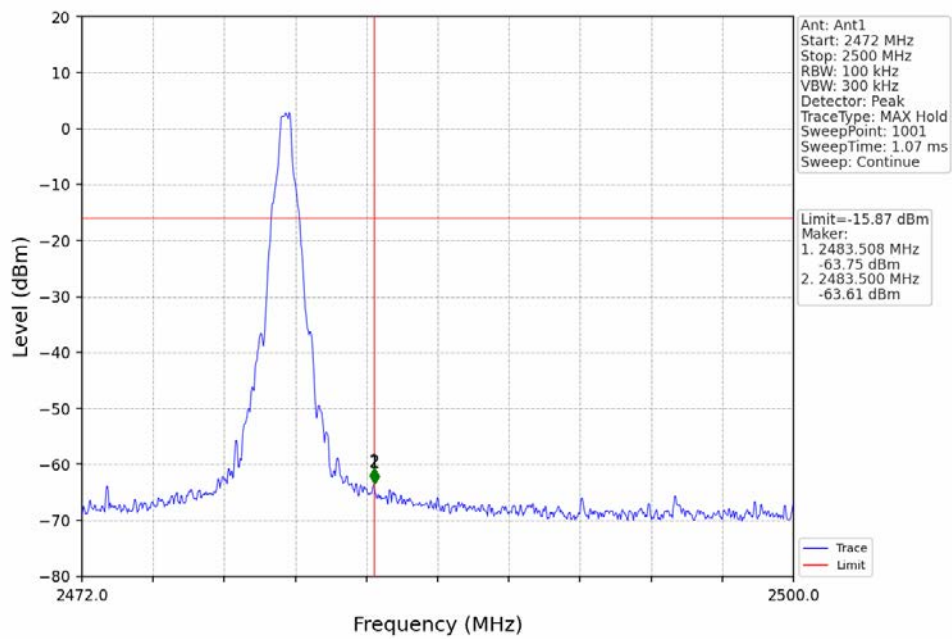
6.2.2 Test Graph



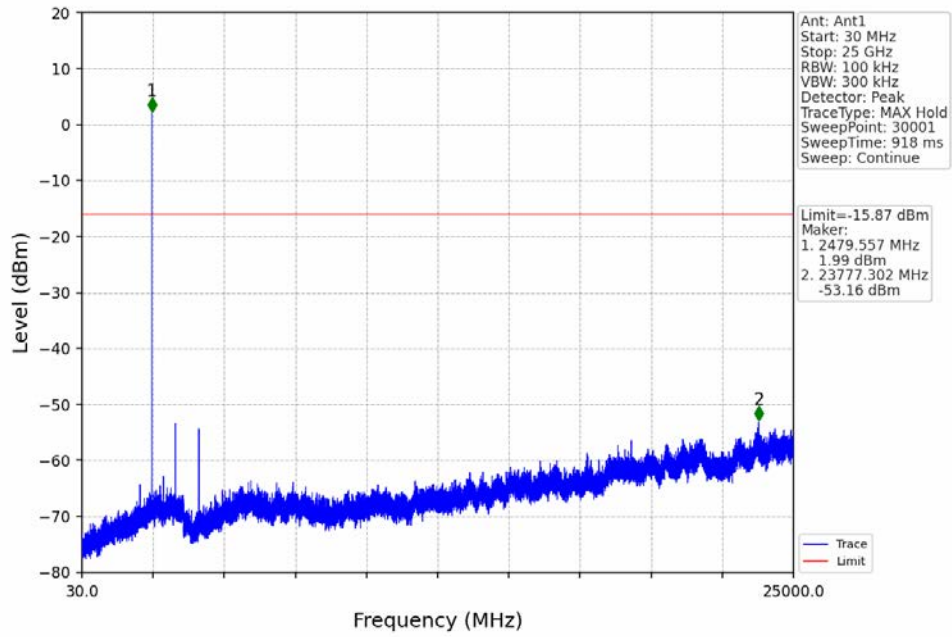
GFSK_DH5_MCH_2441MHz_Ant1_NTNV



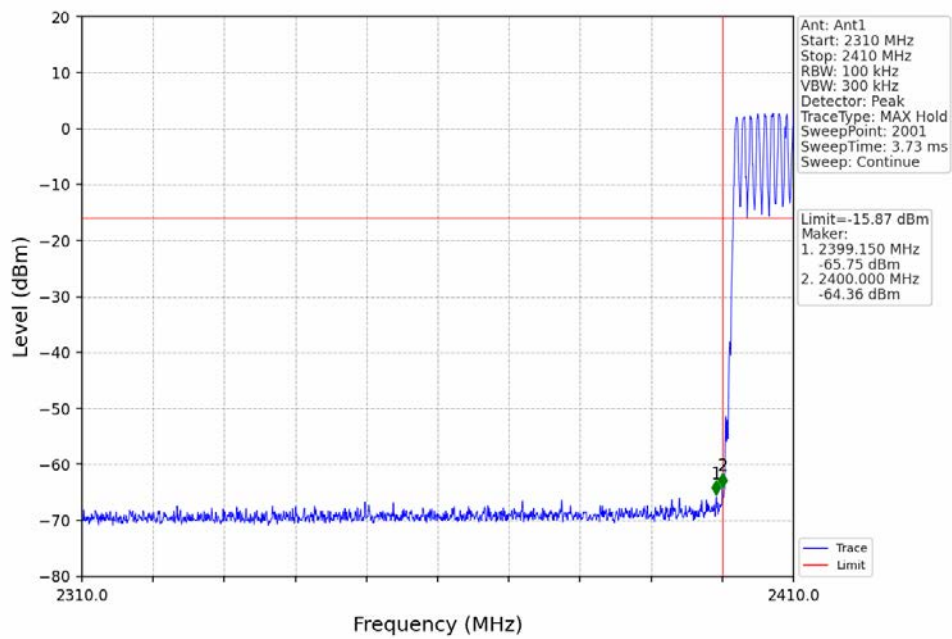
GFSK_DH5_HCH_2480MHz_Ant1_NTNV

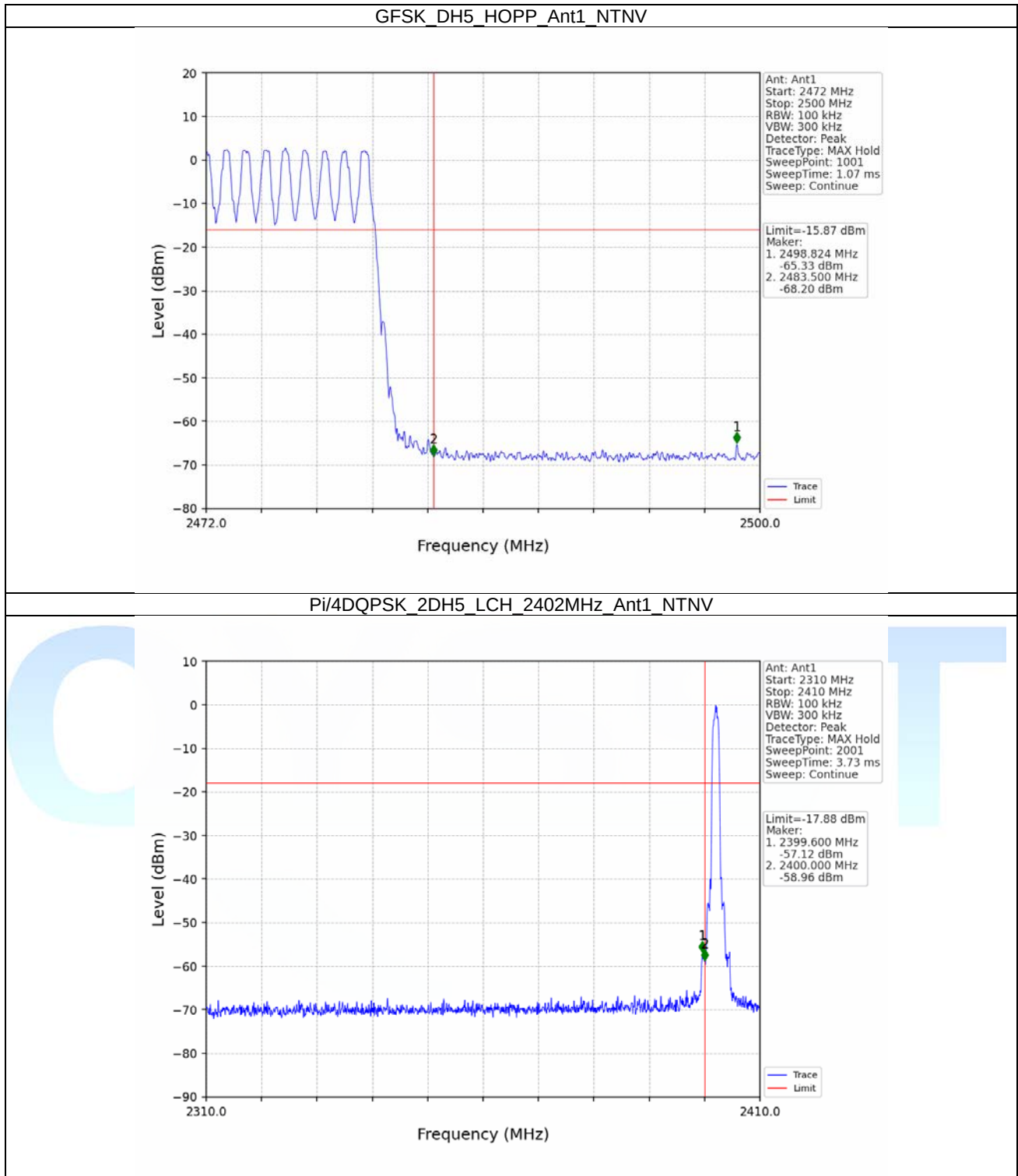


GFSK_DH5_HCH_2480MHz_Ant1_NTNV

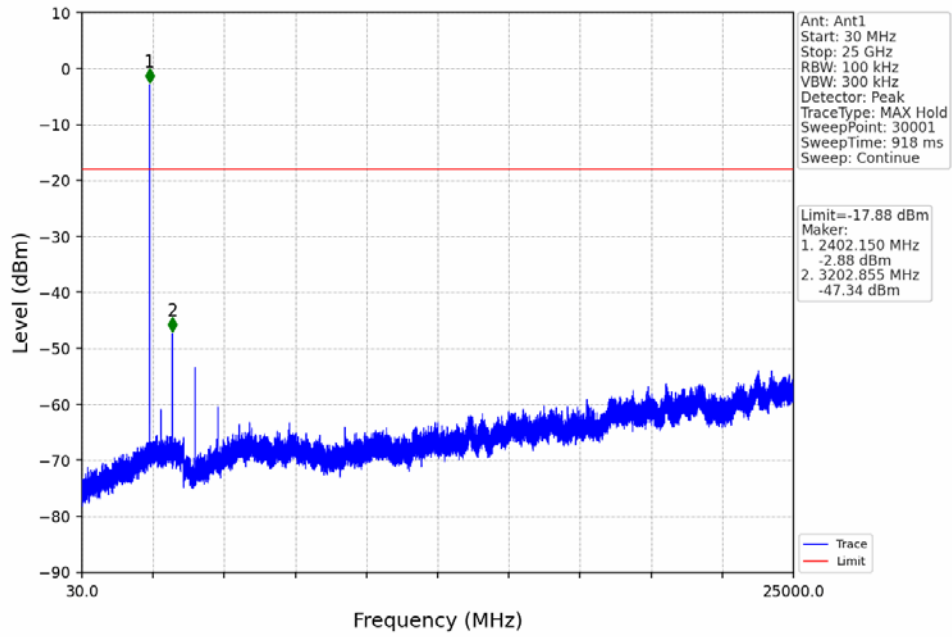


GFSK_DH5_HOPP_Ant1_NTNV

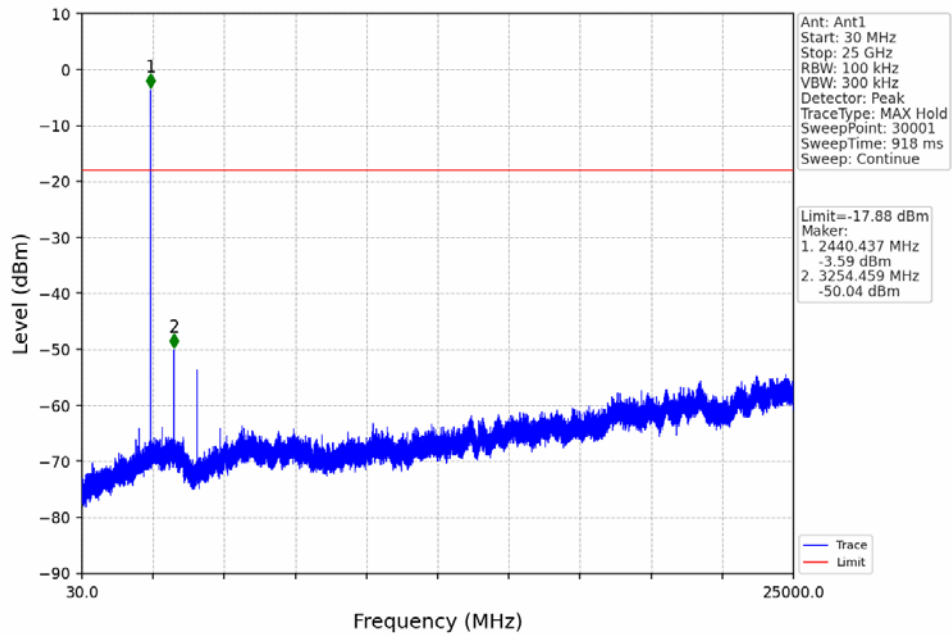




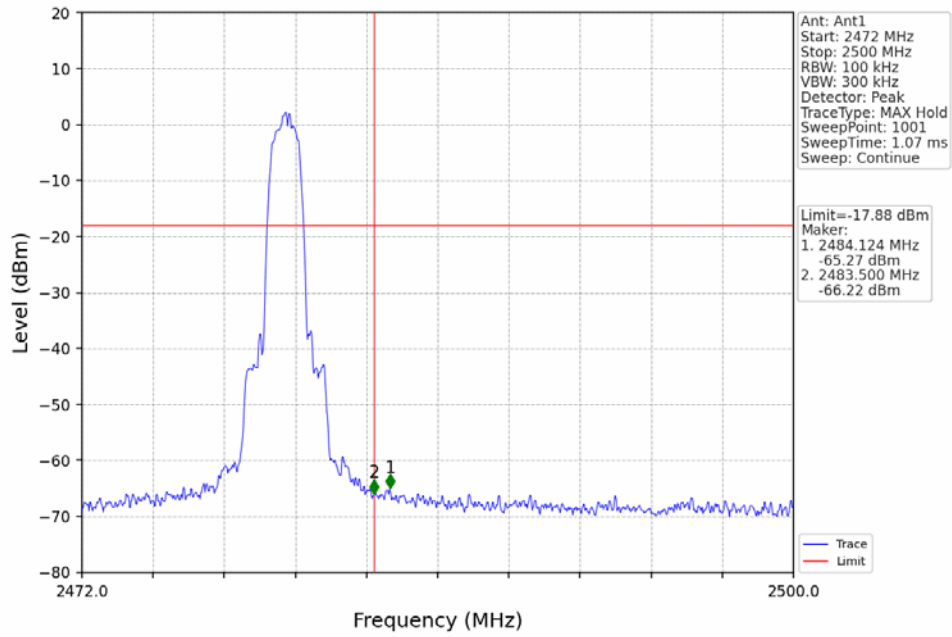
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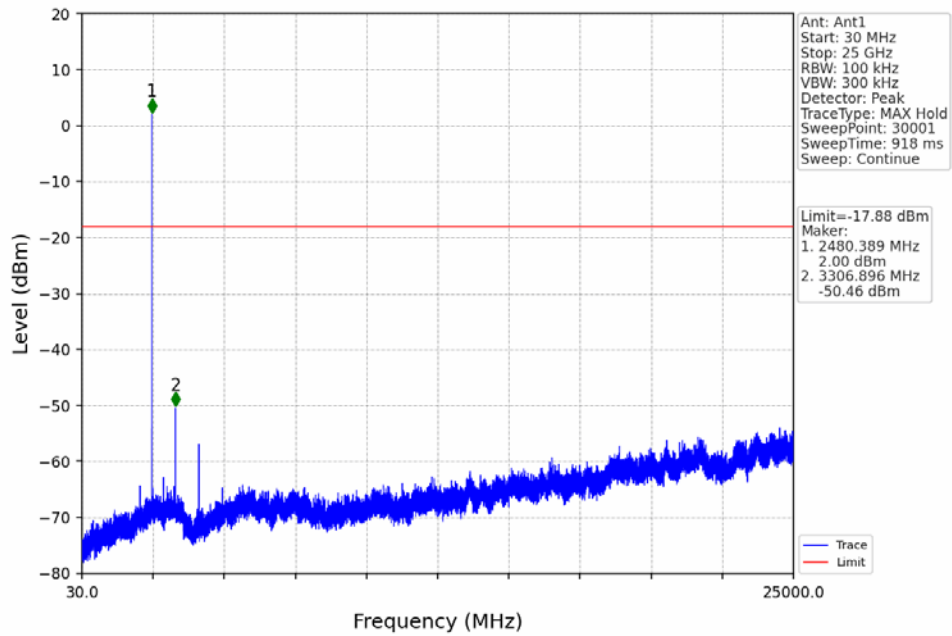
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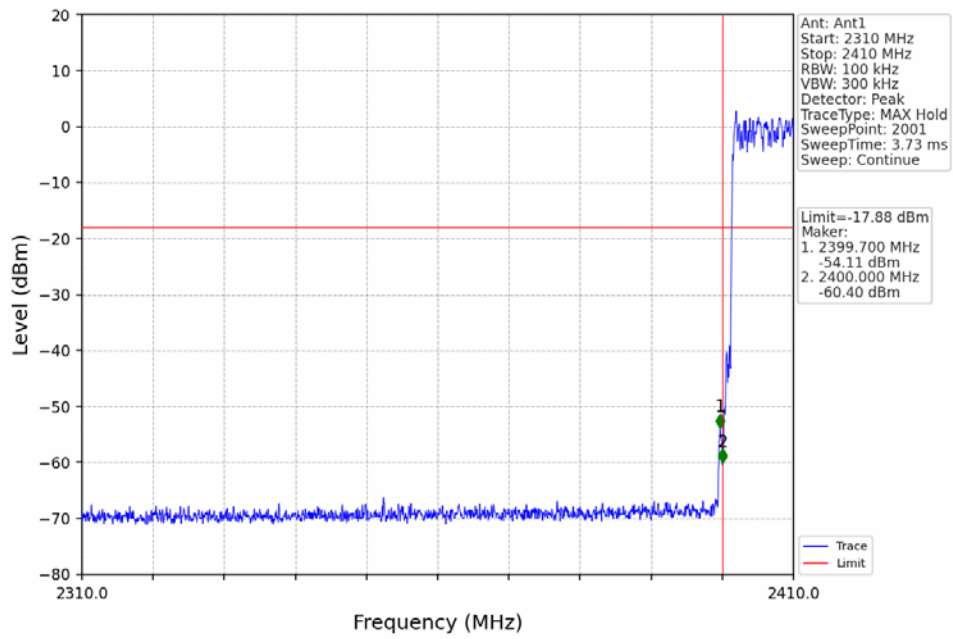
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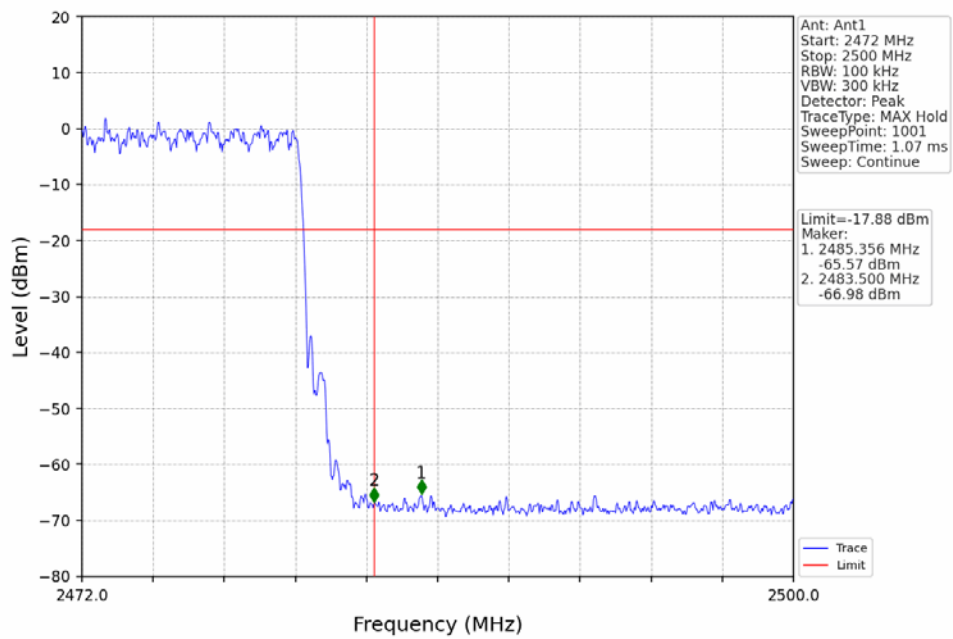
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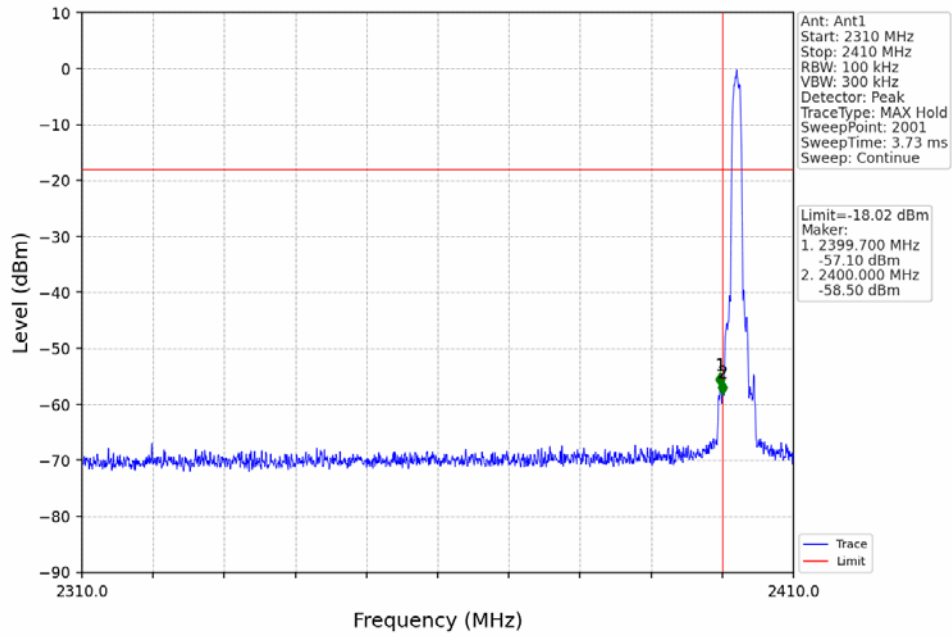
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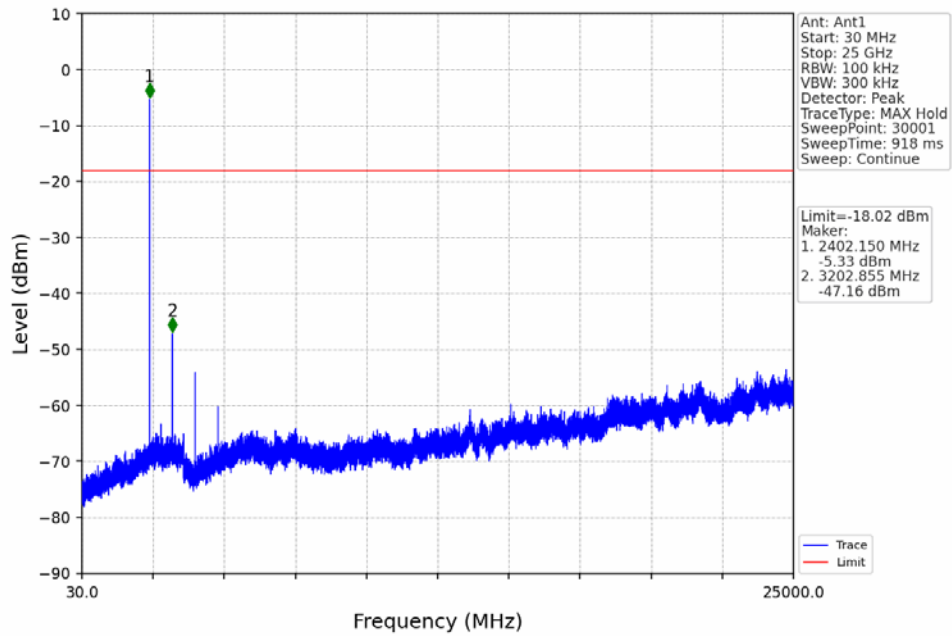
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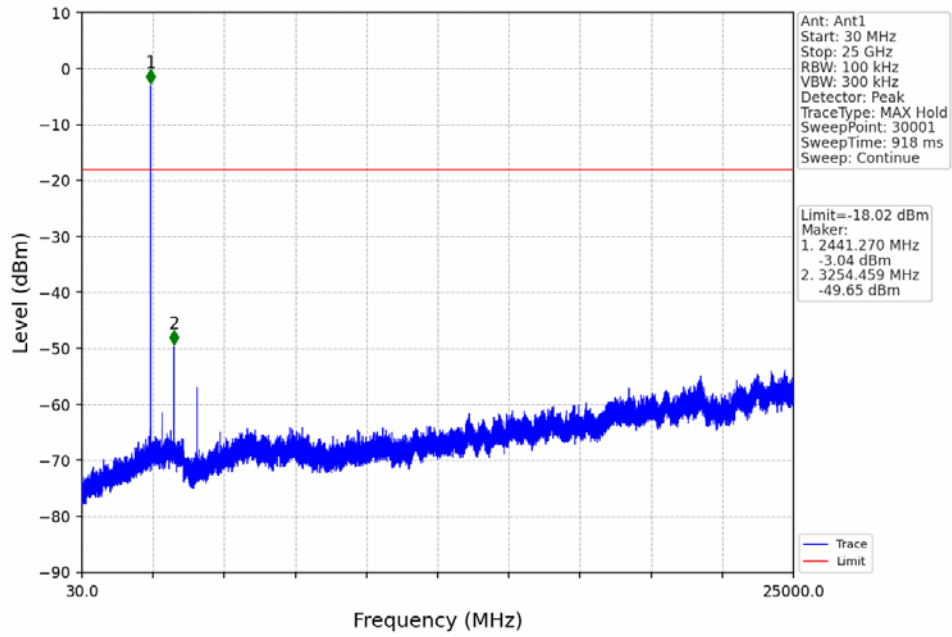
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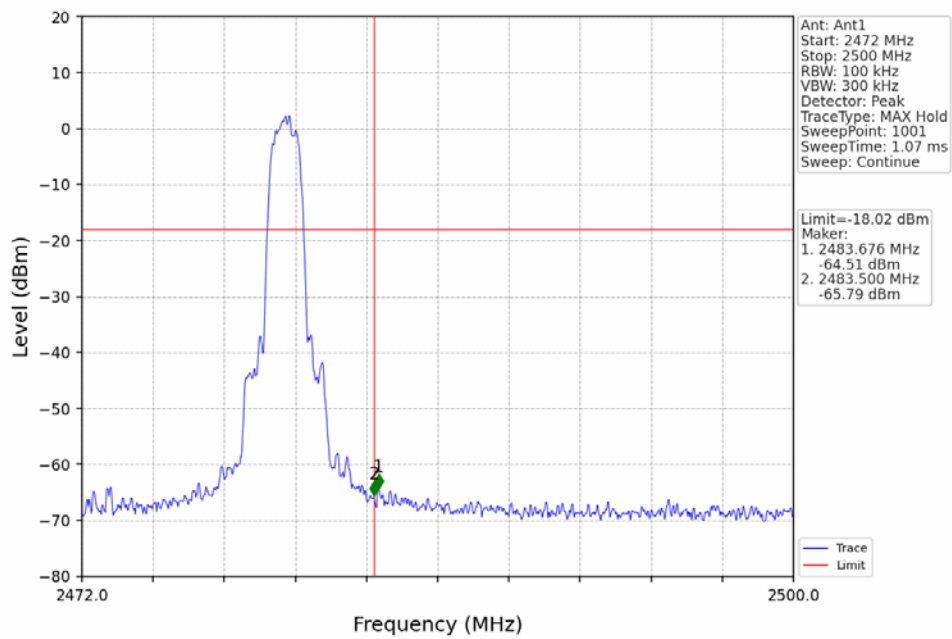
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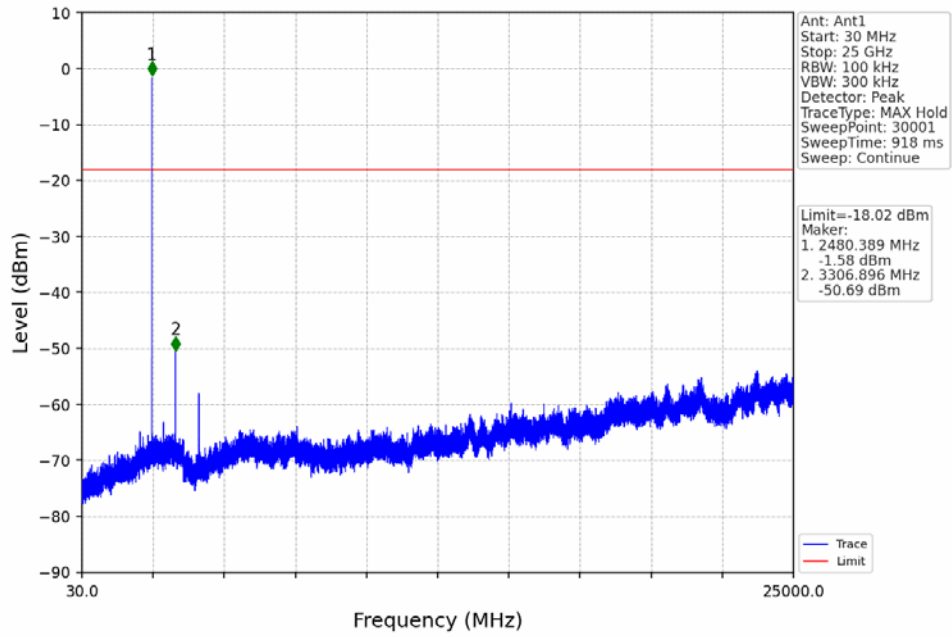
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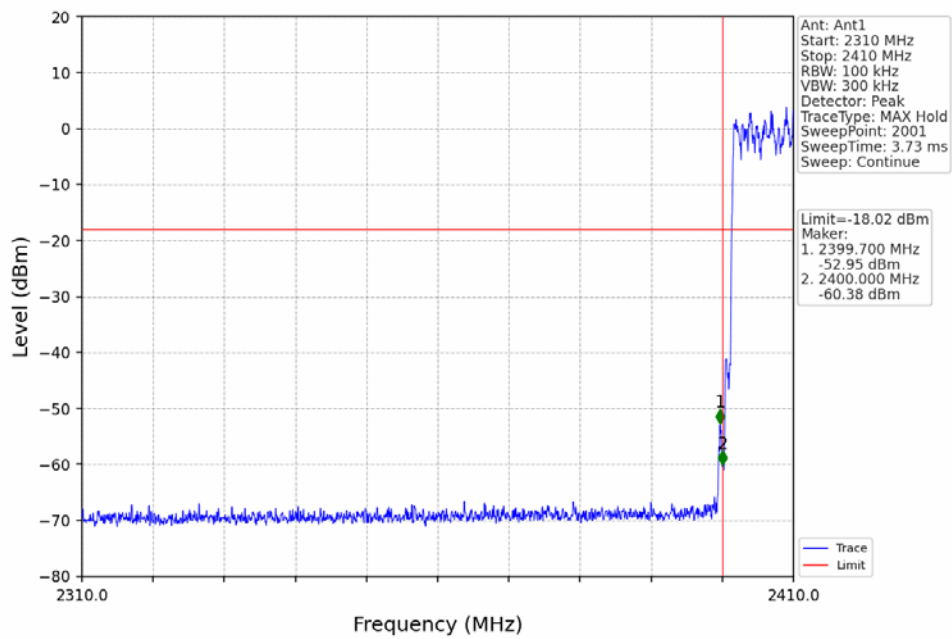
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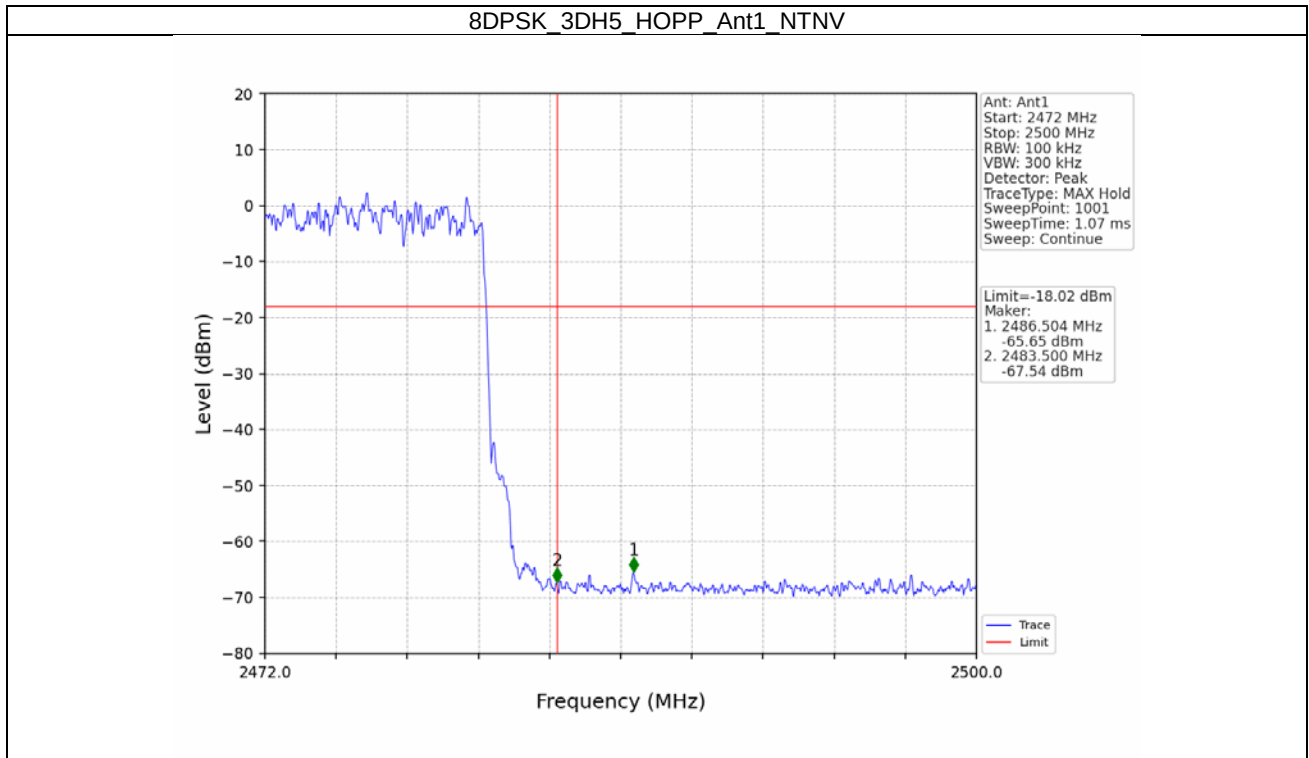


8DPSK_3DH5_HCH_2480MHz_Ant1_NTNV



8DPSK_3DH5_HOPP_Ant1_NTNV





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