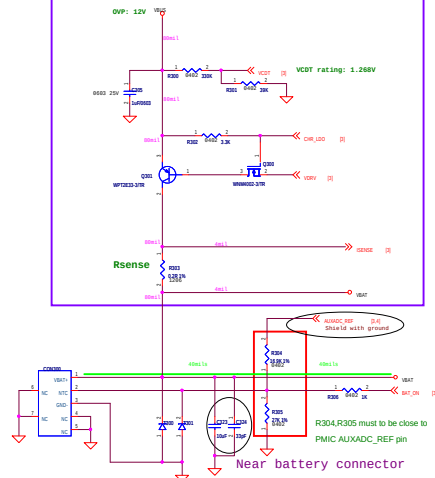


Charger



Add Zener Diode
Place on the path
from VBAT to IC
(Battery connector
or test point or IO
connector)

500mV

Between IC and IO port

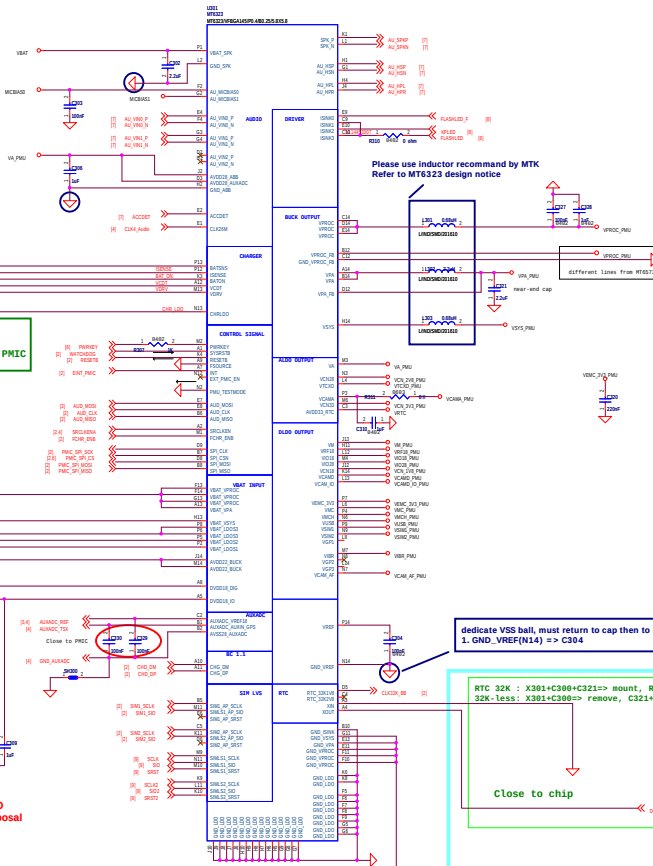
Refer to MT6323 design
notice for Zener selection

Refer to MT6323 design notice
for Buck GND layout rule

ISENSE/BSTNS 4mV
differential to SENSE

Close to PMIC

refer to system analog LDO
performance improve proposal

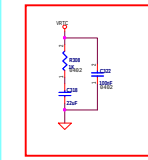


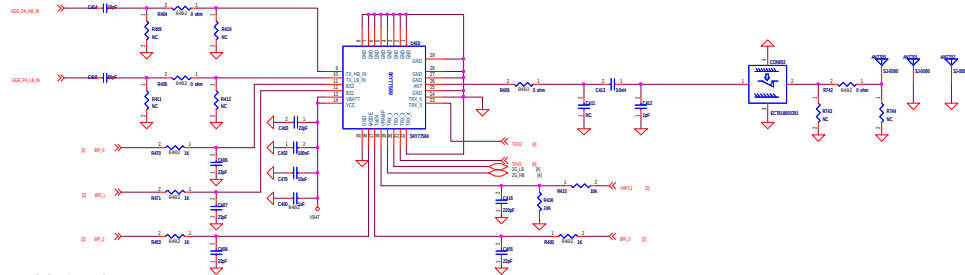
RTC

dedicate VSS ball, must return to cap then to main GND
1. GND_VREF(N14) => C304

RTC 32K : X301+C300+C321=> mount, R300=> NC
32K-1688: X301+C300=> remove, C321+R300=> GN

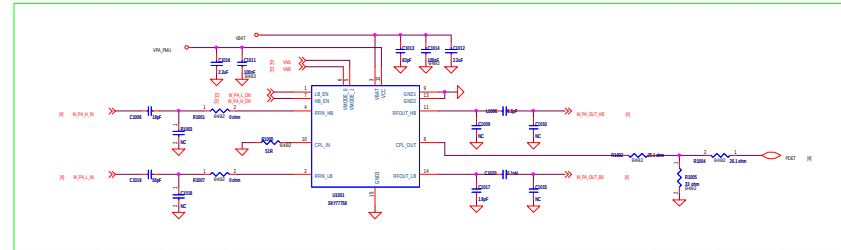
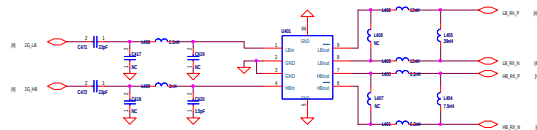
close to chip



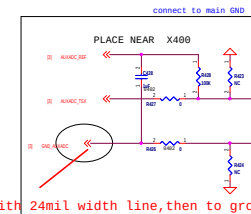


SKY77590 control logic table

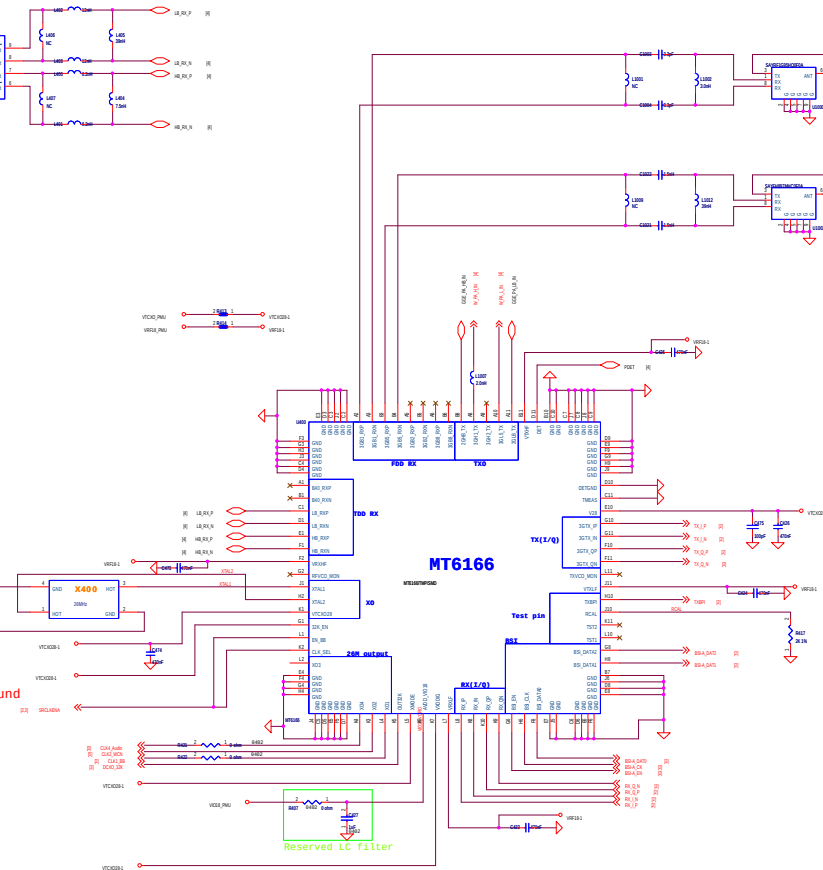
	Enable	VctC	VctB	VctA
LB_GMSK_TX	H	L	L	H
HB_GMSK_TX	H	L	H	H
LB_EDGE_TX	H	H	L	H
HB_EDGE_TX	H	H	H	H
TRX1	L	H	L	L
TRX2	L	H	H	L
TRX3	L	H	L	H
TRX4	L	H	H	H
TRX5	L	L	H	L
TRX6	L	L	L	H

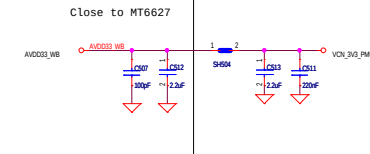
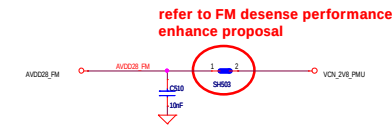
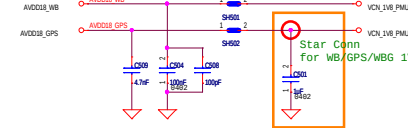
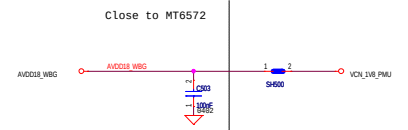
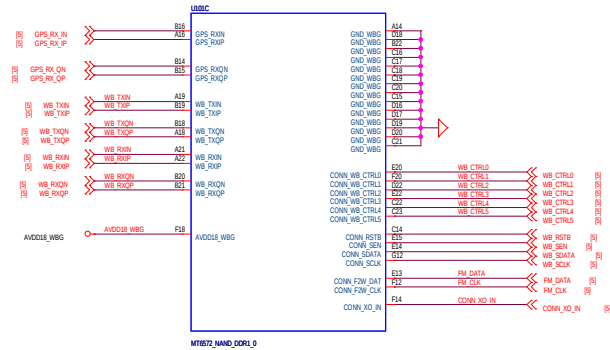
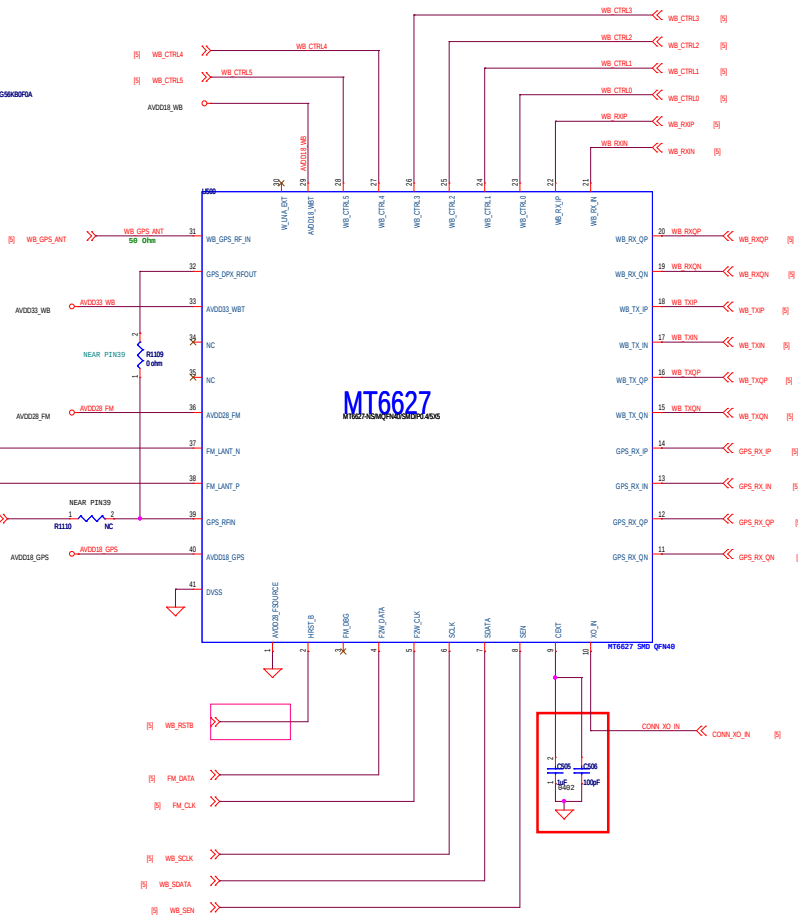
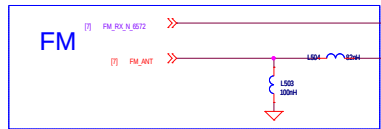
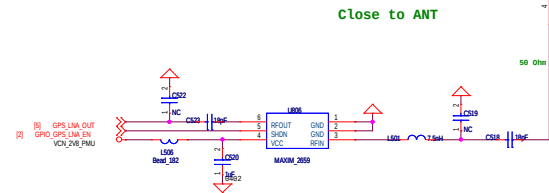
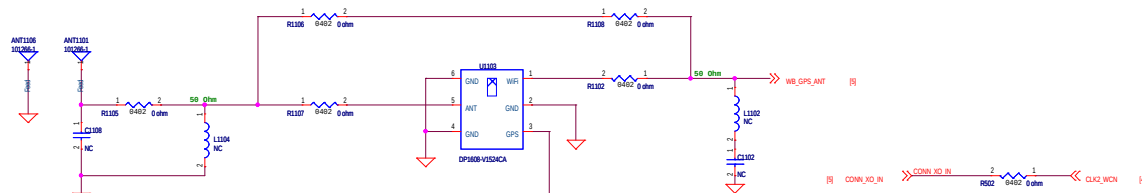


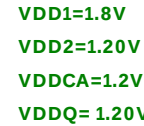
CONNECT TO MT6323 B2pin with 24mil width line, then to ground



MODE	Logic	DCX0_32K_EN	XNODE	VXODIG
DCX0 + 32K X0	0(000)	1(VD016)	1(VD018)	
DCX0 + 32K-Less	1(VT0000)	1(VT0000)	1(VT0000)	







Pin-to-pin comparison diagram for LP-DDR2 and LP-DDR3.

Legend:

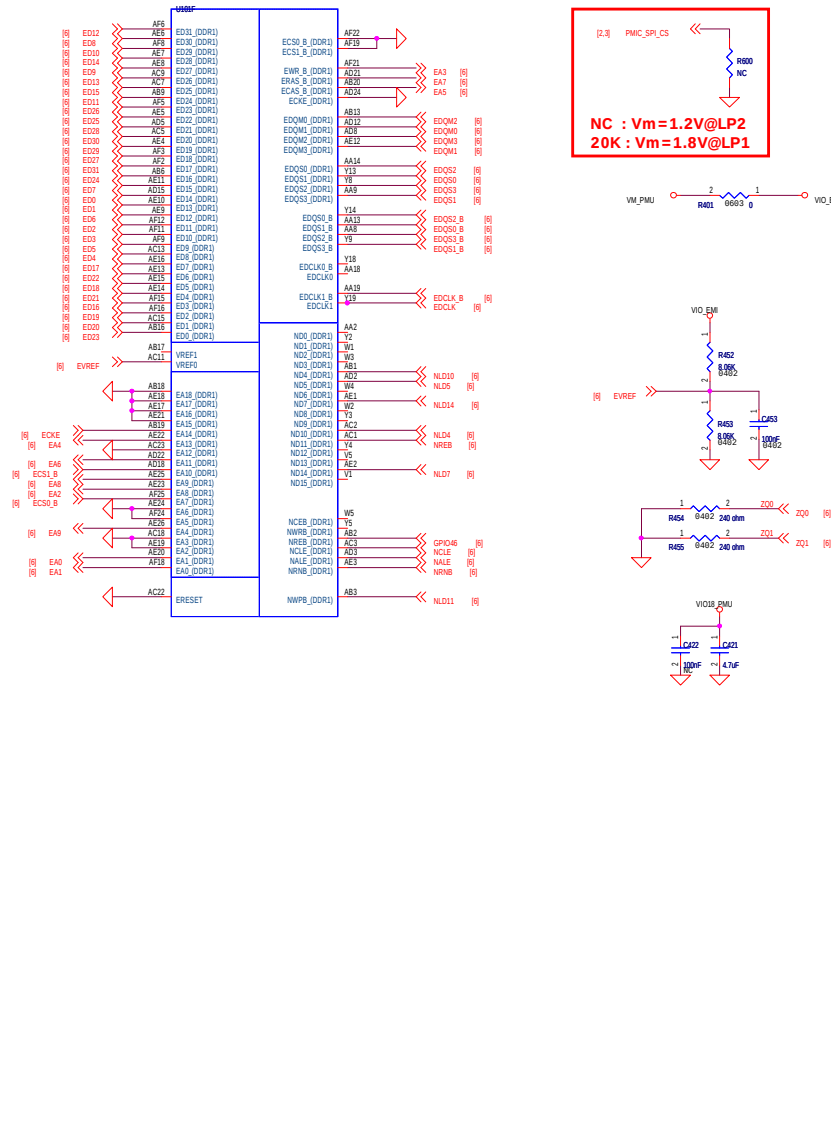
- DQ: Data
- V: Voltage
- NC: Not Connected

Power Section:

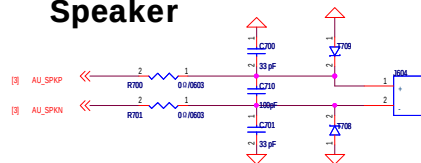
LP-DDR2 Pin	LP-DDR3 Pin	LP-DDR3 Pin Name
E60	E60	T3
E61	E61	R13
E62	E62	R14
E63	E63	R15
E64	E64	K1
E65	E65	J1
E66	E66	J2
E67	E67	K1
E68	E68	H2
E69	E69	H3
E70	E70	T8
E71	E71	R17
E72	E72	R18
E73	E73	R19
E74	E74	R20
E75	E75	P1
E76	E76	P2
E77	E77	P3
E78	E78	K3
E79	E79	K4
E80	E80	J6
E81	E81	J7
E82	E82	J7
E83	E83	J8
E84	E84	H4
E85	E85	W7
E86	E86	U15
E87	E87	W8
E88	E88	T5
E89	E89	U16
E90	E90	W9
E91	E91	U17
E92	E92	T5
E93	E93	H6
E94	E94	D2
E95	E95	E8
E96	E96	G7
E97	E97	H5
E98	E98	E8
E99	E99	G8
E100	E100	E7
E101	E101	Q1
E102	E102	G1
E103	E103	F1
E104	E104	Q1

eMMC Section:

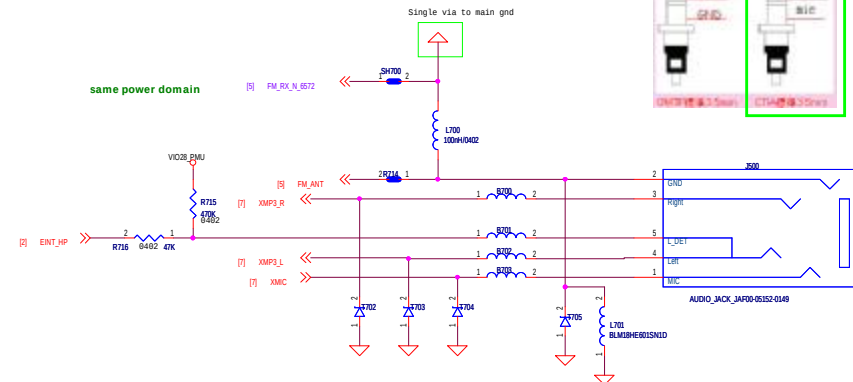
LP-DDR2 Pin	LP-DDR3 Pin	LP-DDR3 Pin Name
F6	F6	VSSQ
F7	F7	VSSQ
F8	F8	VSSQ
F9	F9	VSSQ
F10	F10	VSSQ
F11	F11	VSSQ
F12	F12	VSSQ
F13	F13	VSSQ
F14	F14	VSSQ
F15	F15	VSSQ
F16	F16	VSSQ
F17	F17	VSSQ
F18	F18	VSSQ
F19	F19	VSSQ
F20	F20	VSSQ
F21	F21	VSSQ
F22	F22	VSSQ
F23	F23	VSSQ
F24	F24	VSSQ
F25	F25	VSSQ
F26	F26	VSSQ
F27	F27	VSSQ
F28	F28	VSSQ
F29	F29	VSSQ
F30	F30	VSSQ
F31	F31	VSSQ
F32	F32	VSSQ
F33	F33	VSSQ
F34	F34	VSSQ
F35	F35	VSSQ
F36	F36	VSSQ
F37	F37	VSSQ
F38	F38	VSSQ
F39	F39	VSSQ
F40	F40	VSSQ
F41	F41	VSSQ
F42	F42	VSSQ
F43	F43	VSSQ
F44	F44	VSSQ
F45	F45	VSSQ
F46	F46	VSSQ
F47	F47	VSSQ
F48	F48	VSSQ
F49	F49	VSSQ
F50	F50	VSSQ
F51	F51	VSSQ
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F58	F58	VSSQ
F59	F59	VSSQ
F60	F60	VSSQ
F61	F61	VSSQ
F62	F62	VSSQ
F63	F63	VSSQ
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F70	F70	VSSQ
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F77	F77	VSSQ
F78	F78	VSSQ
F79	F79	VSSQ
F80	F80	VSSQ
F81	F81	VSSQ
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F84	F84	VSSQ
F85	F85	VSSQ
F86	F86	VSSQ
F87	F87	VSSQ
F88	F88	VSSQ
F89	F89	VSSQ
F90	F90	VSSQ
F91	F91	VSSQ
F92	F92	VSSQ
F93	F93	VSSQ
F94	F94	VSSQ
F95	F95	VSSQ
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F97	F97	VSSQ
F98	F98	VSSQ
F99	F99	VSSQ
F100	F100	VSSQ
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F103	F103	VSSQ
F104	F104	VSSQ
F105	F105	VSSQ



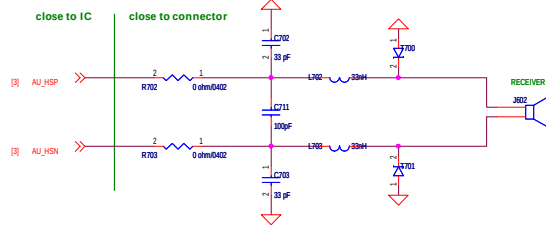
Speaker



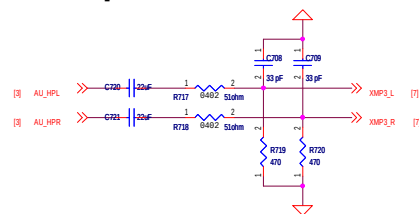
3.5mm AUDIO JACK



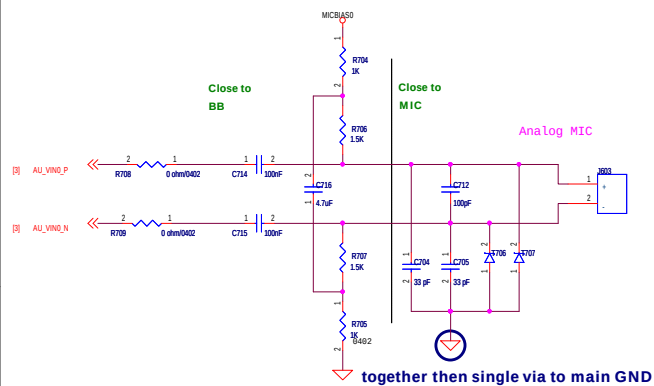
Receiver



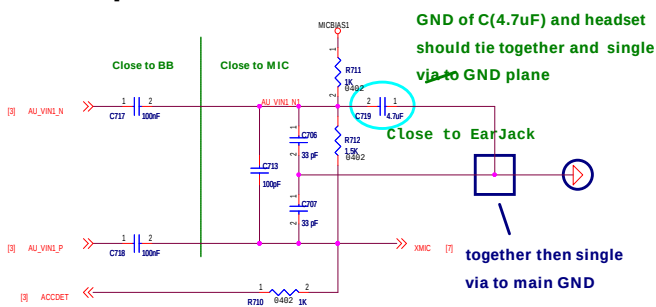
Earphone Audio



Handset Microphone



Earphone MICPHONE



CTP CONNECTOR

The diagram illustrates the LCD Connector Pinout, showing the connection between the LCD module and the system. The LCD module is represented by a blue box on the right, with pins numbered 1 through 28. The system components are represented by various symbols on the left, including power supply rails (VCC, GND), control signals (LPTB, LPTB, LPTB), data signals (D0, D1, D2, D3, D4, D5, D6, D7, D8, D9, D10, D11, D12, D13, D14, D15, D16, D17, D18, D19, D20, D21, D22, D23, D24, D25, D26, D27, D28), and other signals (VLED_N, VLED_P, VLED_0, VLED_1, VLED_2, VLED_3, VLED_4, VLED_5, VLED_6, VLED_7, VLED_8, VLED_9, VLED_10, VLED_11, VLED_12, VLED_13, VLED_14, VLED_15, VLED_16, VLED_17, VLED_18, VLED_19, VLED_20, VLED_21, VLED_22, VLED_23, VLED_24, VLED_25, VLED_26, VLED_27, VLED_28).

The connections are as follows:

- Power Supply:**
 - VCC (Pin 1) connects to the +5V supply.
 - GND (Pin 2) connects to the ground.
- Control Signals:**
 - LPTB (Pin 3) connects to the LPTB signal.
 - LPTB (Pin 4) connects to the LPTB signal.
 - LPTB (Pin 5) connects to the LPTB signal.
 - LPTB (Pin 6) connects to the LPTB signal.
 - LPTB (Pin 7) connects to the LPTB signal.
 - LPTB (Pin 8) connects to the LPTB signal.
 - LPTB (Pin 9) connects to the LPTB signal.
 - LPTB (Pin 10) connects to the LPTB signal.
 - LPTB (Pin 11) connects to the LPTB signal.
 - LPTB (Pin 12) connects to the LPTB signal.
 - LPTB (Pin 13) connects to the LPTB signal.
 - LPTB (Pin 14) connects to the LPTB signal.
 - LPTB (Pin 15) connects to the LPTB signal.
 - LPTB (Pin 16) connects to the LPTB signal.
 - LPTB (Pin 17) connects to the LPTB signal.
 - LPTB (Pin 18) connects to the LPTB signal.
 - LPTB (Pin 19) connects to the LPTB signal.
 - LPTB (Pin 20) connects to the LPTB signal.
 - LPTB (Pin 21) connects to the LPTB signal.
 - LPTB (Pin 22) connects to the LPTB signal.
 - LPTB (Pin 23) connects to the LPTB signal.
 - LPTB (Pin 24) connects to the LPTB signal.
 - LPTB (Pin 25) connects to the LPTB signal.
 - LPTB (Pin 26) connects to the LPTB signal.
 - LPTB (Pin 27) connects to the LPTB signal.
 - LPTB (Pin 28) connects to the LPTB signal.
- Data Signals:**
 - D0 (Pin 1) connects to the D0 signal.
 - D1 (Pin 2) connects to the D1 signal.
 - D2 (Pin 3) connects to the D2 signal.
 - D3 (Pin 4) connects to the D3 signal.
 - D4 (Pin 5) connects to the D4 signal.
 - D5 (Pin 6) connects to the D5 signal.
 - D6 (Pin 7) connects to the D6 signal.
 - D7 (Pin 8) connects to the D7 signal.
 - D8 (Pin 9) connects to the D8 signal.
 - D9 (Pin 10) connects to the D9 signal.
 - D10 (Pin 11) connects to the D10 signal.
 - D11 (Pin 12) connects to the D11 signal.
 - D12 (Pin 13) connects to the D12 signal.
 - D13 (Pin 14) connects to the D13 signal.
 - D14 (Pin 15) connects to the D14 signal.
 - D15 (Pin 16) connects to the D15 signal.
 - D16 (Pin 17) connects to the D16 signal.
 - D17 (Pin 18) connects to the D17 signal.
 - D18 (Pin 19) connects to the D18 signal.
 - D19 (Pin 20) connects to the D19 signal.
 - D20 (Pin 21) connects to the D20 signal.
 - D21 (Pin 22) connects to the D21 signal.
 - D22 (Pin 23) connects to the D22 signal.
 - D23 (Pin 24) connects to the D23 signal.
 - D24 (Pin 25) connects to the D24 signal.
 - D25 (Pin 26) connects to the D25 signal.
 - D26 (Pin 27) connects to the D26 signal.
 - D27 (Pin 28) connects to the D27 signal.
 - D28 (Pin 29) connects to the D28 signal.
- Other Signals:**
 - VLED_N (Pin 1) connects to the VLED_N signal.
 - VLED_P (Pin 2) connects to the VLED_P signal.
 - VLED_0 (Pin 3) connects to the VLED_0 signal.
 - VLED_1 (Pin 4) connects to the VLED_1 signal.
 - VLED_2 (Pin 5) connects to the VLED_2 signal.
 - VLED_3 (Pin 6) connects to the VLED_3 signal.
 - VLED_4 (Pin 7) connects to the VLED_4 signal.
 - VLED_5 (Pin 8) connects to the VLED_5 signal.
 - VLED_6 (Pin 9) connects to the VLED_6 signal.
 - VLED_7 (Pin 10) connects to the VLED_7 signal.
 - VLED_8 (Pin 11) connects to the VLED_8 signal.
 - VLED_9 (Pin 12) connects to the VLED_9 signal.
 - VLED_10 (Pin 13) connects to the VLED_10 signal.
 - VLED_11 (Pin 14) connects to the VLED_11 signal.
 - VLED_12 (Pin 15) connects to the VLED_12 signal.
 - VLED_13 (Pin 16) connects to the VLED_13 signal.
 - VLED_14 (Pin 17) connects to the VLED_14 signal.
 - VLED_15 (Pin 18) connects to the VLED_15 signal.
 - VLED_16 (Pin 19) connects to the VLED_16 signal.
 - VLED_17 (Pin 20) connects to the VLED_17 signal.
 - VLED_18 (Pin 21) connects to the VLED_18 signal.
 - VLED_19 (Pin 22) connects to the VLED_19 signal.
 - VLED_20 (Pin 23) connects to the VLED_20 signal.
 - VLED_21 (Pin 24) connects to the VLED_21 signal.
 - VLED_22 (Pin 25) connects to the VLED_22 signal.
 - VLED_23 (Pin 26) connects to the VLED_23 signal.
 - VLED_24 (Pin 27) connects to the VLED_24 signal.
 - VLED_25 (Pin 28) connects to the VLED_25 signal.
 - VLED_26 (Pin 29) connects to the VLED_26 signal.
 - VLED_27 (Pin 30) connects to the VLED_27 signal.
 - VLED_28 (Pin 31) connects to the VLED_28 signal.

Indication LED

Diagram illustrating the pin configuration and internal components of the CON_BLI25-25RL-TAGF connector. The connector has 28 pins, with pins 1 through 28 labeled. The internal components include capacitors C810, C811, C816, C817, C815, C813, and C814. The diagram also shows the connection to VCMAA, VCMAI, VCMAF, VCMAI2, VCMAI3, VCMAI4, VCMAI5, and VCMAI6. The internal components are connected to the pins via a network of resistors and capacitors.

Micro USB 2.0

Diagram illustrating the pinout for a Micro USB 2.0 connector. The connector pins are labeled 1 through 5. The connections are as follows:

- Pin 1 (VBUS) connects to the VBUS line.
- Pin 2 (USB_DM) connects to the USB_DM line.
- Pin 3 (USB_DP) connects to the USB_DP line.
- Pin 4 (D-) connects to the D- pin of the USB connector.
- Pin 5 (D+) connects to the D+ pin of the USB connector.

The diagram also shows the internal wiring of the USB connector, including the D+ and D- pins, and the connection to the USB 2.0 port.

EUP2986{I(MAX)=0.2/R809}

		硕诺科技（深圳）有限公司	
File		08_LCD Touch Camera USB Sidekey	
设计 甘霖	审核 肖国洪	Board Name SP41_DDR2_MB_V1.0	Rev V1.0
Date	Tuesday, 3/4/07 20:15	Sheet	8 of 11

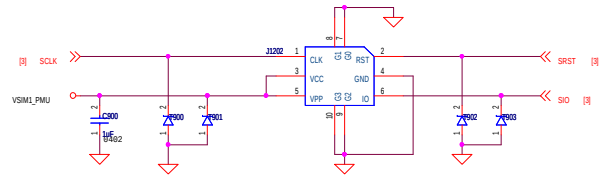
ALS + PS Sensor

The diagram shows the connection of a GP2Y09100F module to a microcontroller. The module's pins are connected as follows:

- VCC** (Pin 6) is connected to the **VIO2B_BMU** supply.
- GND** (Pin 5) is connected to ground.
- INT** (Pin 4) is connected to the **INT** pin of the microcontroller.
- LEDA** (Pin 7) is connected to the **SCL_1** I2C line.
- LEDVC** (Pin 8) is connected to the **SDA_1** I2C line.
- LDR** (Pin 3) is connected to the **ALSPS_INT** interrupt line.

Additional components include a 10k pull-up resistor (R906) on the **VIO2B_BMU** line and a 100nF decoupling capacitor (C911) on the **VIO2B_BMU** supply.

ADDR=0X48



MC1CK with GND shield

