



# LoRa Module User Manual

MP Part Number: 1SJ

(This data sheet is preliminary. Please contact your Murata support for the latest formal revision.)



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## 1. Features

Interfaces : UART, I2C, USB, SPI Main ICs : STM32L, SX1262

Reference Clocks : Integrated 32MHz clock (TCXO with frequency error=±3.0

ppm) and 32.768KHz clock (frequency error= $\pm 20$  ppm)

Transmitting Channel Frequency Allocation:

125KHz LoRa:  $902.3\sim914.9$ MHz, channel space 200KHz 500KHz LoRa:  $903.0\sim914.2$ MHz, channel space 1.6MHz

Module Size : 10.0 mm x 8.0 mm x 1.60 max mm.

Weight : 0.28g (Typ.)
Package : Resin mold

RoHS : This module is compliant with the RoHS directive

## 2. Part Number

Ordering Part Number	Description
LBAA0QB1SJ-295 <b>TEMP</b>	Engineering sample
LBAA0QB1SJ-295 <b>EVK</b>	Evaluation board
1SJ	MP P/N

## 3. Block Diagram

Confidential. Please contact Murata for the block diagram.

## 4. Certification info

#### 4.1. Radio Certification

USA/Canada

FCC ID	VPYLBAA0QB1SJ
IC	772C-LBAA0QB1SJ



## 5. <u>Dimensions, Marking and Terminal Configurations</u>

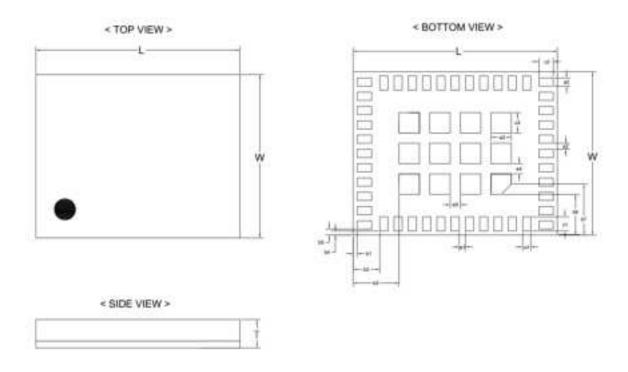


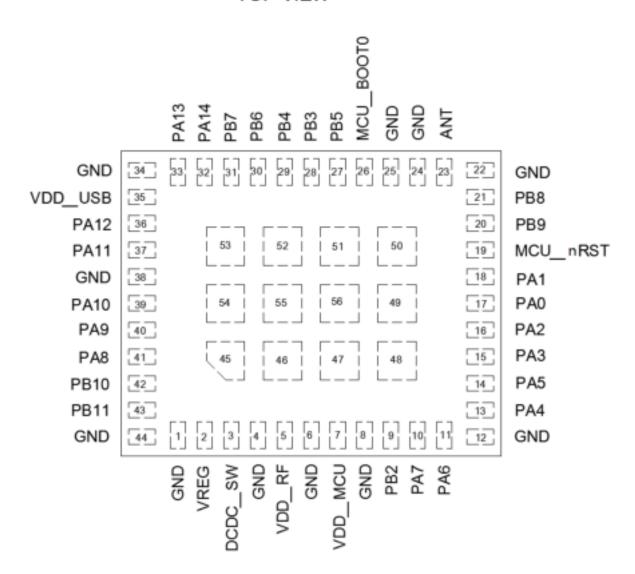
Table 1 Dimension (Unit: mm)

Mark	Dimension	Mark	Dimension	Mark	Dimension
L	10.0±0.2	W	8.0±0.2	T	1.60 max
a1	0.4±0.1	a2	0.4±0.1	a3	1.0±0.1
b1	0.2±0.15	b2	1.3±0.15	b3	2.3±0.15
b4	0.2±0.15	b5	0.3±0.15	b6	2.0±0.15
b7	2.5±0.15	с1	0.7±0.1	c2	0.7±0.1
c3	1.0±0.1	e1	0.3±0.1	e2	0.3±0.1
e3	0.5±0.1	e4	0.5±0.1		



**Terminal Configurations** 

## < TOP VIEW >



**Table 2** Terminal Configurations

Pin NO.	Terminal Name	Type	Connection to IC terminal	Description
1	GND	Ground	-	Ground
2	VREG	0	SX1262_VREG	Regulated output voltage from internal regulator LDO/DC-DC
3	DCDC_SW	0	SX1262_DCC_SW	DC-DC Switcher Output
4	GND	Ground	-	Ground
5	VDD_RF	Power	SX1262_VBAT / VDD_IN	Power supply for RF IC
6	GND	Ground	-	Ground
7	VDD_MCU	Power	SX1262_VBAT_IO,STM32 L072_VDD / VDDA / VREF+	Power supply for MCU and RF IC
8	GND	Ground	-	Ground



PB2	I	1	I	1	CDIO moderDD2
10			1.0	CT. 4201 070 BB0	GPIO mode:PB2
PA7	9	PB2	1/0	S1M32L0/2_PB2	
PA7					
10					
ADC_INT   ADD_INT   ADD_	10	PA7	1/0	STM32L072 PA7	
PA6				-	
PA6					
11					
COMP1_OUT					SPI1_MISO
ADC_IN6   ADC_IN6   ADC_IN6   ADC_IN6   ADC_IN6   ADC_IN6   ADC_IN4   ADC_IN4   ADC_IN4   ADC_IN4   ADC_IN4   ADC_IN4   ADC_IN4   ADC_IN5   ADC_	11	PA6	I/O	STM32L072_PA6	
12					COMP1_OUT
PA4					ADC_IN6
PA4	12	GND	Ground	-	Ground
13					GPIO mode:PA4
14					SPI1_NSS
ADC_IN4   DAC_OUT1	12	DA4	1,0	CTM221072 DA4	UART2_CK
DAC_OUT1   GPIO mode:PA5   SPI1_SCK   CMP2_INM   COMP1_INM/COMP2_INM   ADC_IN5   DAC_OUT2   GPIO mode:PA3   ADC_IN5   DAC_OUT2   GPIO mode:PA3   ADC_IN3   USART2_RX   LPUART1_RX   COMP2_INP   COMP2_OUT/COMP2_INM   GPIO mode:PA0   ADC_IN0   USART2_TX   LPUART1_TX   COMP2_OUT/COMP2_INM   GPIO mode:PA0   ADC_IN0   USART2_CTS   USART4_TX   COMP1_OUT/COMP1_INM   GPIO mode:PA1   ADC_IN1   ADC_IN1   USART2_RTS   USART4_RX   COMP1_INP   COM	13	PA4	1/0	51M32L0/2_PA4	COMP1_INM/COMP2_INM
DAC_OUT1   GPIO mode:PA5   SPI1_SCK   CMP2_INM   COMP1_INM/COMP2_INM   ADC_IN5   DAC_OUT2   GPIO mode:PA3   ADC_IN5   DAC_OUT2   GPIO mode:PA3   ADC_IN3   USART2_RX   LPUART1_RX   COMP2_INP   COMP2_OUT/COMP2_INM   GPIO mode:PA0   ADC_IN0   USART2_TX   LPUART1_TX   COMP2_OUT/COMP2_INM   GPIO mode:PA0   ADC_IN0   USART2_CTS   USART4_TX   COMP1_OUT/COMP1_INM   GPIO mode:PA1   ADC_IN1   ADC_IN1   USART2_RTS   USART4_RX   COMP1_INP   COM					ADC_IN4
14					DAC_OUT1
14					GPIO mode:PA5
14					SPI1_SCK
COMP1_INN/COMP2_INM     ADC_INS     DAC_OUT2     GPIO mode:PA3     ADC_IN3     USART2_RX     LPUART1_RX     COMP2_INP     GPIO mode:PA2     ADC_IN2     GPIO mode:PA2     ADC_IN2     COMP2_INP     GPIO mode:PA2     ADC_IN2     USART2_TX     LPUART1_TX     COMP2_OUT/COMP2_INM     GPIO mode:PA0     ADC_IN0     ADC_IN0     USART2_CTS     USART4_TX     COMP1_OUT/COMP1_INM     GPIO mode:PA1     ADC_IN1     USART2_RTS     USART4_RX     COMP1_INP     DESTINABLED OF PERSON     COMP1_INP     COM		<b>.</b>	1.0	CT. 4201 070 D. F	
DAC_OUT2   GPIO mode:PA3   ADC_IN3   USART2_RX   LPUART1_RX   COMP2_INP	14	PA5	1/0	S1M32L0/2_PA5	COMP1_INM/COMP2_INM
STM32L072_PA3					ADC_IN5
ADC_IN3 USART2_RX LPUART1_RX COMP2_INP GPIO mode:PA2 ADC_IN2 USART2_TX LPUART1_TX COMP2_INP GPIO mode:PA2 ADC_IN2 USART2_TX LPUART1_TX COMP2_OUT/COMP2_INM GPIO mode:PA0 ADC_IN0 USART2_TX LPUART1_TX COMP2_OUT/COMP2_INM GPIO mode:PA0 ADC_IN0 USART2_CTS USART4_TX COMP1_OUT/COMP1_INM GPIO mode:PA1 ADC_IN1 USART2_RTS USART4_RX COMP1_IN1 USART2_RTS USART4_RX COMP1_INP  MCU_nRST I STM32L072_nRST Reset for MCU GPIO mode: PB9					DAC_OUT2
15					GPIO mode:PA3
LPUART1_RX   COMP2_INP					ADC_IN3
COMP2_INP   GPIO mode:PA2	15	PA3	I/O	STM32L072_PA3	USART2_RX
GPIO mode:PA2					LPUART1_RX
ADC_IN2 USART2_TX LPUART1_TX COMP2_OUT/COMP2_INM  PA0  I/O  STM32L072_PA0  GPIO mode:PA0 ADC_IN0 USART2_CTS USART4_TX COMP1_OUT/COMP1_INM  GPIO mode:PA0 ADC_IN0  USART4_TX COMP1_OUT/COMP1_INM  GPIO mode:PA1 ADC_IN1 USART4_TX COMP1_OUT/COMP1_INM  GPIO mode:PA1 ADC_IN1 USART2_RTS USART4_RX COMP1_INP  MCU_nRST  I STM32L072_PR9  GPIO mode: PB9					COMP2_INP
16					GPIO mode:PA2
LPUART1_TX					ADC_IN2
COMP2_OUT/COMP2_INM	16	PA2	I/O	STM32L072_PA2	USART2_TX
PA0					LPUART1_TX
ADC_IN0  I/O STM32L072_PA0  BADC_IN0  USART2_CTS  USART4_TX  COMP1_OUT/COMP1_INM  GPIO mode:PA1  ADC_IN1  USART2_RTS  USART4_RX  COMP1_INP  MCU_nRST  I STM32L072_nRST  Reset for MCU  GPIO mode: PB9  GPIO mode: PB9					COMP2_OUT/COMP2_INM
17       PA0       I/O       STM32L072_PA0       USART2_CTS					
USART4_TX COMP1_OUT/COMP1_INM  GPIO mode:PA1 ADC_IN1 USART2_RTS USART4_RX COMP1_INP  19 MCU_nRST I STM32L072_nRST Reset for MCU  PB9 I/O STM32L072_PB9  GPIO mode: PB9					ADC_IN0
USART4_TX COMP1_OUT/COMP1_INM  GPIO mode:PA1 ADC_IN1 USART2_RTS USART4_RX COMP1_INP  19 MCU_nRST I STM32L072_nRST Reset for MCU  PB9 I/O STM32L072_PB9  GPIO mode: PB9	17	PA0	I/O	STM32L072_PA0	USART2_CTS
COMP1_OUT/COMP1_INM					
PA1					_
18     PA1     I/O     STM32L072_PA1     USART2_RTS       USART4_RX     USART4_RX     COMP1_INP       19     MCU_nRST     I     STM32L072_nRST     Reset for MCU       20     PB9     I/O     STM32L072_PB9     GPIO mode: PB9					
18     PA1     I/O     STM32L072_PA1     USART2_RTS       USART4_RX     USART4_RX     COMP1_INP       19     MCU_nRST     I     STM32L072_nRST     Reset for MCU       20     PB9     I/O     STM32L072_PB9     GPIO mode: PB9					ADC_IN1
USART4_RX COMP1_INP  19 MCU_nRST I STM32L072_nRST Reset for MCU  20 PB9 I/O STM32L072_PB9  GPIO mode: PB9	18	PA1	1/0	STM32L072_PA1	
COMP1_INP  19 MCU_nRST I STM32L072_nRST Reset for MCU  20 PB9 I/O STM32L072_PB9 GPIO mode: PB9					
19 MCU_nRST I STM32L072_nRST Reset for MCU 20 PB9 I/O STM32L072_PB9 GPIO mode: PB9					
20 PB9 I/O STM32L072 PB9 GPIO mode: PB9	19	MCU nRST	ı	STM32L072 nRST	
L 20				_	
	20	PB9	I/O	STM32L072_PB9	I2C1_SDA



	_	_		_
				SPI2_NSS
				12S2_WS
21	PB8	I/O	CTM221072 DD0	GPIO mode:PB8
21	PBO	1/0	STM32L072_PB8	I2C1_SCL
22	GND	Ground	-	Ground
23	ANT	A, I/O	-	Transmit / Receive antenna
24	GND	Ground	-	Ground
25	GND	Ground	-	Ground
26	MCU_BOOT0	ı	STM32L072_BOOT0	BOOT0 for MCU
				GPIO mode:PB5
				LPTIM1_IN1
				SPI1_MOSI
27	PB5	I/O	STM32L072_PB5	I2C1_SMBA
				USART1_CK
				USART5_RTS
İ				COMP2_INP
				GPIO mode:PB3
1				COMP2_INM
28	PB3	I/O	STM32L072_PB3	SPI1_CLK
				USART1_RTS
				USART5_TX
			CTM221.072. DB4	GPIO mode:PB4
				COMP2_INP
29	DD 4	1/0		SPI1_MISO
29	PB4	I/O	STM32L072_PB4	USART1_CTS
				USART5_RX
				I2C3_SDA
				GPIO mode:PB6
				I2C1_SCL
30	PB6	I/O	STM32L072_PB6	USART1_TX
				LPTIM1_ETR
				COMP2_INP
				GPIO mode:PB7
				I2C1_SDA
				USART1_RX
31	PB7	I/O	STM32L072_PB7	LPTIM1_IN2
				USART4_CTS
				COMP2_INP
				VREF_PVD_IN
				GPIO mode:PA14
32	PA14	I/O	STM32L072_PA14	SWCLK
				USART2_TX
				LPUART1_TX
				GPIO mode:PA13
33	PA13	I/O	STM32L072_PA13	SWDIO
			_	USB_OE
				LPUART1_RX
34	GND	Ground	-	Ground

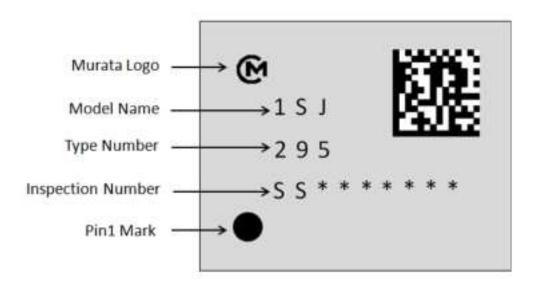


35	VDD_USB	Power	STM32L072_VDD_USB	Power supply for MCU USB Interface
36	PA12	1/0	STM32L072_PA12	GPIO mode:PA12(power supply by VDD_USB) USB_DP SPI1_MOSI USART1_RTS COMP2_OUT
37	PA11	1/0	STM32L072_PA11	GPIO mode:PA11(power supply by VDD_USB) USB_DM SPI1_MISO USART1_CST COMP1_OUT
38	GND	Ground	-	Ground
39	PA10	I/O	STM32L072_PA10	GPIO mode:PA10 USART1_RX I2C1_SDA
40	PA9	1/0	STM32L072_PA9	GPIO mode:PA9 USART1_TX I2C1_SCL I2C3_SMBA MCO
41	PA8	1/0	STM32L072_PA8	GPIO mode:PA8 MCO USB_CRS_SYNC USART1_CK I2C3_SCL
42	PB10	I/O	STM32L072_PB10	GPIO mode:PB10 LPUART1_TX/LPUART1_RX SPI2_SCK I2C2_SCL
43	PB11	1/0	STM32L072_PB11	GPIO mode:PB11 LPUART1_RX/ LPUART1_TX I2C2_SDA
44	GND	Ground	-	Ground
45~56	GND	Ground	-	Ground

Notes: GPIO pins are to be left OPEN if not used.



## 6. Label Information



## 7. Absolute Maximum Ratings

Table 3 Maximum ratings

Table & Maximani	allige				
	Parameters	Min	Тур	Max	Unit
Storage Temperat	ure	-40	25	+85	degC
Input RF Level		-	-	10	dBm
Supply Voltage	VDD_RF, VDD_MCU, VDD_USB	-0.3	-	3.9	V

## 8. Operating Condition

**Table 4** Operating specification

Parameters		Min	Тур	Max	Unit
Operating 7	Temperature	-40	25	+85	degC
Cupply	VDD_RF, VDD_MCU	2.0	-	3.6	V
Supply Voltage	VDD_USB (USB peripheral used) (1)	3.0	-	3.6	V
voilage	VDD_USB(USB peripheral not used) (1)	VDD_MCU_min	VDD_MCU	VDD_MCU_max	V

- (1) VDD\_USB must respect the following conditions:
  - When VDD\_MCU is powered on (VDD\_MCU < VDD\_MCU\_min), VDD\_USB should be always lower than VDD\_MCU.
  - When VDD\_MCU is powered down (VDD\_MCU < VDD\_MCU\_min), VDD\_USB should be always lower than VDD\_MCU.
  - In operating mode, VDD\_USB could be lower or higher than VDD\_MCU.
  - If the USB is not used, VDD\_USB must be tied to VDD\_MCU to be able to use PA11 and PA12 as standard I/O.



## 9. Electrical Characteristics

#### 9.1. FSK Transceiver Specification

Conditions:

Supply voltage =3.3 V, Temperature = 25 °C, TCXO = 32 MHz, F<sub>RF</sub> =868MHz, all RF impedances matched, transmit mode output power defined in 50 Ohm load, FSK BER = 0.1%, 2-level FSK modulation without pre-filtering, BR = 4.8 kb/s, FDA = 5 kHz, BW\_F = 19.5 kHz double-sided, unless otherwise specified.

Note: FSK transmission is only available in the EU868 band.

**FSK Receive Specification** 

Symbol	Description	Conditions	Min	Тур	Max	Unit
RXS_2FB	Sensitivity 2-FSK, RX Boosted, Split RF path for RX and TX	BR_F = 4.8 kb/s, FDA = 5 kHz, BW_F = 19.5 kHz		-117		dBm

**FSK Transmit Specification** 

Symbol	Description	Conditions	Min	Тур	Max	Unit
TXOP	Power setting = 22 dBm	868MHZ Band		21.5		dBm
IDDTX	Power setting = 22 dBm	868MHZ Band		118		mA

## 9.2. <u>LoRa Transceiver Specification</u>

Conditions:

The table below gives the electrical specifications for the transceiver operating with LoRaTM modulation. Following conditions apply unless otherwise specified: Supply voltage = 3.3 V, Temperature = 25 °C, TCXO = 32 MHz, F<sub>RF</sub> =868/915 MHz, all RF impedances matched, transmit mode output power defined in 50 Ohm load, Error Correction Code (EC) = 4/5, Packet Error Rate (PER)= 1%, CRC on payload enabled, Payload length = 10 bytes.

LoRa Receive Specification

Symbol	Description	Conditions	Min	Тур	Max	Unit
IDDRX	Receive mode, DC-DC mode used, RX Boosted,	868MHZ Band		15.5 <sup>(1)</sup>		mA
		915MHz Band		15.5 <sup>(1)</sup>		mA
IDDRX	Receive mode, LDO mode used, RX Boosted	868MHZ Band		20.0(1)		mA
		915MHz Band		20.0(1)		mA
RXS_LB (125KHz)	Sensitivity Lora, RX Boosted, Split RF path for RX and TX. 125KHz bandwidth	SF = 7		-124		dBm
		SF = 12		-137		dBm
RXS_LB (250KHz)	Sensitivity Lora, RX Boosted, Split RF path for RX and TX. 250KHz bandwidth	SF = 7		-121		dBm
		SF = 12		-134		dBm

<sup>(1)</sup> IDDRX is tested under test FW, and MCU is not in sleep mode.

**LoRa Transmit Specification** 

Symbol	Description	Conditions	Min	Тур	Max	Unit	
TXOP	TXOP setting = 22 dBm	868MHZ Band			22	dBm	
		915MHZ Band			22	dBm	
IDDTX	TXOP setting = 22 dBm	868MHZ Band		118		mA	
		915MHZ Band		109		mA	



## 9.3. Low power mode current

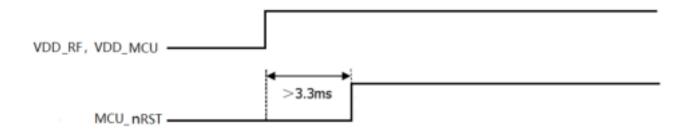
Conditions: Power supply=3.3 V, Temperature=25 °C;

Description		Тур	Max	Unit
STM32L072 in stop mode with RTC (Real Time Clock) SX1262 in cold start sleep mode <sup>1</sup>		1.3		uA
STM32L072 in stop mode with RTC (Real Time Clock) SX1262 in warm start sleep mode without RC64K(64kHz RC oscillator) <sup>2</sup>		2.0		uA
STM32L072 in stop mode with RTC (Real Time Clock) SX1262 in warm start sleep mode with RC64K(64kHz RC oscillator) <sup>2</sup>		2.5		uA

- 1. Cold start is equivalent to device at POR or when the device is waking up from sleep mode with all blocks OFF.
- 2. Warm start is only happening when device is waking up from sleep mode with its configuration retained.

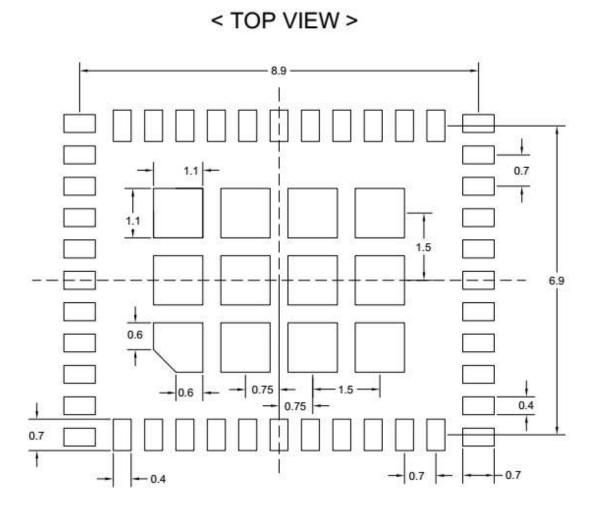
## 10. Power Sequences

## 9.1 Power Up Sequence





## 11. Recommend Land Pattern



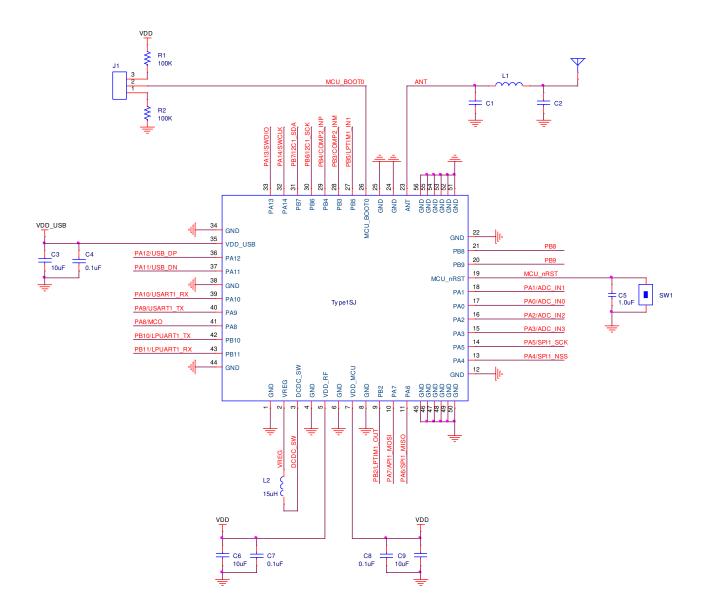
#### Notes:

- 1. Dimension unit is millimeter (Angle unit is degree).
- 2. This land pattern is for reference only.

  Consult your manufacturing group to ensure your company's manufacturing guidelines are met.
- 3. Thermal vias in the land pattern of the exposed pad shall be connected to a system ground plane. Failure to do so may compromise the thermal and/or functional performance of the device.
- 4. Square package-dimension apply in both "X" and "Y" directions.



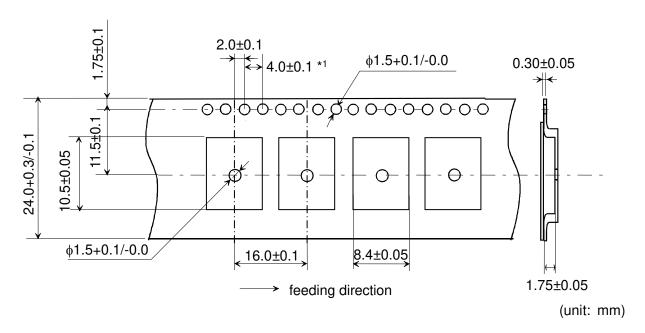
## 12. Reference Circuit



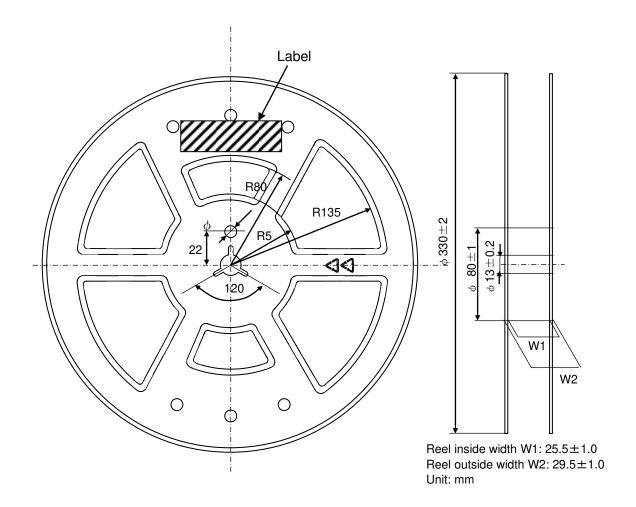


## 13. Tape and Reel Packing

## 13.1. <u>Dimension of Tape</u>



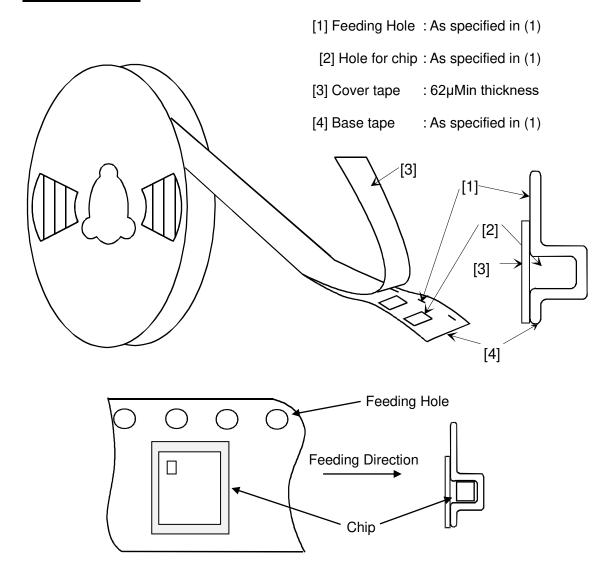
## 13.2. <u>Dimensions of Reel</u>



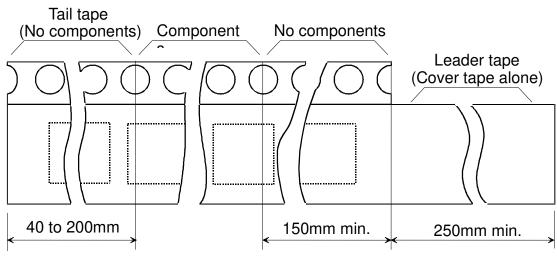
Preliminary & Confidential < Specification may be changed by Murata without notice >



## 13.3. <u>Taping Diagrams</u>



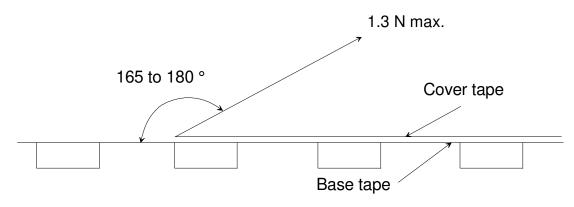
## 13.4. Leader and Tail Tape



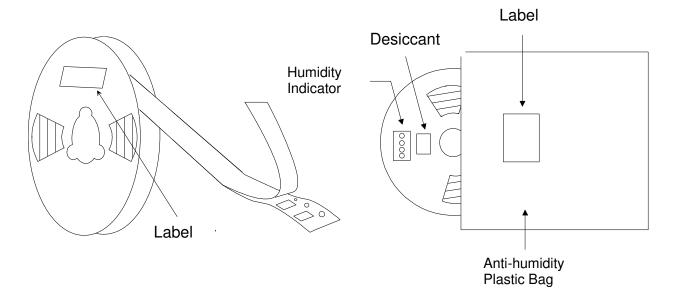
Feeding direction



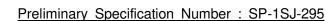
- The tape for chips are wound clockwise, the feeding holes to the right side as the tape is pulled toward the user.
- The cover tape and base tape are not adhered at no components area for 250mm Min.
- Tear off strength against pulling of cover tape: 5N Min.
- Packaging unit: 1000 pcs/ reel
- Material
  - Base tape : PlasticReel : Plastic
  - > Cover tape, cavity tape and reel are made the anti-static processing.
- Peeling of force: 1.3N max. in the direction of peeling as shown below.



Packaging (Humidity proof Packing)



Tape and reel must be sealed with the anti-humidity plastic bag. The bag contains the desiccant and the humidity indicator.





#### 14. Notice

#### 14.1. Storage Conditions

Please use this product within 6month after receipt.

- The product shall be stored without opening the packing under the ambient temperature from 5 to 35  $^{\circ}$ C and humidity from 20  $^{\sim}$  70  $^{\circ}$ RH.

(Packing materials, in particular, may be deformed at the temperature over 40 °C)

- The product left more than 6months after reception, it needs to be confirmed the solderbility before used.
- The product shall be stored in non-corrosive gas (CL2, NH3, SO2, NOx, etc.).
- Any excess mechanical shock including, but not limited to, sticking the packing materials by sharp object and dropping the product, shall not be applied in order not to damage the packing materials.

This product is applicable to MSL3 (Based on IPC/JEDEC J-STD-020)

- After the packing opened, the product shall be stored at <30  $^{\circ}$ C / <60  $^{\circ}$ RH and the product shall be used within 168 hours.
- When the color of the indicator in the packing changed, the product shall be baked before soldering.

Baking condition: 125 +5/-0 °C, 24 hours, 1 time

The products shall be baked on the heat-resistant tray because the material (Base Tape, Reel Tape and Cover Tape) are not heat-resistant.

#### 14.2. Handling Conditions

Be careful in handling or transporting products because excessive stress or mechanical shock may break products.

Handle with care if products may have cracks or damages on their terminals, the characteristics of products may change. Do not touch products with bear hands that may result in poor solderability.

#### 14.3. Standard PCB Design (Land Pattern and Dimensions)

All the ground terminals should be connected to the ground patterns. Furthermore, the ground pattern should be provided between IN and OUT terminals. Please refer to the specifications for the standard land dimensions.

The recommended land pattern and dimensions is as Murata's standard. The characteristics of products may vary depending on the pattern drawing method, grounding method, land dimensions, land forming method of the NC terminals and the PCB material and thickness. Therefore, be sure to verify the characteristics in the actual set. When using non-standard lands, contact Murata beforehand.

#### 14.4. Notice for Chip Placer:

When placing products on the PCB, products may be stressed and broken by uneven forces from a worn-out chucking locating claw or a suction nozzle. To prevent products from damages, be sure to follow the specifications for the maintenance of the chip placer being used. For the positioning of products on the PCB, be aware that mechanical chucking may damage products.

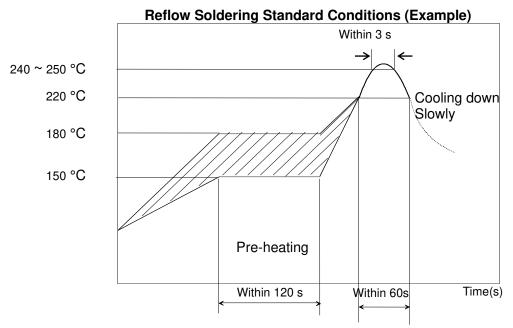
#### 14.5. Soldering Conditions:

The recommendation conditions of soldering are as in the following figure.

When products are immersed in solvent after mounting, pay special attention to maintain the temperature difference within 100 °C. Soldering must be carried out by the above mentioned conditions to prevent products from damage. Set up the highest temperature of reflow within 260 °C.

Contact Murata before use if concerning other soldering conditions.





Please use the reflow within 2 times.

Use rosin Type flux or weakly active flux with a chlorine content of 0.2 wt % or less.

#### 14.6. Cleaning:

Since this Product is Moisture Sensitive, any cleaning is not permitted.

## 14.7. Operational Environment Conditions:

Products are designed to work for electronic products under normal environmental conditions (ambient temperature, humidity and pressure). Therefore, products have no problems to be used under the similar conditions to the above-mentioned. However, if products are used under the following circumstances, it may damage products and leakage of electricity and abnormal temperature may occur.

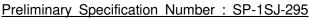
- In an atmosphere containing corrosive gas (CL2, NH3, SOx, NOx, etc.).
- In an atmosphere containing combustible and volatile gases.
- Dusty place.
- Direct sunlight place.
- Water splashing place.
- Humid place where water condenses.
- Freezing place.

If there are possibilities for products to be used under the preceding clause, consult with Murata before actual use.

As it might be a cause of degradation or destruction to apply static electricity to products, do not apply static electricity or excessive voltage while assembling and measuring.

#### 14.8. Input Power Capacity:

Products shall be used in the input power capacity as specified in this specification. Inform Murata beforehand, in case that the components are used beyond such input power capacity range.







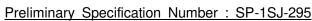
## 15. Regulatory Statements

#### 15.1 FCC Statements

This product has been tested and found to comply with the limits for a Class B digital device, pursuant to Part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference in a residential installation. This product generates, uses, and can radiate radio frequency energy and, if not installed and used in accordance with the instructions, may cause harmful interference to radio communications. However, there is no guarantee that interference will not occur in a particular installation. If this product does cause harmful interference to radio or television reception, which can be determined by turning the equipment off and on, the user is encouraged to try to correct the interference by one or more of the following measures:

- —Reorient or relocate the receiving antenna.
- —Increase the separation between the equipment and receiver.
- —Connect the equipment into an outlet on a circuit different from that to which the receiver is connected.
- —Consult the dealer or an experienced radio/TV technician for help.

Please take attention that changes or modification not expressly approved by the party responsible for compliance could void the user's authority to operate the equipment.





#### 15.2 IC Statements

This device complies with Industry Canada licence-exempt RSS standard(s). Operation is subject to the following two conditions:

- (1) this device may not cause interference, and
- (2) this device must accept any interference, including interference that may cause undesired operation of the device.

Le présent appareil est conforme aux CNR d'Industrie Canada applicables aux appareils radioexempts de licence. L'exploitation est autorisée aux deux conditions suivantes :

- (1) l'appareil ne doit pas produire de brouillage, et
- (2) l'utilisateur de l'appareil doit accepter tout brouillage radioélectrique subi, même si le brouillage est susceptible d'en compromettre le fonctionnement.

Under Industry Canada regulations, this radio transmitter may only operate using an antenna of a type and maximum (or lesser) gain approved for the transmitter by Industry Canada. To reduce potential radio interference to other users, the antenna type and its gain should be so chosen that the equivalent isotropically radiated power (e.i.r.p.) is not more than that necessary for successful communication.

Conformément à la réglementation d'Industrie Canada, le présent émetteur radio peut fonctionner avec une antenne d'un type et d'un gain maximal (ou inférieur) approuvé pour l'émetteur par Industrie Canada. Dans le but de réduire les risques de brouillage radioélectrique à l'intention des autres utilisateurs, il faut choisir le type d'antenne et son gain de sorte que la puissance isotrope rayonnée équivalente (p.i.r.e.) ne dépasse pas l'intensité nécessaire à l'établissement d'une communication satisfaisante.

When the Industry Canada certification number is not visible when the module is installed inside another device, then the outside of the device into which the module is installed must also display a label referring to the enclosed module. This exterior label can be use wording "Contains transmitter module IC: 772C-LBAA0QB1SJ" or "Contains IC: 772C-LBAA0QB1SJ".

#### 15.3 General Statements

The module is limited to OEM installation ONLY.

The OEM integrator is responsible for ensuring that the end-user has no manual instruction to remove or install module.

Therefore, the final host product must be submitted to Murata for confirmation that the installation for the module into the host is in compliance with regulations of FCC and IC Canada. Specially, if an antenna other than the model documented in the Filing is used, a Class 2 Permissive Change must be filed with the FCC.

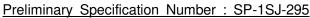
Changes or modifications not expressly approved by the manufacturer could void the user's authority to operate the equipment.

The LoRa module is for use with external antenna ONLY.

The certified antennas include:

- (1) PCB pattern Antenna with peak gain 1.0dBi.
- (2) Flexible Antenna (Molex: 2111400100) with peak gain 1.0dBi.

This module has been approved by FCC to operate with the antenna types with the maximum permissible gain indicated. Antenna types not included in this list, having a gain greater than the maximum gain indicated for that type, are strictly prohibited for use with this device.



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#### PLEASE READ THIS NOTICE BEFORE USING OUR PRODUCTS.

Please make sure that your product has been evaluated and confirmed from the aspect of the fitness for the specifications of our product when our product is mounted to your product.

All the items and parameters in this product specification/datasheet/catalog have been prescribed on the premise that our product is used for the purpose, under the condition and in the environment specified in this specification. You are requested not to use our product deviating from the condition and the environment specified in this specification.

Please note that the only warranty that we provide regarding the products is its conformance to the specifications provided herein. Accordingly, we shall not be responsible for any defects in products or equipment incorporating such products, which are caused under the conditions other than those specified in this specification.

WE HEREBY DISCLAIMS ALL OTHER WARRANTIES REGARDING THE PRODUCTS, EXPRESS OR IMPLIED, INCLUDING WITHOUT LIMITATION ANY WARRANTY OF FITNESS FOR A PARTICULAR PURPOSE, THAT THEY ARE DEFECT-FREE, OR AGAINST INFRINGEMENT OF INTELLECTUAL PROPERTY RIGHTS.

The product shall not be used in any application listed below which requires especially high reliability for the prevention of such defect as may directly cause damage to the third party's life, body or property. You acknowledge and agree that, if you use our products in such applications, we will not be responsible for any failure to meet such requirements.

Furthermore, YOU AGREE TO INDEMNIFY AND DEFEND US AND OUR AFFILIATES AGAINST ALL CLAIMS, DAMAGES, COSTS, AND EXPENSES THAT MAY BE INCURRED, INCLUDING WITHOUT LIMITATION, ATTORNEY FEES AND COSTS, DUE TO THE USE OF OUR PRODUCTS IN SUCH APPLICATIONS.

- Aircraft equipment.
- Aerospace equipment
- Undersea equipment.
- Power plant control equipment
- Medical equipment.
- Transportation equipment (vehicles, trains, ships, elevator, etc.).
- Traffic signal equipment.
- Disaster prevention / crime prevention equipment.
- Burning / explosion control equipment
- Application of similar complexity and/ or reliability requirements to the applications listed in the above.

We expressly prohibit you from analyzing, breaking, Reverse-Engineering, remodeling altering, and reproducing our product. Our product cannot be used for the product which is prohibited from being manufactured, used, and sold by the regulations and laws in the world.

We do not warrant or represent that any license, either express or implied, is granted under any our patent right, copyright, mask work right, or our other intellectual property right relating to any combination, machine, or process in which our products or services are used. Information provided by us regarding third-party products or services does not constitute a license from us to use such products or services or a warranty or endorsement thereof. Use of such information may require a license from a third party under the patents or other intellectual property of the third party, or a license from us under our patents or other intellectual property.

Please do not use our products, our technical information and other data provided by us for the purpose of developing of mass-destruction weapons and the purpose of military use. Moreover, you must comply with "foreign exchange and foreign trade law", the "U.S. export administration regulations", etc.

Please note that we may discontinue the manufacture of our products, due to reasons such as end of supply of materials and/or components from our suppliers.

Customer acknowledges that Murata will, if requested by you, conduct a failure analysis for defect or alleged defect of Products only at the level required for consumer grade Products, and thus such analysis may not always be available or be in accordance with your request (for example, in cases where the defect was caused by components in Products supplied to Murata from a third party).

By signing on specification sheet or approval sheet, you acknowledge that you are the legal representative for your company and that you understand and accept the validity of the contents herein. When you are not able to return the signed version of specification sheet or approval sheet within 90 days from receiving date of specification sheet or approval sheet, it shall be deemed to be your consent



Preliminary Specification Number: SP-1SJ-295

on the content of specification sheet or approval sheet.

Customer acknowledges that engineering samples may deviate from specifications and may contain defects due to their development status.

- We reject any liability or product warranty for engineering samples.

  In particular we disclaim liability for damages caused by

   the use of the engineering sample other than for evaluation purposes, particularly the installation or integration in the product to be sold by you,

   deviation or lapse in function of engineering sample,

   improper use of engineering samples.

  We disclaim any liability for consequential and incidental damages.

If you can't agree the above contents, you should inquire our sales.



## **OEM Guidance**

## 1. Applicable FCC rules

This module is granted by Single Modular Approval. It complies to the requirements of FCC part 15C, section 15.205, 15.207, 15.209, and 15.247 rules.

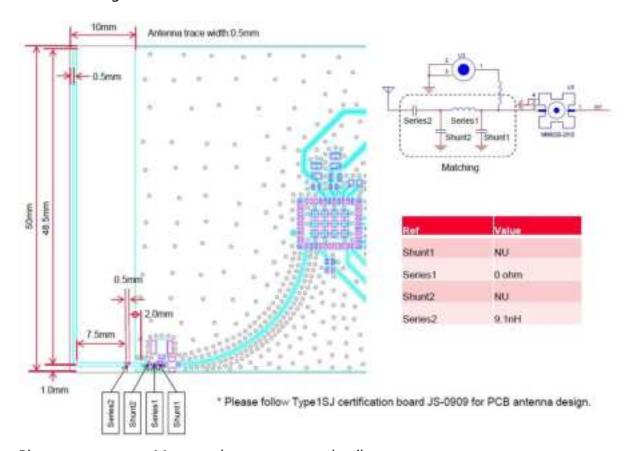
## 2. The specific operational use conditions

This module can be used in IoT devices. The input voltage to the module is nominally 3.3VDC. The operational ambient temperature of the module is -40 to 85 degree C.

#### 3. Limited module procedures

N/A.

#### 4. Trace antenna design



Please contact your Murata sales to get more details.

#### 5. RF exposure considerations

The equipment complies with FCC radiation exposure limits set forth for an uncontrolled environment. This equipment should be installed and operated with minimum distance 20cm between the radiator and your body. If the equipment built into a host as a portable usage, the additional RF exposure evaluation may be required as specified by 2.1093.



#### 6. Antenna

The certified antennas include:

PCB pattern Antenna with peak gain 1.0dBi.

Flexible Antenna (Molex: 2111400100) with peak gain 1.0dBi.

### 7. Label and compliance information

An exterior label on OEM's end product can use wording such as the following: "Contains Transmitter Module FCC ID: VPYLBAA0QB1SJ" or "Contains FCC ID: VPYLBAA0QB1SJ." In addition, the following statements shall also appear on the surface of the product.

This device complies with part 15 of the FCC Rules. Operation is subject to the following two conditions: (1) This device may not cause harmful interference, and (2) this device must accept any interference received, including interference that may cause undesired operation.

## 8. Information on test modes and additional testing requirements

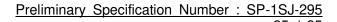
a)The modular transmitter has been fully tested by the module grantee on the required number of channels, modulation types, and modes, it should not be necessary for the host installer to re-test all the available transmitter modes or settings. It is recommended that the host product manufacturer, installing the modular transmitter, perform some investigative measurements to confirm that the resulting composite system does not exceed the spurious emissions limits or band edge limits (e.g., where a different antenna may be causing additional emissions).

b)The testing should check for emissions that may occur due to the intermixing of emissions with the other transmitters, digital circuitry, or due to the physical properties of the host product (enclosure). This investigation is especially important when integrating multiple modular transmitters where the certification is based on testing each of them in a stand-alone configuration. It is important to note that host product manufacturers should not assume that because the modular transmitter is certified that they do not have any responsibility for final product compliance.

c)If the investigation indicates a compliance concern the host product manufacturer is obligated to mitigate the issue. Host products using a modular transmitter are subject to all the applicable individual technical rules as well as to the general conditions of operation in Sections 15.5, 15.15, and 15.29 to not cause interference. The operator of the host product will be obligated to stop operating the device until the interference has been corrected.

#### 9. Additional testing, Part 15B disclaimer

The final host/module combination needs to be evaluated against the FCC Part 15B criteria for unintentional radiators to be properly authorized for operation as a Part 15 digital device.





The host integrator installing this module into their product must ensure that the final composite product complies with the FCC requirements by a technical assessment or evaluation to the FCC rules, including the transmitter operation and should refer to guidance in KDB 996369. For host products with the certified modular transmitter, the frequency range of investigation of the composite system is specified by rule in Sections 15.33(a)(1) through (a)(3), or the range applicable to the digital device, as shown in Section 15.33(b)(1), whichever is the higher frequency range of investigation. When testing the host product, all the transmitters must be operating. The transmitters can be enabled by using publicly available drivers and turned on, so the transmitters are active. When testing for emissions from the unintentional radiator, the transmitter shall be placed in the receive mode or idle mode, if possible. In these cases, this would need to enable activity on the communication BUS (i.e., PCIe, SDIO, USB) to ensure the unintentional radiator circuitry is enabled. Testing laboratories may need to add attenuation or filters depending on the signal strength of any active beacons (if applicable) from the enabled radio(s). See ANSI C63.4, ANSI C63.10 and ANSI C63.26 for further general testing details.

The product under test is set into a link/association with a partnering device, as per the normal intended use of the product. To ease testing, the product under test is set to transmit

at a high duty cycle, such as by sending continuous packets.