

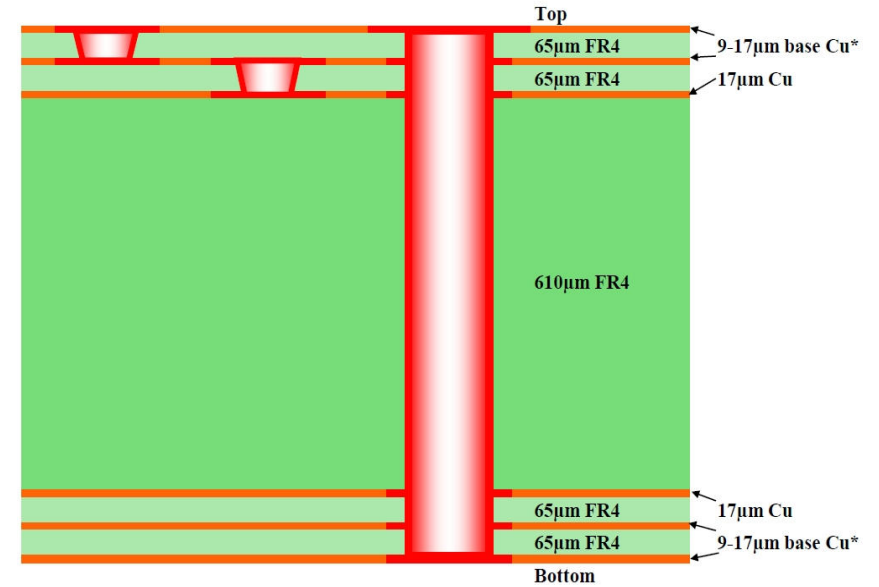
WMI Mainboard

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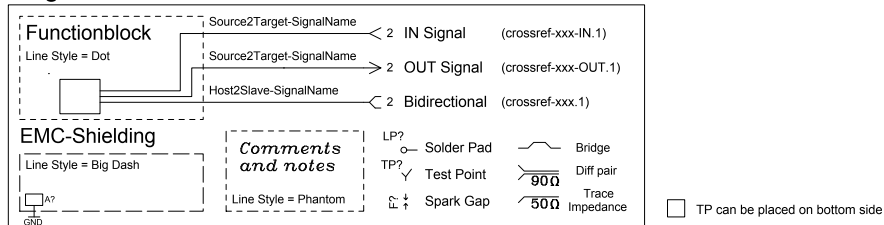
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PCB-Stackup:

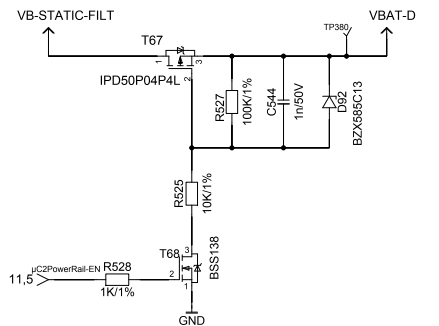
6M12i65i65i610iMicV1



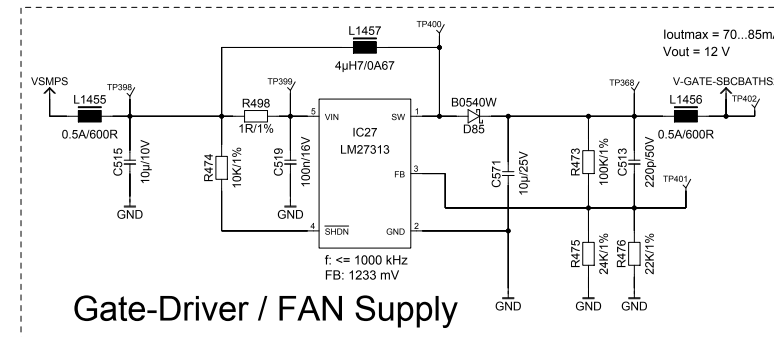
Legend:



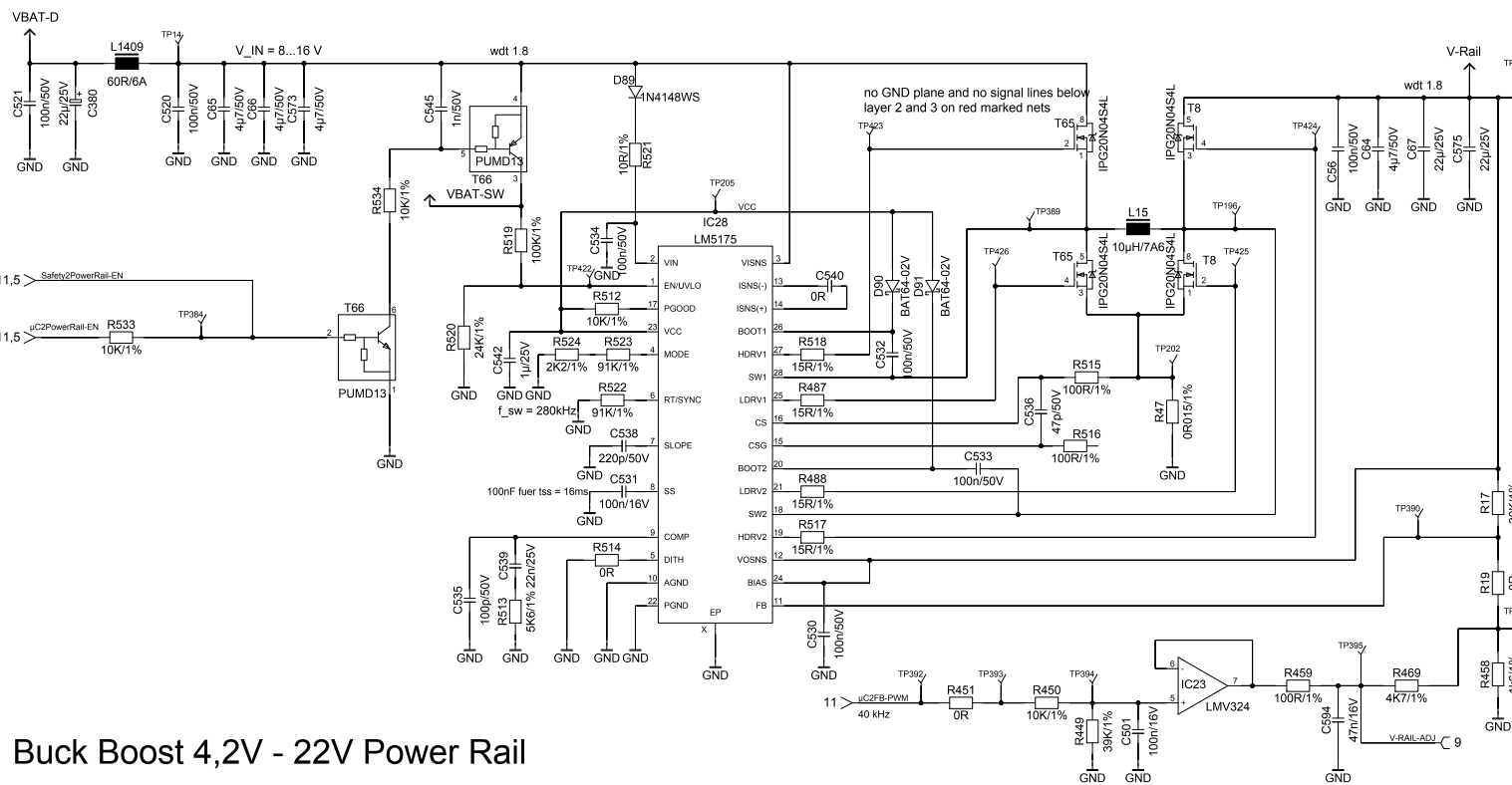
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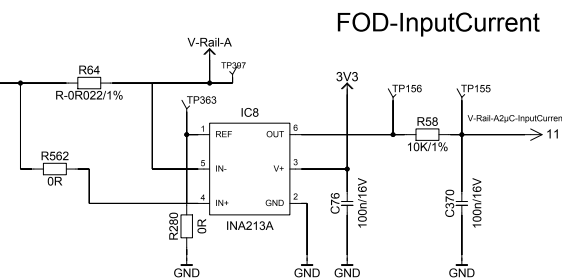
Reverse Polarity Protection



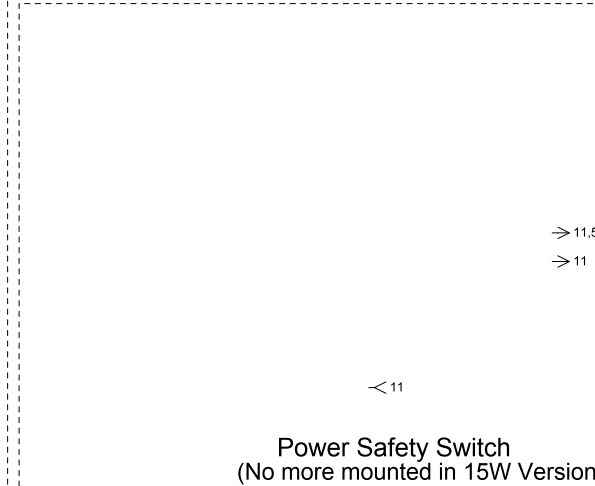
Gate-Driver / FAN Supply



Buck Boost 4,2V - 22V Power Rail



FOD-InputCurrent

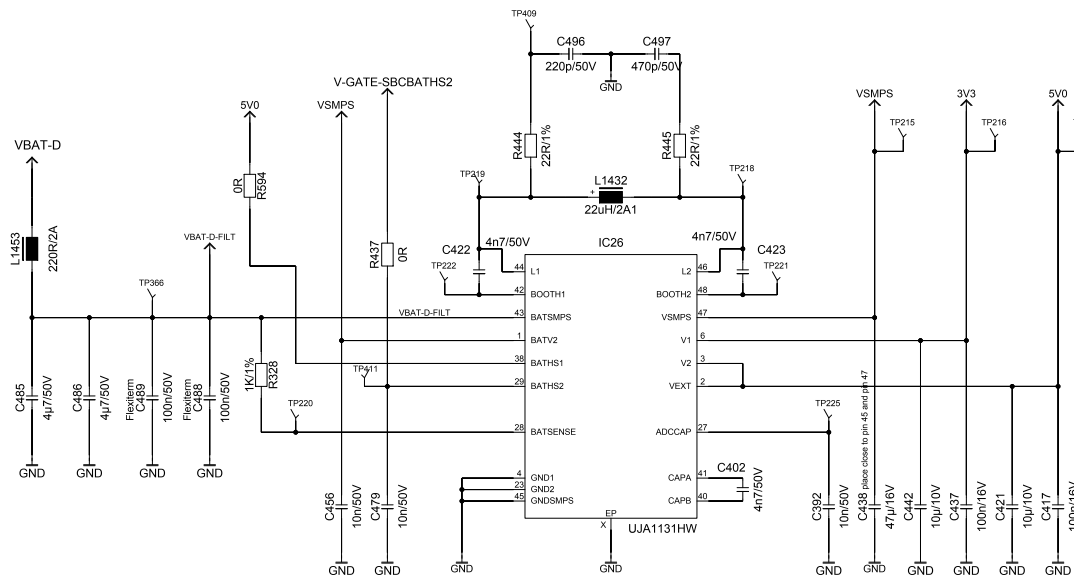


Power Safety Switch
(No more mounted in 15W Version)

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		Viewed		see page 1		see page 1			
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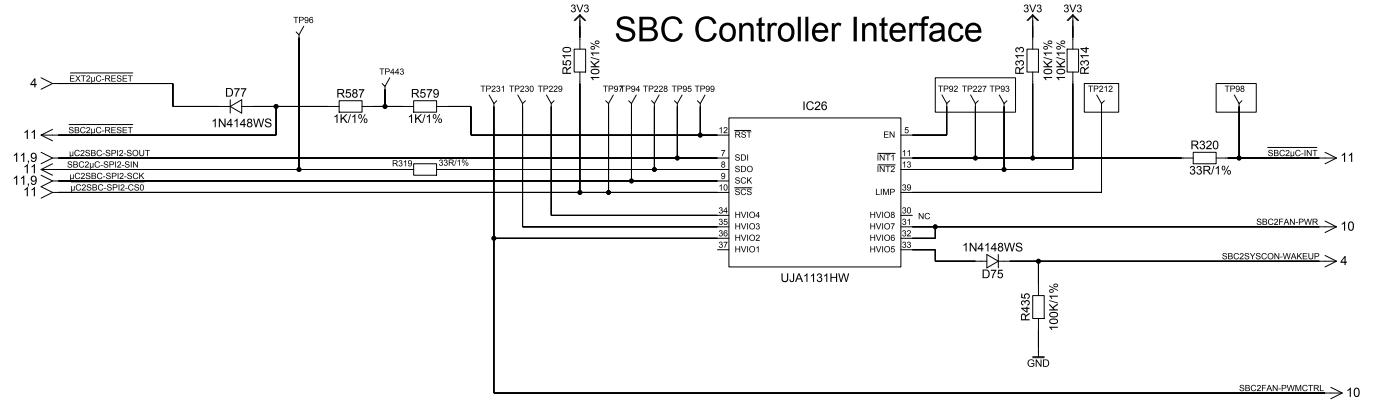
Circuit Diagrams and Component Placement - Drawings are also available in PDF - Format II.

SBC Power Supply

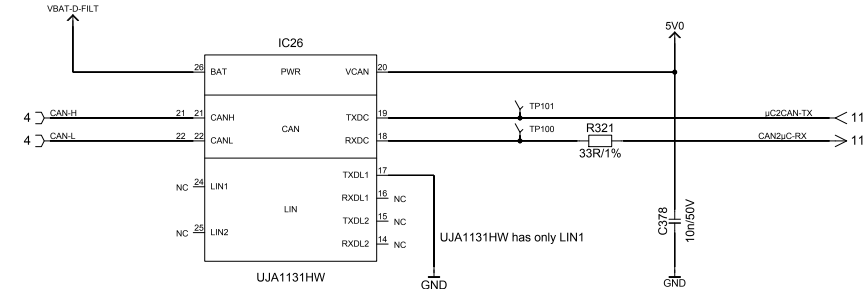


TP443 can be used to separate the reset between the μ C and SBC during the flash process by applying 3,3V to the TP.

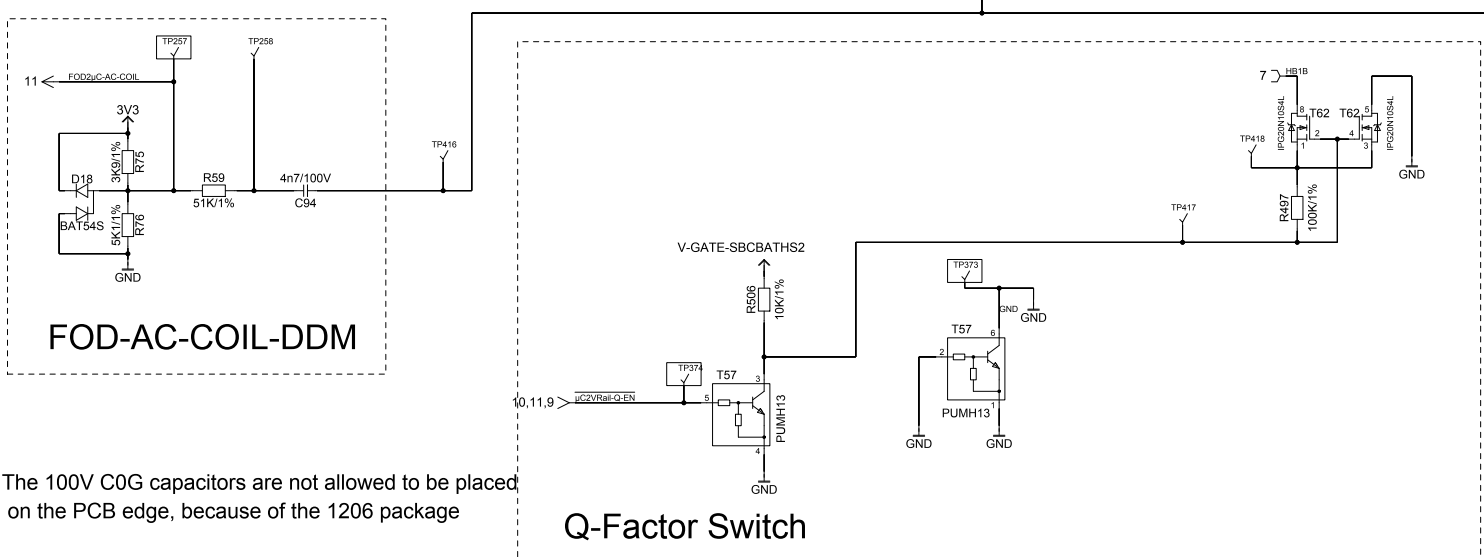
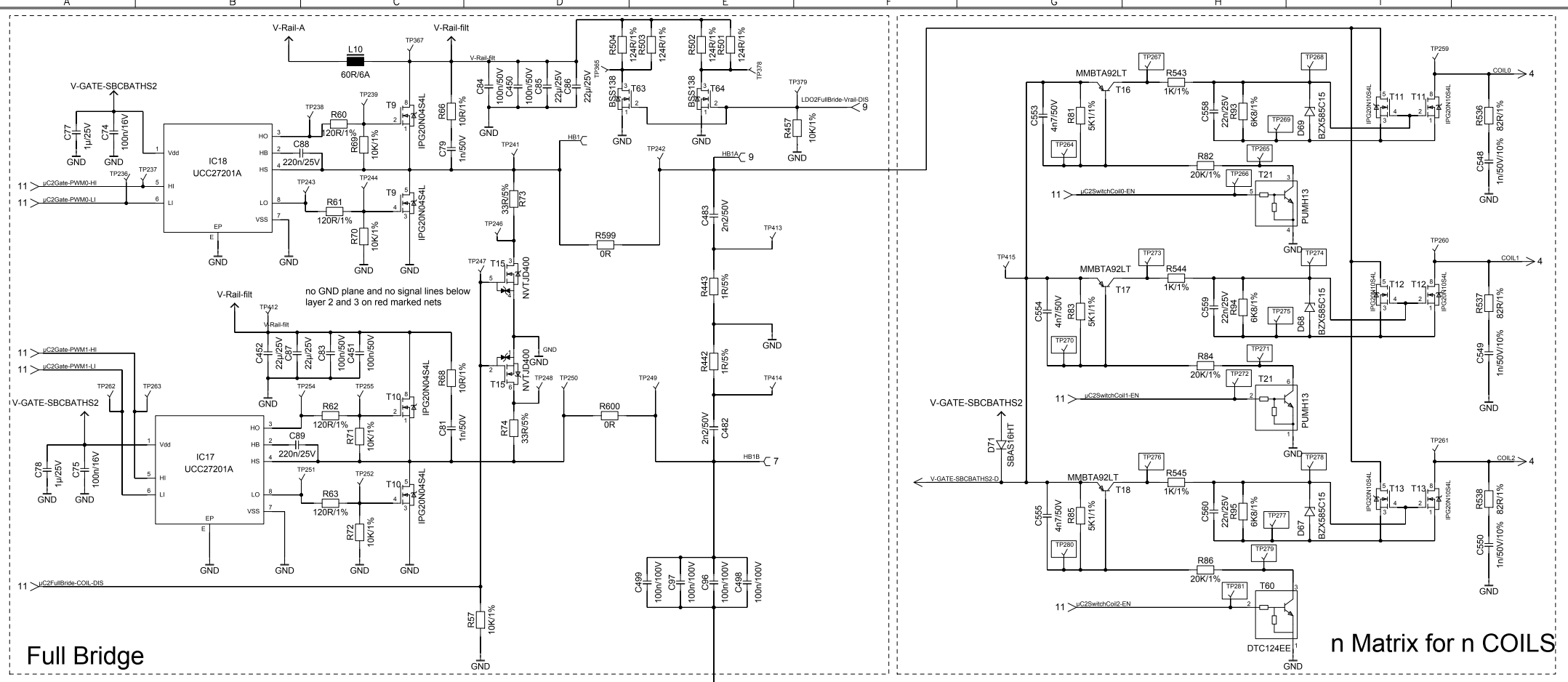
SBC Controller Interface



SBC CAN / LIN Interface

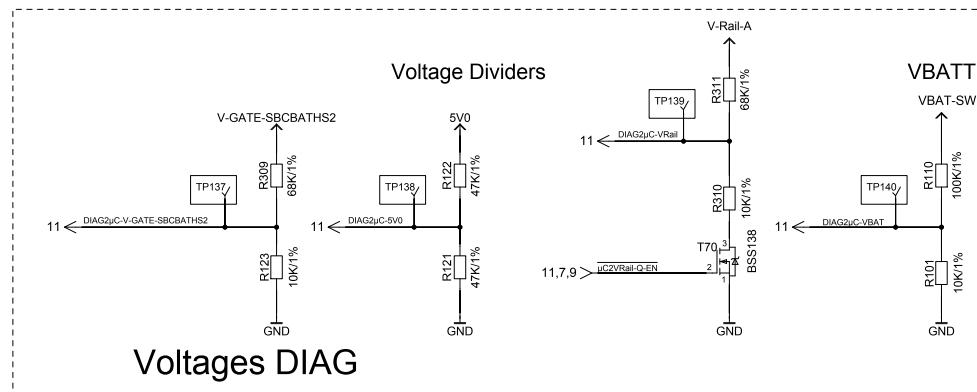


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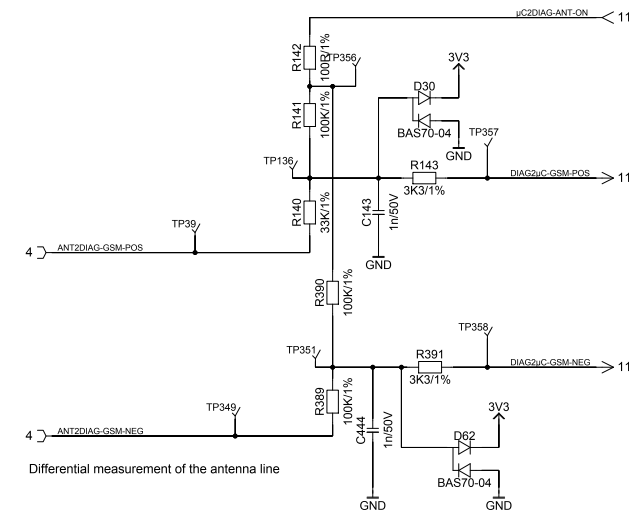


The 100V C0G capacitors are not allowed to be placed on the PCB edge, because of the 1206 package

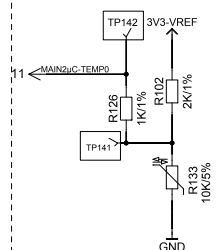
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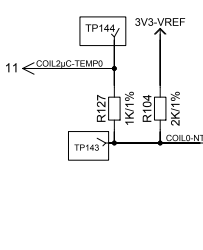
Antenna DIAG for LP1450-xx3 and xx4



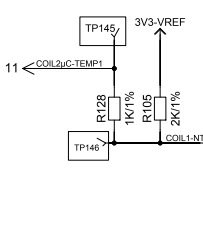
Main-Temperature



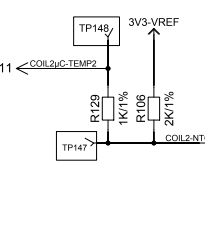
COIL0-Temperature



COIL1-Temperature



COIL2-Temperature

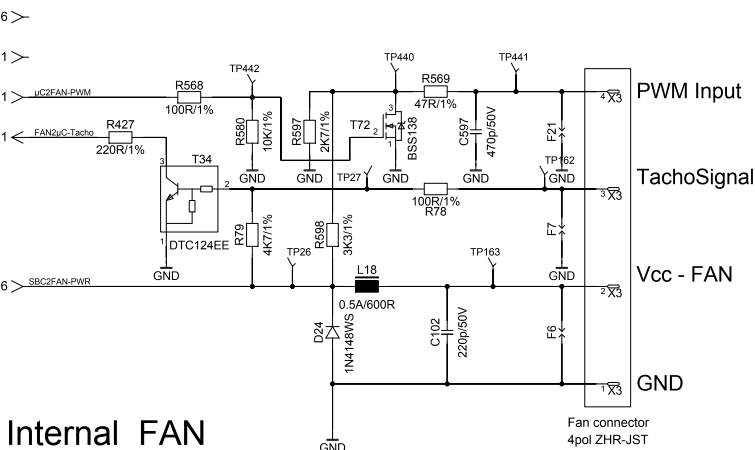
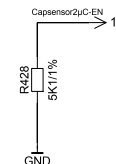


Temperature DIAG

Neue TP müssen mit der PME abgeklärt werden !

Capacitive Sensor (depending on variant)

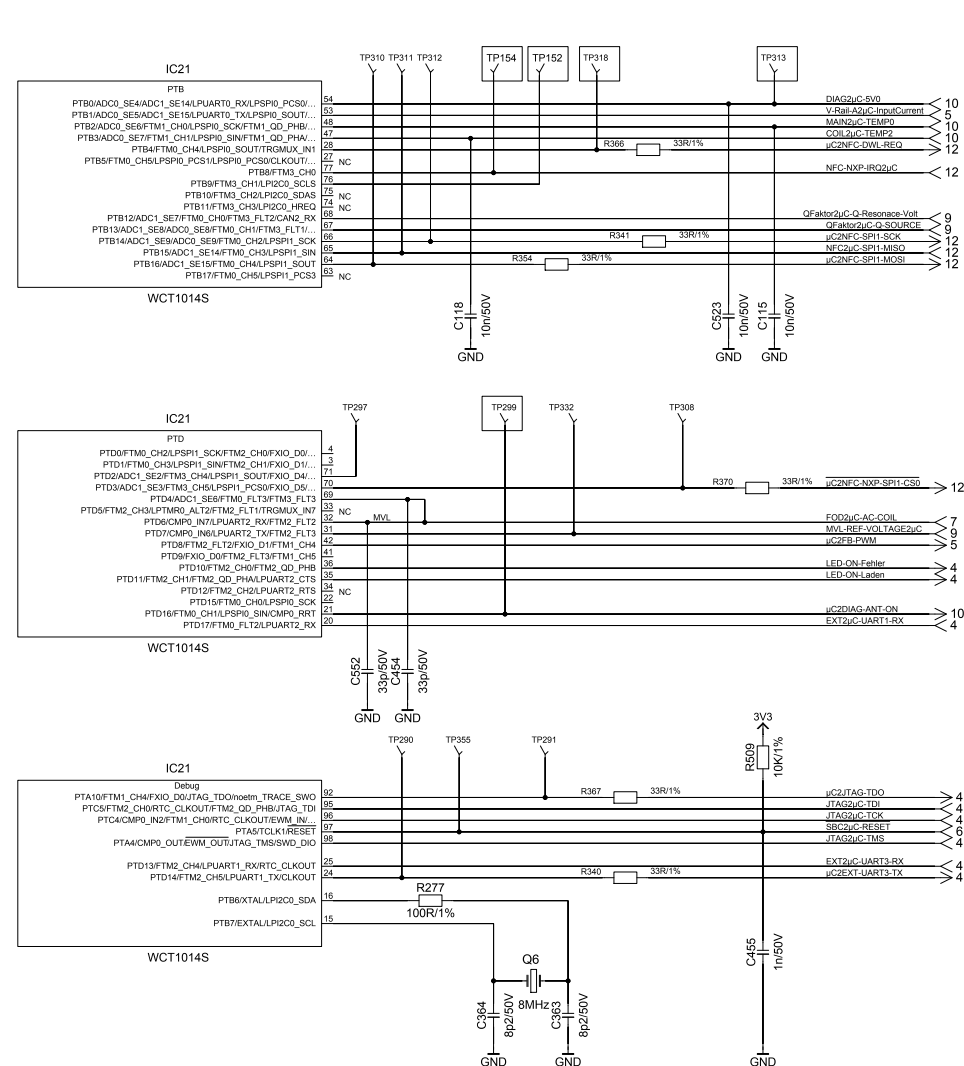
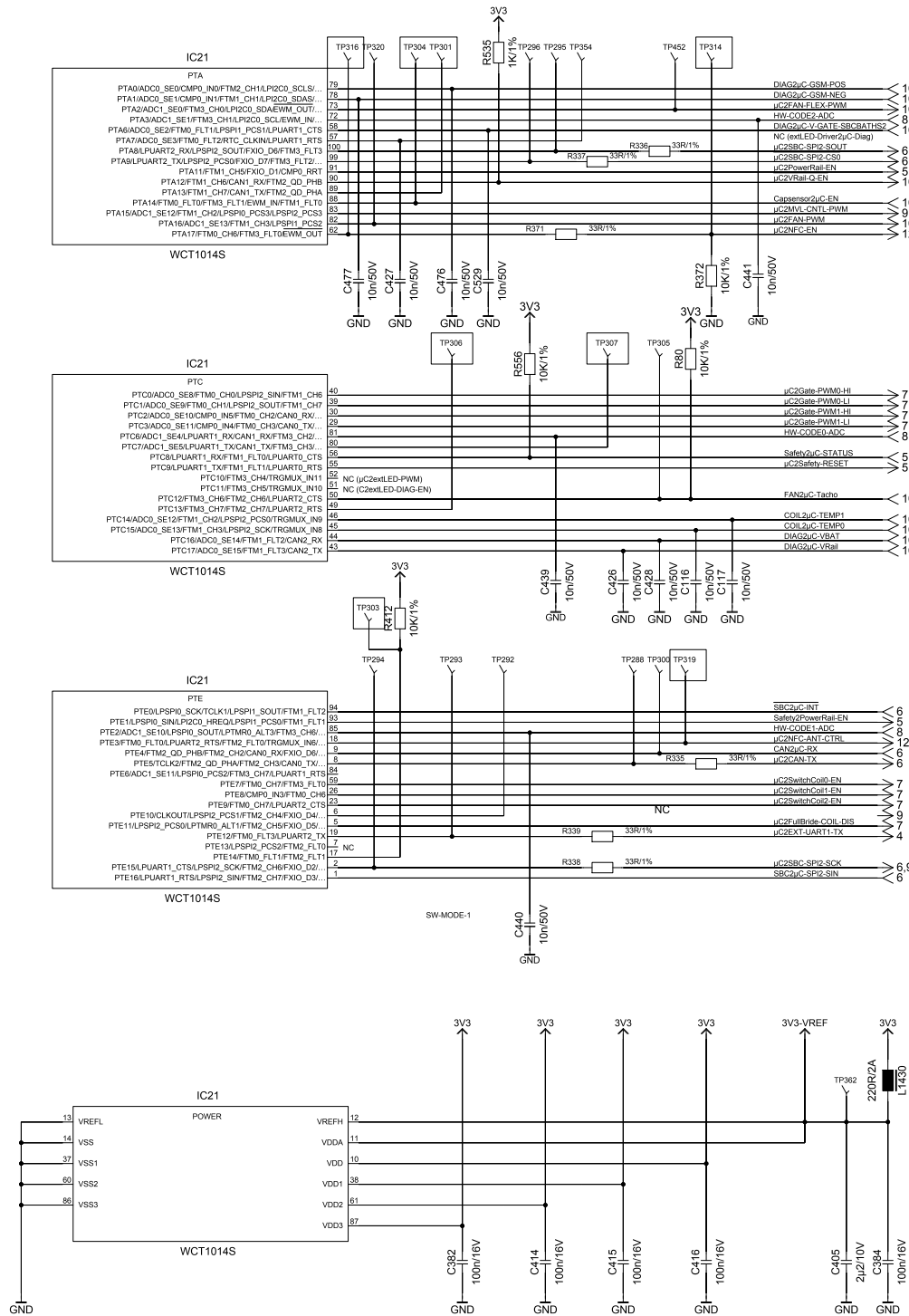
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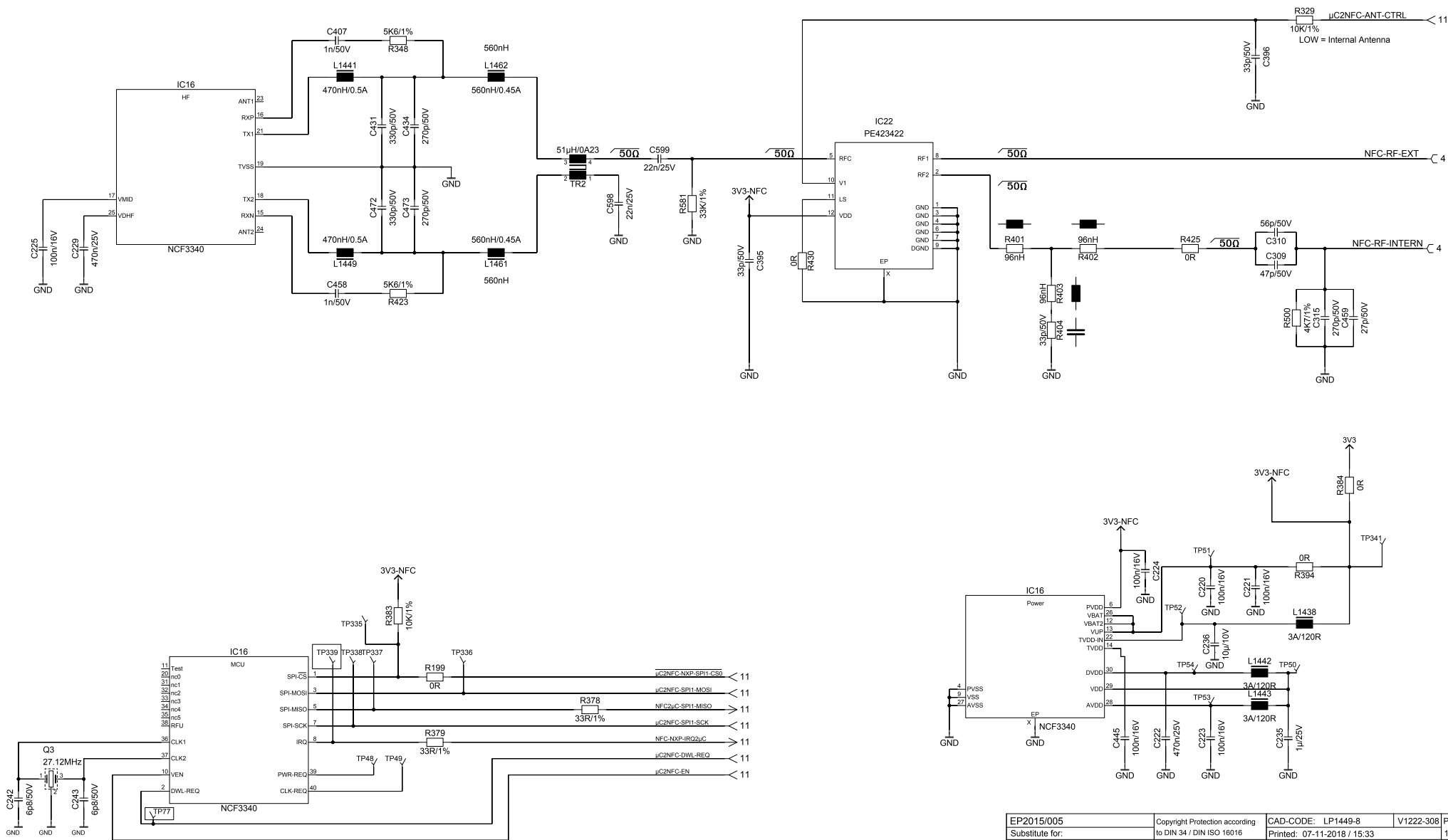
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Valeo

Circuit Diagrams and Component Placement - Drawings are also available in PDF - Format II.



NFC-Chip NXP Controller



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