## RYDER ELECTRONICS(SHENZHEN)LTD

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Date: 2024-12-18

Federal Communications Commission Authorization and Evaluation Division 7435 Oakland Mills Road Columbia, MD 21046

SUBJECT: FCC Application for FCC ID: 2A3BF-RPW001

To Whom It May Concern:

RF001 and RF002 chips are uncompressed digital audio wireless transmitter and receiver chips operating in the 5.8GHz frequency band.

DPDT makes transmit and receive path into one antenna. After TX model(RF001 chip) and RX model(RF002 chip) are paired, bi-directional control is performed by data encryption data messages. This is the data encryption for private protocols. Encryption is performed using the same IP address and is not connected to other host products.

The RF001 and RF002 chips itself provides the basic functions of Audio Processing and buffering, Data Link Layer and Physical Layer. The chips integrates all functionality for a wireless digital audio connection, comprising: Wireless Audio Processor, I2C control interface, The stamp hole connector for power, digital audio and control interface and GPIOs.

Sincerely,

Signatory

[Gary Zhang, DQA Deputy Manager]