

Appendix A: 20dB Emission Bandwidth (EBW)



1 Result Table

EUT Conf.	EBW [MHz]	Verdict
TM1_DH5_Ch0	0.95	Pass
TM1_DH5_Ch39	0.95	Pass
TM1_DH5_Ch78	0.96	Pass
TM2_2DH5_Ch0	1.28	Pass
TM2_2DH5_Ch39	1.28	Pass
TM2_2DH5_Ch78	1.28	Pass
TM3_3DH5_Ch0	1.28	Pass
TM3_3DH5_Ch39	1.28	Pass
TM3_3DH5_Ch78	1.28	Pass

2 Test Plot

2.1 TM1_DH5_Ch0





2.2 TM1_DH5_Ch39



2.3 TM1_DH5_Ch78



2.4 TM2_2DH5_Ch0



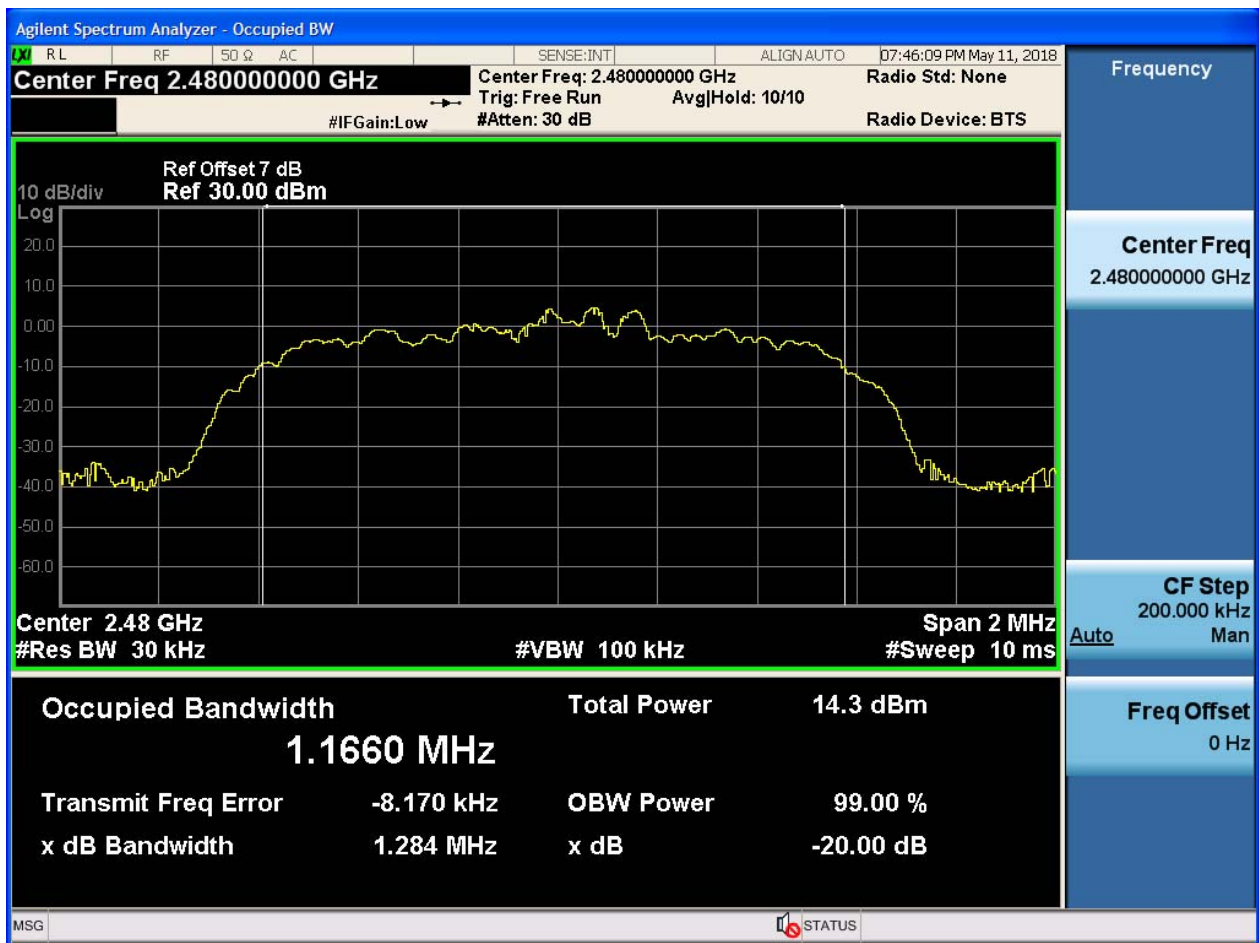


2.5 TM2_2DH5_Ch39





2.6 TM2_2DH5_Ch78





2.7 TM3_3DH5_Ch0





2.8 TM3_3DH5_Ch39





2.9 TM3_3DH5_Ch78



Appendix B: Carrier Frequency Separation

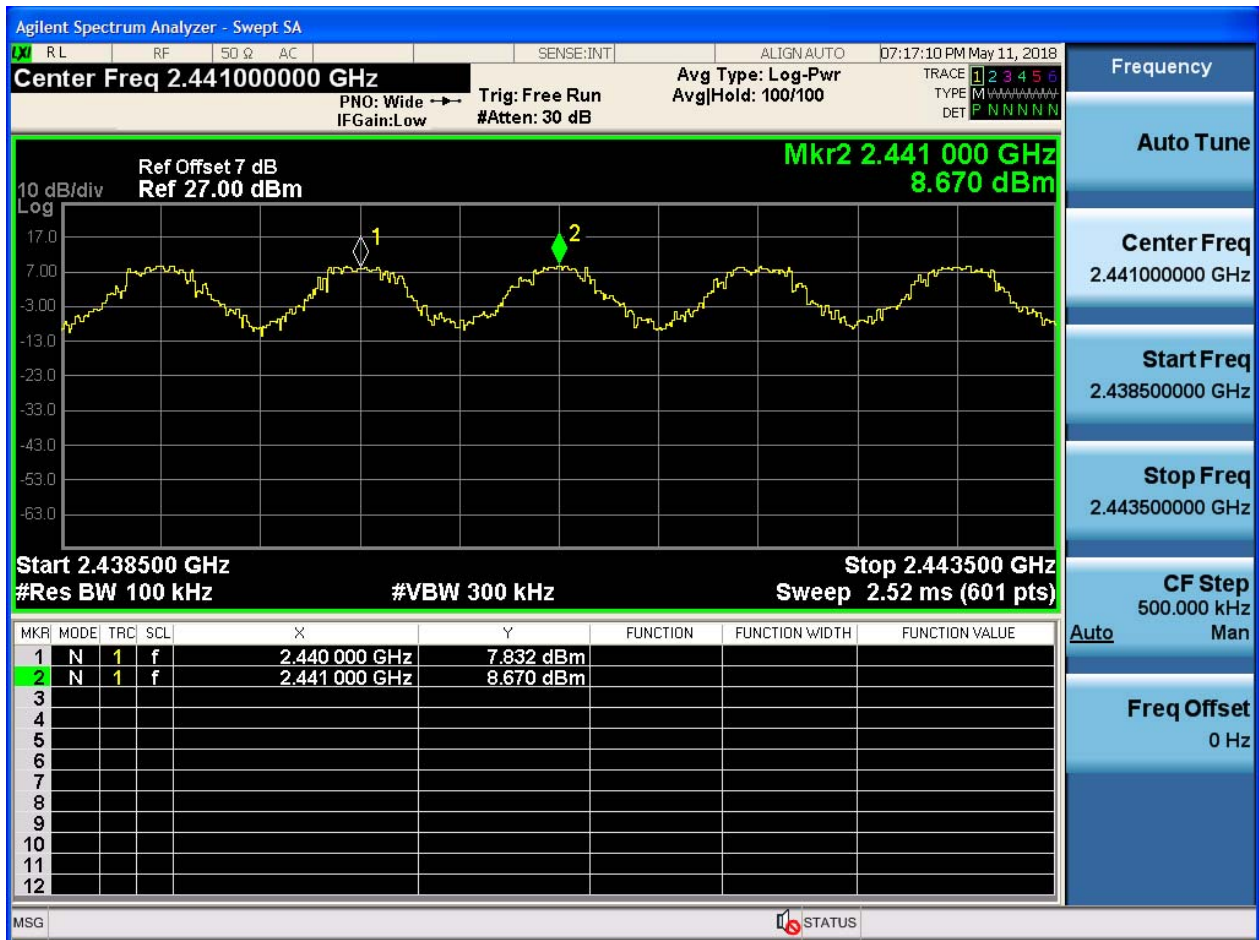


1 Result Table

EUT Conf.	Carrier Frequency Separation [MHz]	Verdict
TM1_DH5_Hop	1	Pass
TM2_2DH5_Hop	1	Pass
TM3_3DH5_Hop	1.35	Pass

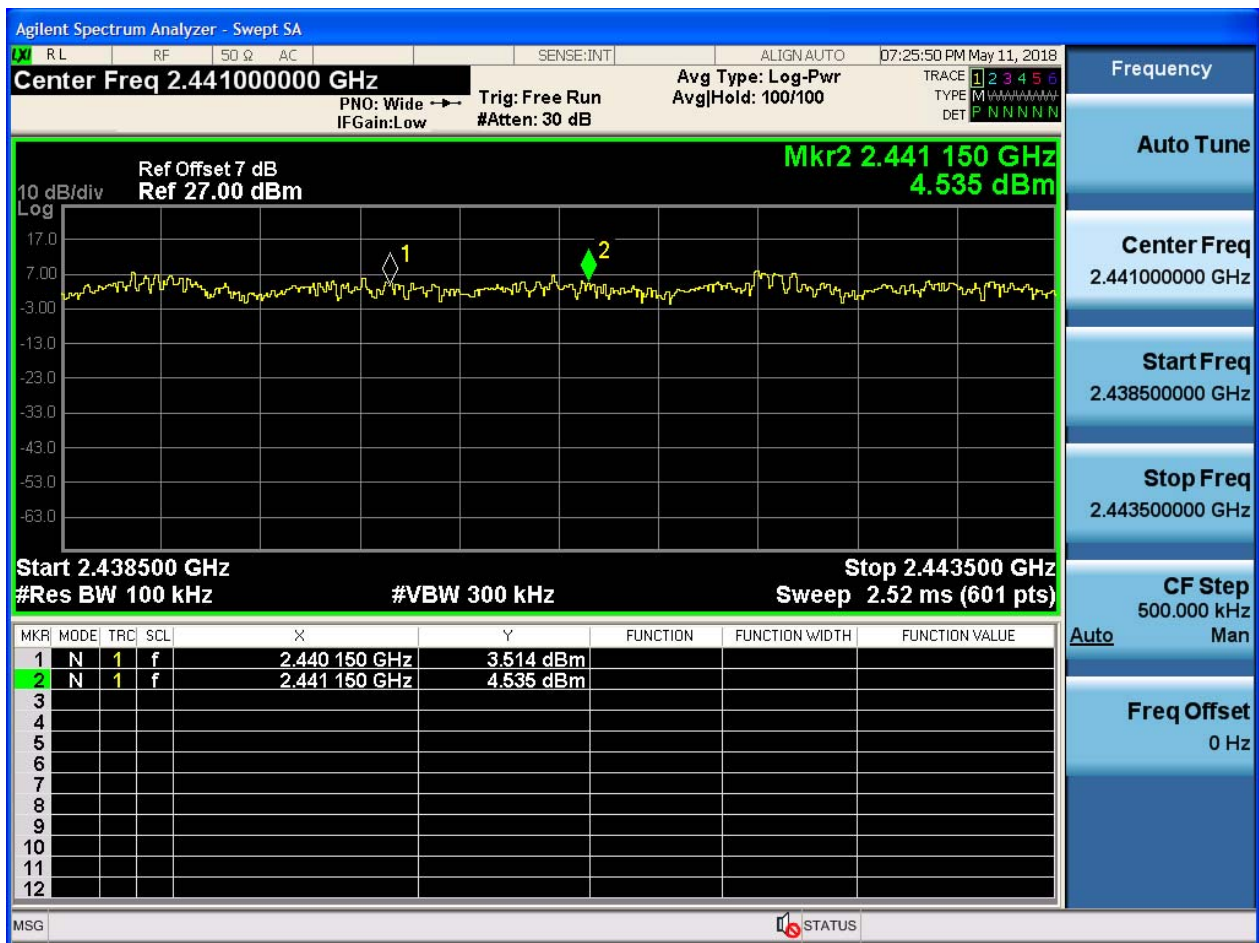
2 Test Plot

2.1 TM1_DH5_Hop

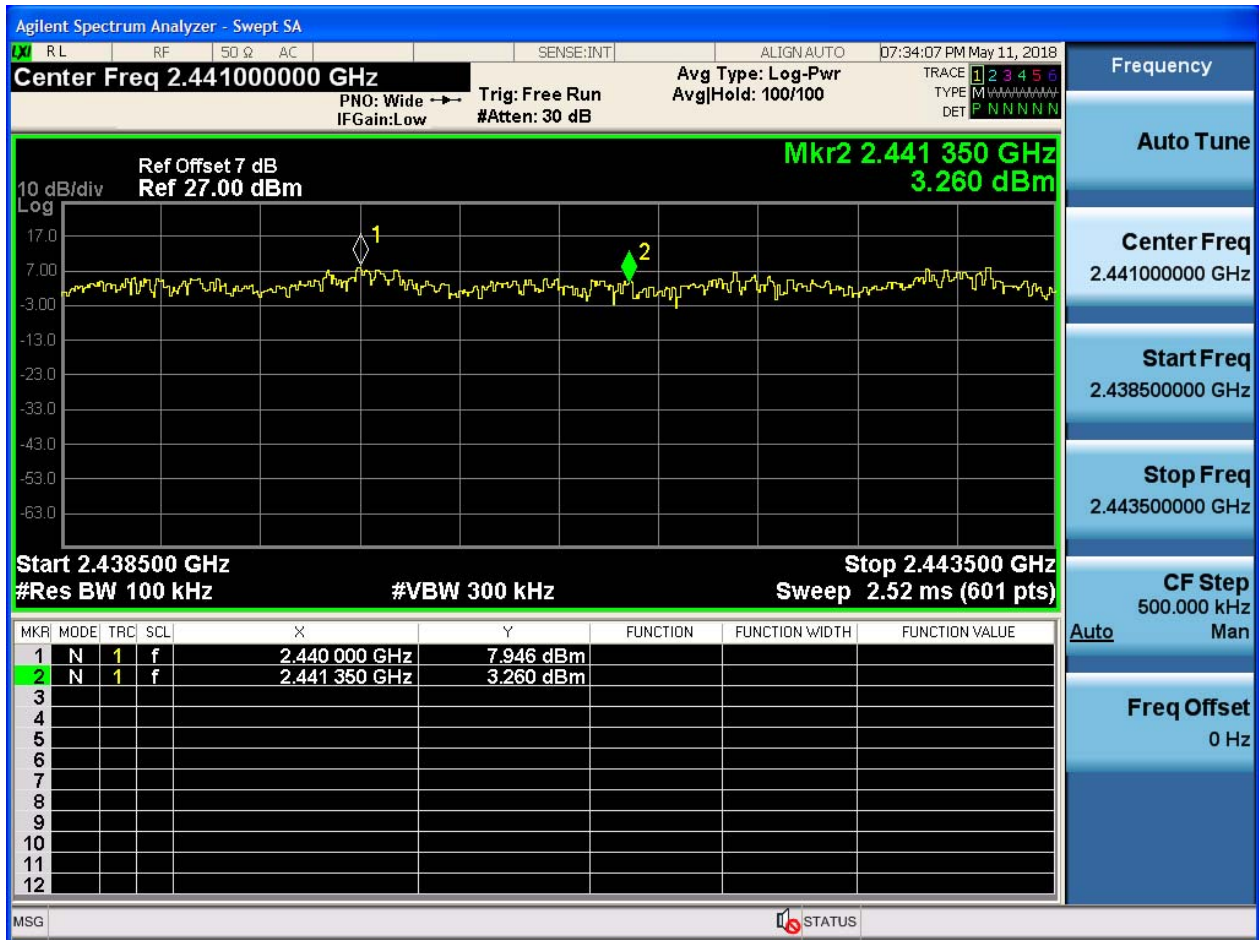




2.2 TM2_2DH5_Hop



2.3 TM3_3DH5_Hop



Appendix C: Number of Hopping Channel



1 Result Table

EUT Conf.	Number of Hopping Channel	Verdict
TM1_DH5_Hop	79	Pass
TM2_2DH5_Hop	79	Pass
TM3_3DH5_Hop	79	Pass

2 Test Plot

2.1 TM1_DH5_Hop

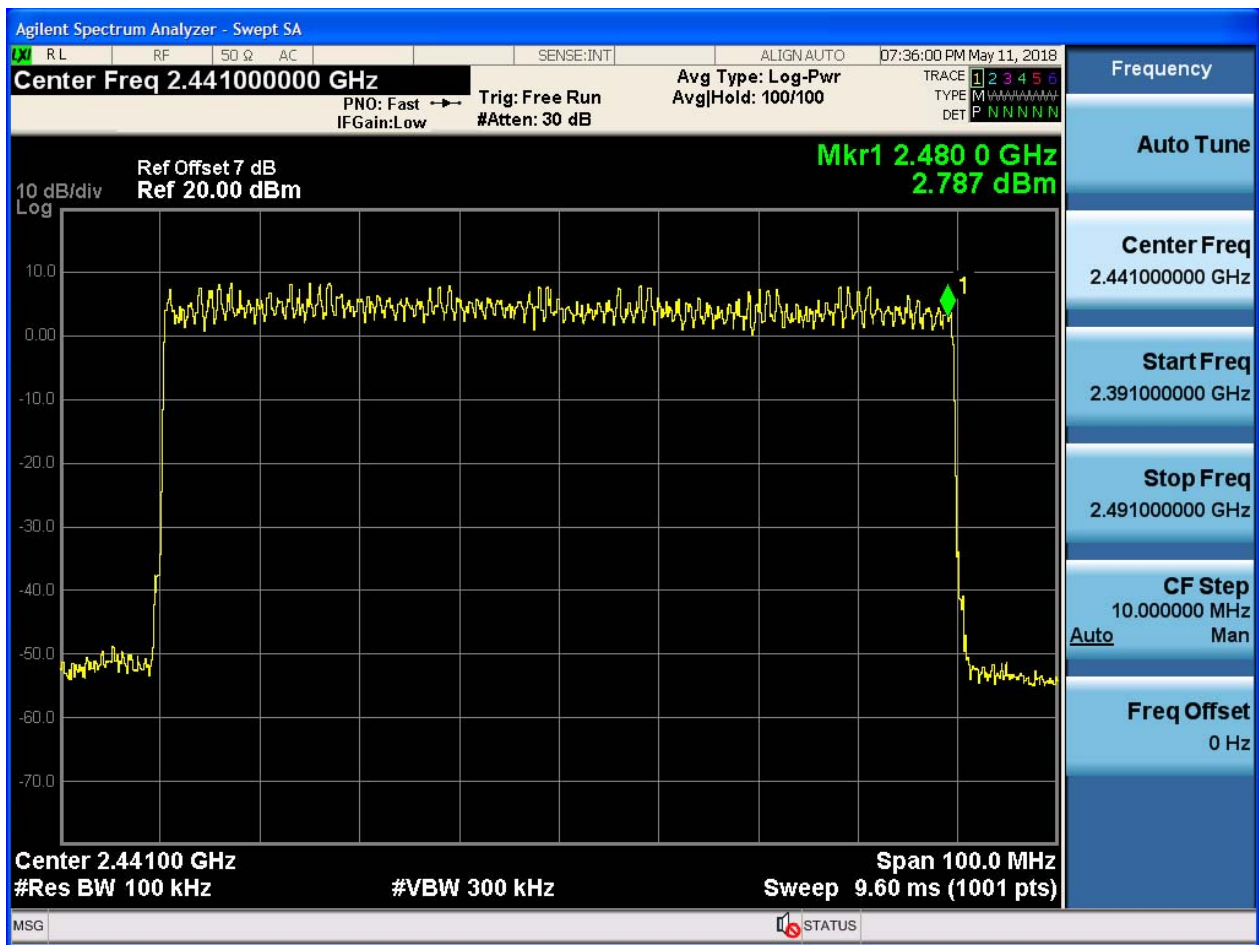


2.2 TM2_2DH5_Hop





2.3 TM3_3DH5_Hop



Appendix D: Time of Occupancy (Dwell Time)

1 Result Table

The Dwell Time = Burst Width * Total Hops. The detailed calculations are showed as follows:

- The duration for dwell time calculation: $0.4 \text{ [s]} * \text{hopping number} = 0.4 \text{ [s]} * 79 \text{ [ch]} = 31.6 \text{ [s*ch]}$;
- The burst width [ms/hop/ch], which is directly measured, refers to the duration on one channel hop.
- The hops per second for all channels: The selected EUT Conf uses a slot type of 5-Tx&1-Rx and a hopping rate of 1600 [ch*hop/s] for all channels. So the final hopping rate for all channels is $1600 / 6 = 266.67 \text{ [ch*hop/s]}$;
- The hops per second on one channel: $266.67 \text{ [ch*hop/s]} / 79 \text{ [ch]} = 3.38 \text{ [hop/s]}$;
- The total hops for all channels within the dwell time calculation duration: $3.38 \text{ [hop/s]} * 31.6 \text{ [s*ch]} = 106.67 \text{ [hop*ch]}$;
- The dwell time for all channels hopping: $106.67 \text{ [hop*ch]} * \text{Burst Width [ms/hop/ch]}$.

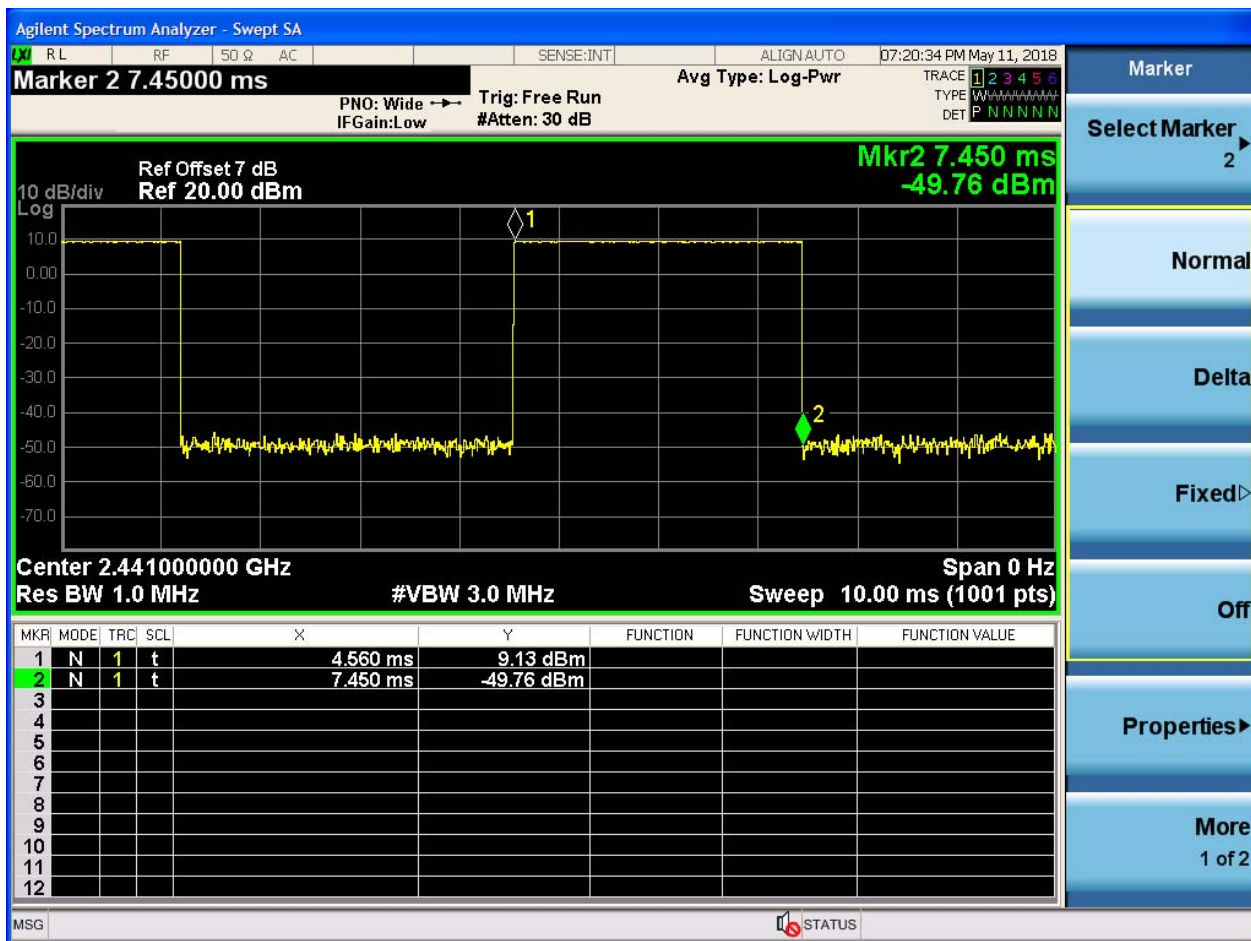
EUT Conf.	Burst Width [s/hop/ch]	Total Hops [hop*ch]	Dwell Time [ms]	Verdict
TM1_DH5_Ch39	0.0029	106.67	0.309	Pass
TM2_2DH5_Ch39	0.0029	106.67	0.309	Pass
TM3_3DH5_Ch39	0.0029	106.67	0.309	Pass



2 Test Plot

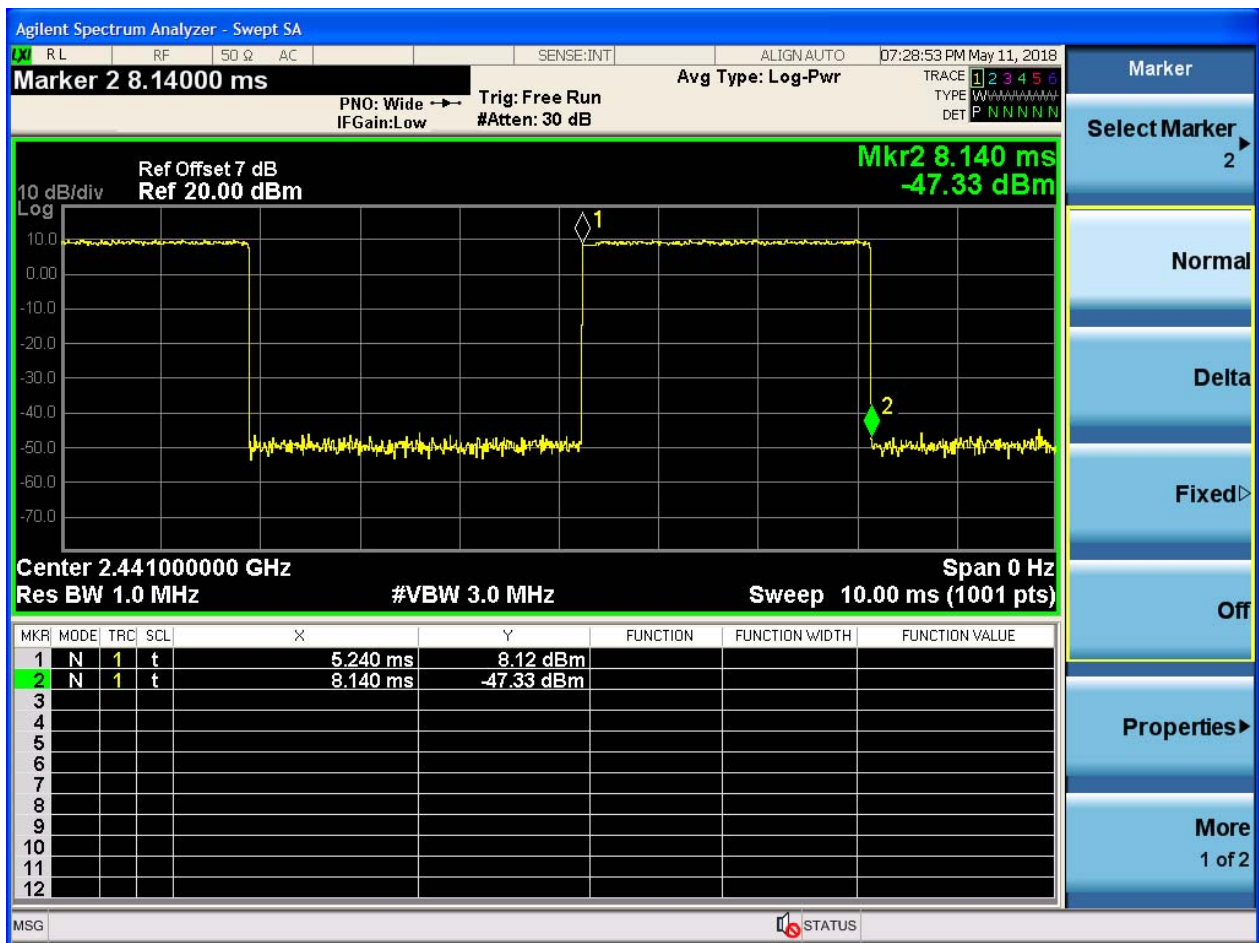
NOTE: The test plots are only for Burst Width measurements.

2.1 TM1_DH5_Ch39



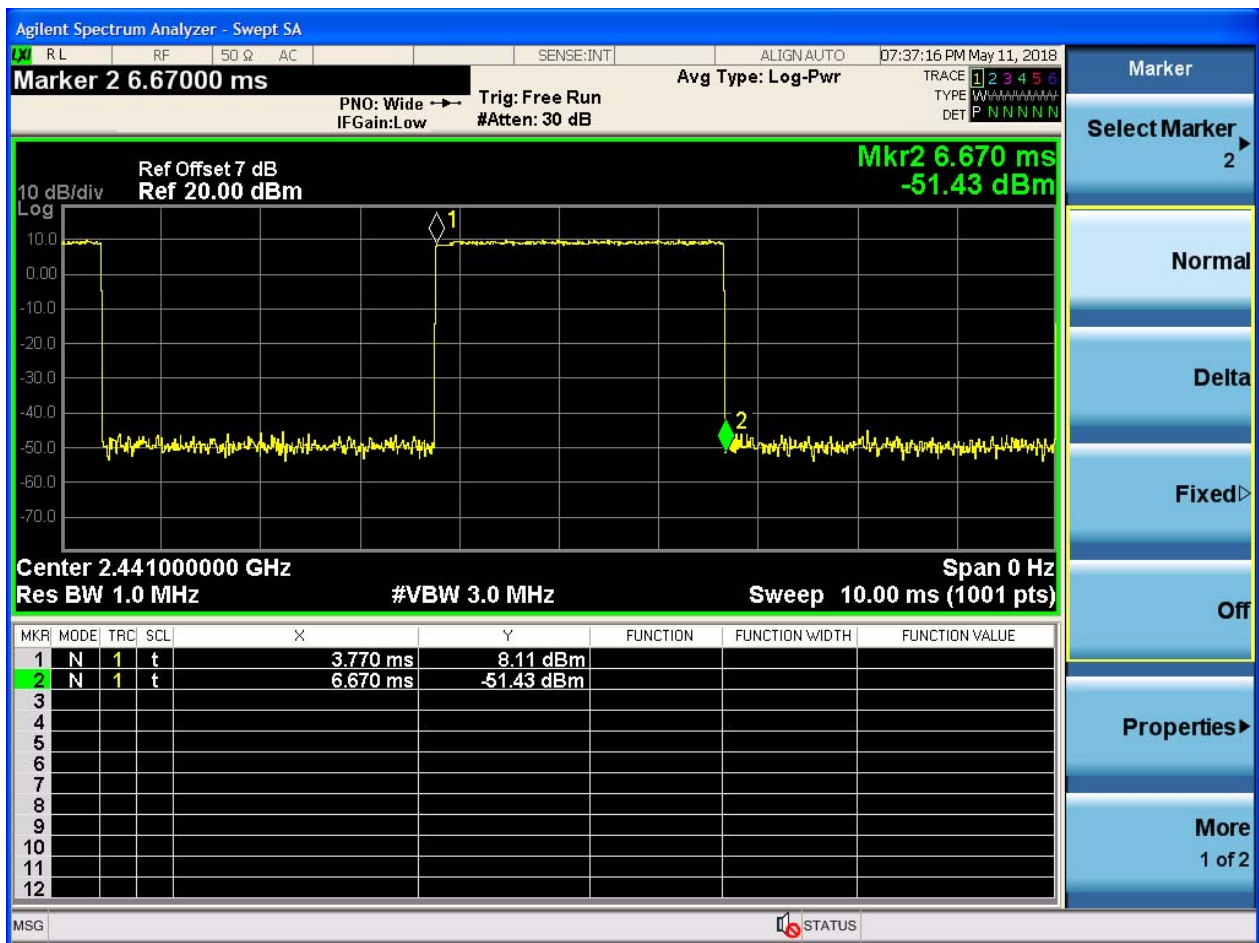


2.2 TM2_2DH5_Ch39





2.3 TM3_3DH5_Ch39



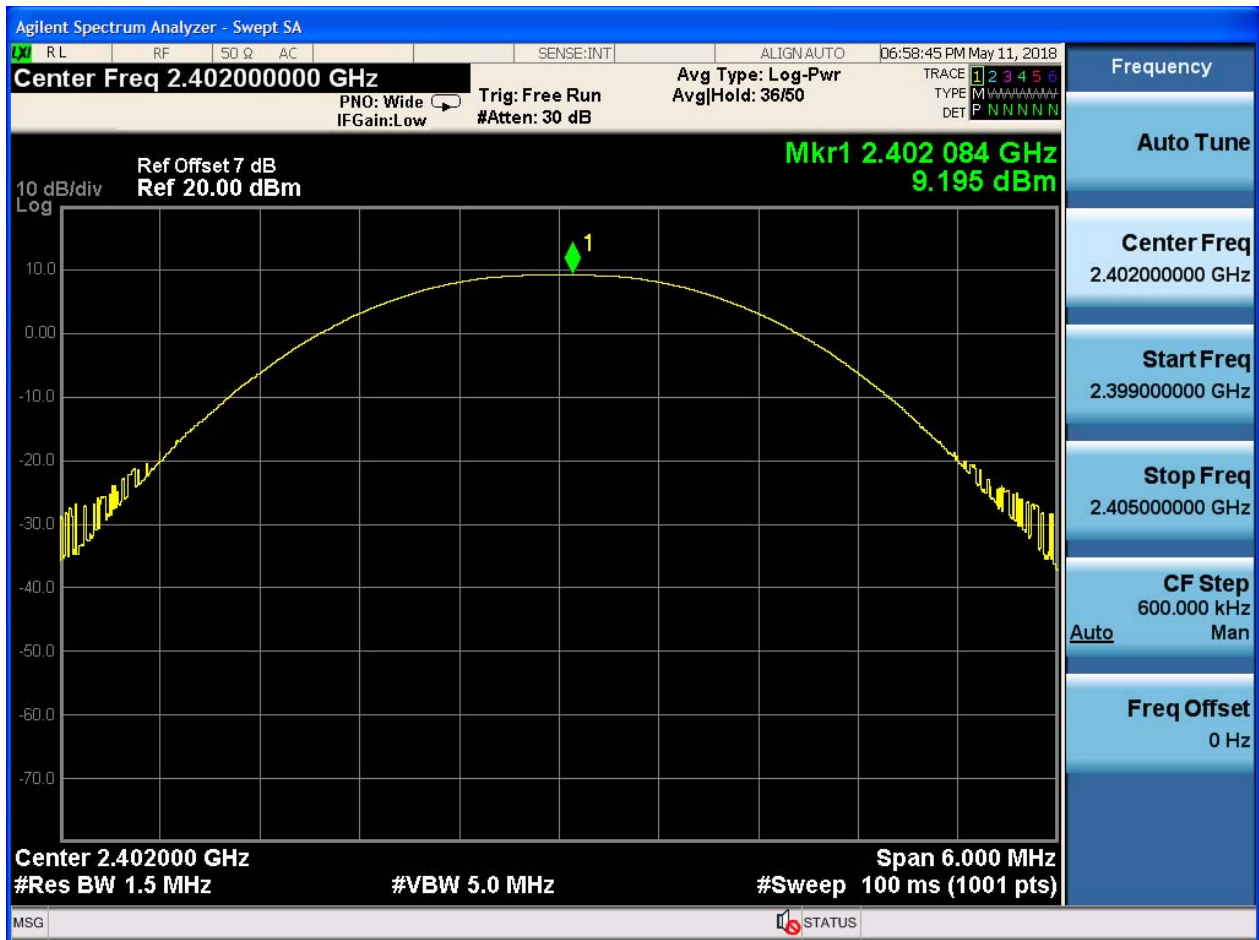
Appendix E: Maximum Peak Conducted Output Power

1 Result Table

EUT Conf.	Max. Peak Power [dBm]	Verdict
TM1_DH5_Ch0	9.195	Pass
TM1_DH5_Ch39	9.468	Pass
TM1_DH5_Ch78	8.218	Pass
TM2_2DH5_Ch0	9.935	Pass
TM2_2DH5_Ch39	10.213	Pass
TM2_2DH5_Ch78	9.061	Pass
TM3_3DH5_Ch0	10.116	Pass
TM3_3DH5_Ch39	10.216	Pass
TM3_3DH5_Ch78	9.072	Pass

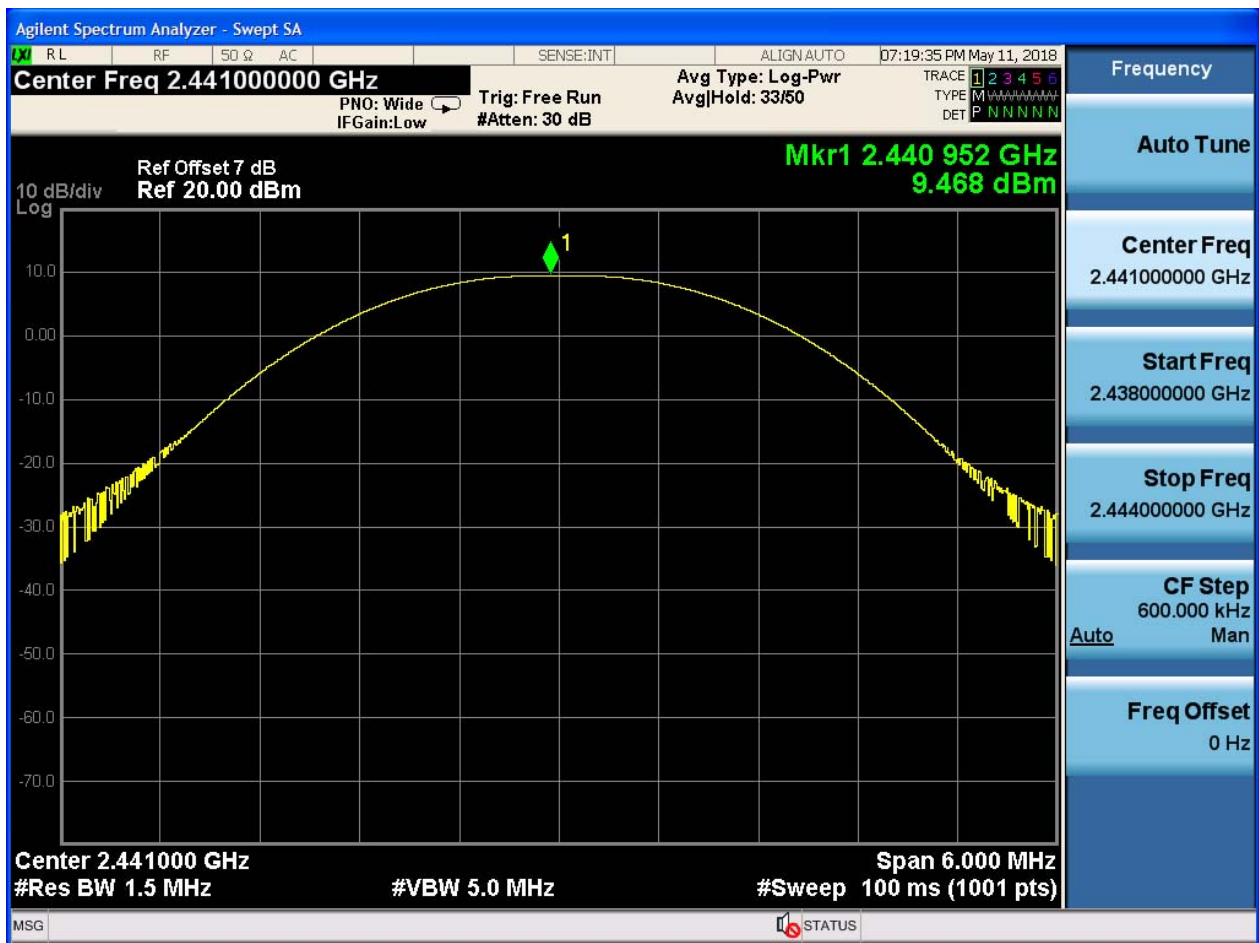
2 Test Plot

2.1 TM1_DH5_Ch0





2.2 TM1_DH5_Ch39



2.3 TM1_DH5_Ch78

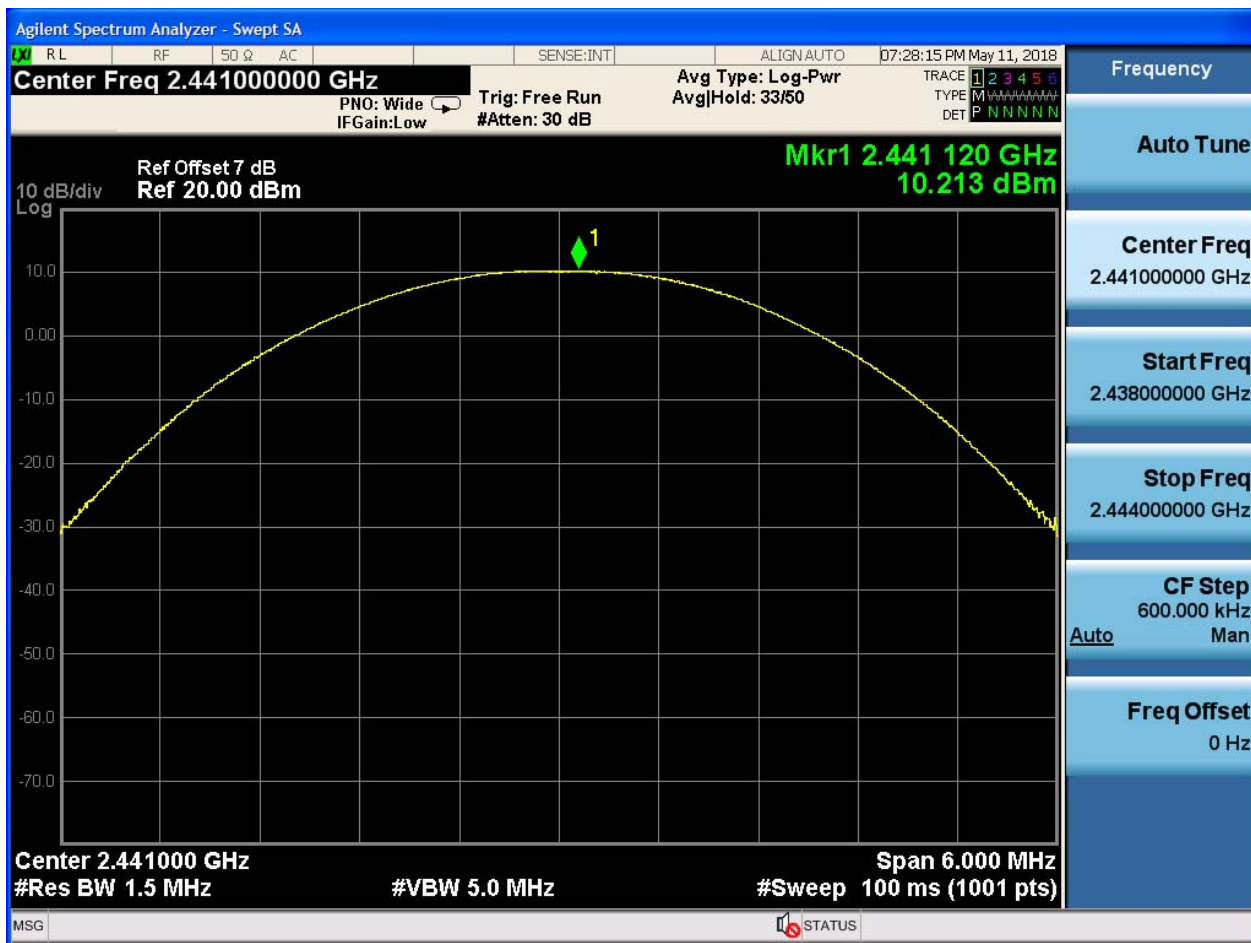




2.4 TM2_2DH5_Ch0



2.5 TM2_2DH5_Ch39



2.6 TM2_2DH5_Ch78





2.7 TM3_3DH5_Ch0



2.8 TM3_3DH5_Ch39



2.9 TM3_3DH5_Ch78



Appendix F: Band edge spurious emission

1 Result Table

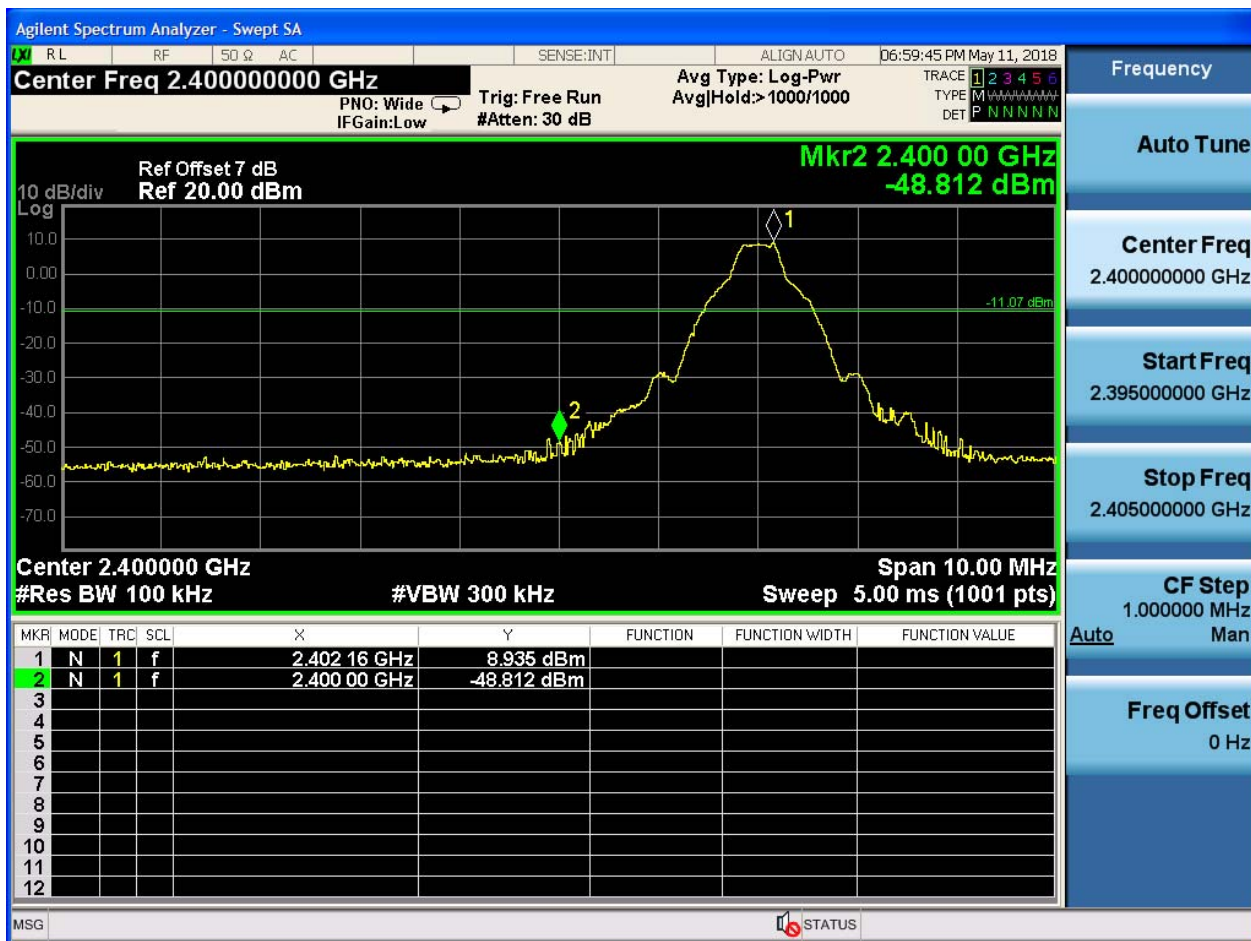
EUT Conf.	Channel No.	Carrier Frequency [MHz]	Max. Spurious Level [dBm]	Frequency Hopping	Carrier Power [dBm]	Limit [dBm]	Result
TM1_DH5_Ch0	0	2402	-48.812	Off	8.935	-11.065	Pass
	-	-	-51.276	On	8.239	-11.761	Pass
TM1_DH5_Ch78	78	2480	-55.359	Off	7.941	-12.059	Pass
	-	-	-51.807	On	7.486	-12.514	Pass
TM2_2DH5_Ch0	0	2402	-49.802	Off	7.043	-12.957	Pass
	-	-	-52.042	On	5.583	-14.417	Pass
TM2_2DH5_Ch78	78	2480	-53.948	Off	6.893	-13.107	Pass
	-	-	-55.87	On	6.769	-13.231	Pass
TM3_3DH5_Ch0	0	2402	-47.938	Off	7.915	-12.085	Pass
	-	-	-52.636	On	7.803	-12.197	Pass
TM3_3DH5_Ch78	78	2480	-55.161	Off	6.642	-13.358	Pass
	-	-	-54.549	On	5.923	-14.077	Pass



2 Test Plot

2.1 TM1_DH5_Ch0

No hopping



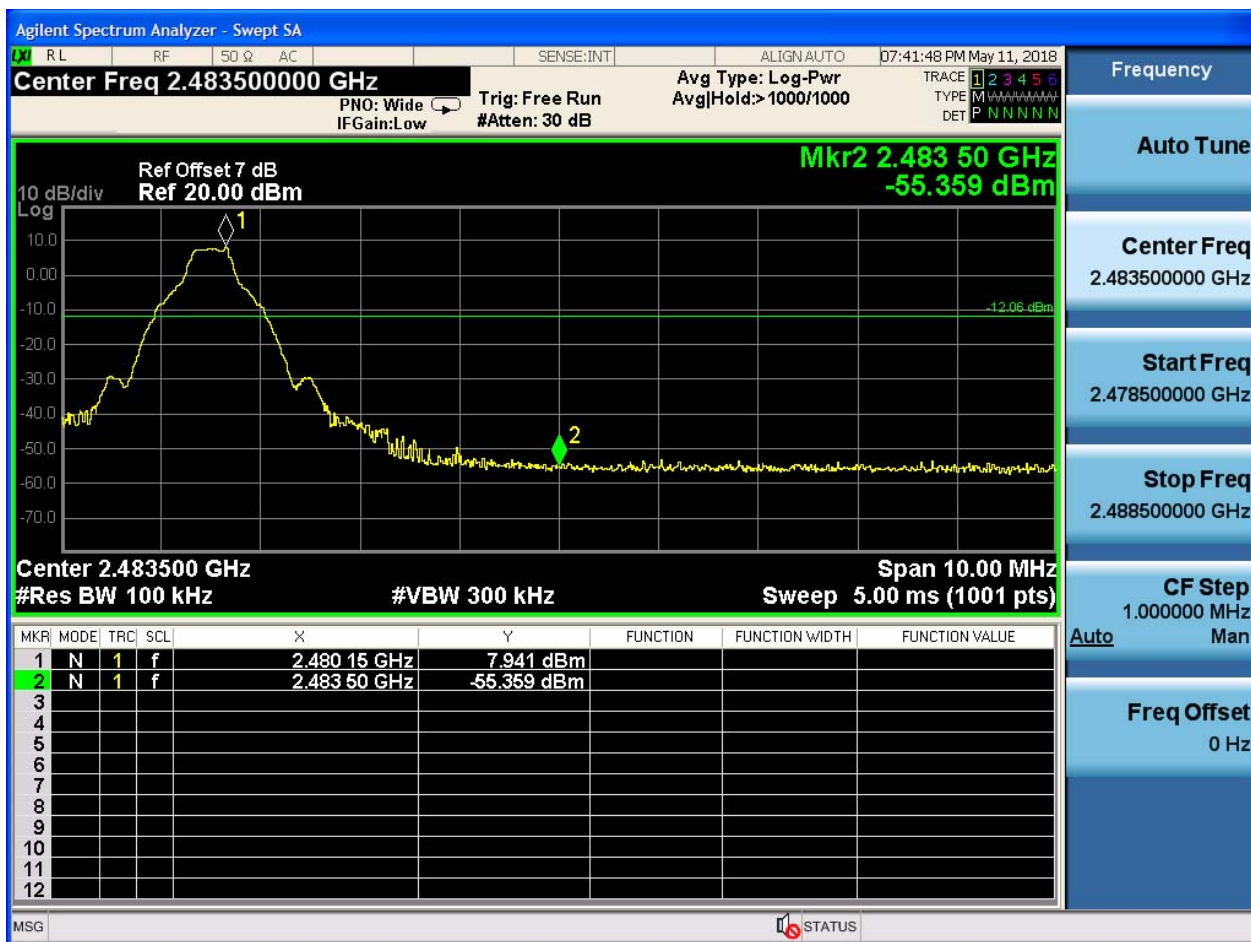
With hopping



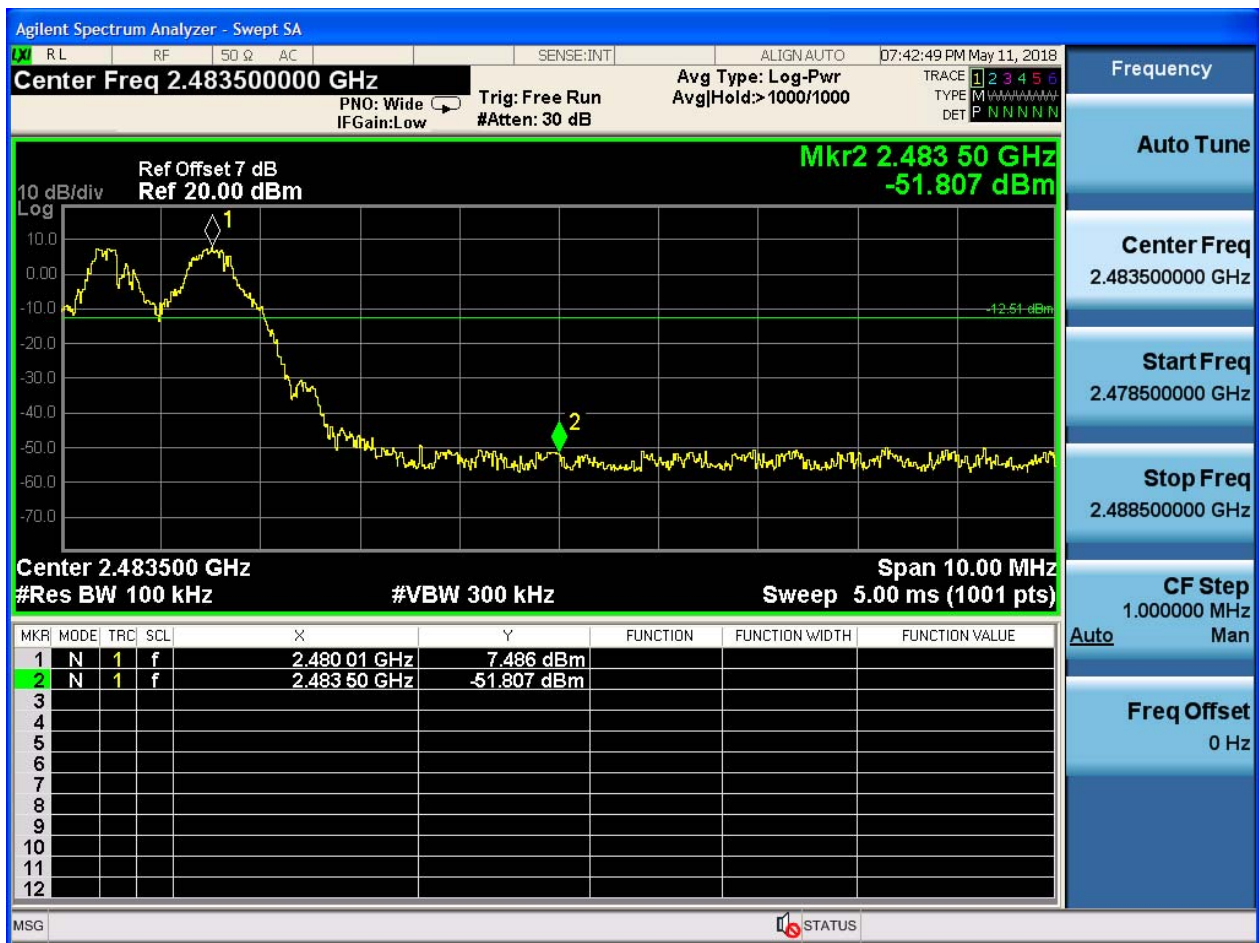


2.2 TM1_DH5_Ch78

No hopping

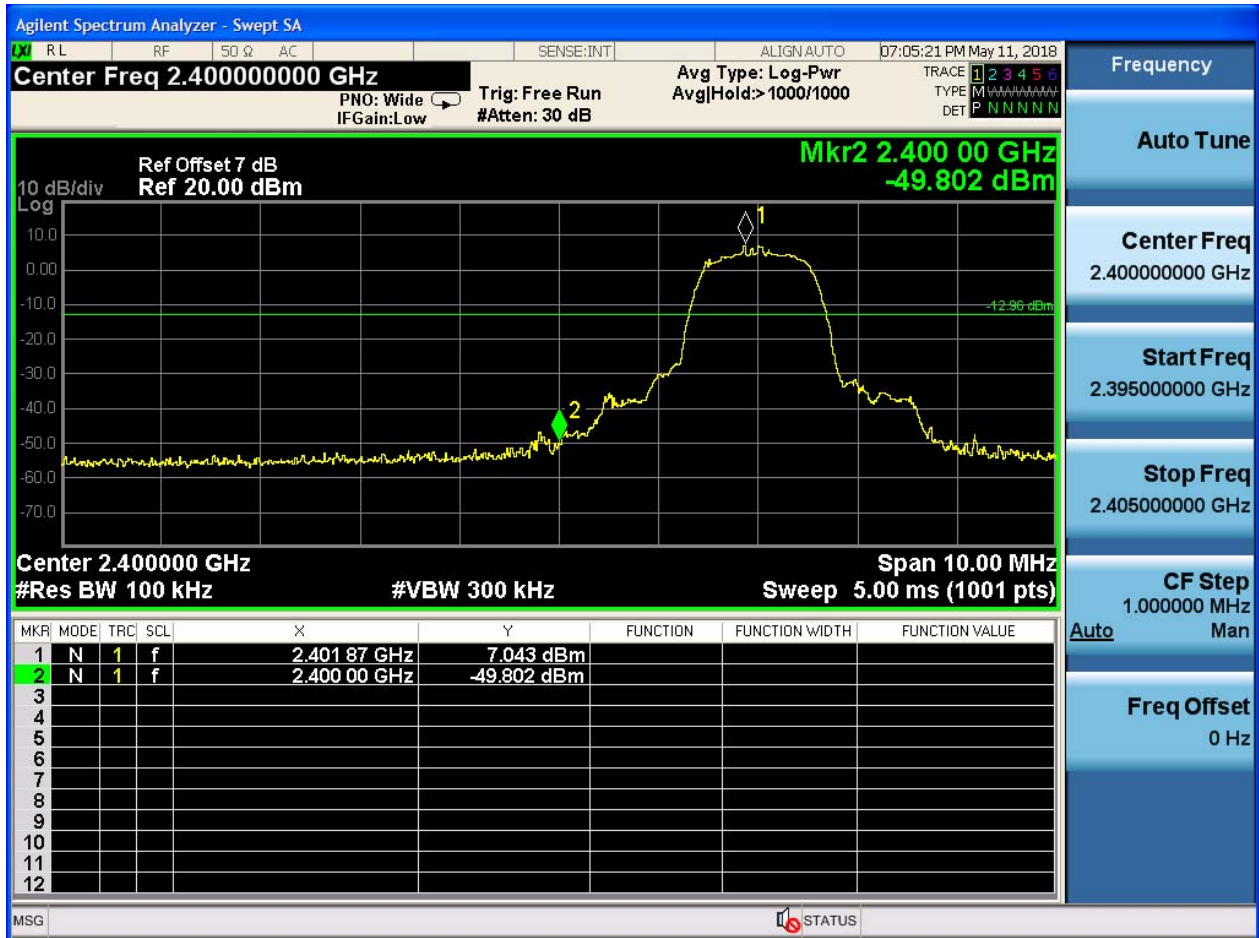


With hopping

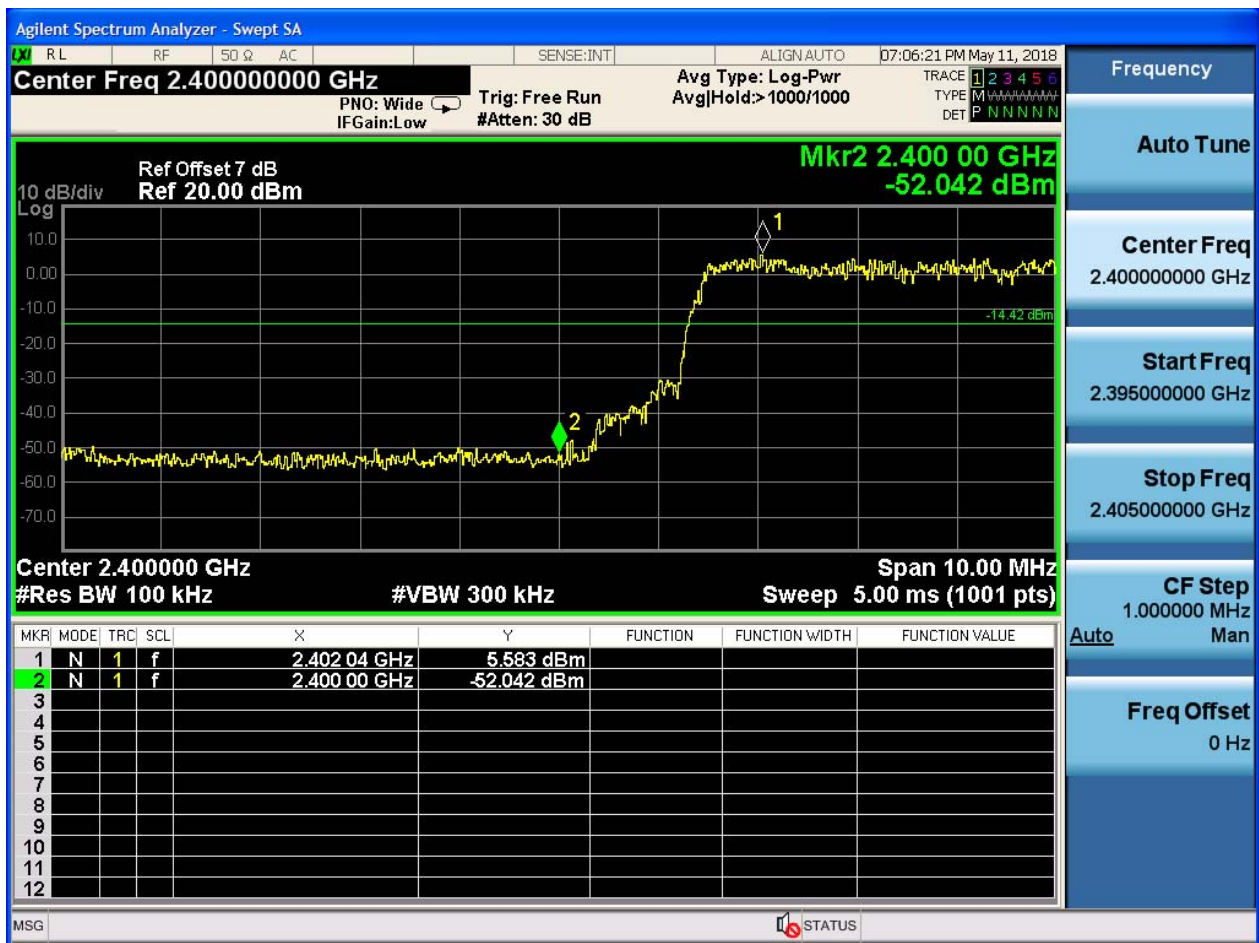


2.3 TM2_2DH5_Ch0

No hopping

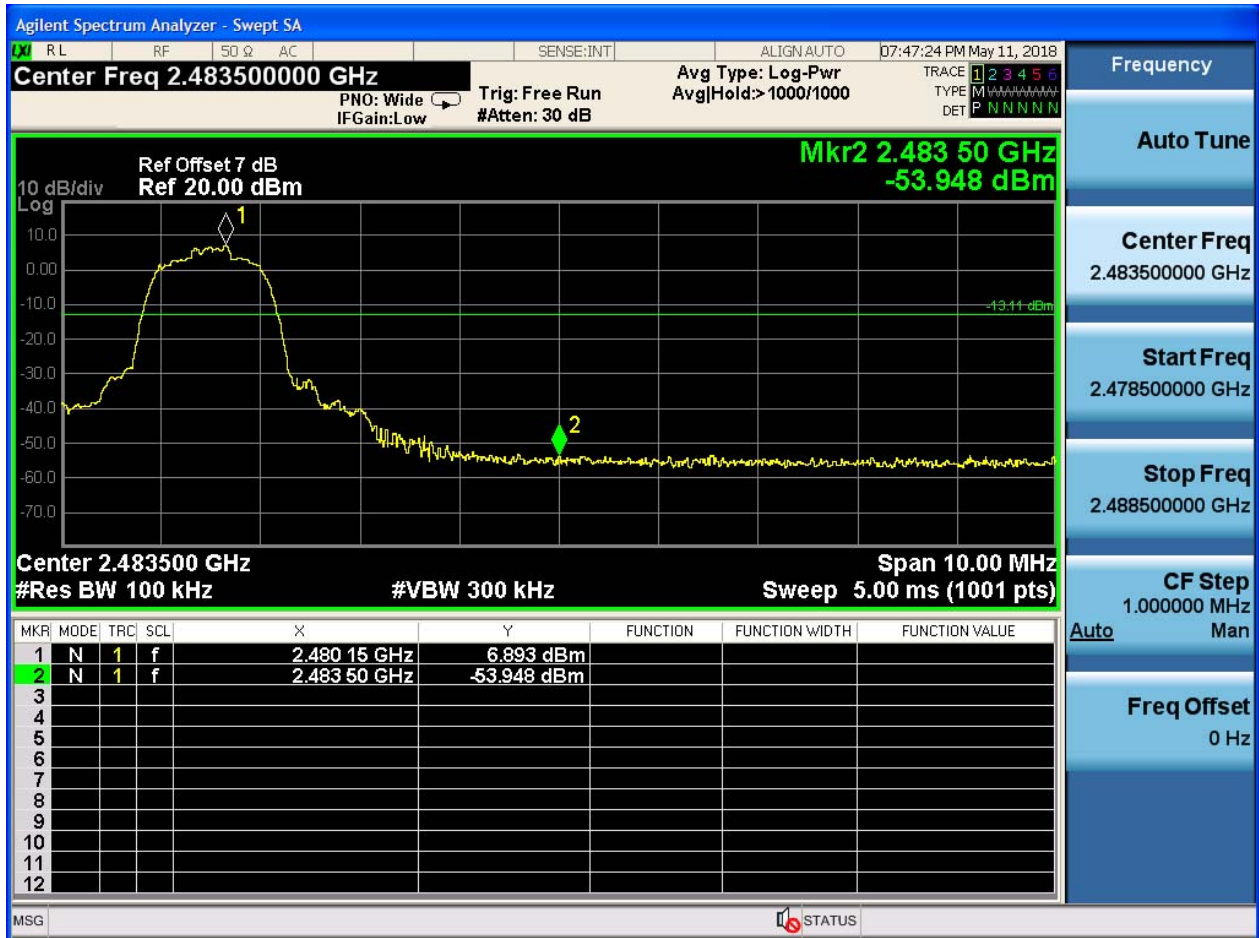


With hopping

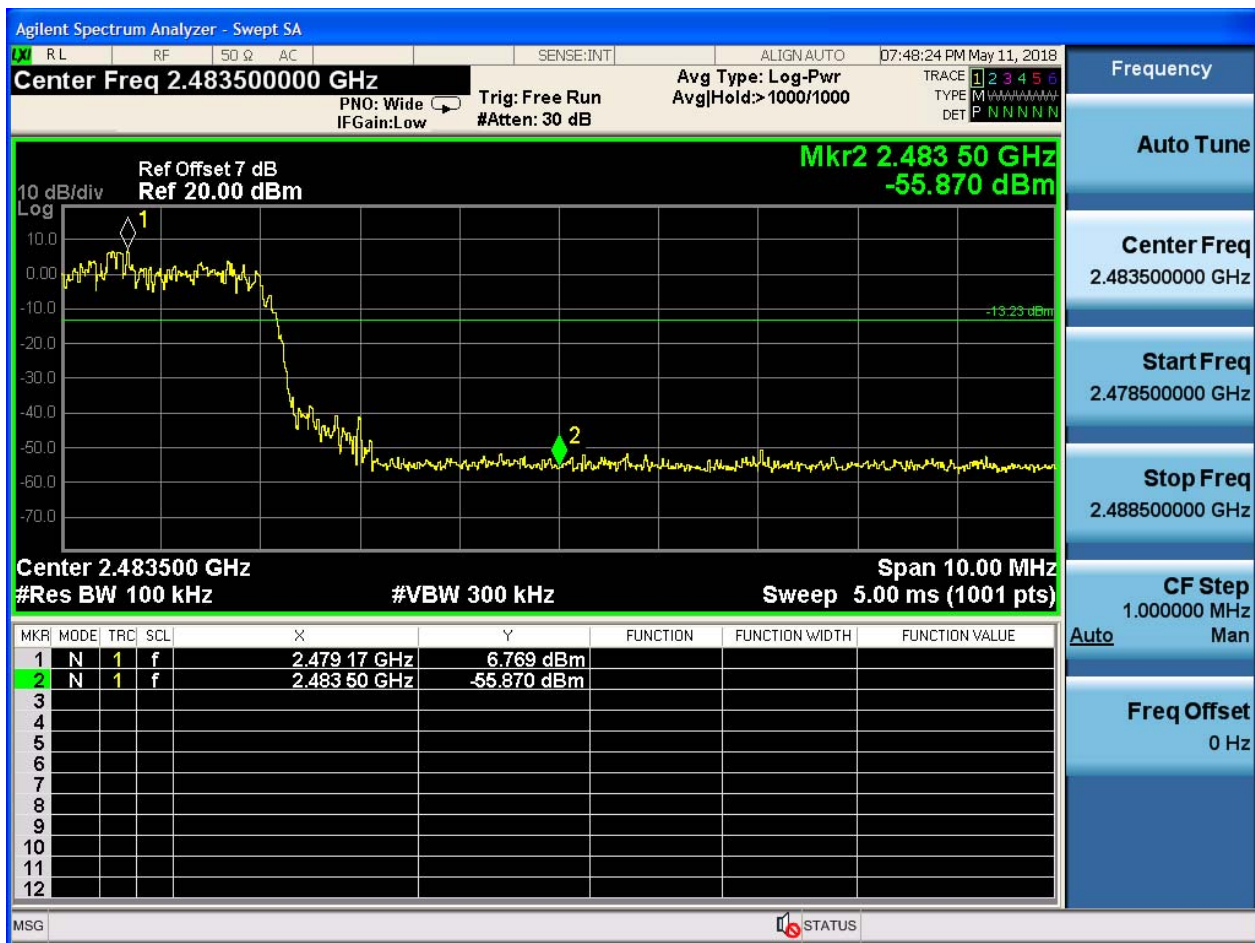


2.4 TM2_2DH5_Ch78

No hopping



With hopping





2.5 TM3_3DH5_Ch0

No hopping





With hopping





No hopping

