

BLOCK DIAGRAMS – EXHIBIT D

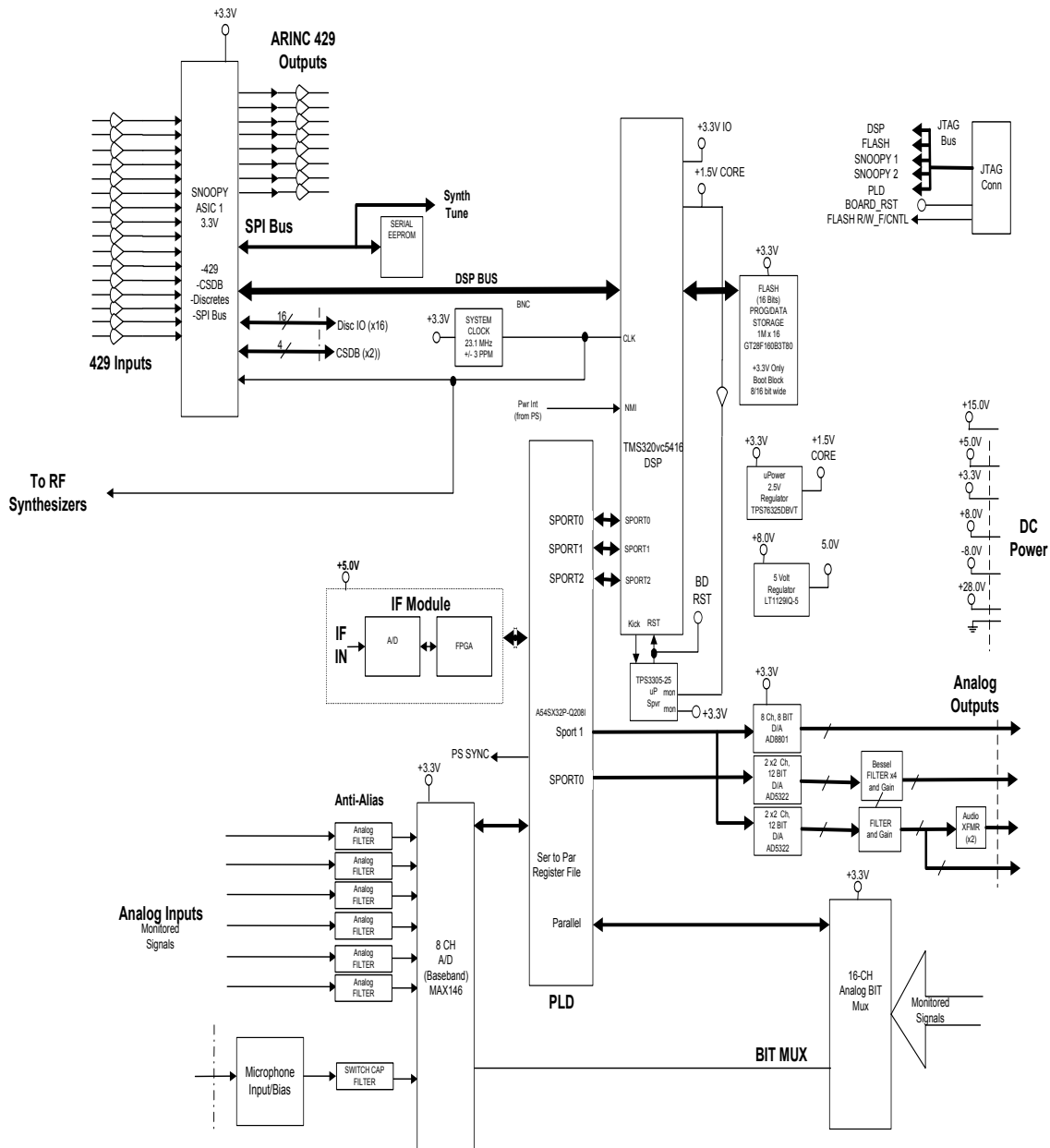


Figure # 1 – Controller/ Signal Processing Block Diagram

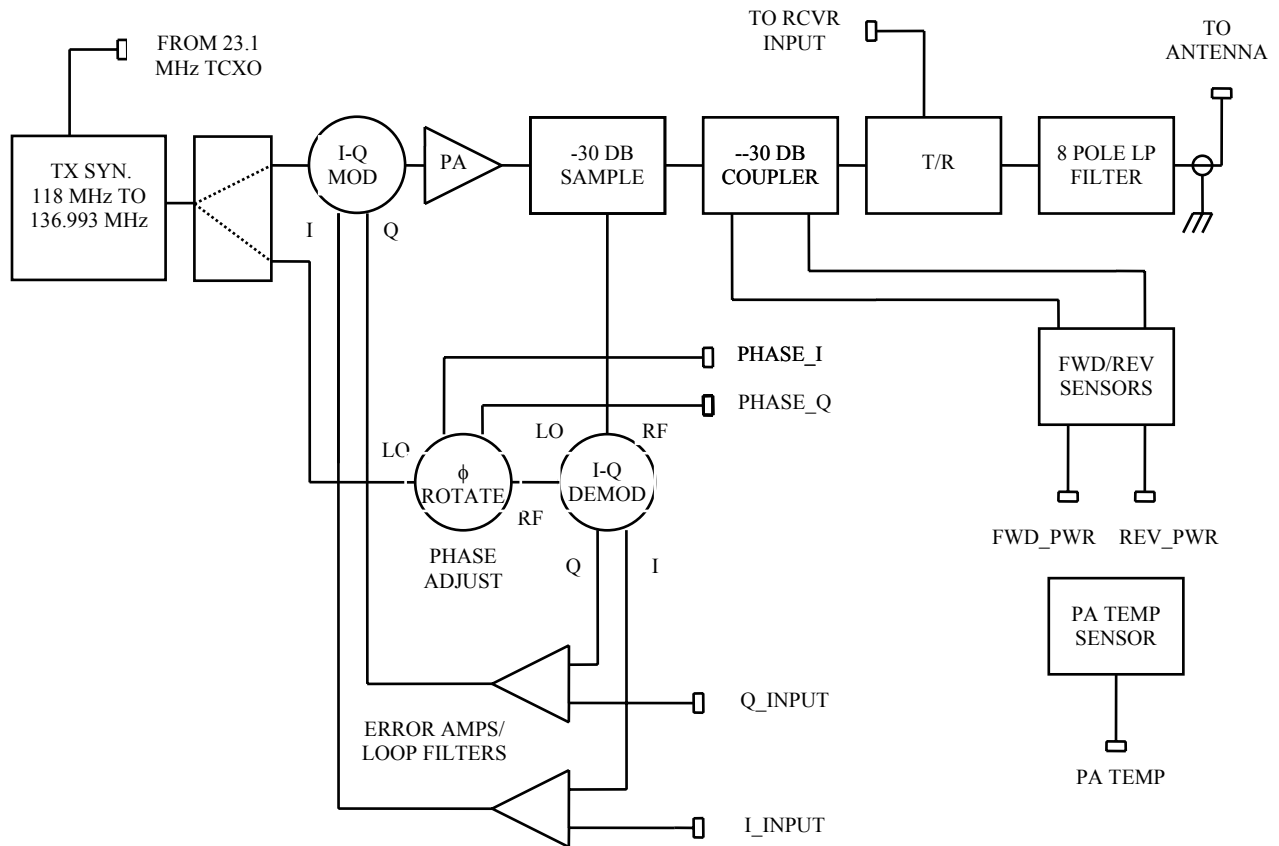


Figure # 2 – Transmitter

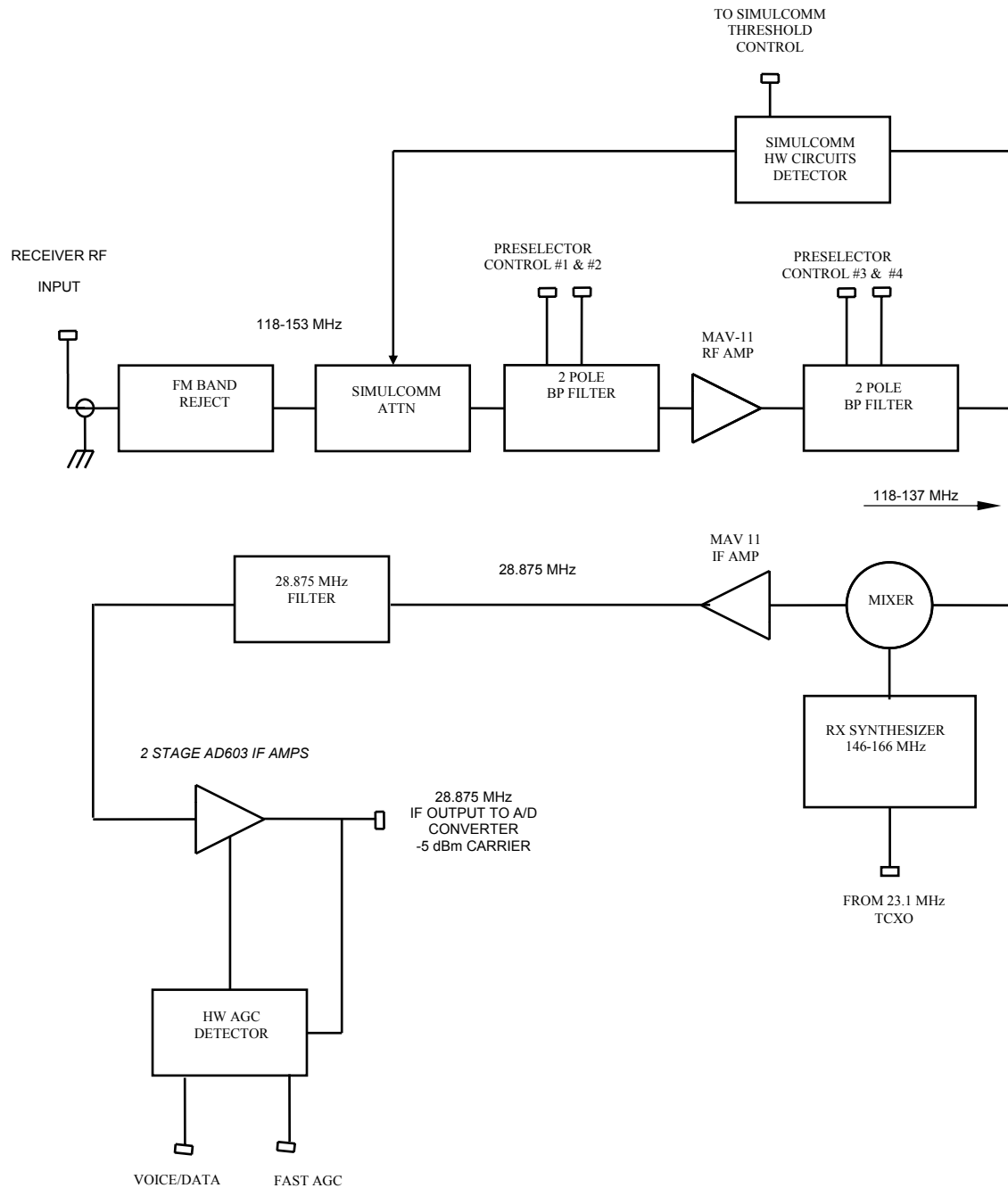


Figure # 3 – Receiver Block Diagram

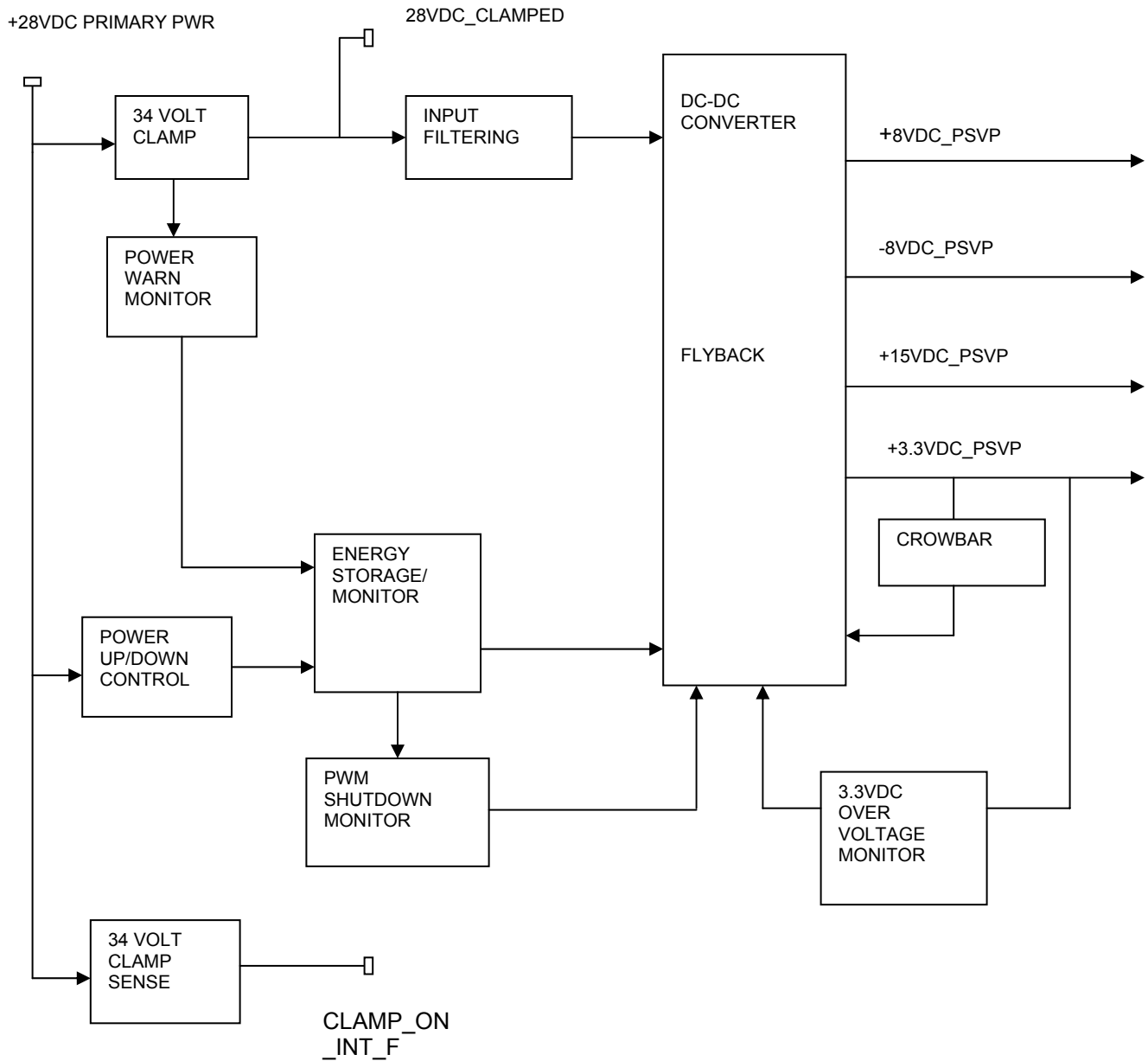


Figure # 4 – Power Supply Block Diagram