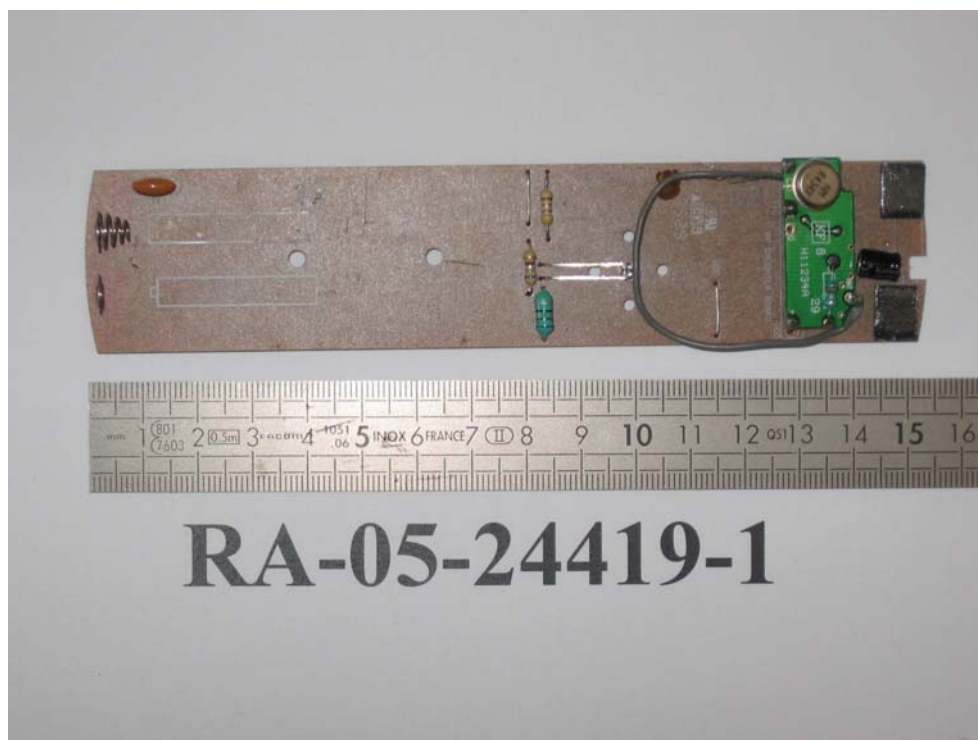


ANNEX 1: PHOTOS OF THE EQUIPMENT UNDER TEST

GENERAL VIEW



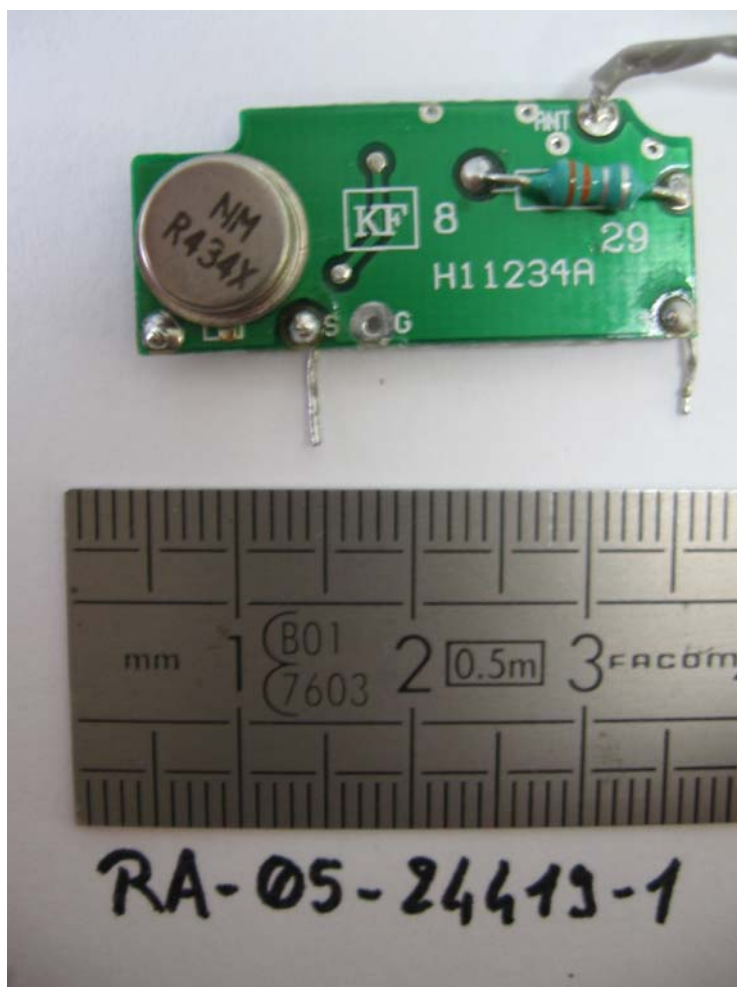
Printed circuit board: face 1



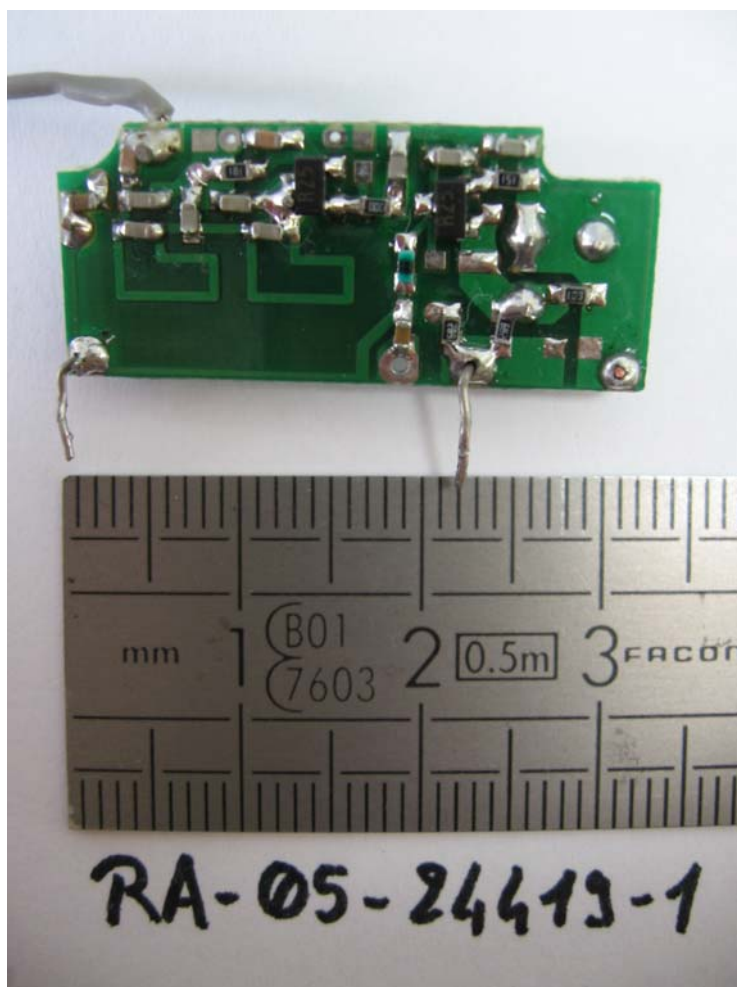
Printed circuit board: face 2



Radio module: face 1



Radio module: face 2

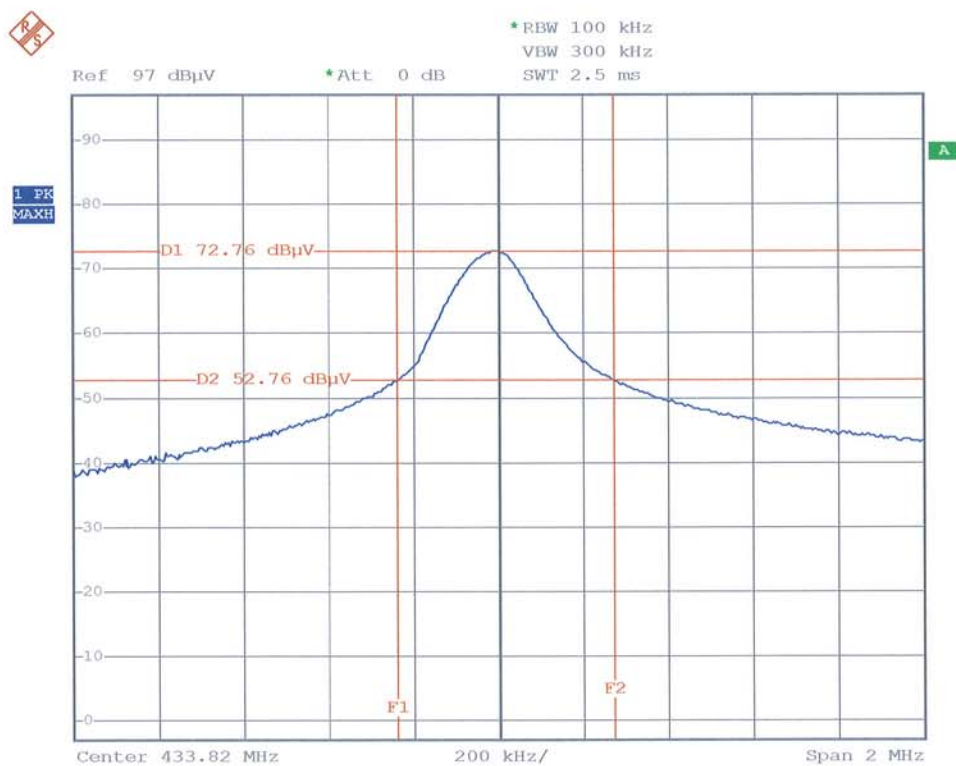


ANNEX 2: OPEN AREA TEST SITE, TEST SET UP



TEST SET UP

ANNEX 3: EMISSION BANDWIDTH

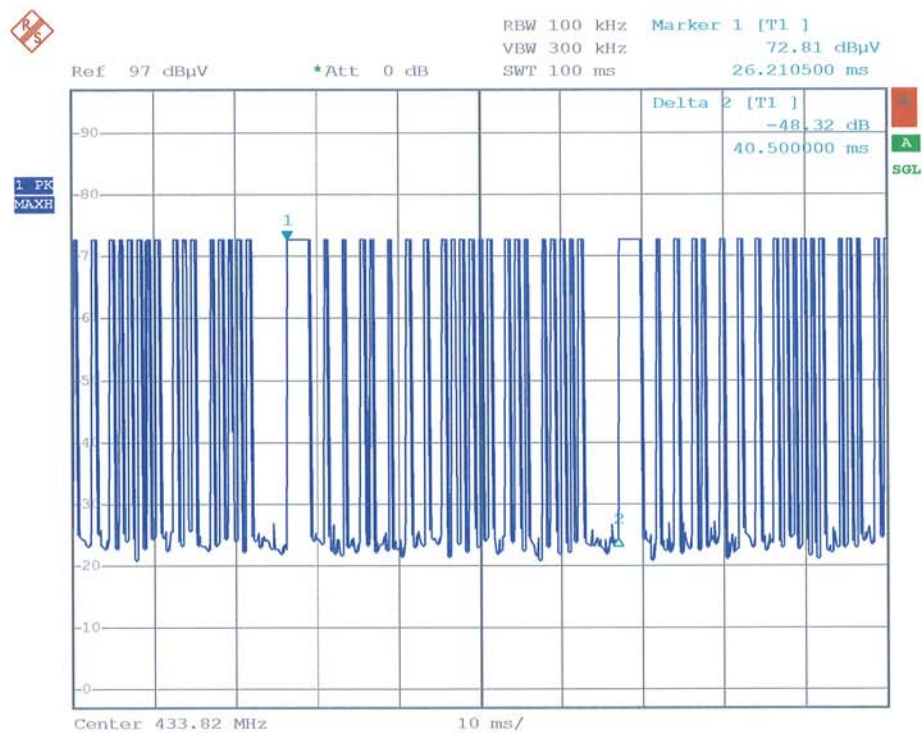


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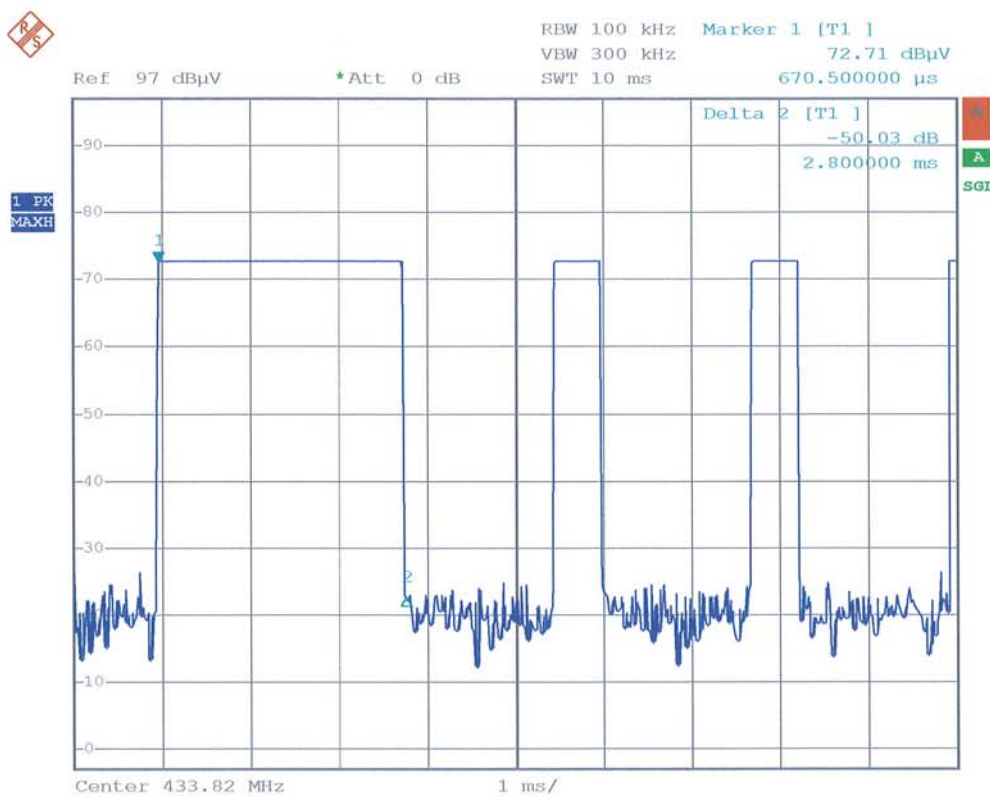
$$F_1 = 433,580 \text{ MHz}$$

$$F_2 = 434,038 \text{ MHz}$$

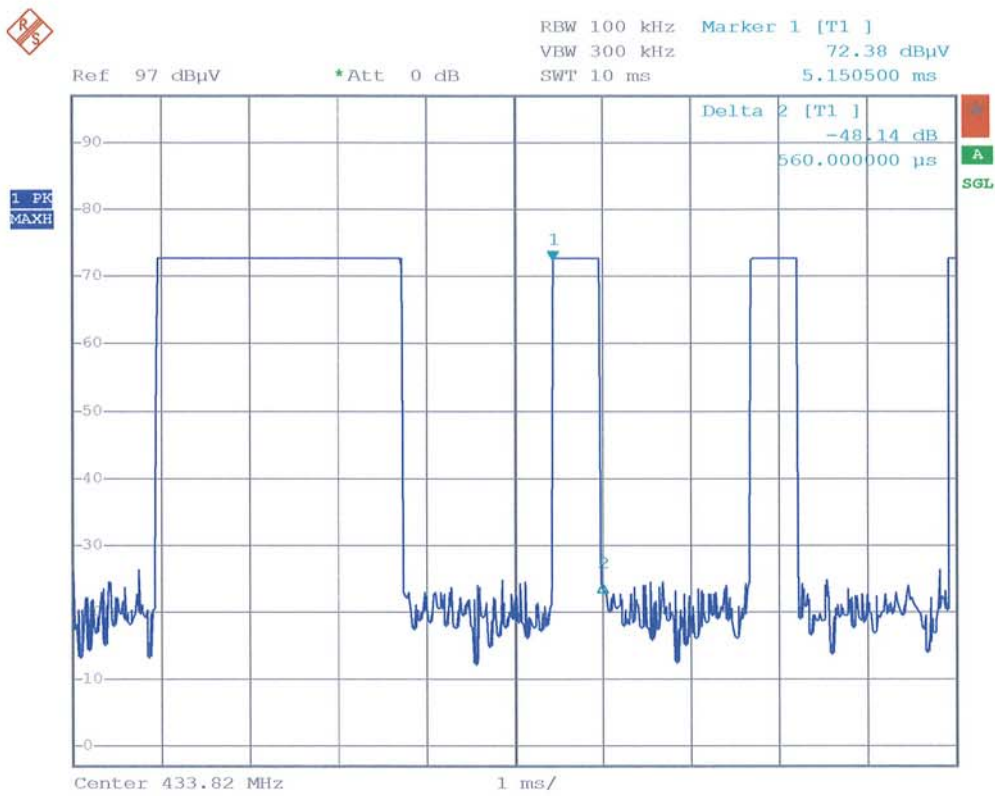
ANNEX 4: DUTY CYCLE DETERMINATION



Date: 18.OCT.2005 14:05:21



Date: 18.OCT.2005 14:08:33



Date: 18.OCT.2005 14:11:56