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RSC14

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Accredited Testing Laboratory

DAR-Registration number:

TTI-P-G 166/98-00

Test report no.:2_2488-B/01

FCC Part 15.247

Bluetooth™ SD card

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1 General information

1.1 Notes

The test results of this test report relate exclusively to the test item specified in 1.5. The CETECOM ICT Services GmbH does not assume responsibility for any conclusions and generalisations drawn from the test results with regard to other specimens or samples of the type of the equipment represented by the test item. The test report may only be reproduced or published in full. Reproduction or publication of extracts from the report requires the prior written approval of the CETECOM ICT Services GmbH.

1.2 Testing laboratory

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Accredited testing laboratory

DAR-registration number : TTI-P-G 166/98-20

1.3 Details of applicant

Name : TOSHIBA CORPORATION
Street : 2-9, Suehiro-Cho, Ome
City : Tokyo 198-8710
Country : Japan
Telephone : +81 428 33 1343
Telefax : +81 428 30 7919
Contact : Mr, Katsuhiko Kinoshita
Telephone : +81 428 33 1343

1.4 Application details

Date of receipt of application : 13.03.01
Date of receipt of test item : 06.04.01
Date of test : 06.04.01

1.5 Test item

Type of equipment : **Radio LAN**
Type designation : **Bluetooth[®] SD card**
Manufacturer : applicant
Street :
City :
Country :
Serial number : -.-

Additional informations: :

Frequency : 2400 – 2483.5 MHz
Type of modulation : 1M00FXD / 79M8FXD (FHSS)
Number of channels : 79
Antenna : integral antenna
Power supply : 3,3V powered by a PC
Output power : EIRP: 0,389 mW
Type of equipment : Temperature range : 0°C - +55°C

1.6 Test standards: FCC Part 15 §15.247

2 Technical test

2.1 Summary of test results

Technical responsibility for area of testing :

18.04.01 RSC 8411 Berg M.



Date	Section	Name	Signature
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Technical responsibility for area of testing :

18.04.01 RSC8414 Ames H.



Date	Section	Name	Signature
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2.2 Testreport

TEST REPORT

Testreport no. : 2_2488-B/01

TEST REPORT REFERENCE

LIST OF MEASUREMENTS

Paragraph	PARAMETER TO BE MEASURED	PAGE
	Transmitter parameters	
§ 15.247 (f)	Processing gain	7

Introduction

This report describes the results of the processing gain measurement for the Toshiba Bluetooth™ SD card using the FCC CW jamming margin method.

Requirement

Hybrid systems that employ a combination of both direct sequence and frequency hopping modulation techniques shall achieve a processing gain of at least 17 dB from the combined technique.

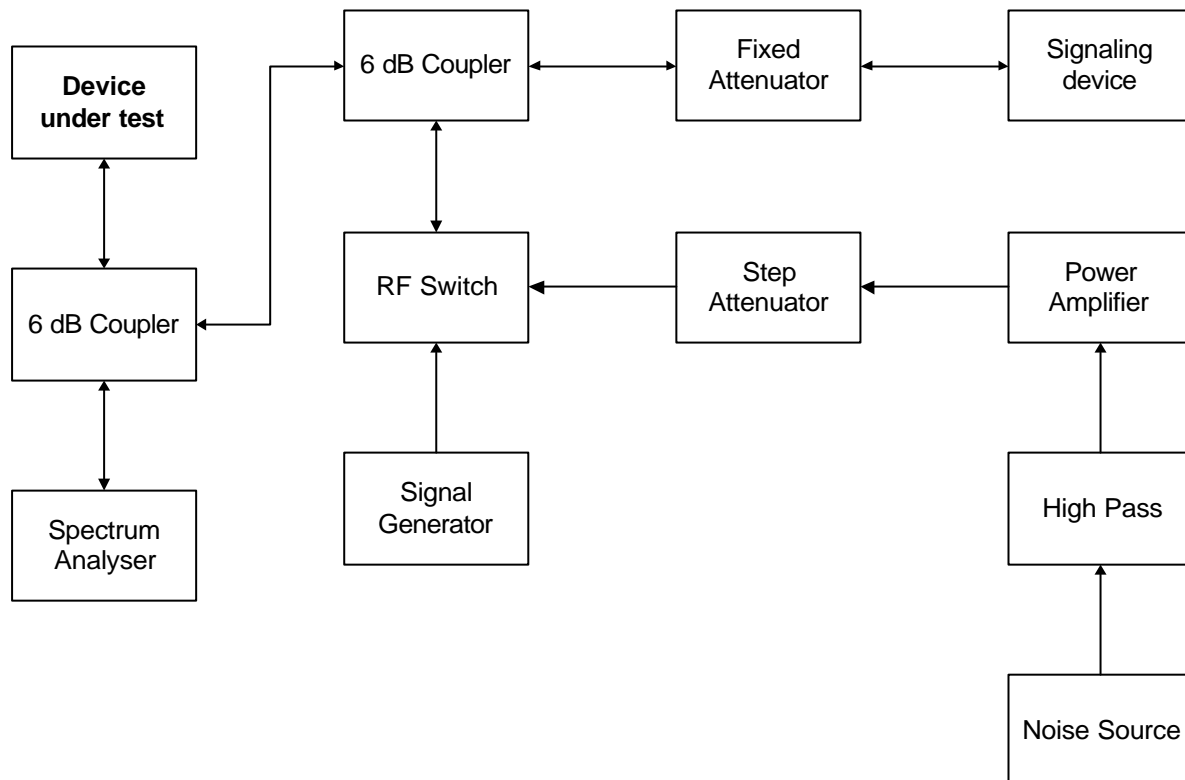
System losses

In the formula for calculation the processing gain appears the term system losses. In Bluetooth there are two major causes for the system loss:

1. The non optimal sampling time. The CW jamming method assures that the optimal sampling time as determined before, in fact, as described above, it will be determined via the access code.
2. Losses due to attenuation in the RF part.

Although this two points produce a system loss which will be much greater than 2 dB, we calculated with this value, because this is the maximum that will be accepted by FCC.

Test setup:



To set-up the connection a special test program was used. It is installed on the laptop and communicates with the Toshiba Bluetooth™ SD card.

The BER was determined with a special test software over 1600000 Bits .

Measurement Results :

S/N Measurement :

The measurement for S/N was performed by adding noise to the wanted signal. The noise level was adjusted until the BER reached 0.1 %.

With this measurement configuration the S/N ratio was 7.8 dB.

The jammer signal was stepped in 50 kHz steps over the receiving channel.
For every step the jammer level was adjusted until the BER was 0.1 %. The ratio between wanted signal power and the jammer power at the RF input of the device is the required Jammer to Signal ratio.

With this measurement configuration the JSR has the following results:

Jammer Offset(KHz)	<u>Jammer-Signal</u>	Jammer/Signal ratio	BER
-500	-63.6	14.4	0.1%
-450	-63.8	14.6	0.1%
-400	-63.8	14.6	0.1%
-350	-62.8	13.6	0.1%
-300	-61.9	12.7	0.1%
-250	-60.3	11.1	0.1%
-200	-58.2	9.0	0.1%
-150	-55.2	6.7	0.1%
-100	-52.6	3.4	0.1%
-50	-50.9	1.7	0.1%
0	-51.0	1.8	0.1%
+50	-51.5	2.3	0.1%
+100	-55.0	5.8	0.1%
+150	-58.0	8.8	0.1%
+200	-60.1	10.9	0.1%
+250	-61.9	12.7	0.1%
+300	-62.3	13.1	0.1%
+350	-63.2	14.0	0.1%
+400	-63.3	14.1	0.1%
+450	-63.4	14.2	0.1%
+500	-63.0	13.8	0.1%

Wanted signal : -49.2 dBm

Signal/Noise : -7.8 dBm

Disregarding the marked data points –500, -450, -400, +400, +450 kHz) the worst data point is at –400 / -450 kHz with a Jammer / Signal value of 14.6 dB.

Processing gain calculation

With these values the processing gain for the DSS part of the system is calculated to:

$$M_j = JSR$$

$$G_p = S/N + M_j + L_{sys} = -7,8 + 14,6 + (-2,0) = 4,8 \text{ dB}$$

The processing gain for FHSS part is calculated as:

$$10 * \log 32 = 15 \text{ dB (32 hopping channels in hybrid mode)}$$

This means for the total processing gain of the hybrid system:

$$15 \text{ dB} + 4,8 \text{ dB} = 19,8 \text{ dB}$$

This is above the minimum value of 17 dB stated in FCC rules.

The device passes the requirement of this clause.