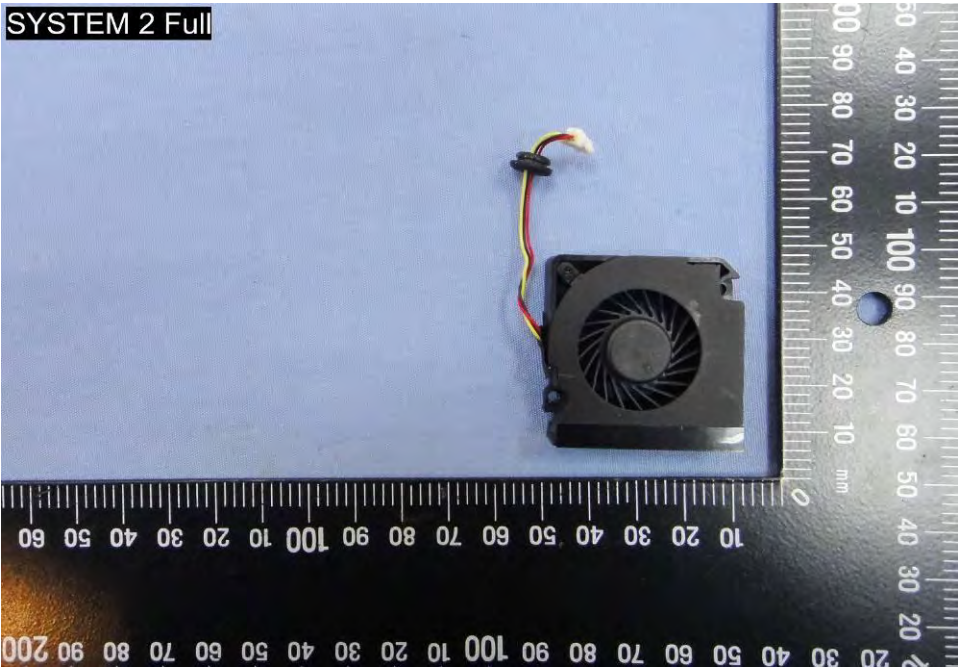
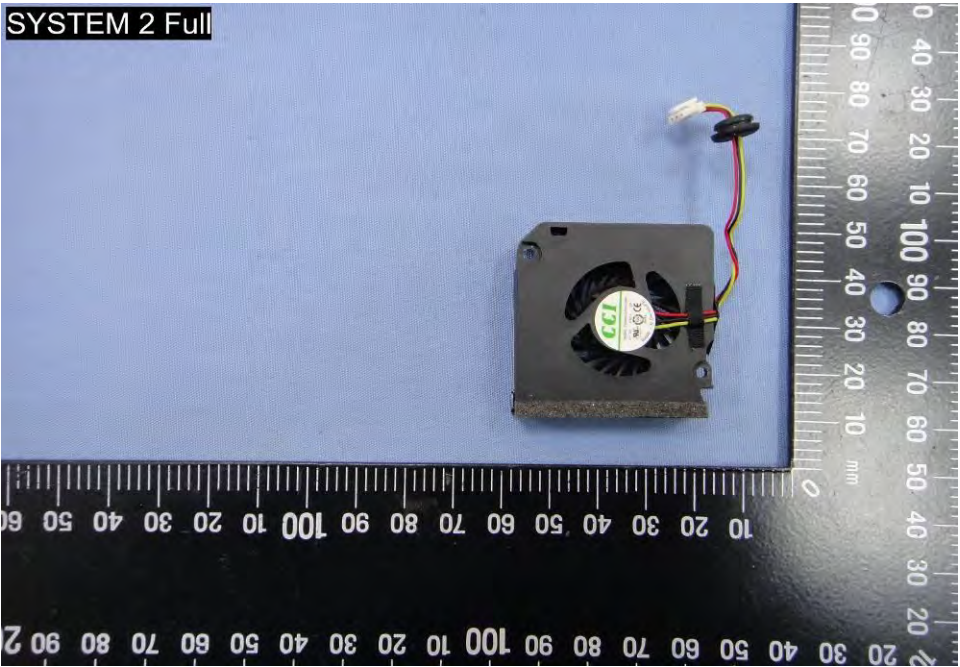


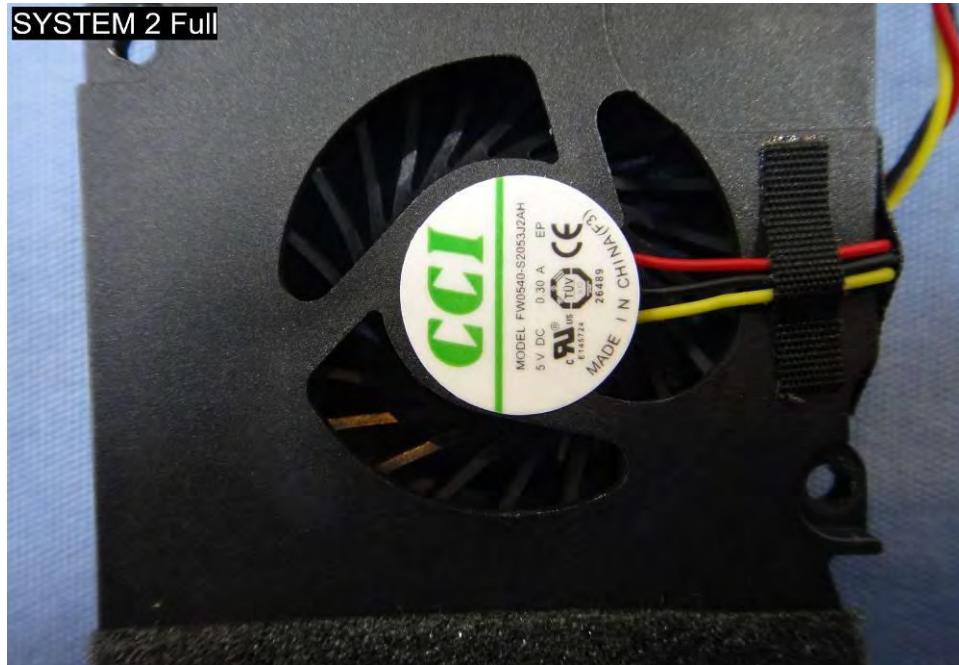
EUT Photo (69)



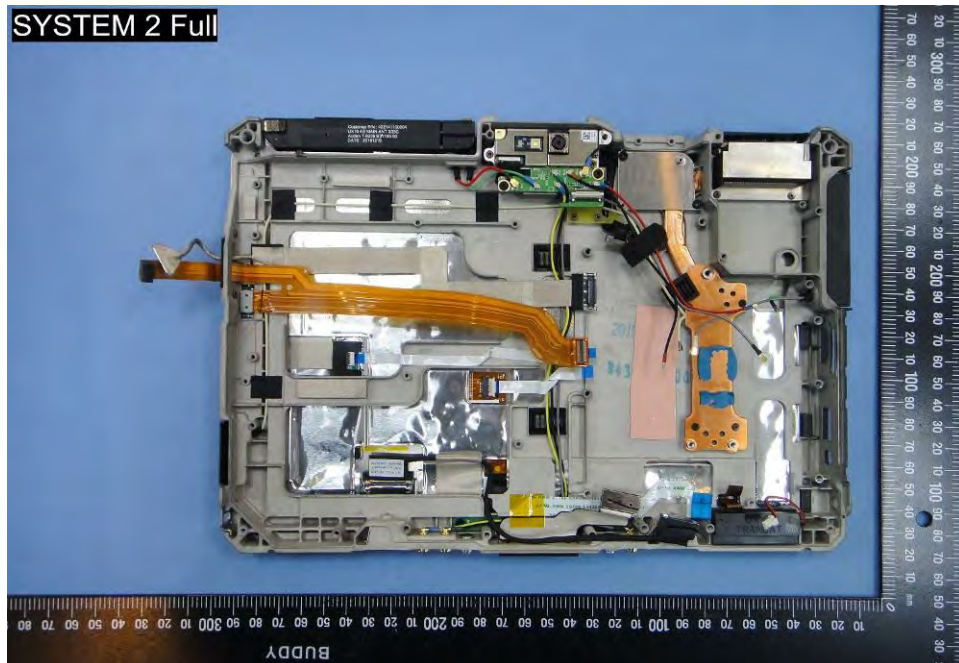
EUT Photo (70)



EUT Photo (71)

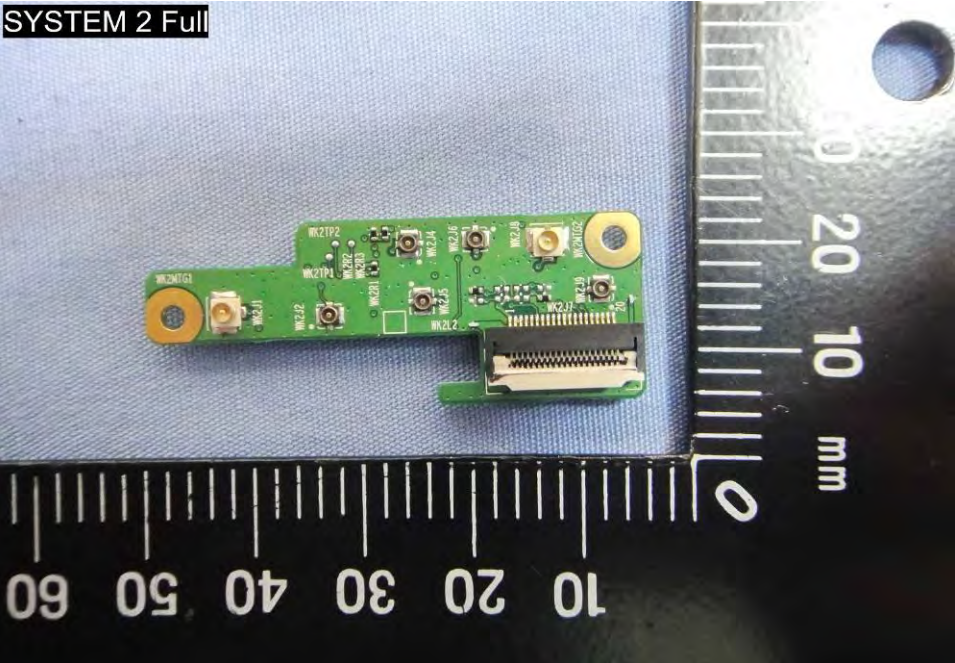


EUT Photo (72)

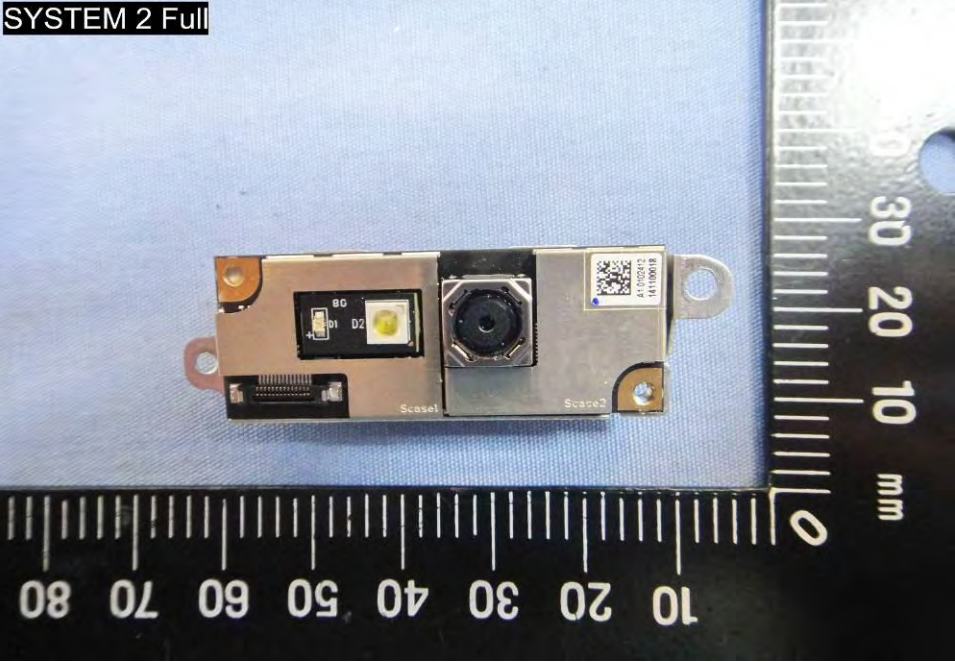




EUT Photo (75)



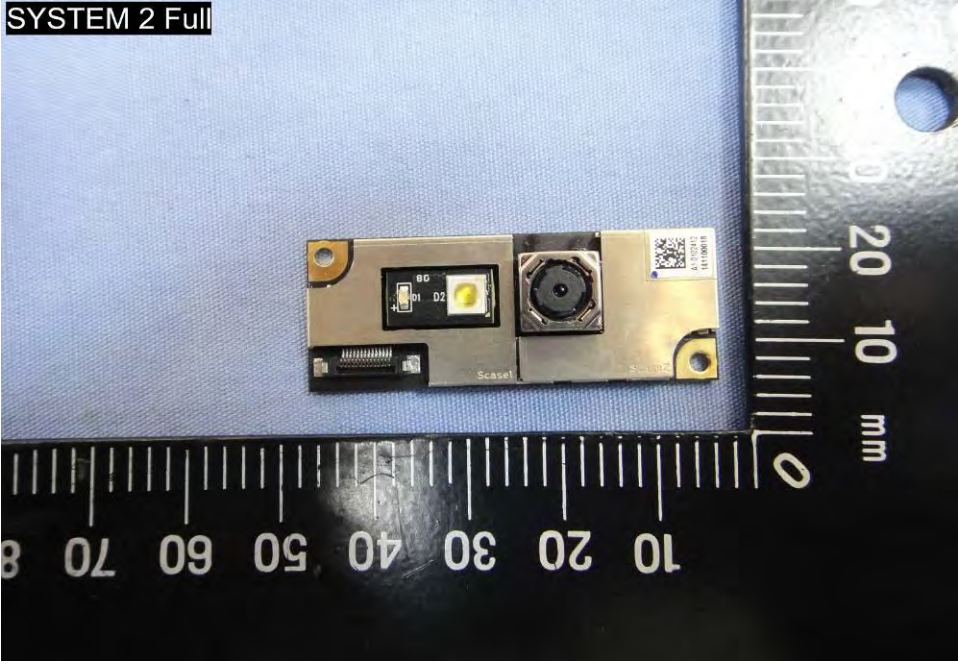
EUT Photo (76)



EUT Photo (77)

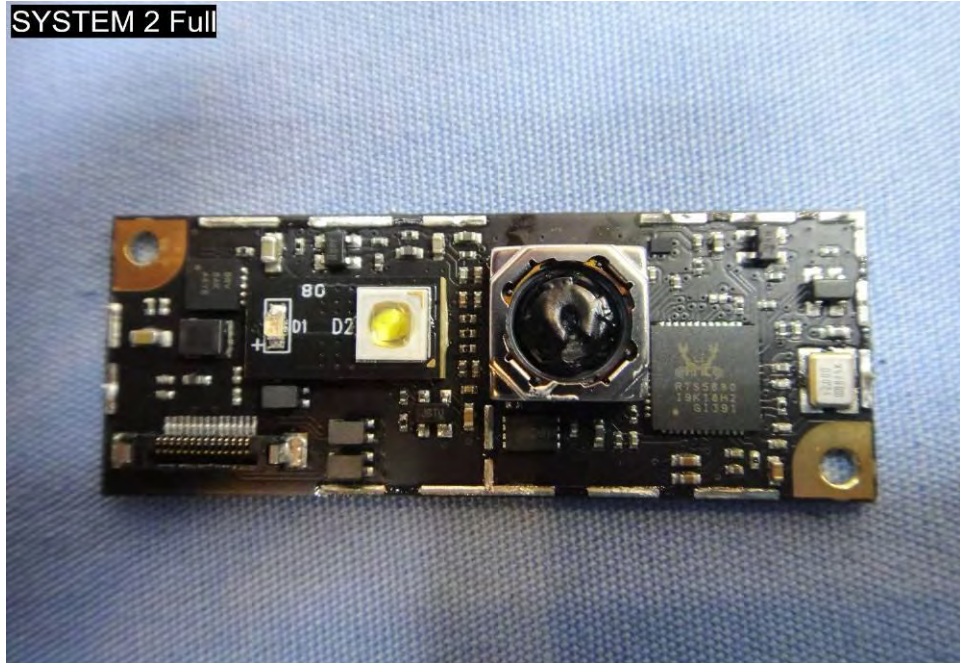


EUT Photo (78)



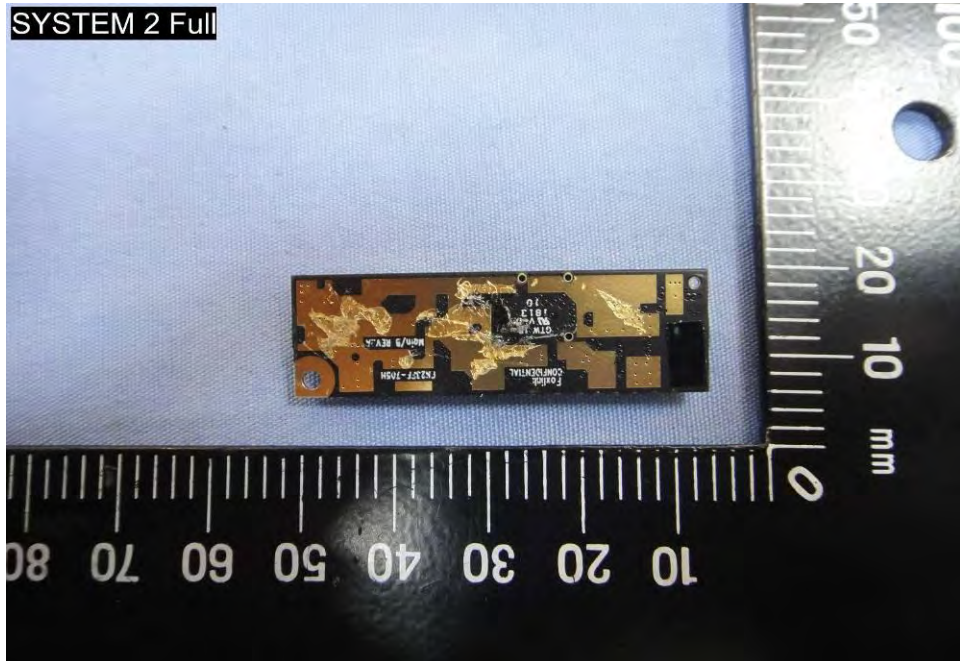
EUT Photo (79)

SYSTEM 2 Full

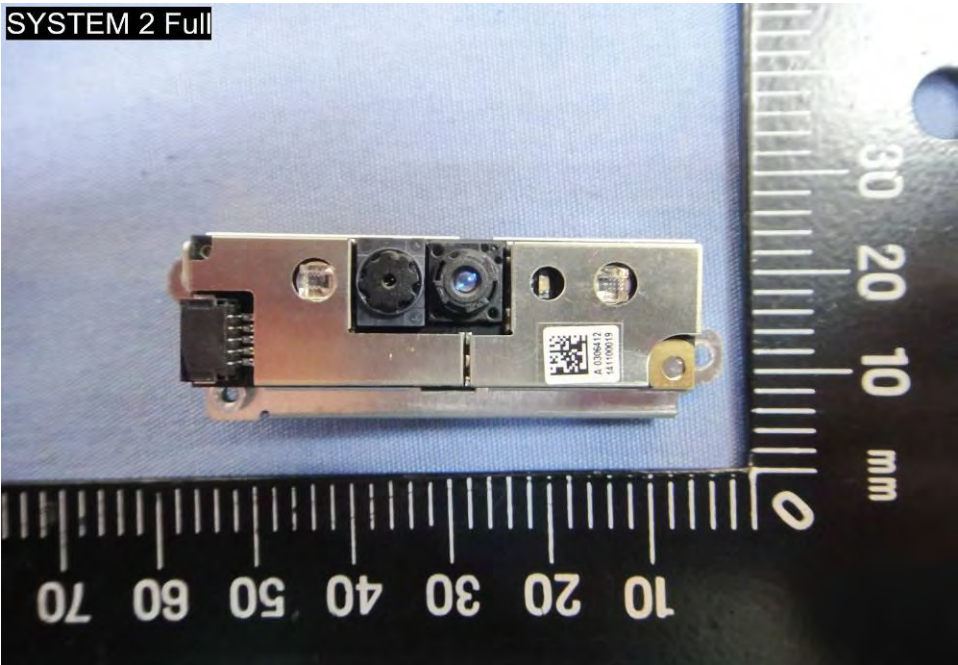


EUT Photo (80)

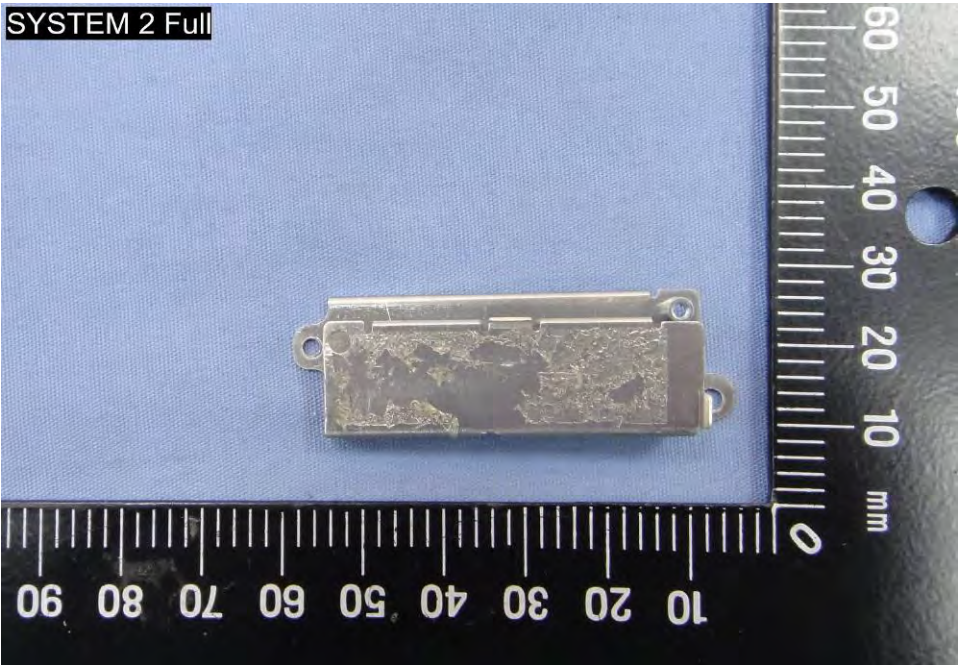
SYSTEM 2 Full



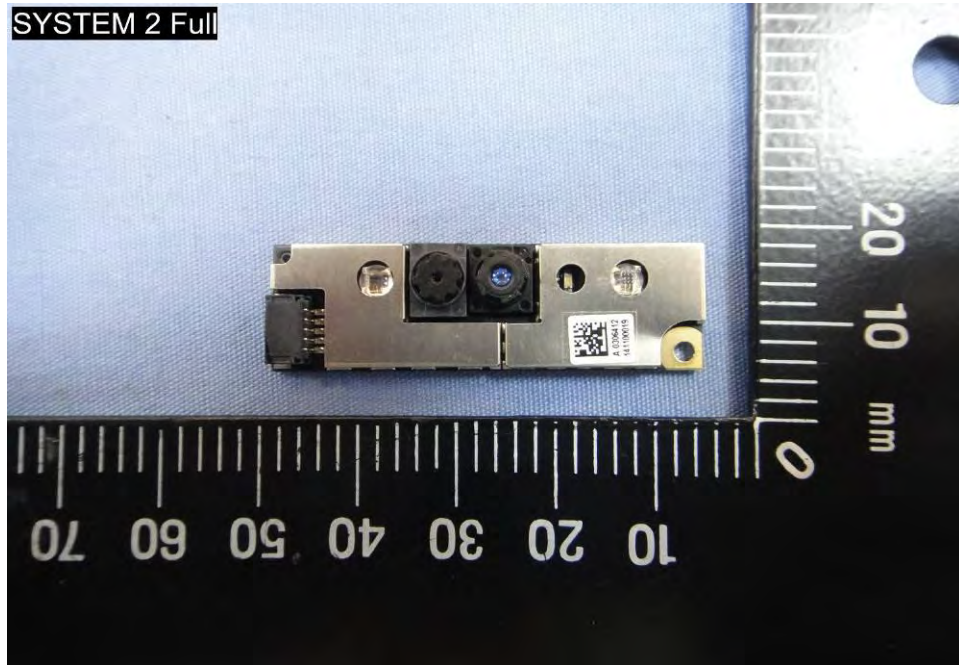
EUT Photo (81)



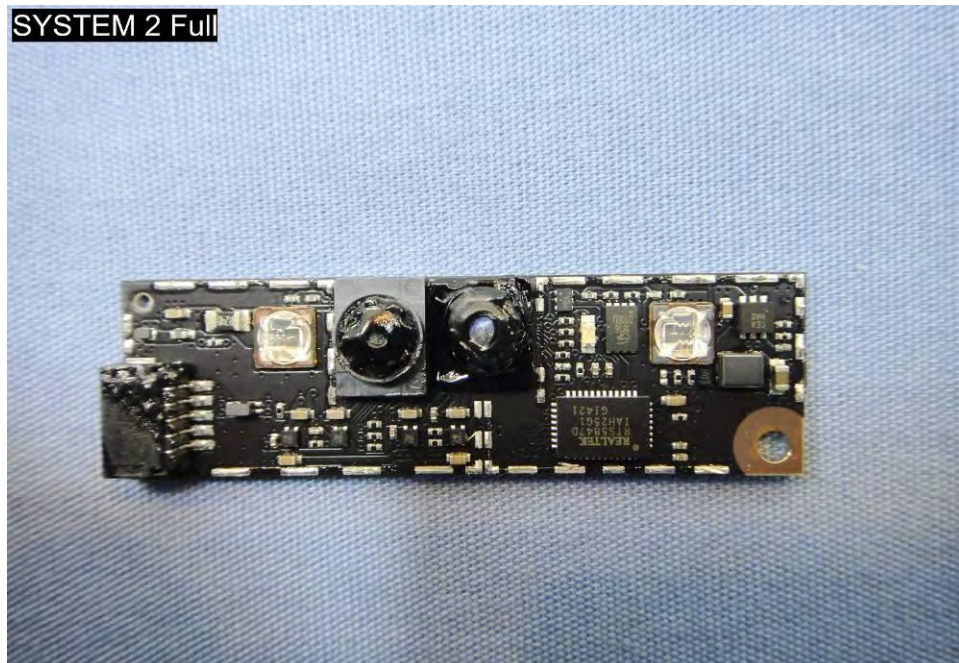
EUT Photo (82)



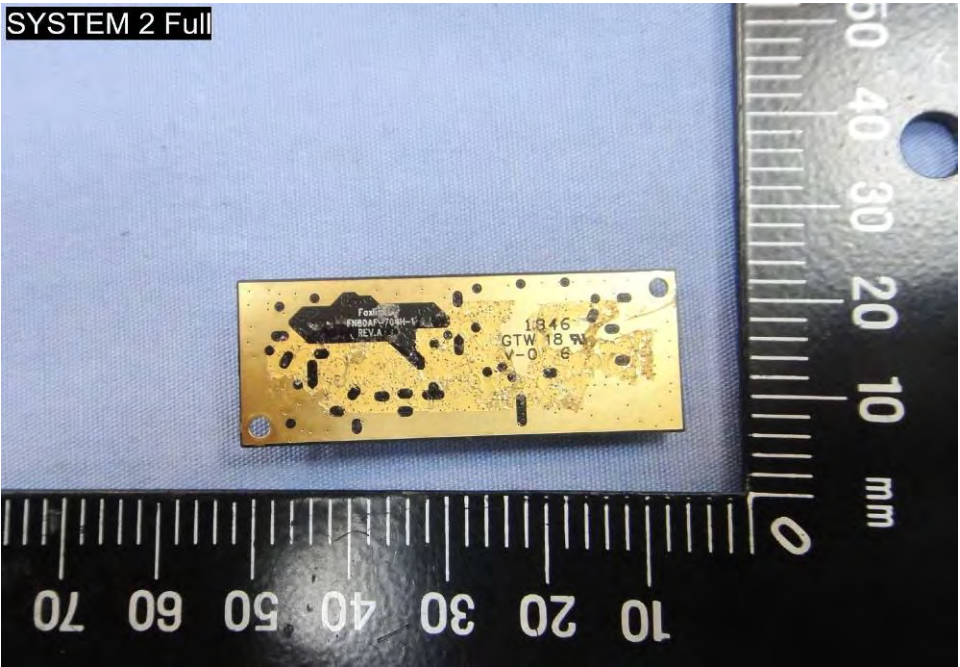
EUT Photo (83)



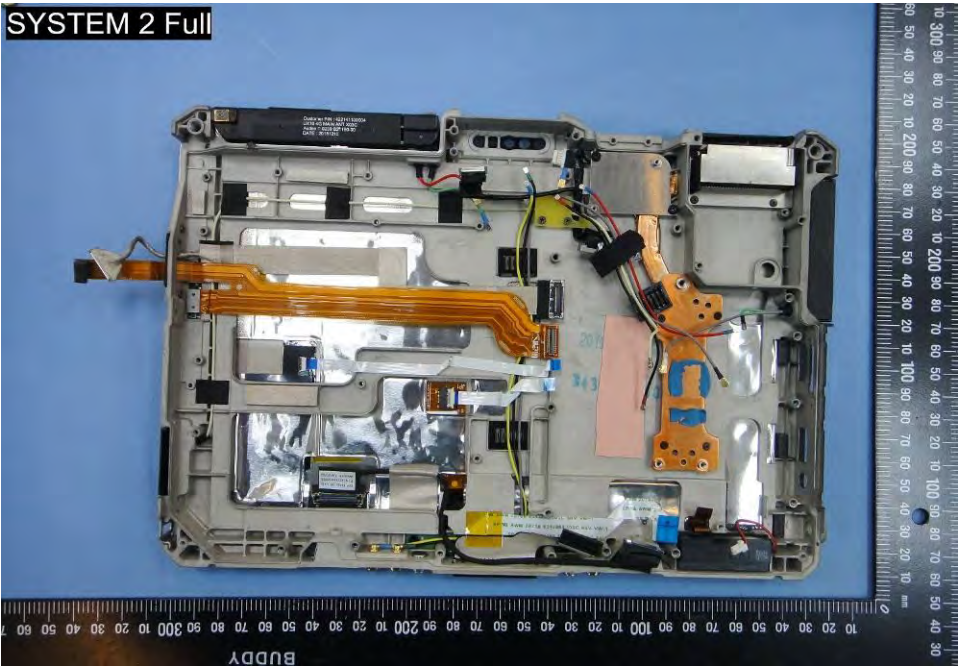
EUT Photo (84)



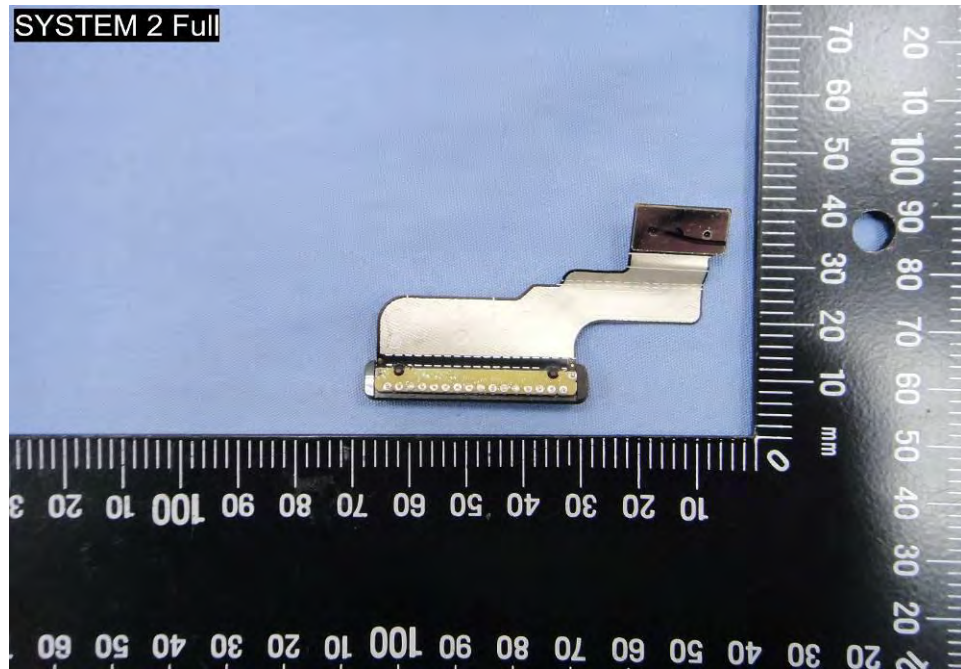
EUT Photo (85)



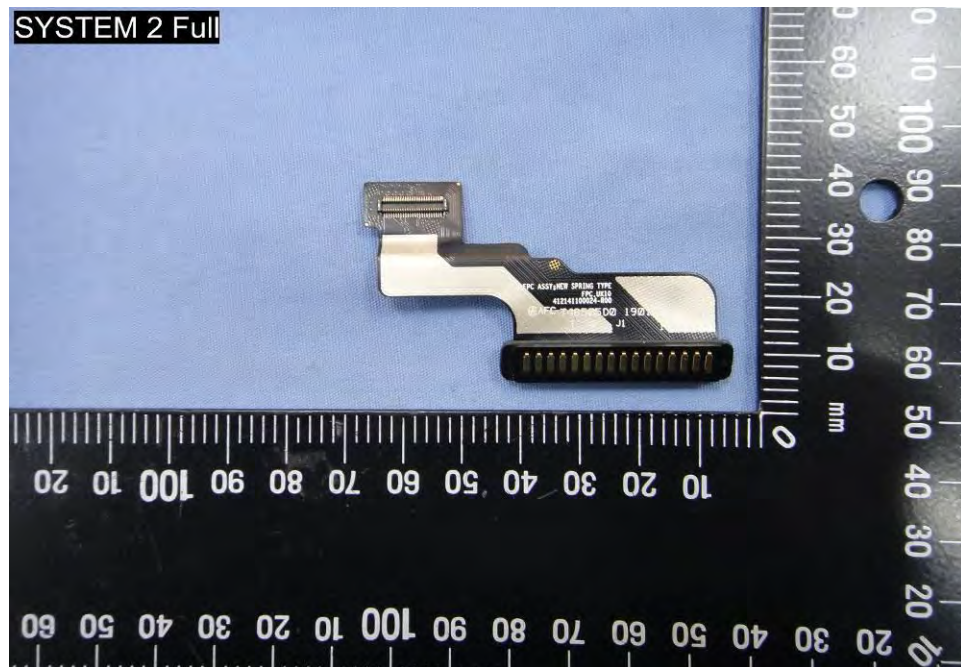
EUT Photo (86)



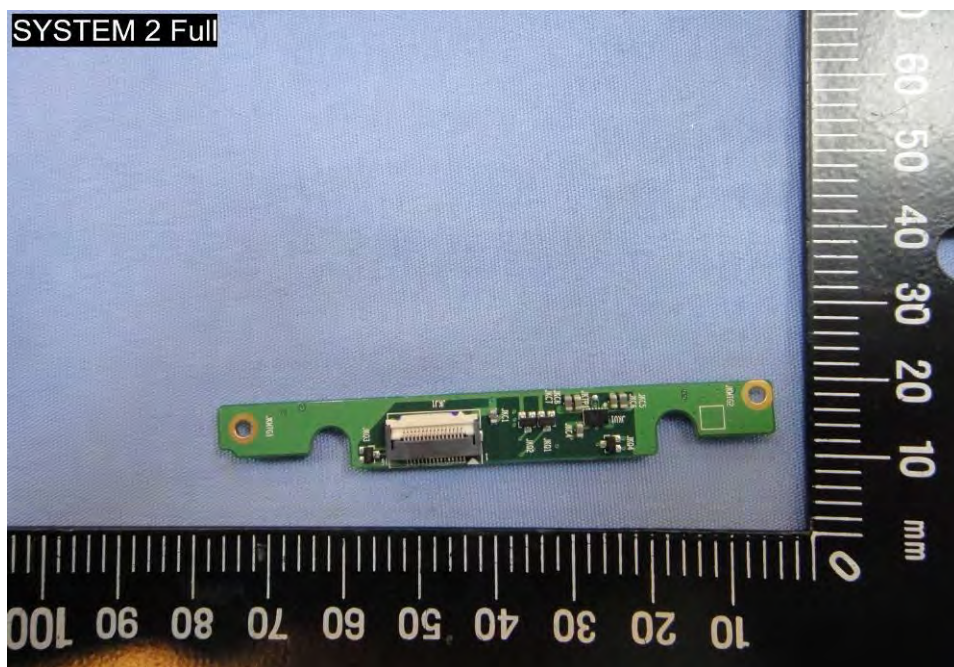
EUT Photo (87)



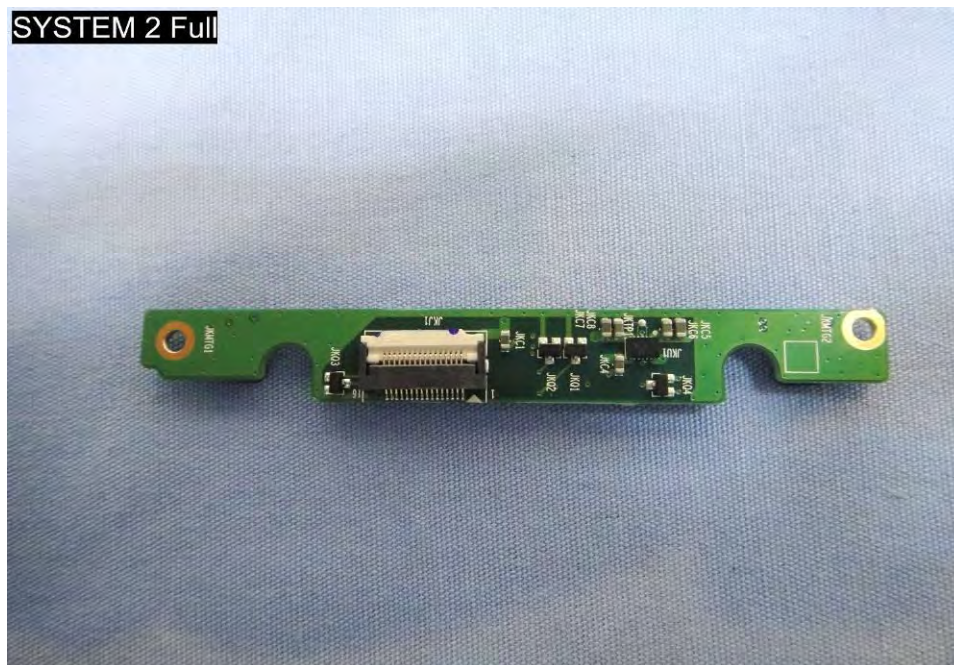
EUT Photo (88)



EUT Photo (89)

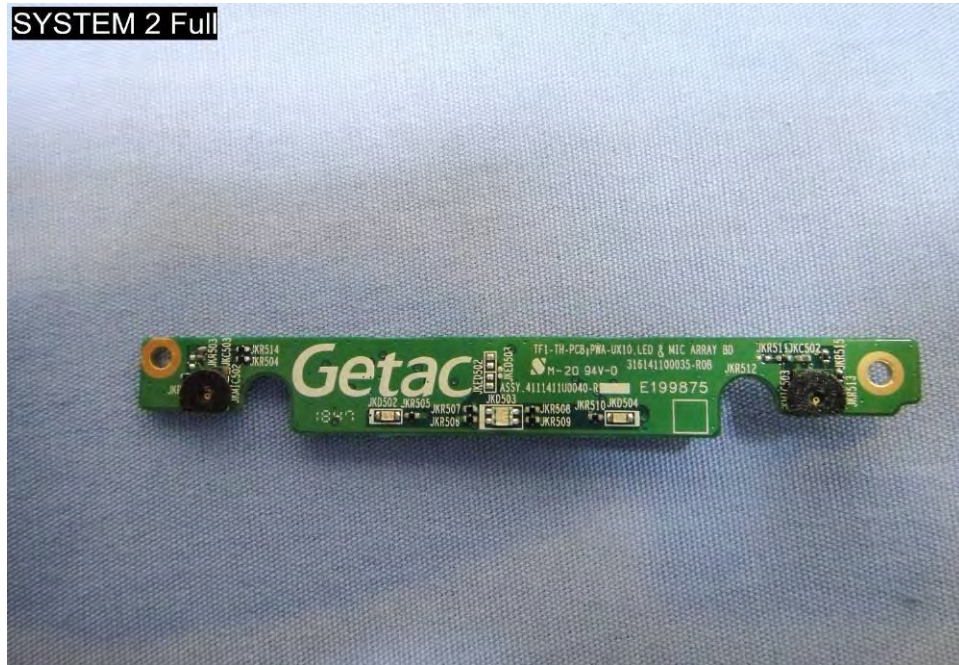


EUT Photo (90)



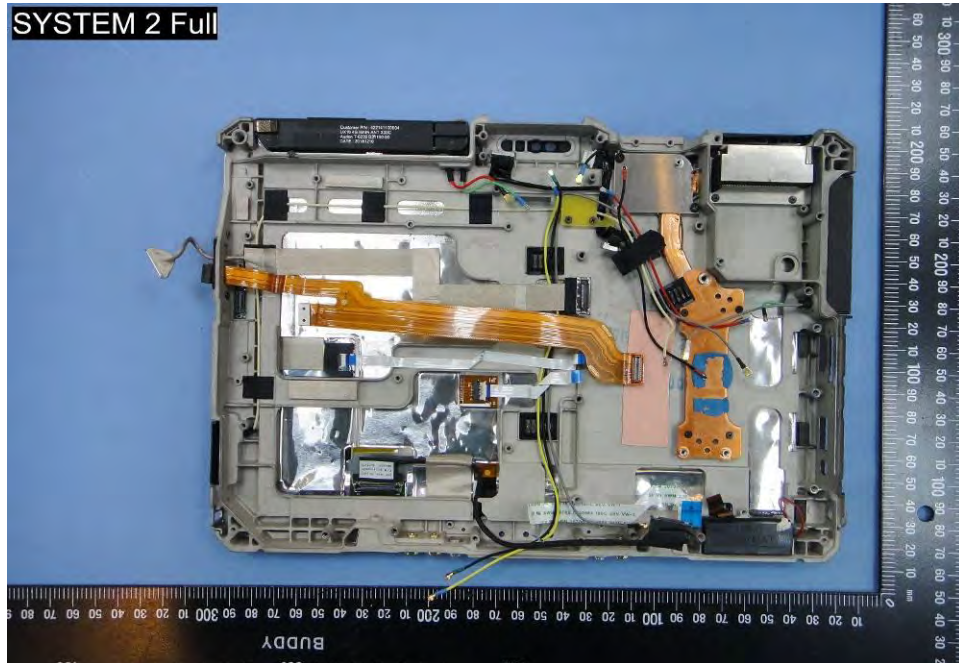
EUT Photo (91)

SYSTEM 2 Full



EUT Photo (92)

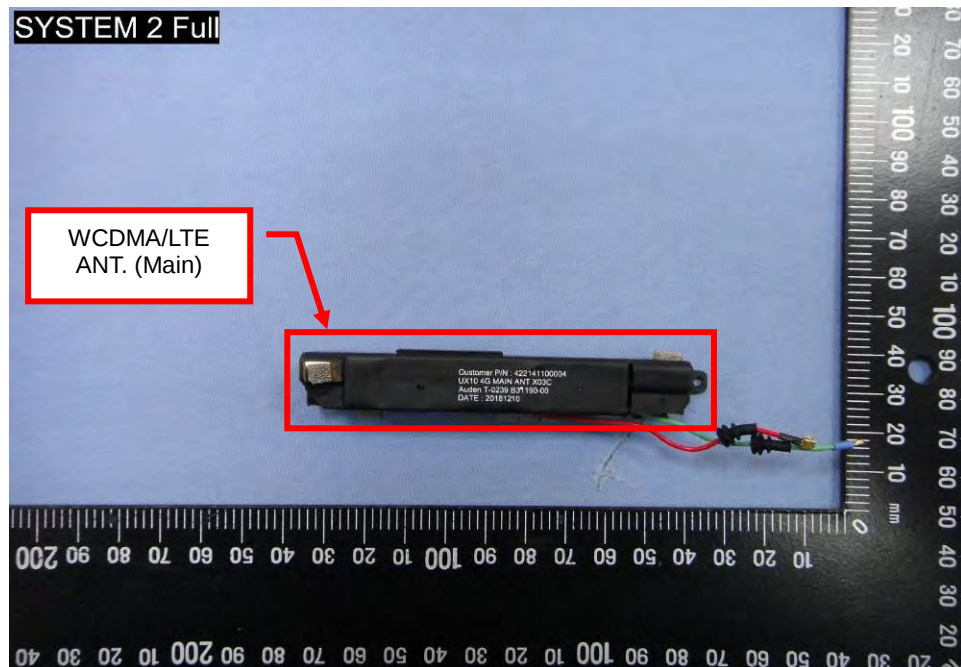
SYSTEM 2 Full



EUT Photo (93)



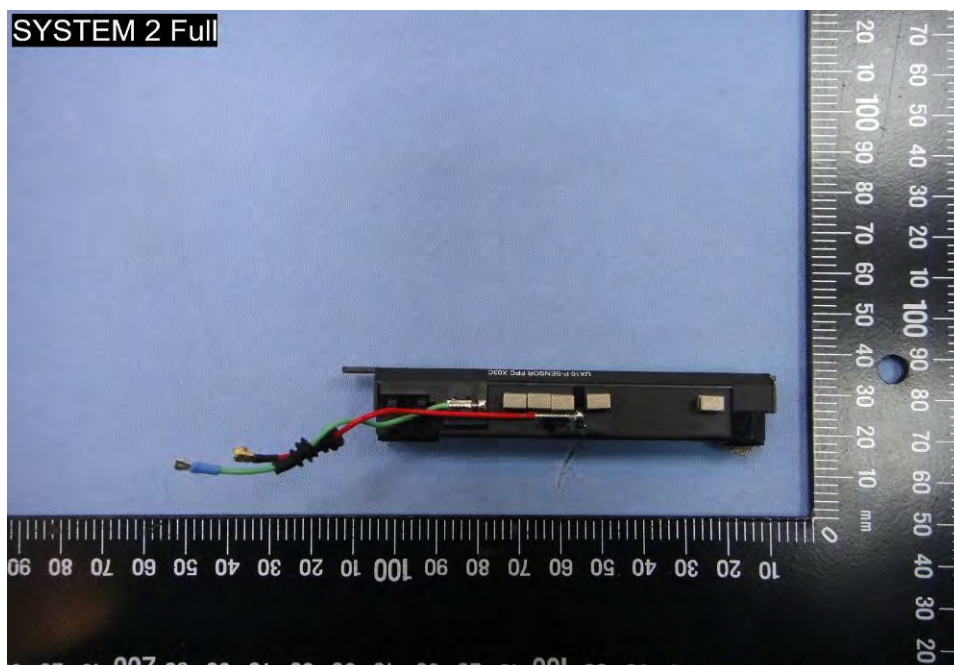
EUT Photo (94)



EUT Photo (95)



EUT Photo (96)



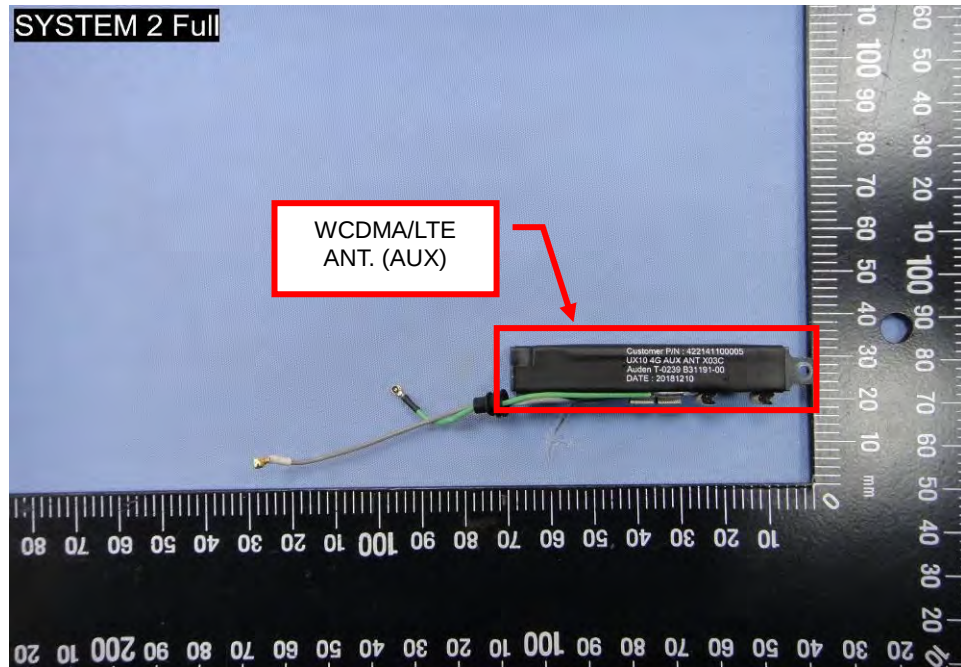
EUT Photo (97)



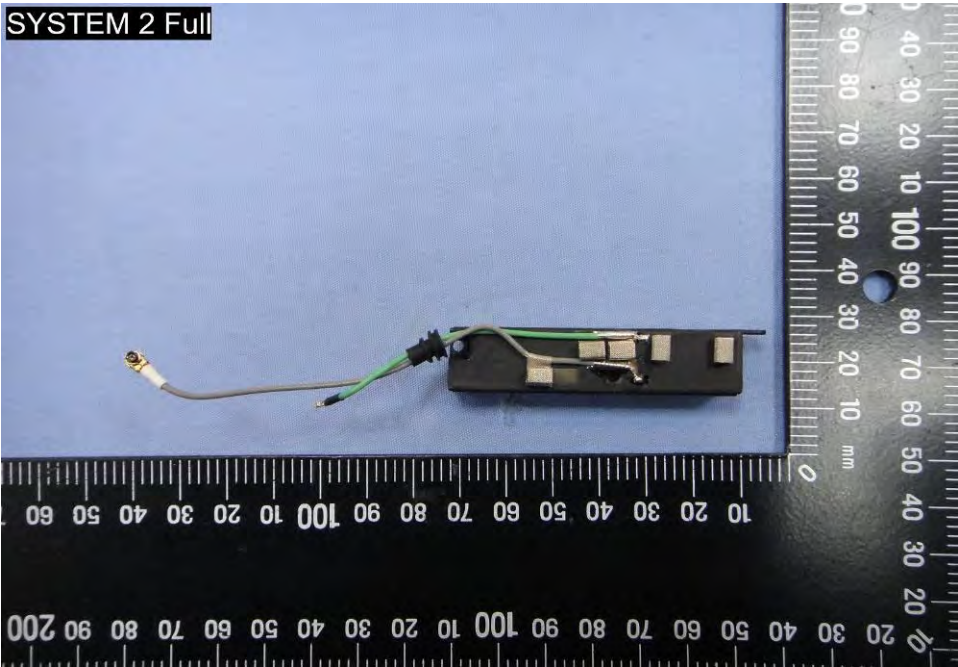
EUT Photo (98)



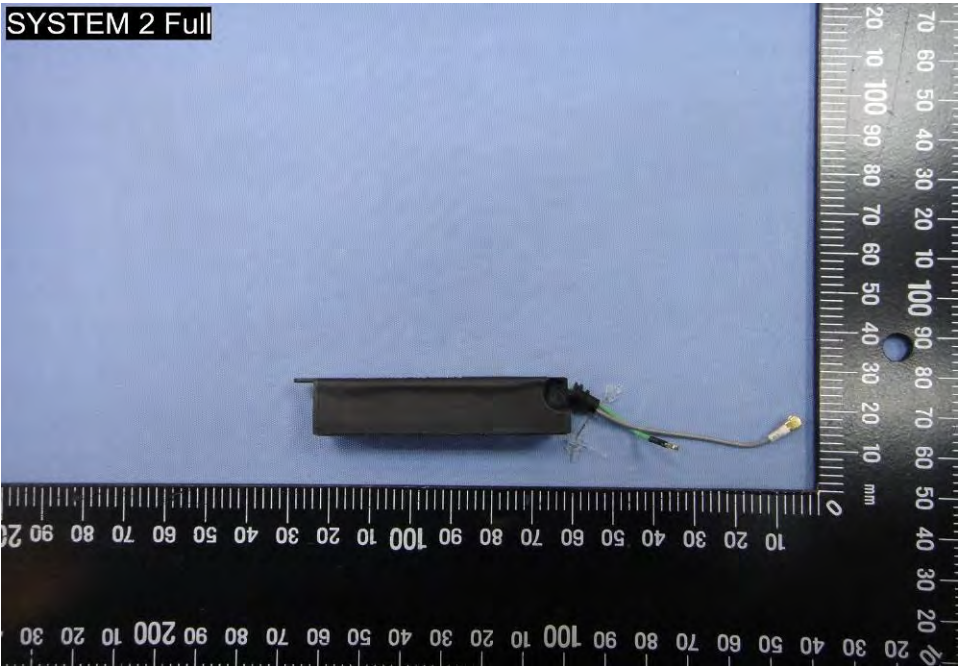
EUT Photo (99)



EUT Photo (101)



EUT Photo (102)



EUT Photo (103)



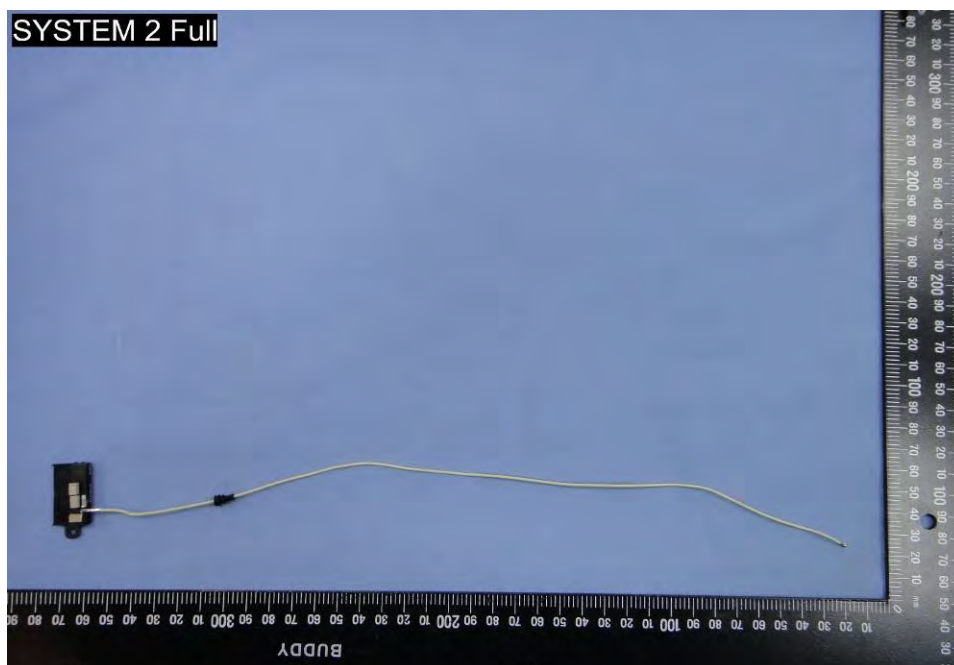
EUT Photo (104)



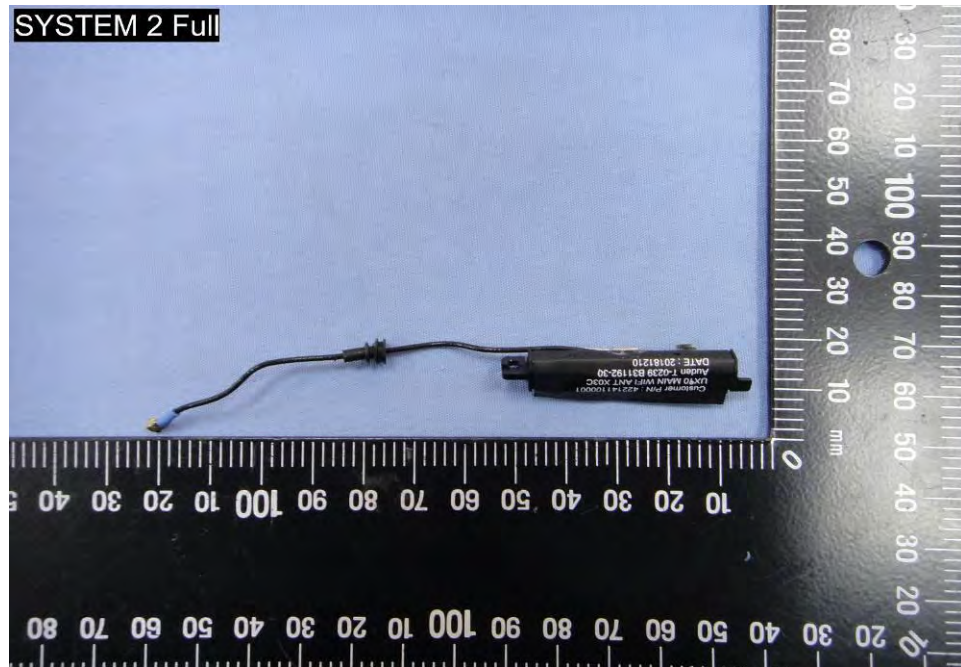
EUT Photo (105)



EUT Photo (106)



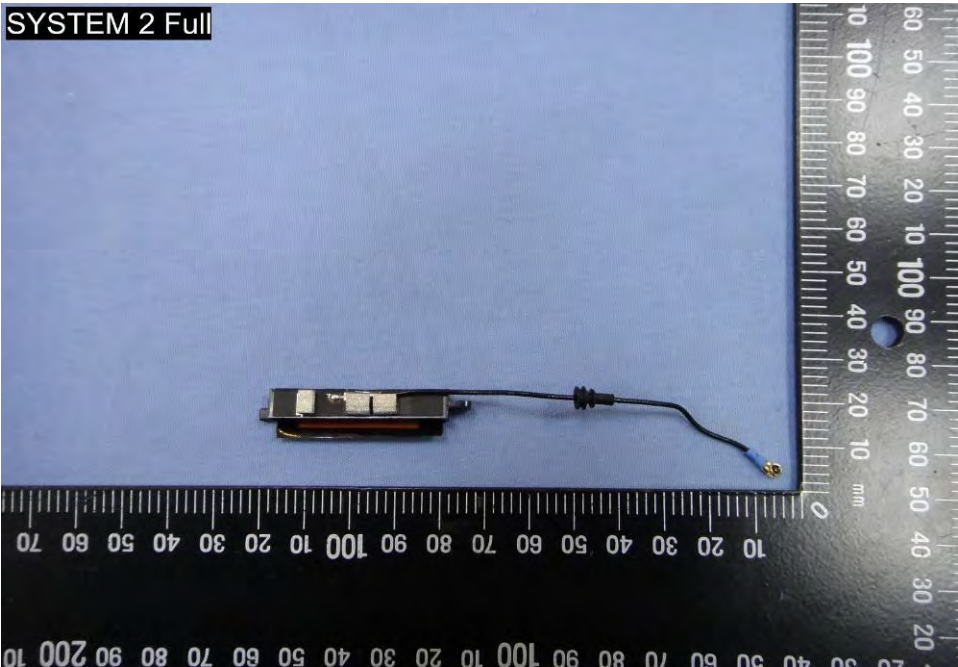
EUT Photo (107)



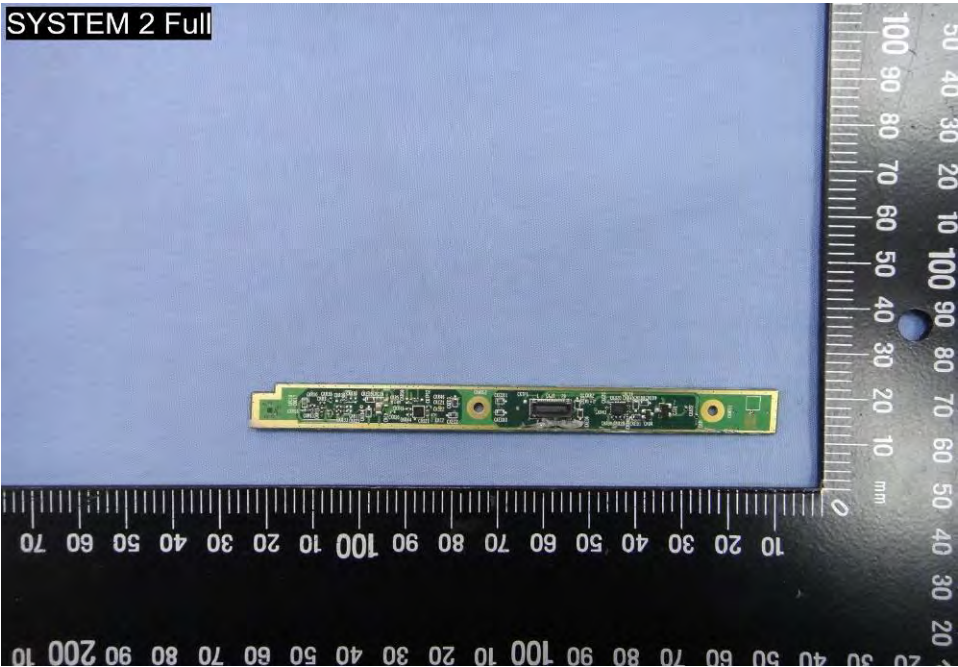
EUT Photo (108)



EUT Photo (109)

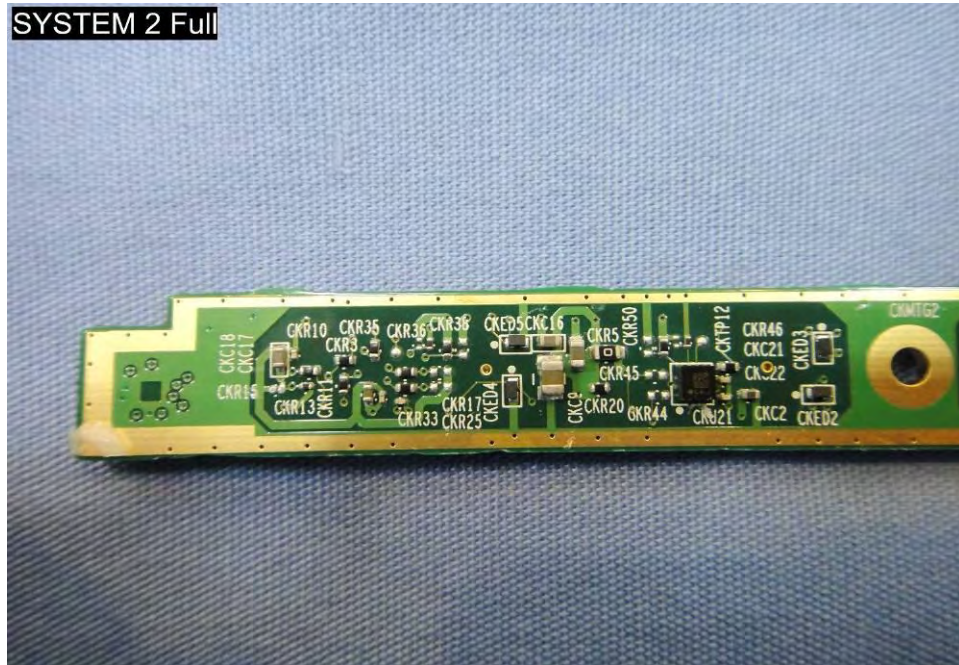


EUT Photo (110)



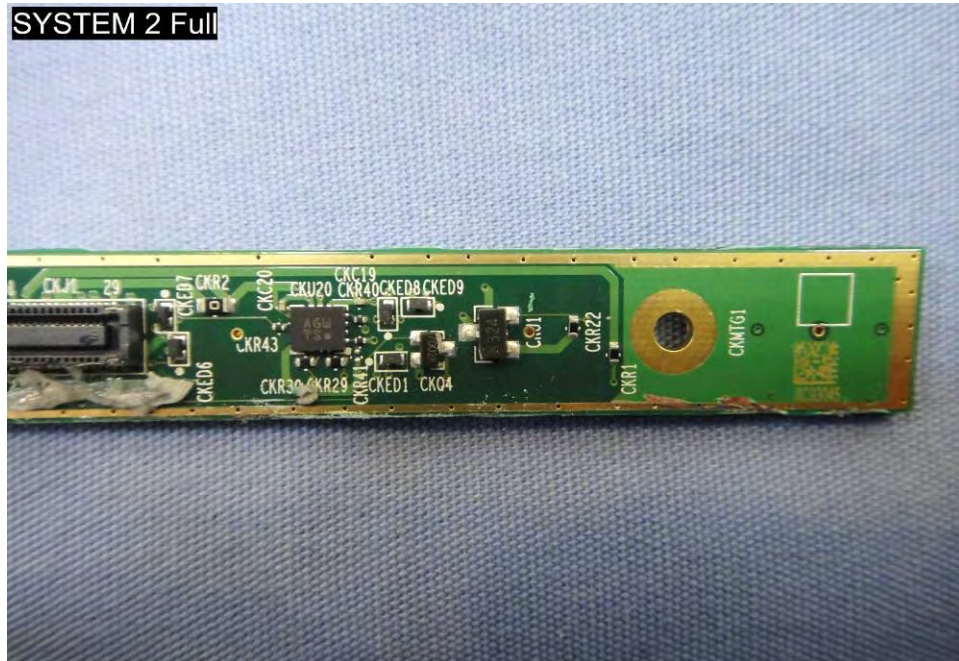
EUT Photo (111)

SYSTEM 2 Full

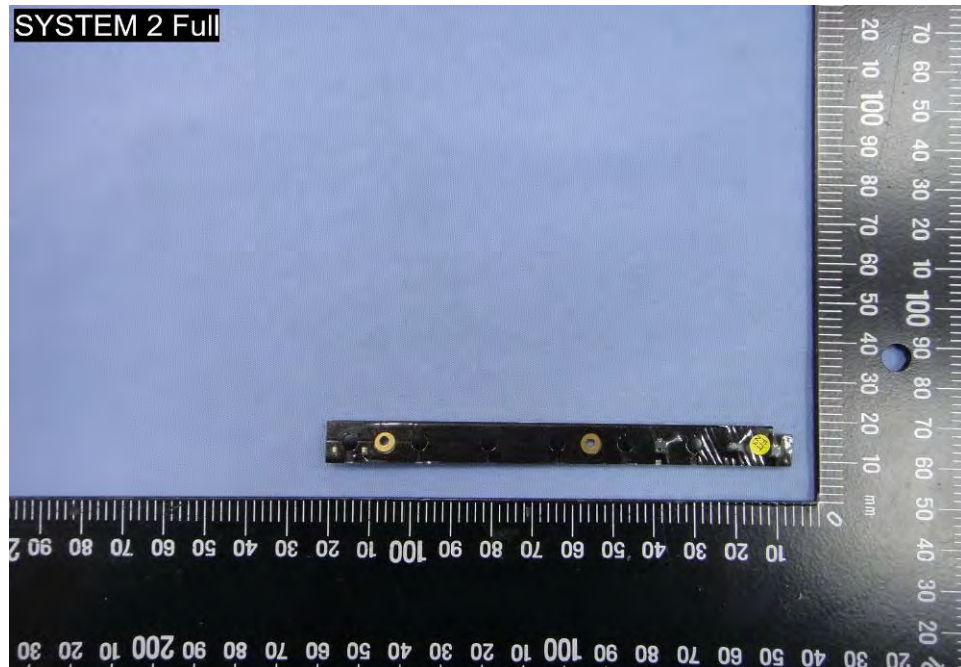


EUT Photo (112)

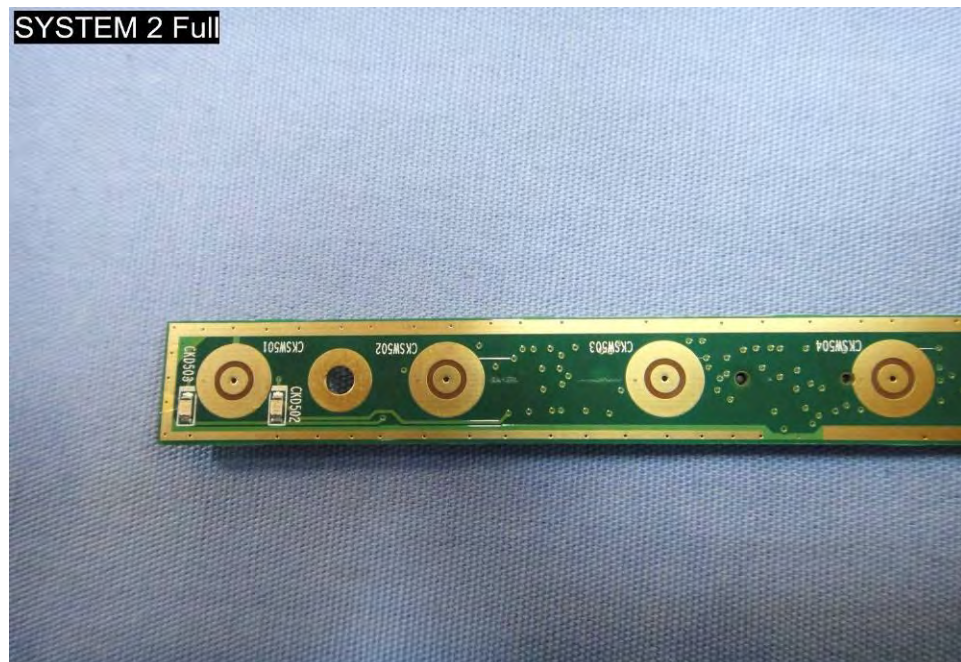
SYSTEM 2 Full



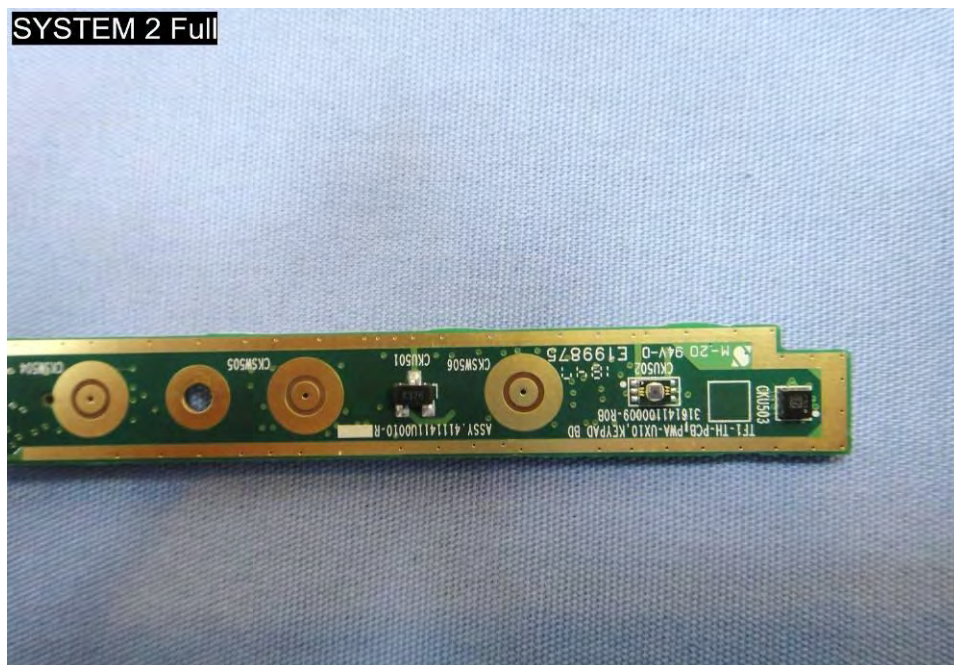
EUT Photo (113)



EUT Photo (114)



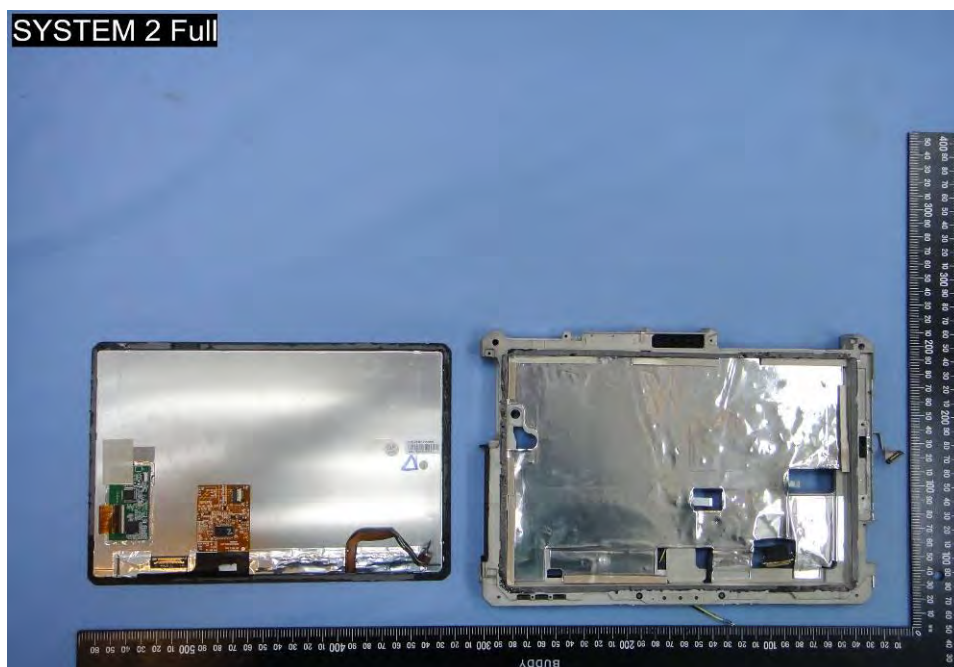
EUT Photo (115)



EUT Photo (116)



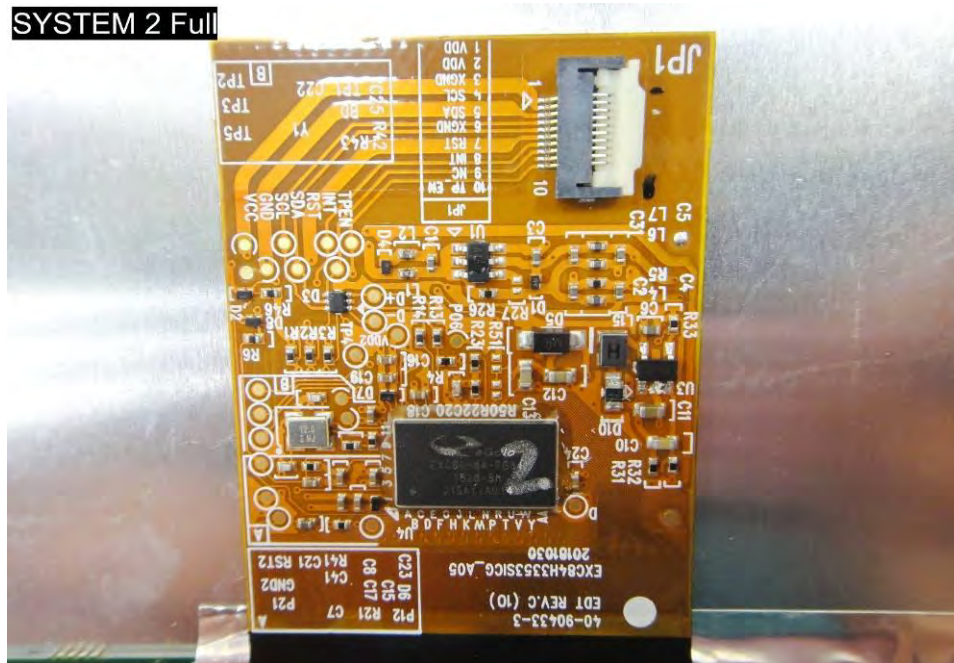
EUT Photo (117)



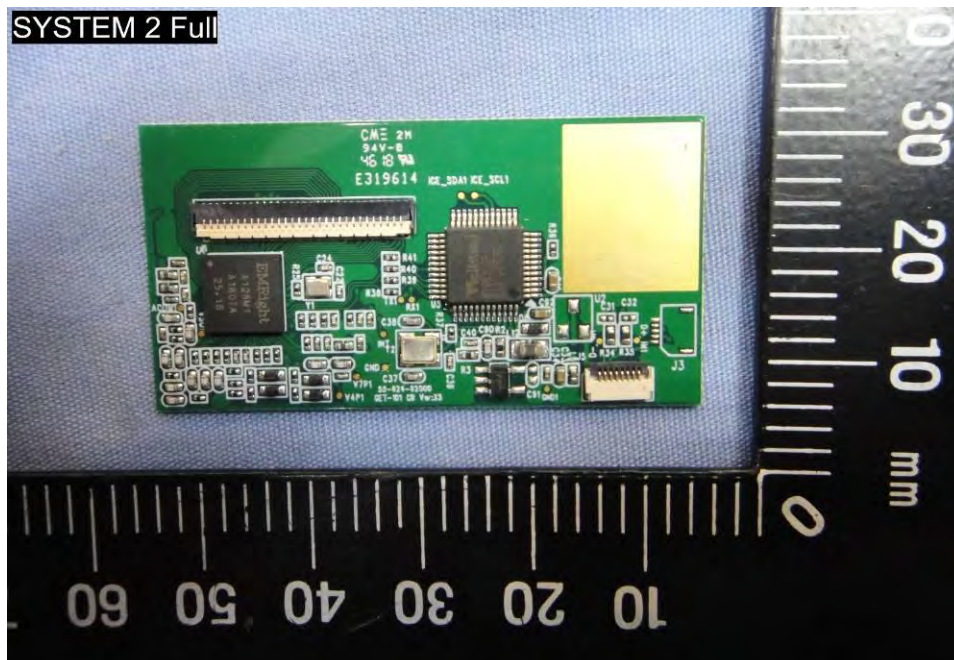
EUT Photo (118)



EUT Photo (119)

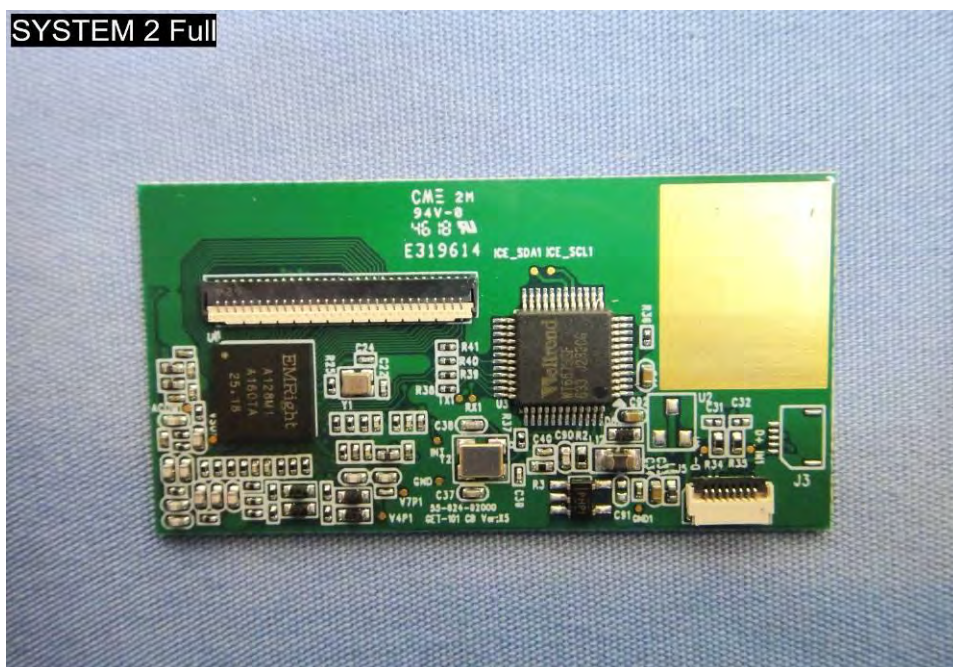


EUT Photo (120)



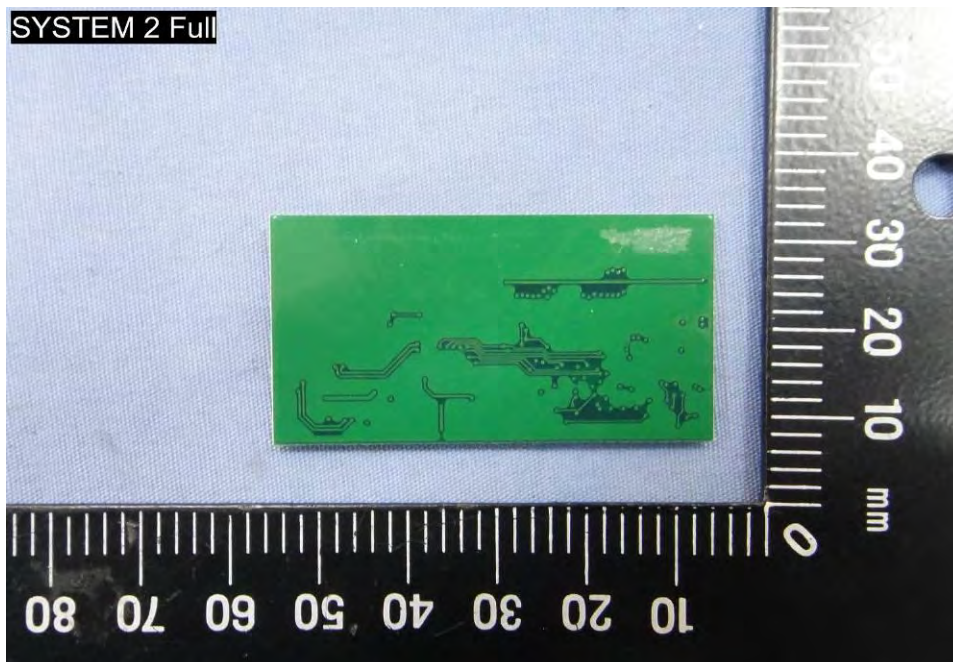
EUT Photo (121)

SYSTEM 2 Full



EUT Photo (122)

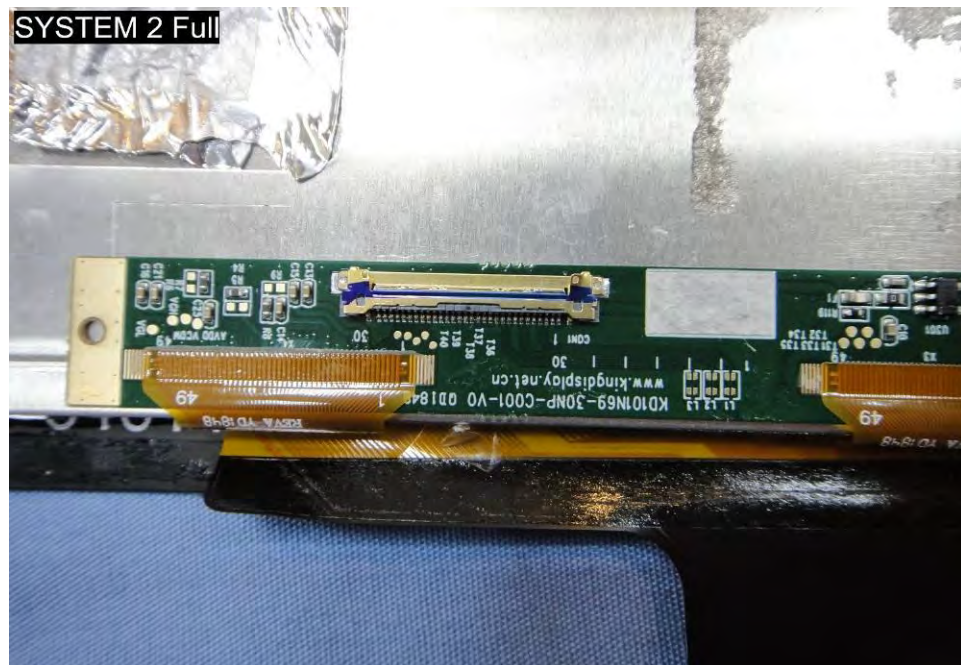
SYSTEM 2 Full



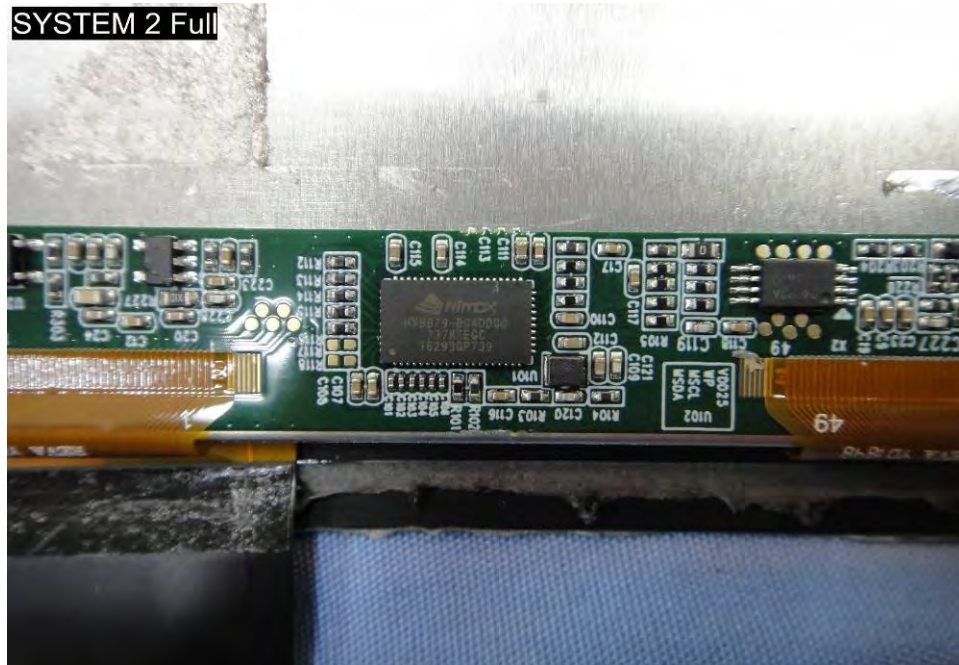
EUT Photo (123)



EUT Photo (124)



EUT Photo (125)



EUT Photo (126)



EUT Photo (127)

SYSTEM 2 Full

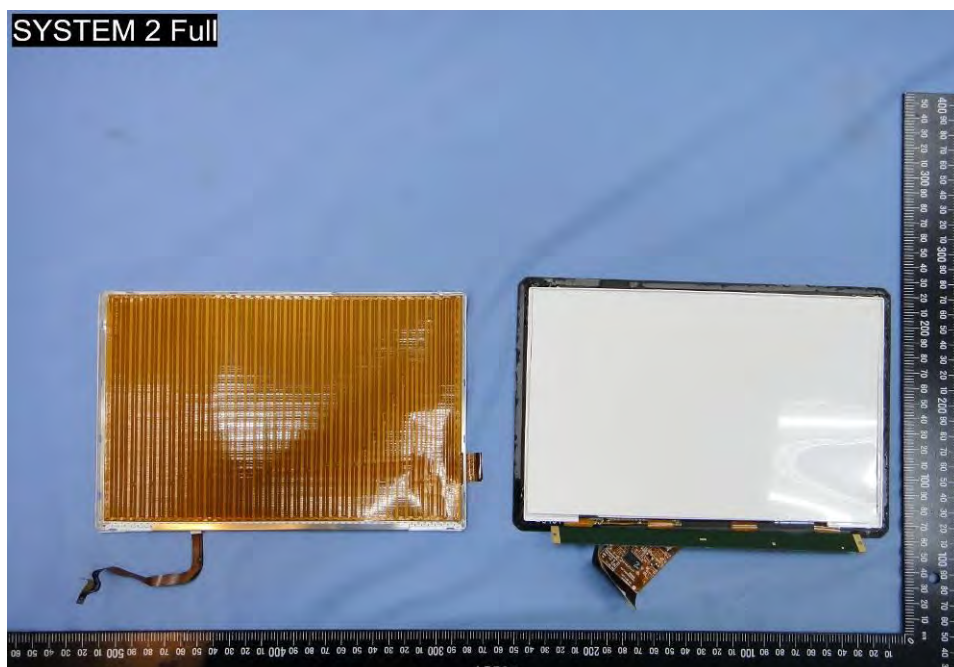


EUT Photo (128)

SYSTEM 2 Full



EUT Photo (129)



EUT Photo (130)

