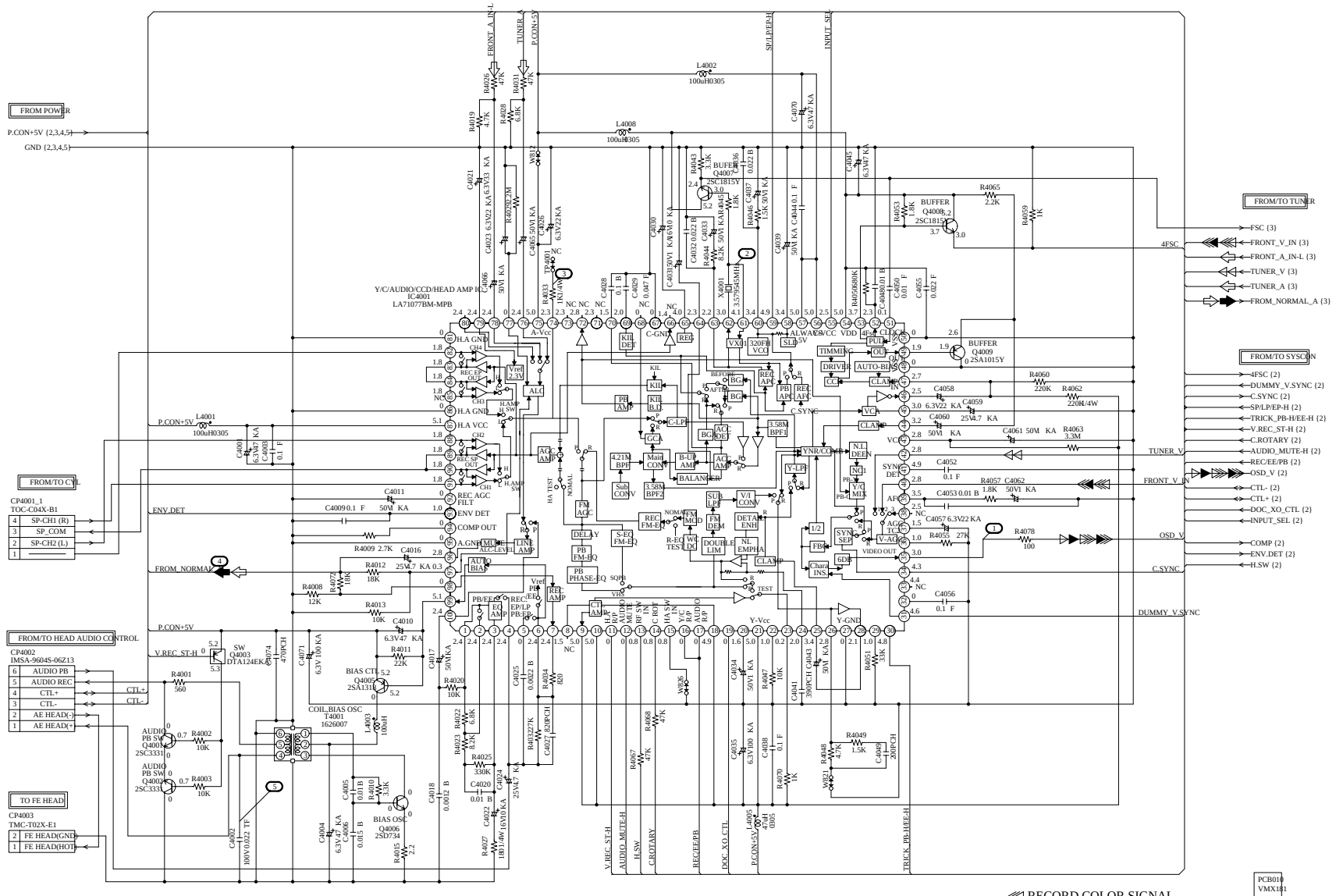


Y/C/AUDIO/CCD/HEAD AMP SCHEMATIC DIAGRAM (SYSCON PCB)



NOTES: THIS SCHEMATIC DIAGRAM IS THE LATEST AT THE TIME OF PRINTING AND SUBJECT TO CHANGE WITHOUT NOTICE. MEASURED WITH THE DIGITAL TESTER DURING PLAYBACK.

- RECORD COLOR SIGNAL
- RECORD LUMINANCE SIGNAL
- AUDIO SIGNAL (REC)
- AUDIO SIGNAL (PB)
- PLAYBACK COLOR SIGNAL
- PLAYBACK LUMINANCE SIGNAL
- TUNER VIDEO SIGNAL

DET. CHKD BY	NTSC	FUNCTION	V/C/AUDIO/CCD/HEAD	PCB NAME	MM01A_C_ANKI
CHKD BY	STANDARD	DWG. NO.	MAC7VCH	Rev. No.	3.0
ULFPC				TSU	B
DESIGN BY	Y. OKUMURA	MODEL	ORION ELECTRIC CO., LTD.	SECTION	TECH. DATA/13/90
DRAWN BY	M. HAMANO	DISTRIBUTE		PUBLICATION	TECH. HQ2 SEC.

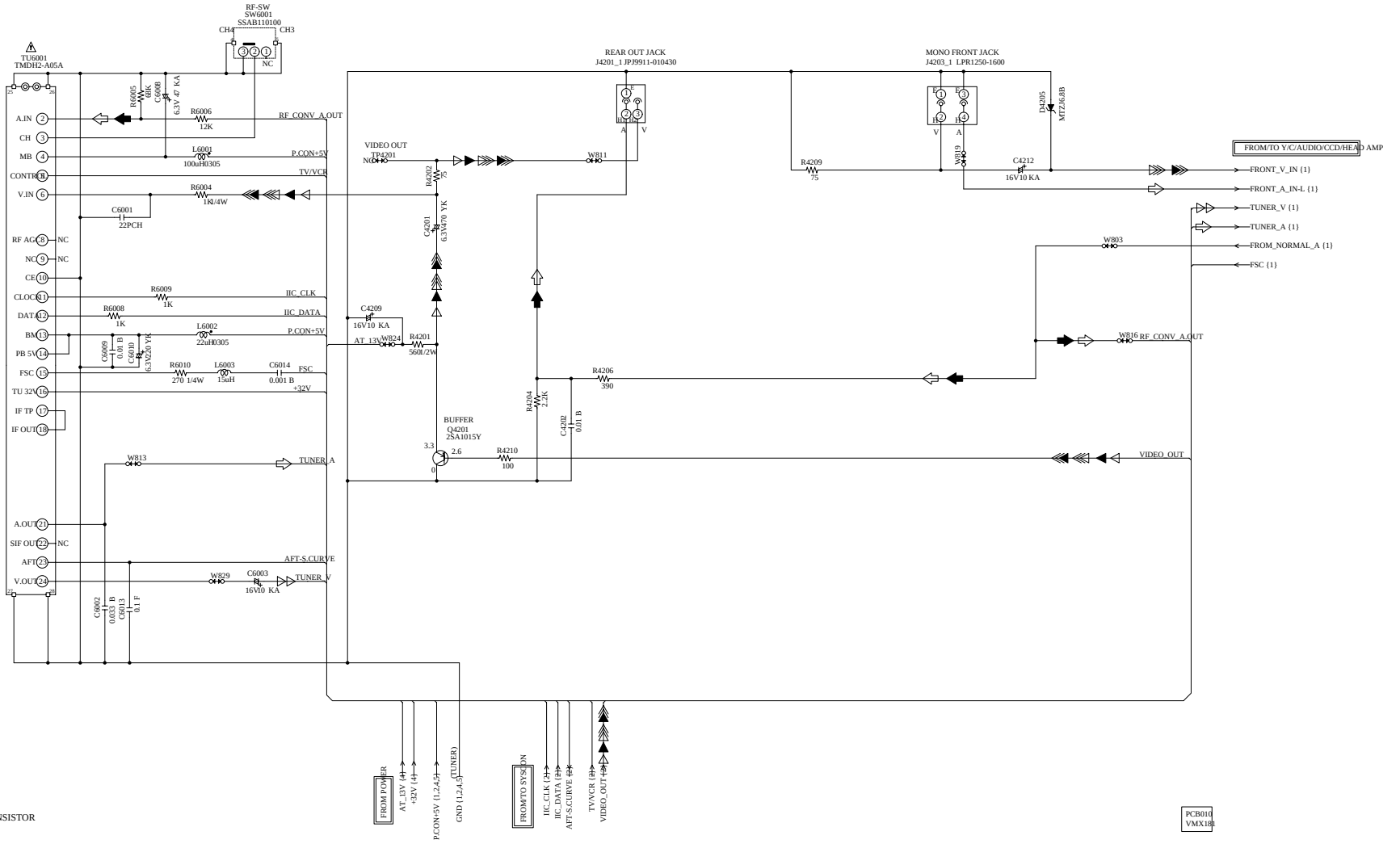
[illegible]

 RECORD COLOR SIGNAL
 RECORD LUMINANCE SIGNAL
 PLAYBACK COLOR SIGNAL
 PLAYBACK LUMINANCE SIGNAL

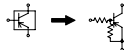
NOTE: THIS SCHEMATIC DIAGRAM IS THE LATEST AT THE TIME OF PRINTING AND SUBJECT TO CHANGE WITHOUT NOTICE

PCB010 VMX181				
DET.CHKD BY	DESCRIPTION NTSC	FUNCTION SYSCON	PCB NAME VMX181A_C	ANKI
CHKD BY	STANDARD UL/FCC	DWG.NO.	1/3 Rev.No: 3.0 ISSU: A	1
DESIGN BY Y.OKUMURA	MODEL M4C7-A0A,B	ORION ELECTRIC CO.LTD		
DRAWN BY M.HAMANO	SECTION: TEC-HQ3		DATE:13/90	
		DISTRIBUTE: PUBLICATION: TEC-HQ3 SEC.		

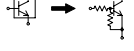
TUNER SCHEMATIC DIAGRAM (SYSCON PCB)



CAUTION: DIGITAL TRANSISTOR



CAUTION: DIGITAL TRANSISTOR. THE DC VOLTAGE AT EACH PART OF THIS SCHEMATIC DIAGRAM IS THE LATEST AT THE TIME OF PRINTING AND SUBJECT TO CHANGE WITHOUT NOTICE. CRITICAL FOR SAFETY USE ONES DESCRIBED IN PARTS LIST ONLY.



ATTENTION: LES PIÈCES RÉPARÉES PAR UN MANUTENANT DANGEREUSES AN POINT DE VUE SECURITE. N'UTILISER QUE CELLES DÉCRITES DANS LA NOMENCLATURE DES PIÈCES.

PLAYBACK LUMINANCE SIGNAL (REC)
PLAYBACK COLOR SIGNAL (REC)
AUDIO SIGNAL (REC)
AUDIO SIGNAL (PB)
TUNER VIDEO SIGNAL

PCB010
VMX181

DET./CHKD BY	DESCRIPTION	FUNCTION	PCB NAME
NTSC	TUNER	TUNER	VMX181A_C_ANKI
CHKD BY	STANDARD	DWG.NO.	Rev.No:3.0
UL/PC	ORION ELECTRIC CO., LTD	TSU: A	
DESIGN BY	Y.OKUMURA	SECTION: TECH	DATE:13/90
DRAWN BY	M.HAMANO	PUBLICATION: TEC-HQ	SEC.

[illegible]

PCB010 VMX181					
DET.CHKD BY	DESCRIPTION NTSC	FUNCTION POWER	PCB NAME VMX181A_C		
CHKD BY	STANDARD UL FCC	DWG.NO.	Rev.No: 3.0		
DESIGN BY Y.OKUMURA	MODEL MAC7-A0A	ORION ELECTRIC CO.LTD		ISSU: A	
DRAWN BY M.HAMANO	DISTRIBUTE		DATE:1/3/90		
		PUBLICATION: TEC-HQ 3SEC.			
H					

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NOTE: THIS SCHEMATIC DIAGRAM IS THE LATEST AT THE TIME OF PRINTING AND SUBJECT TO CHANGE WITHOUT NOTICE

DET.CHKD BY	DESCRIPTION	FUNCTION	PCB NAME
	NTSC	OPERATION/DISPLAY	VM181A_C
CHKD BY	STANDARD	DWG.NO.	Rev.No.3
	UL/FCC	M4C70P13	ISSU: A
DESIGN BY	MODEL	ORION ELECTRIC CO.LTD	
Y.OKUMURA	M4C7-A0A,B	SECTION: TEC-HQ	DATE: 2/15/99
DRAWN BY	-A4A,B	DISTRIBUTE:	
M.HAMANO		PUBLICATION: TEC-HQ 2 SEC.	

