

Block Diagram

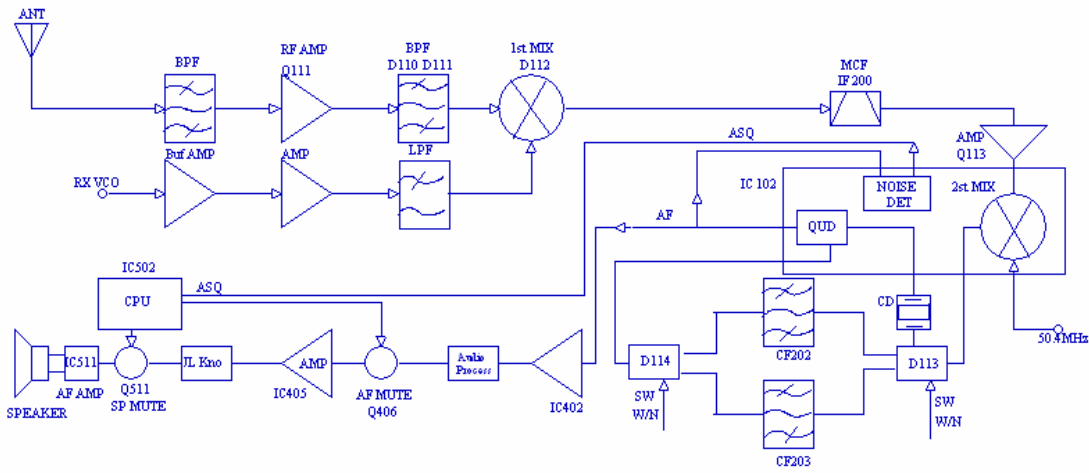


Fig 1 Receiver Circuit

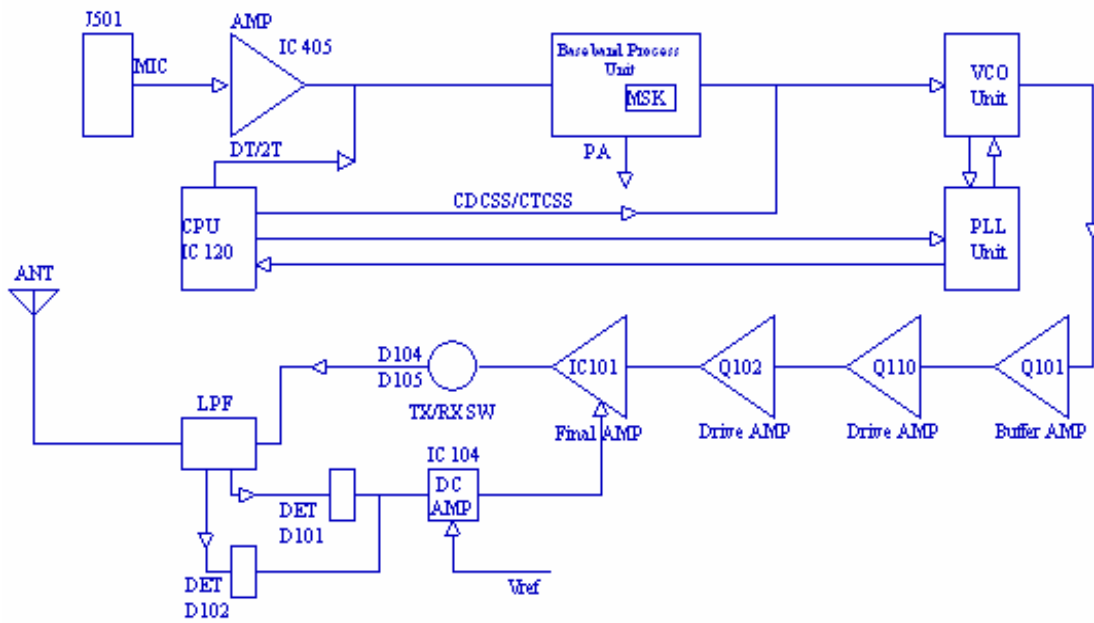
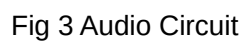


Fig 2 Transmitter Circuit



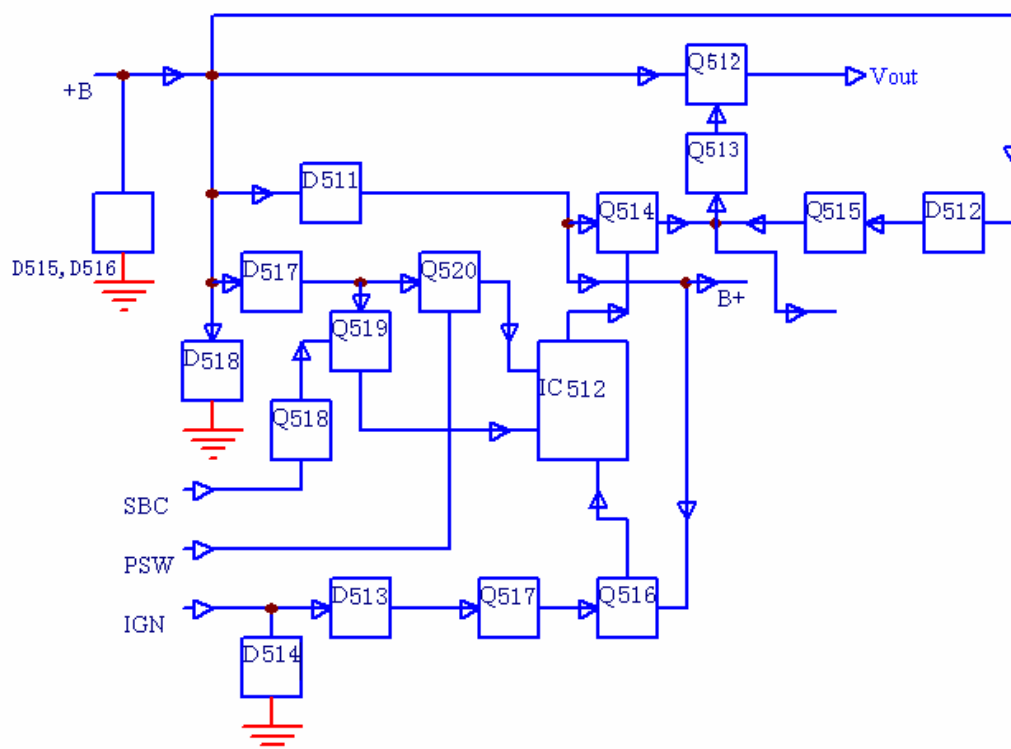


Fig 5 Power Supply Circuit

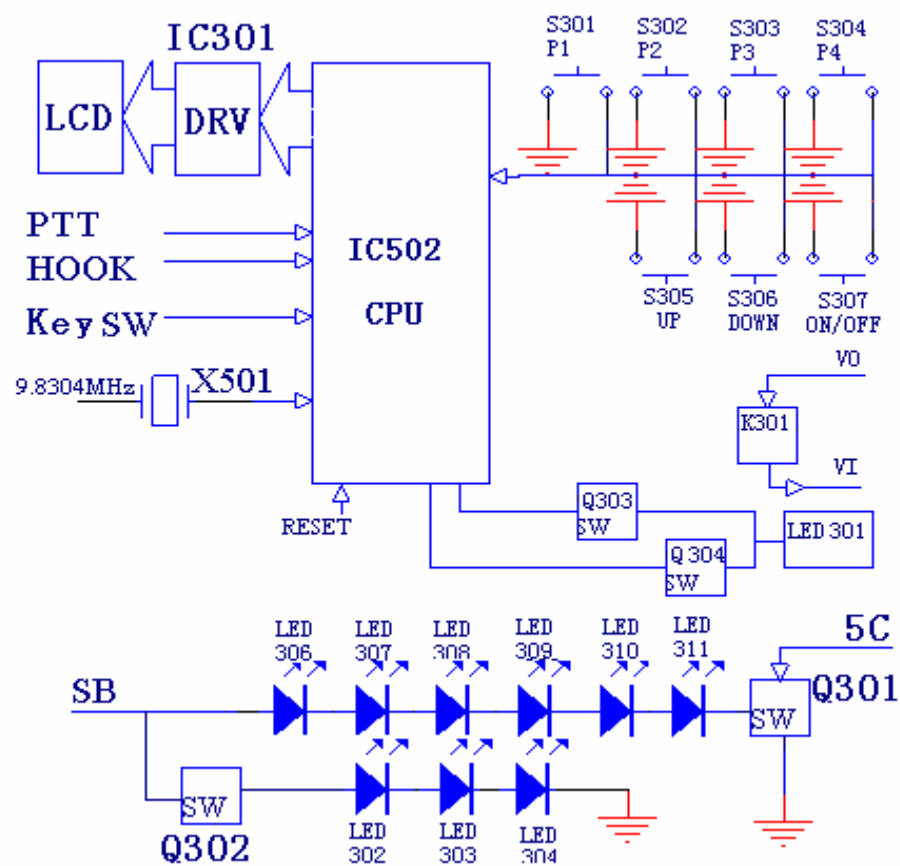


Fig 6. Display Circuit