



Spectrum

Ref Level 20.00 dBm Offset BW 12.04 dB RBW 100 kHz Mode Auto Sweep
 Att 20 dB BWT 30.1 ms VBW 300 kHz

Count 10/10

1Pk View

MI[1] -56.00 dBm
 150.7960 MHz

10 dBm
 0 dBm
 -10 dBm
 -20 dBm
 -30 dBm
 -40 dBm
 -50 dBm
 -60 dBm
 -70 dBm

O1 -23.810 dBm

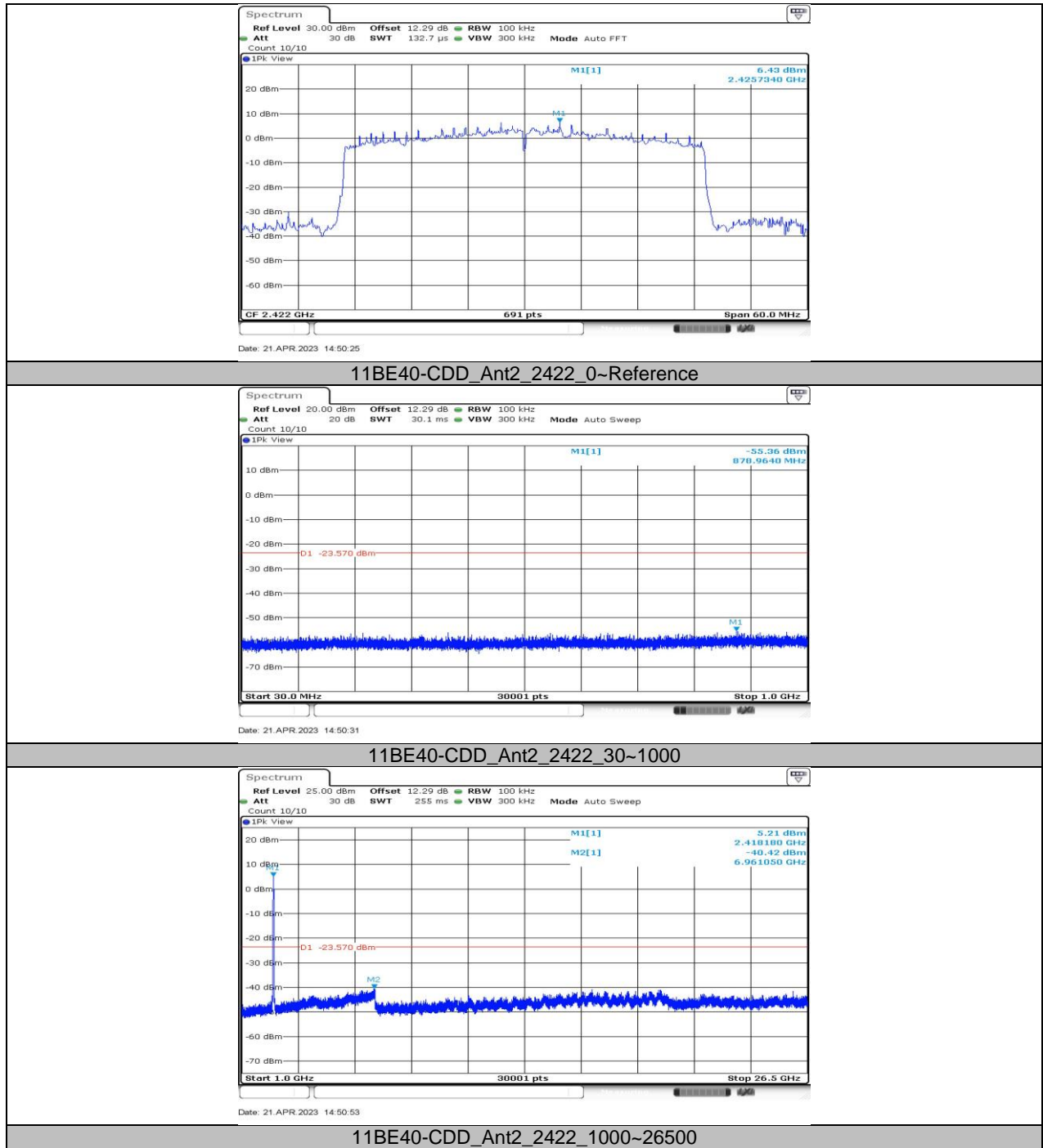
MI[1]

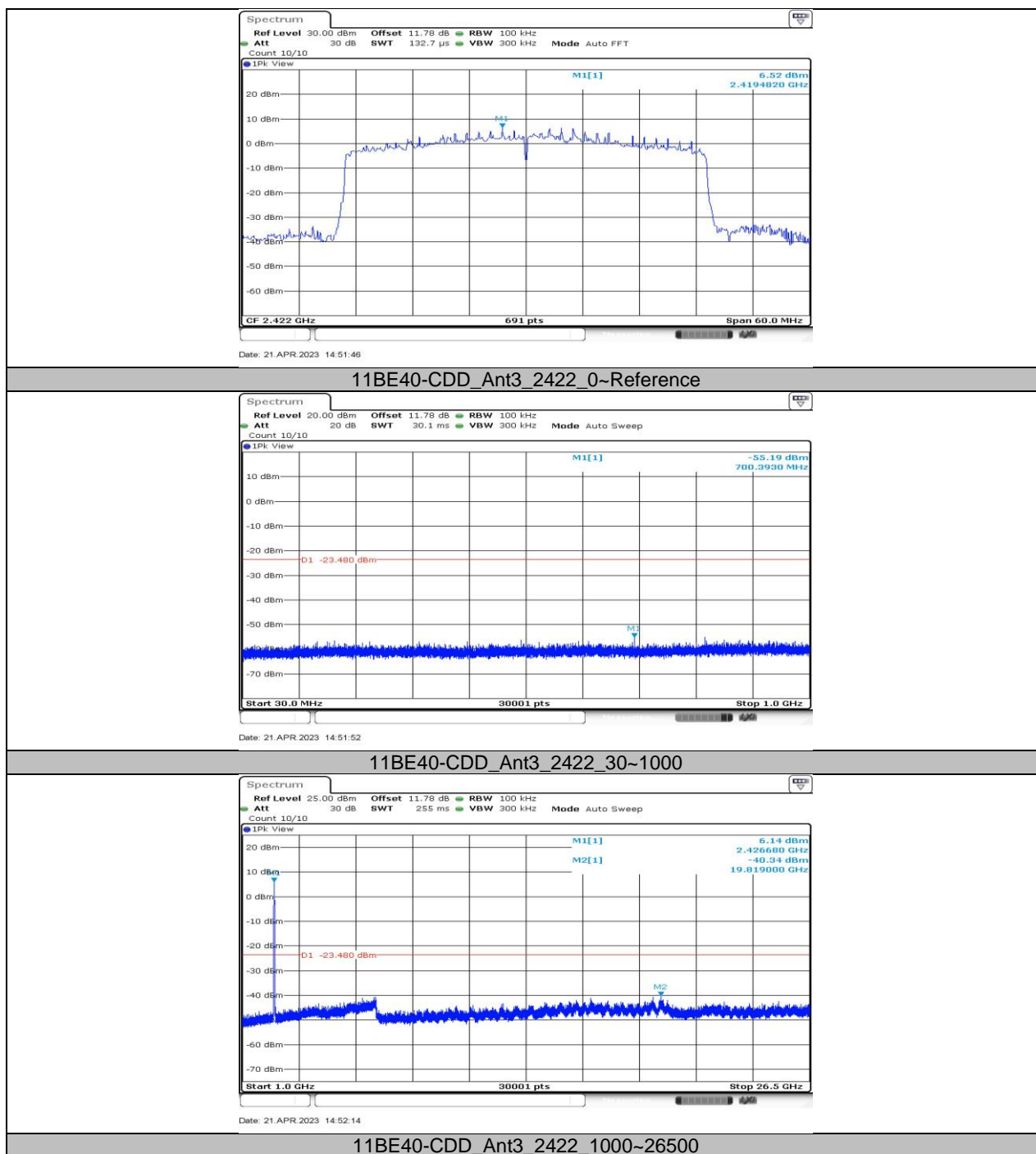
Start 30.0 MHz 30001 pts Stop 1.0 GHz

Date: 21 APR 2023 14:49:10

The screenshot displays a Spectrum Analyzer interface. At the top, the title is "Spectrum". Below it, the "Ref Level" is set to 25.00 dBm, and the "Att" is 30 dB. The "Offset BWT" is 12.04 dB, "RBW" is 100 kHz, and "VBW" is 300 kHz. The "Mode" is set to "Auto Sweep". The "Count" is 10/10. The "1Pk View" is selected. The main display shows a blue trace representing the spectrum, with a noise floor around -50 dBm. A red horizontal line is drawn at -23.810 dBm, labeled "D1". Two frequency markers are present: "M1[1]" at 5.41 dBm and "M2[1]" at -39.73 dBm. The x-axis is labeled "Start 1.0 GHz", "30001 pts", and "Stop 26.5 GHz". The y-axis is labeled with dBm values from -70 to 20. The date and time at the bottom are "Date: 21 APR 2023 14:49:31".

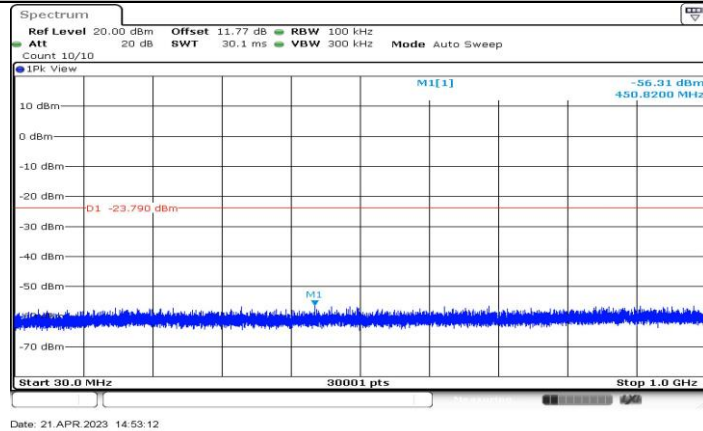
11BE40-CDD	Ant1	2422	1000~26500
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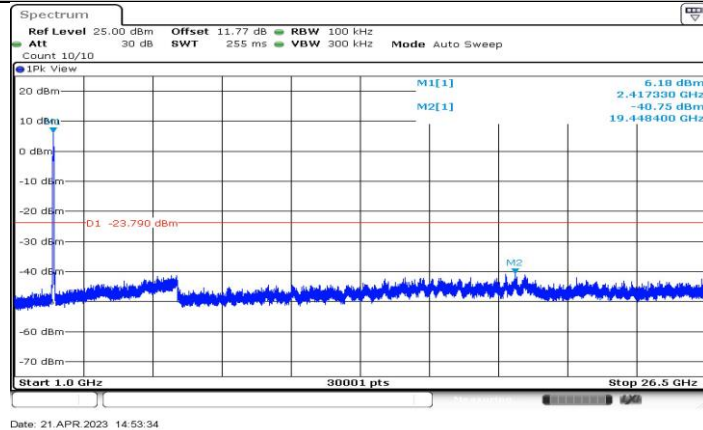




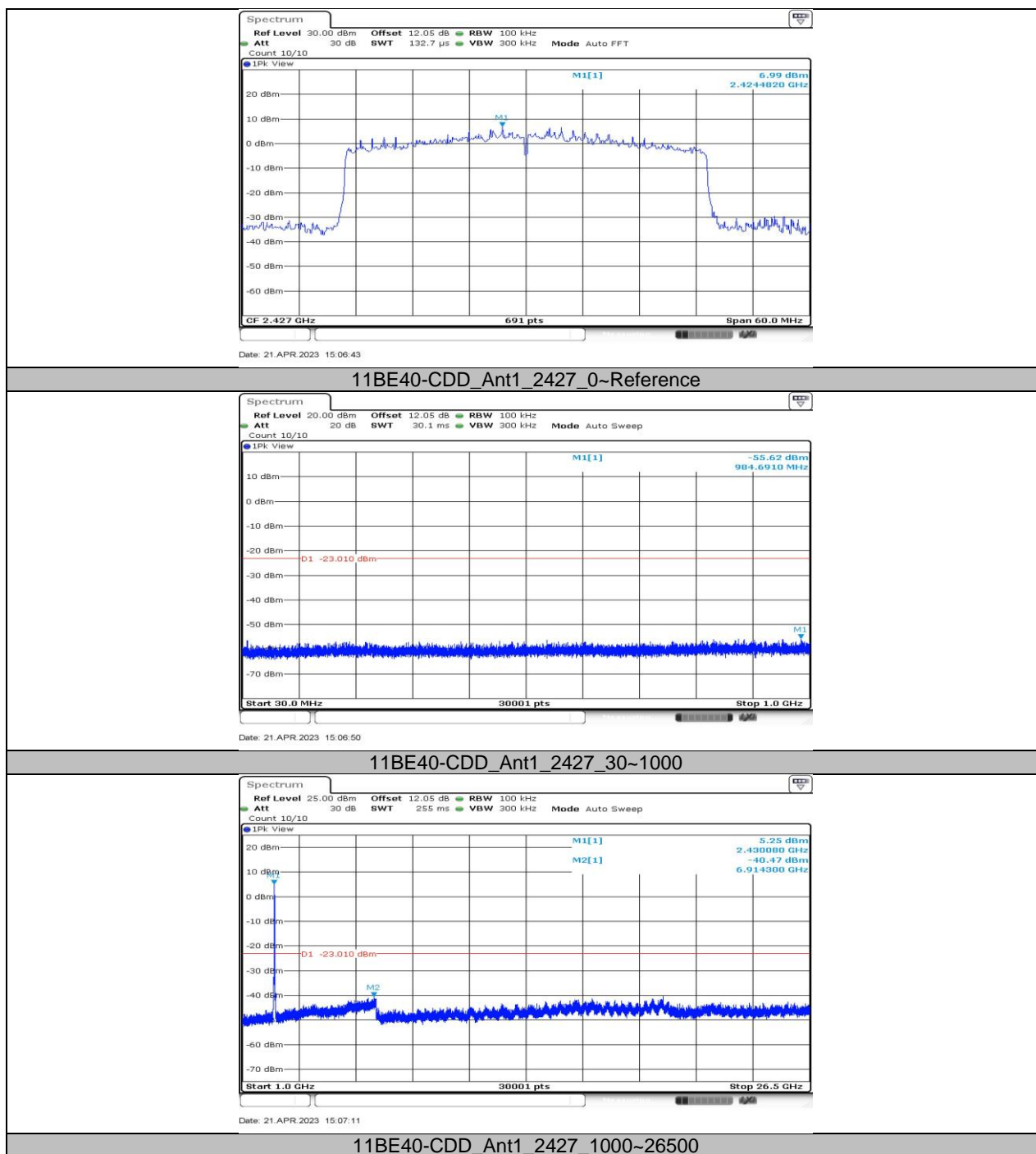
11BE40-CDD_Ant4_2422_0~Reference

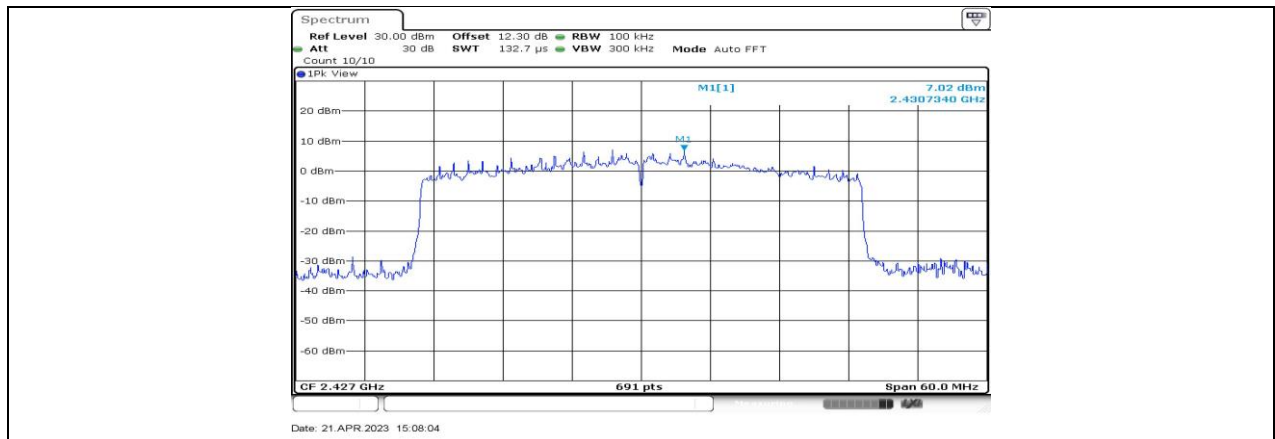


11BE40-CDD_Ant4_2422_30~1000

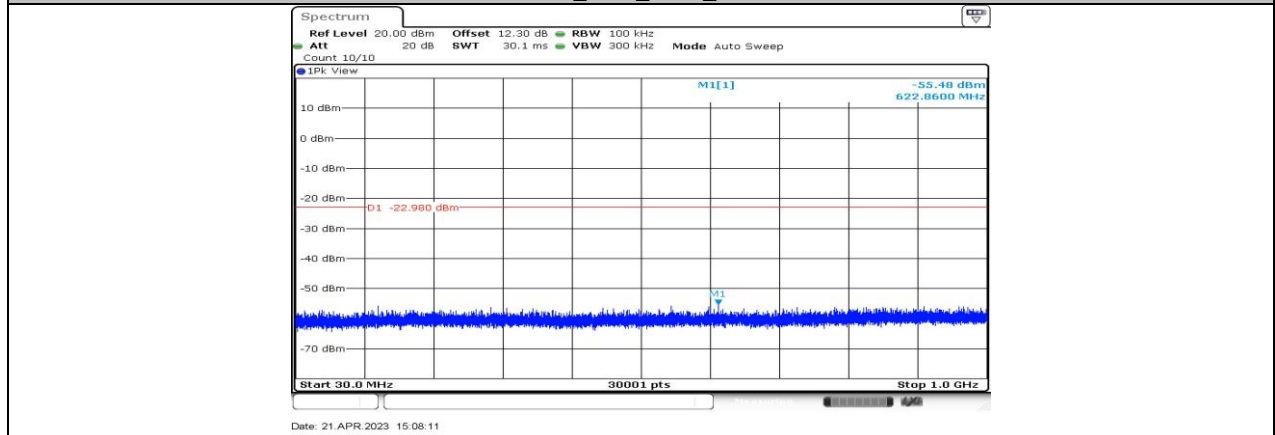


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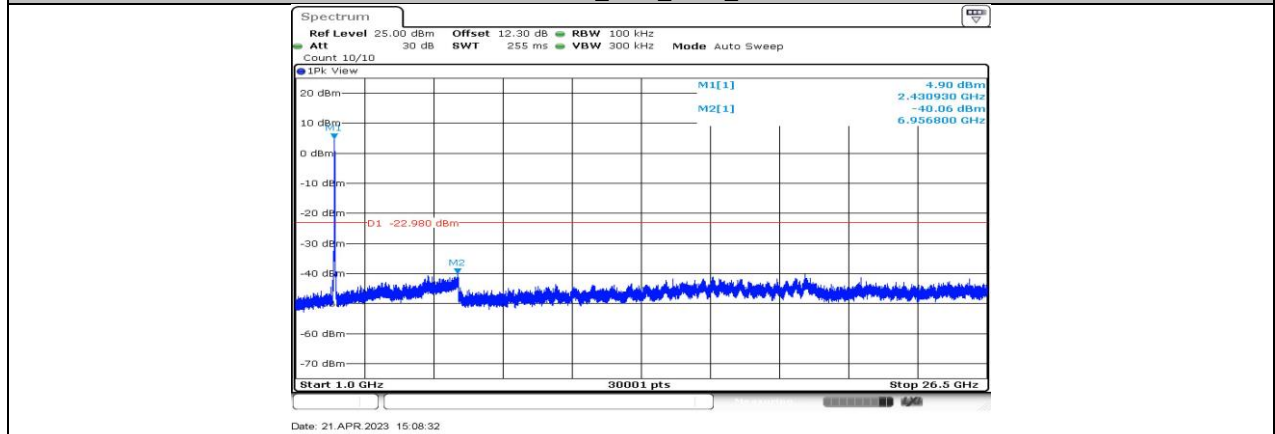




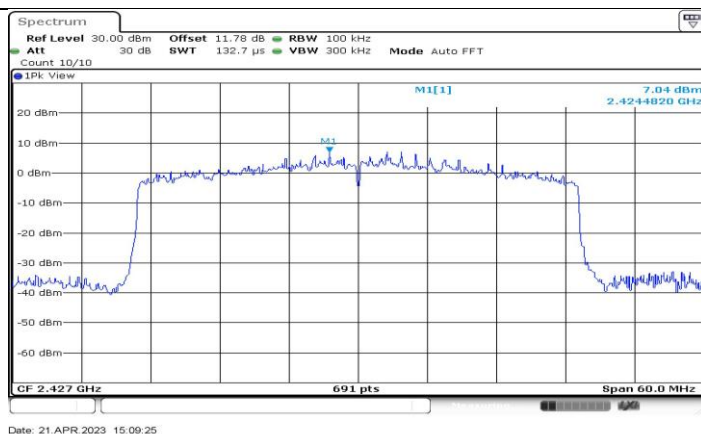
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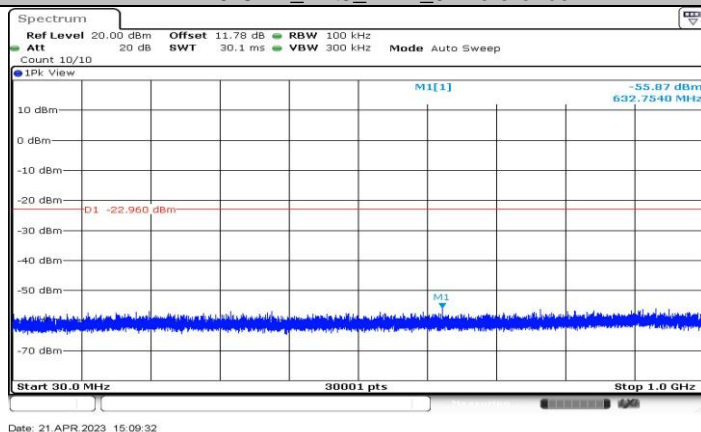
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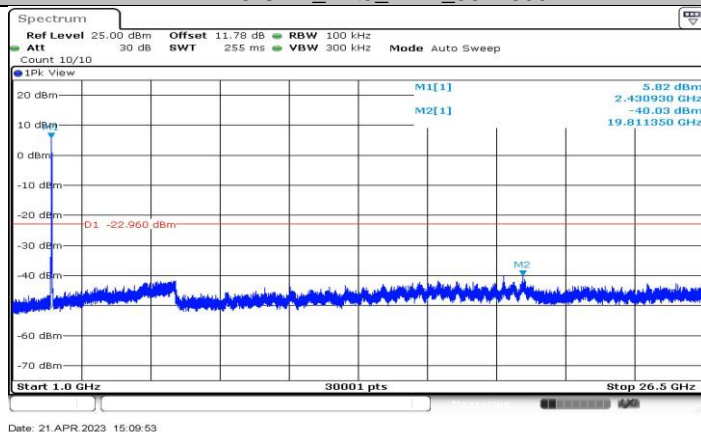
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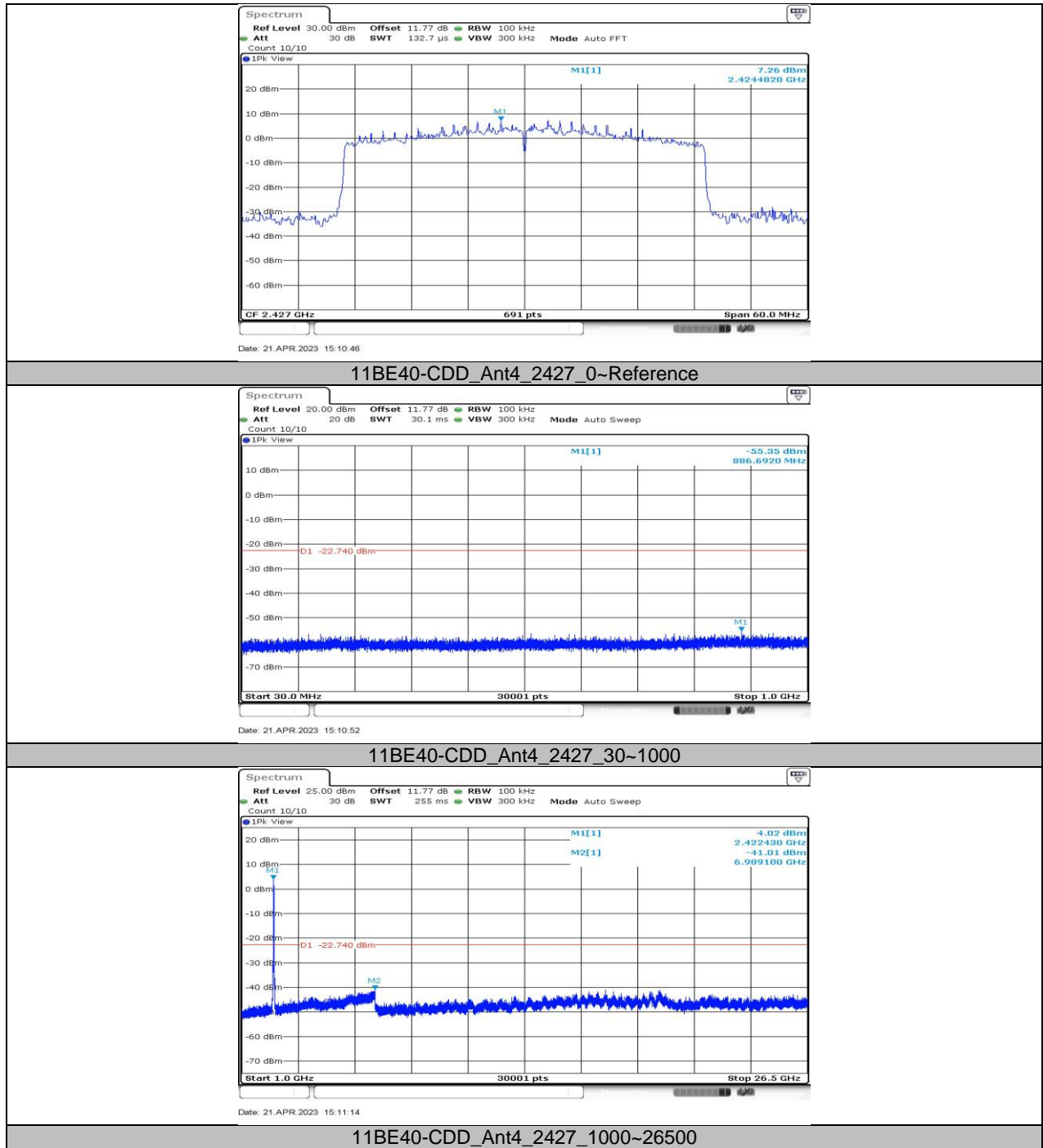
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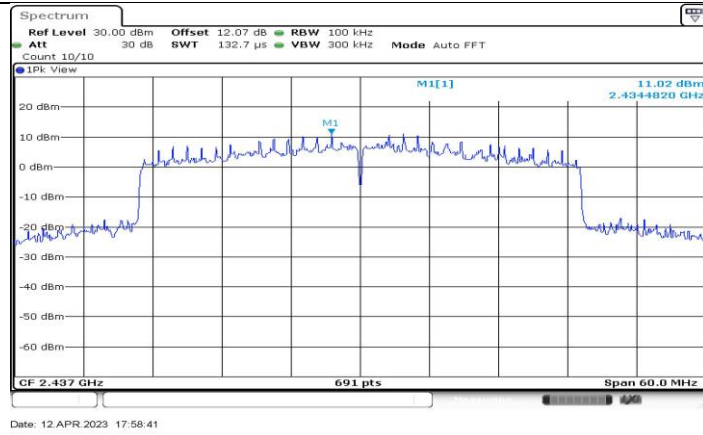


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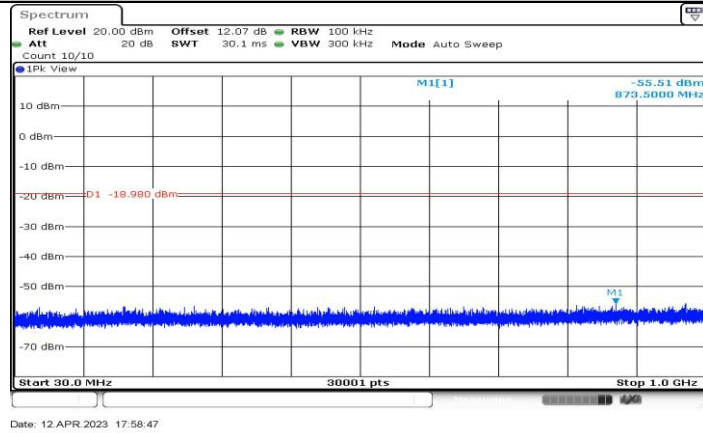


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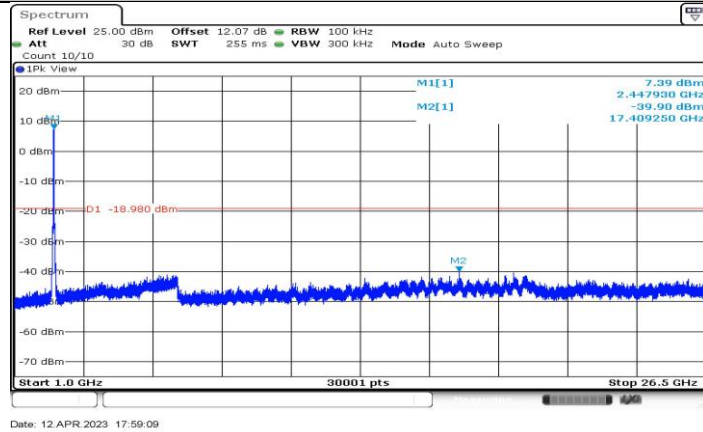




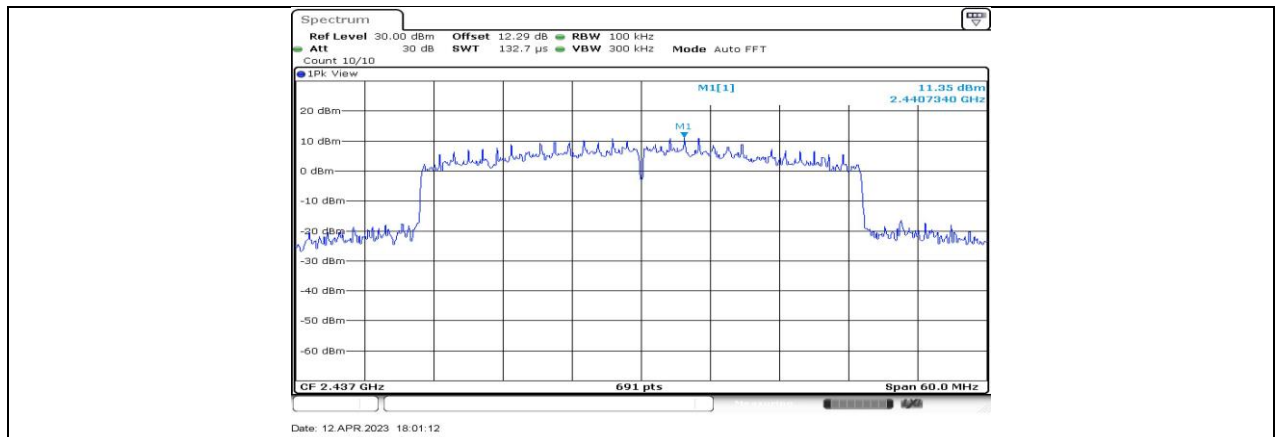
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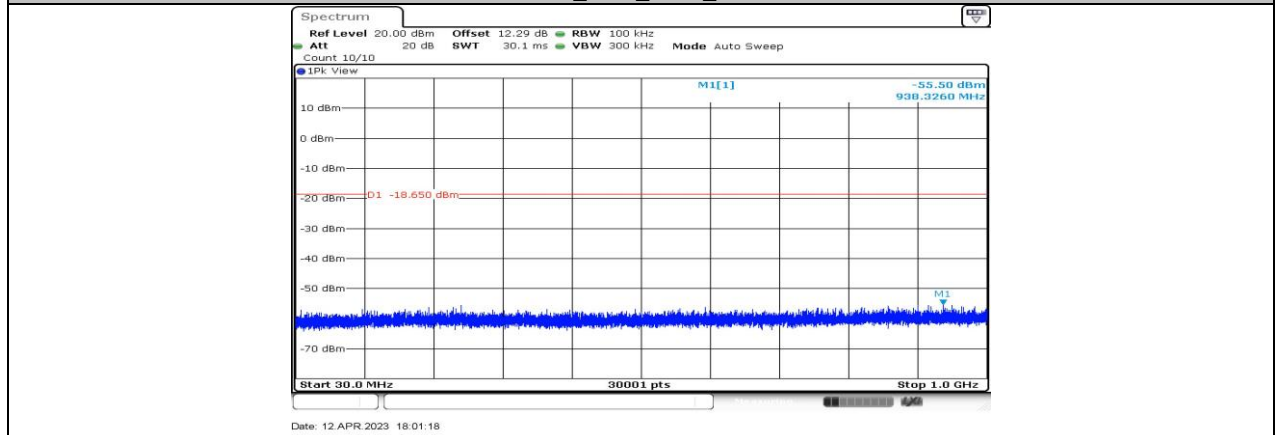
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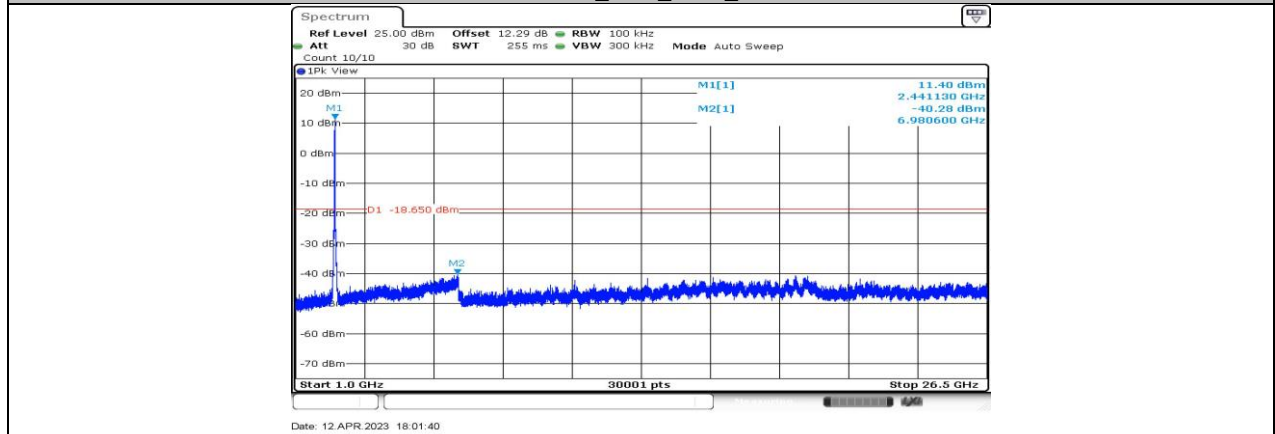
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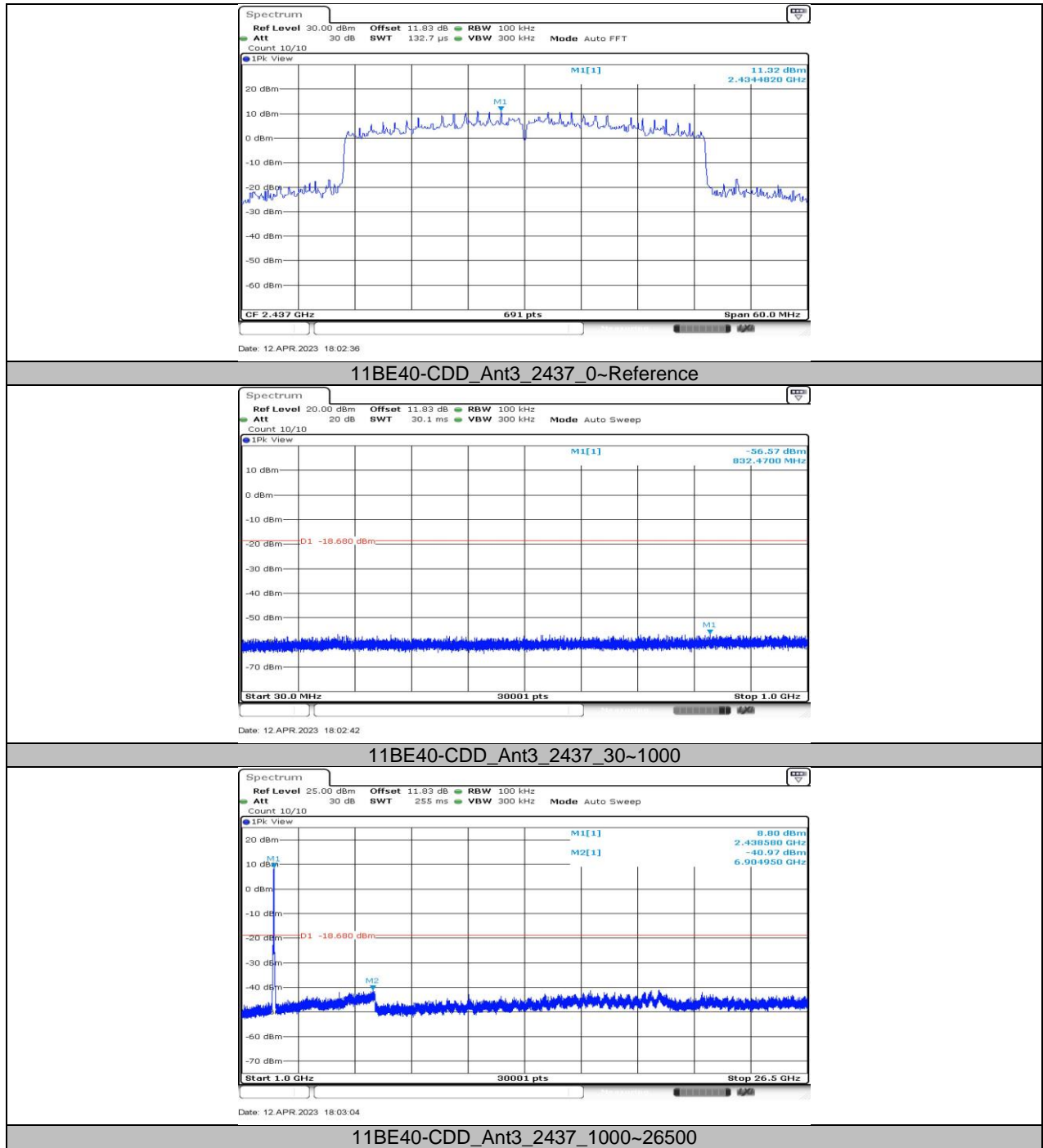
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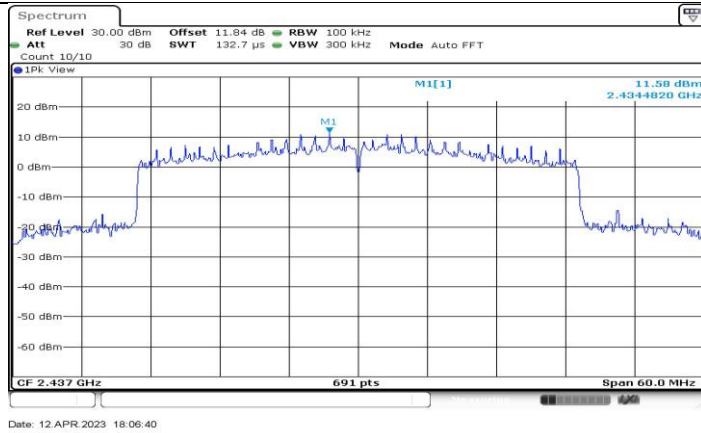


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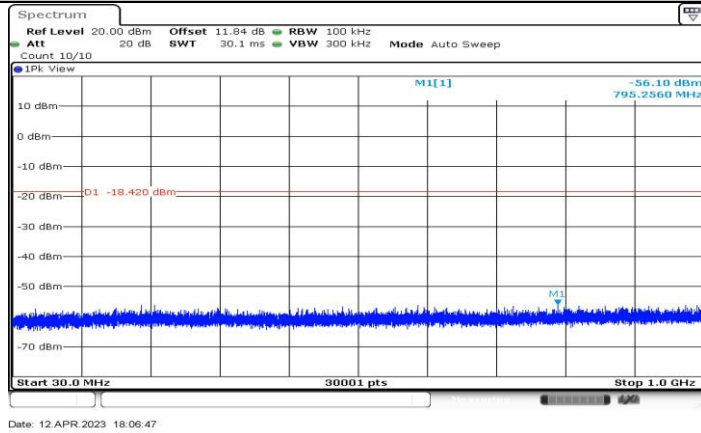


11BE40-CDD_Ant2_2437_1000~26500

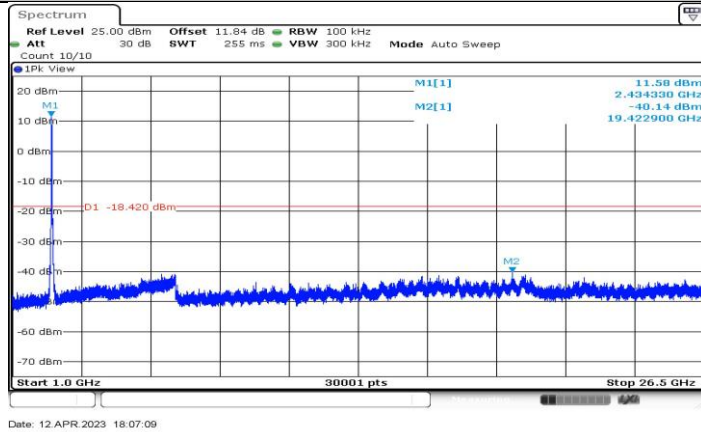




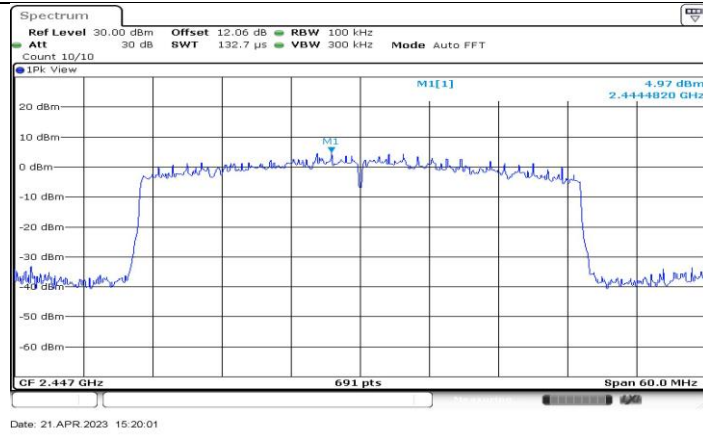
11BE40-CDD_Ant4_2437_0~Reference



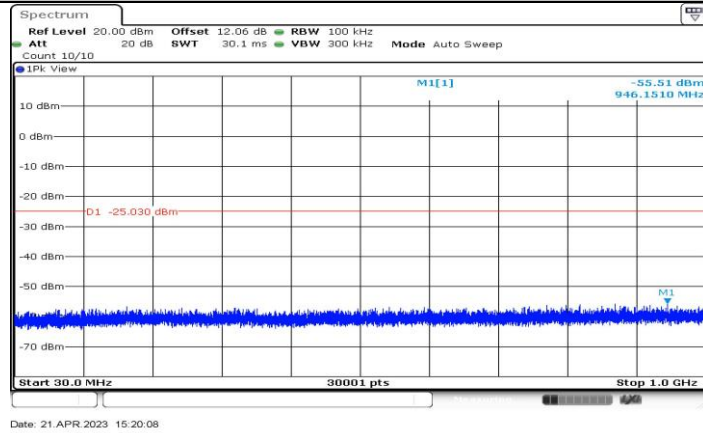
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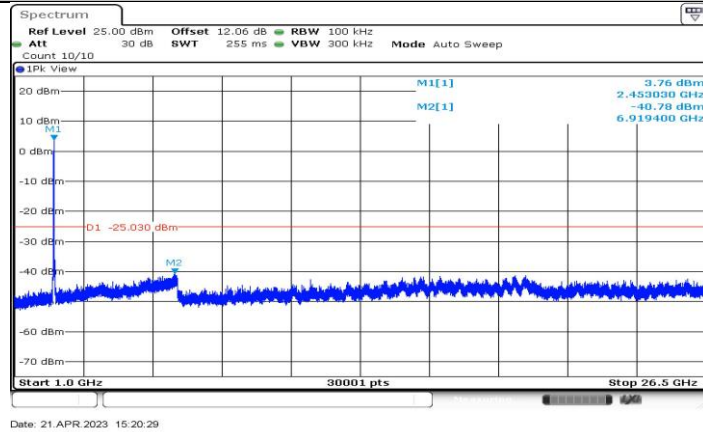
11BE40-CDD_Ant4_2437_1000~26500



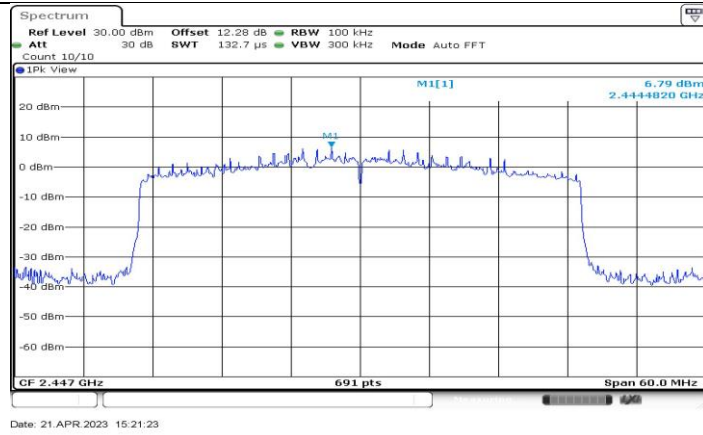
11BE40-CDD_Ant1_2447_0~Reference



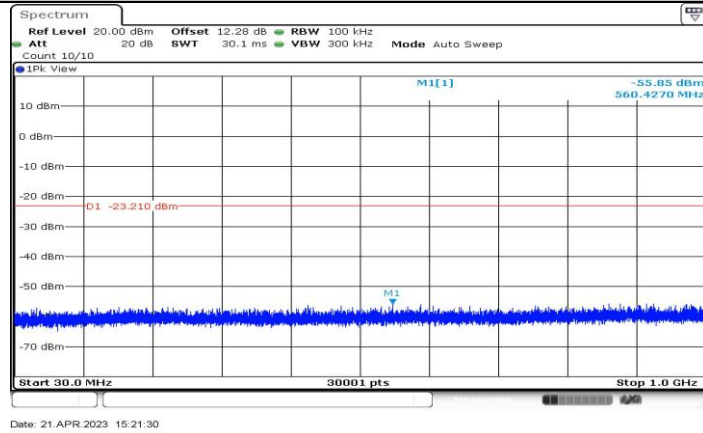
11BE40-CDD_Ant1_2447_30~1000



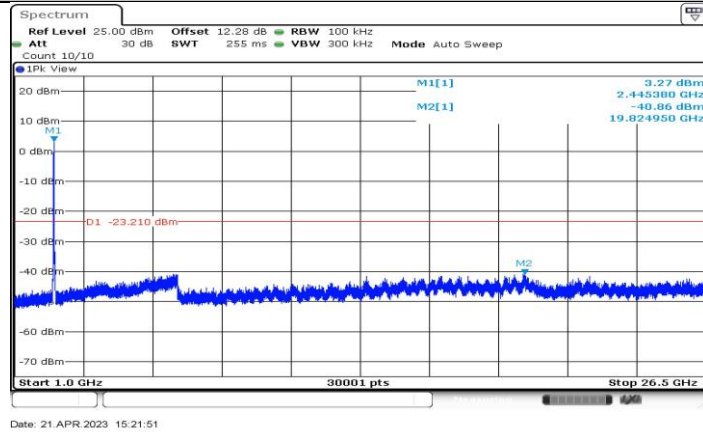
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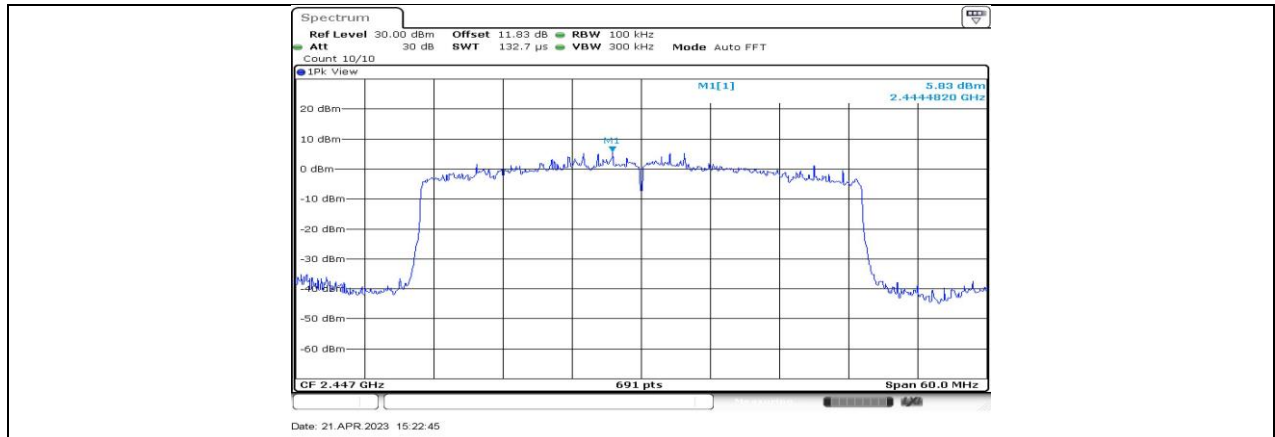
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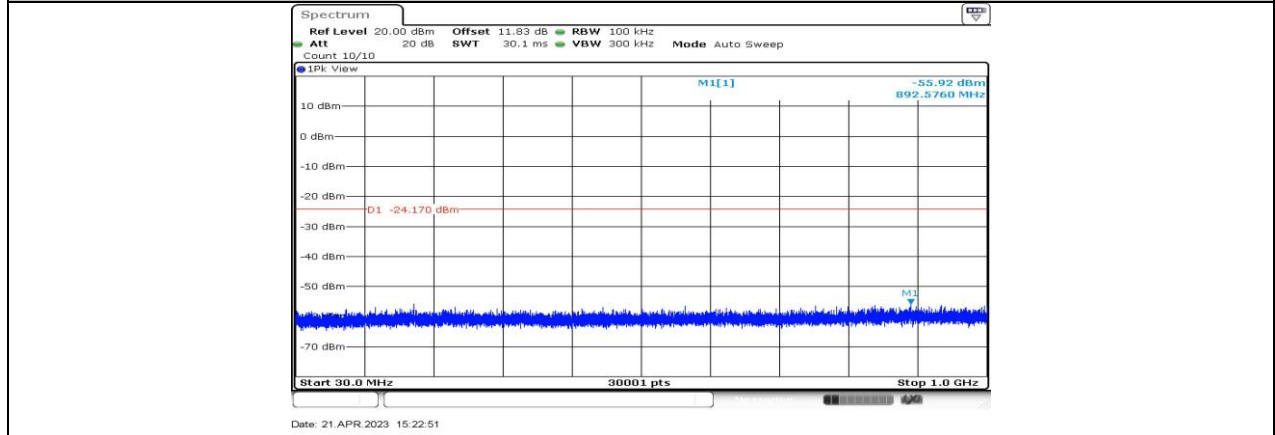
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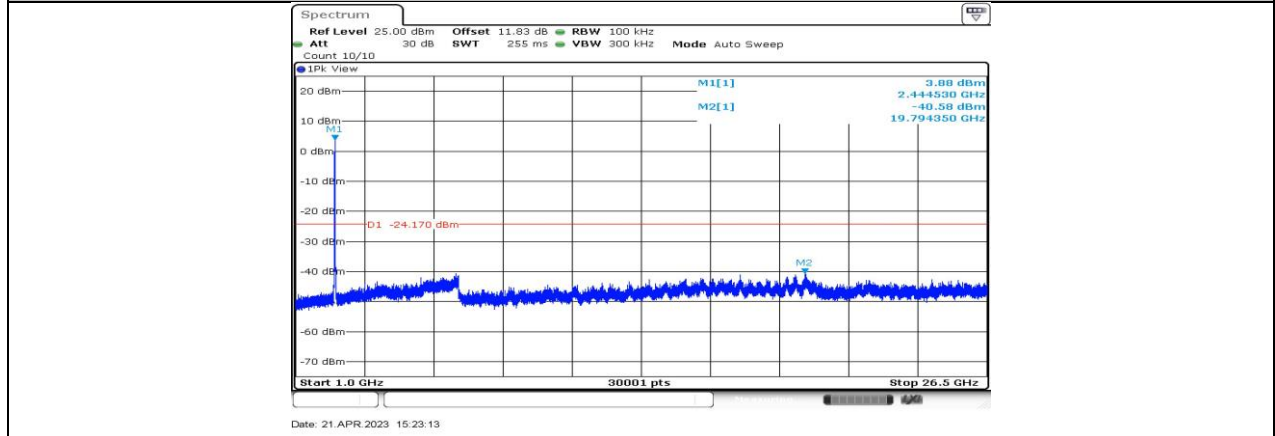
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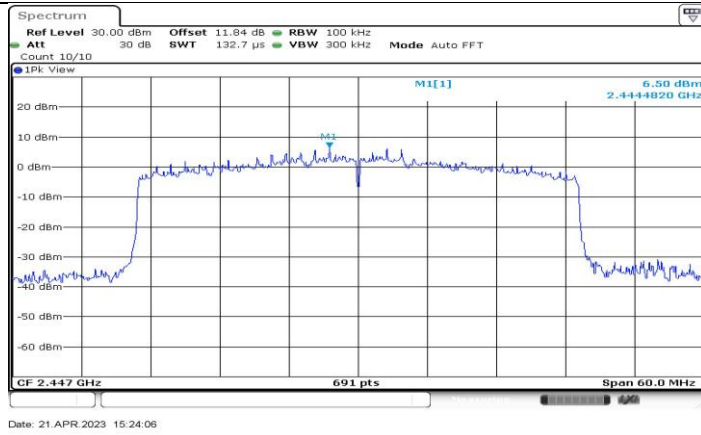
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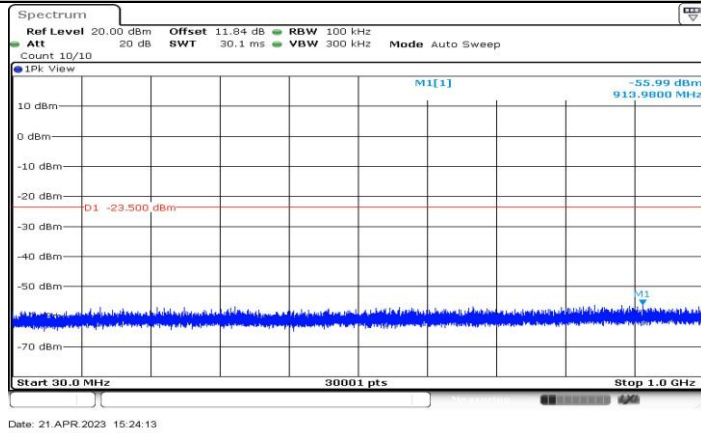
11BE40-CDD_Ant3_2447_30~1000



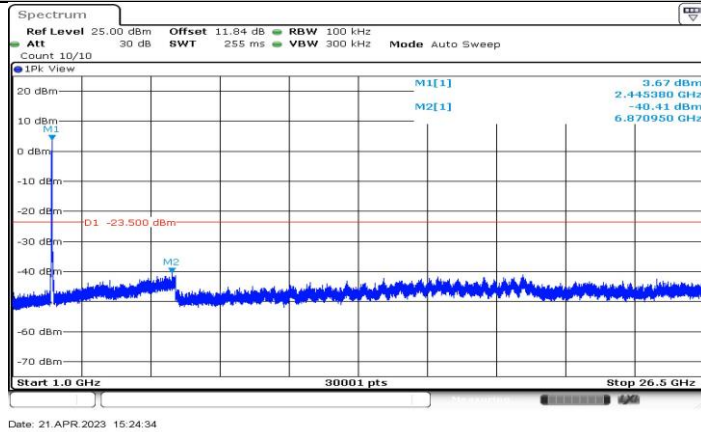
11BE40-CDD_Ant3_2447_1000~26500



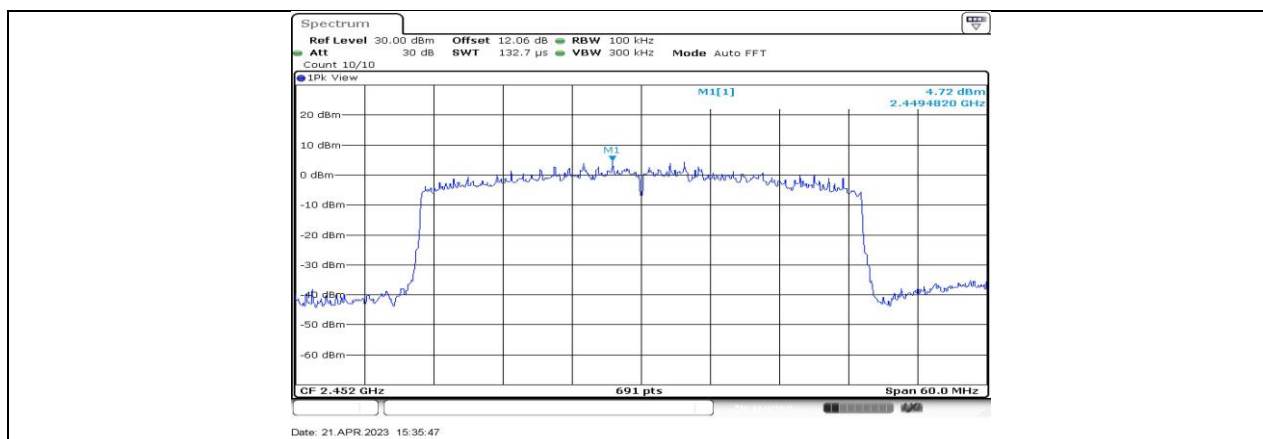
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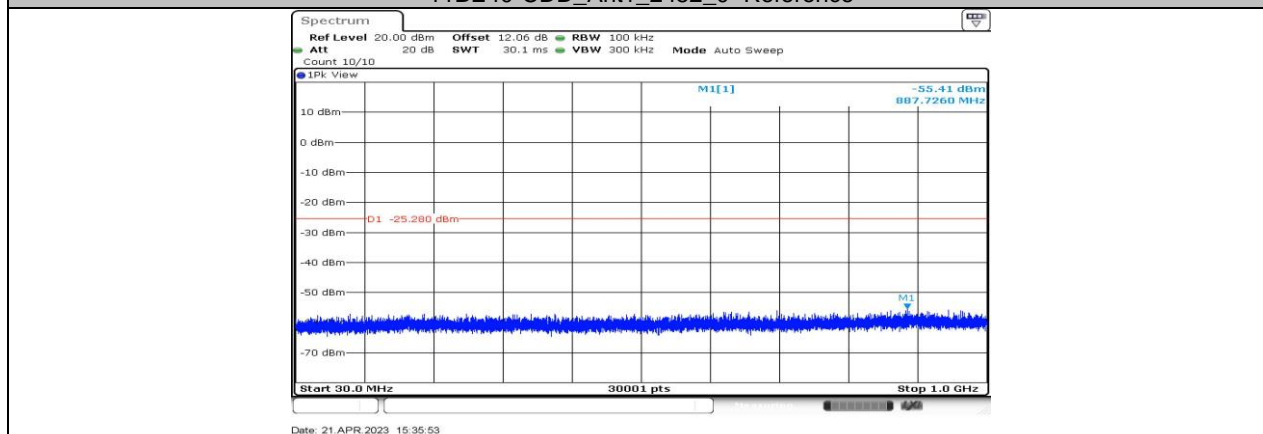
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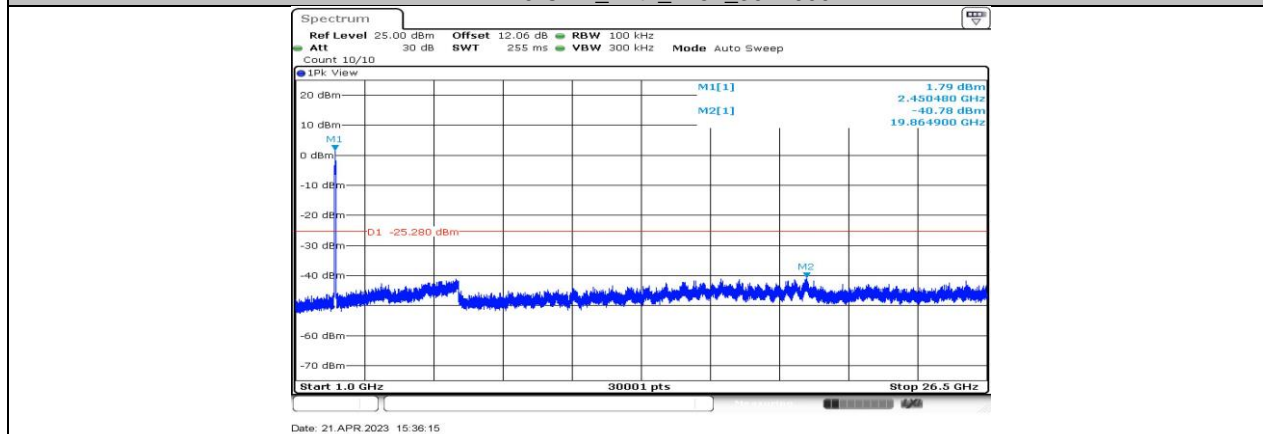
11BE40-CDD_Ant4_2447_1000~26500



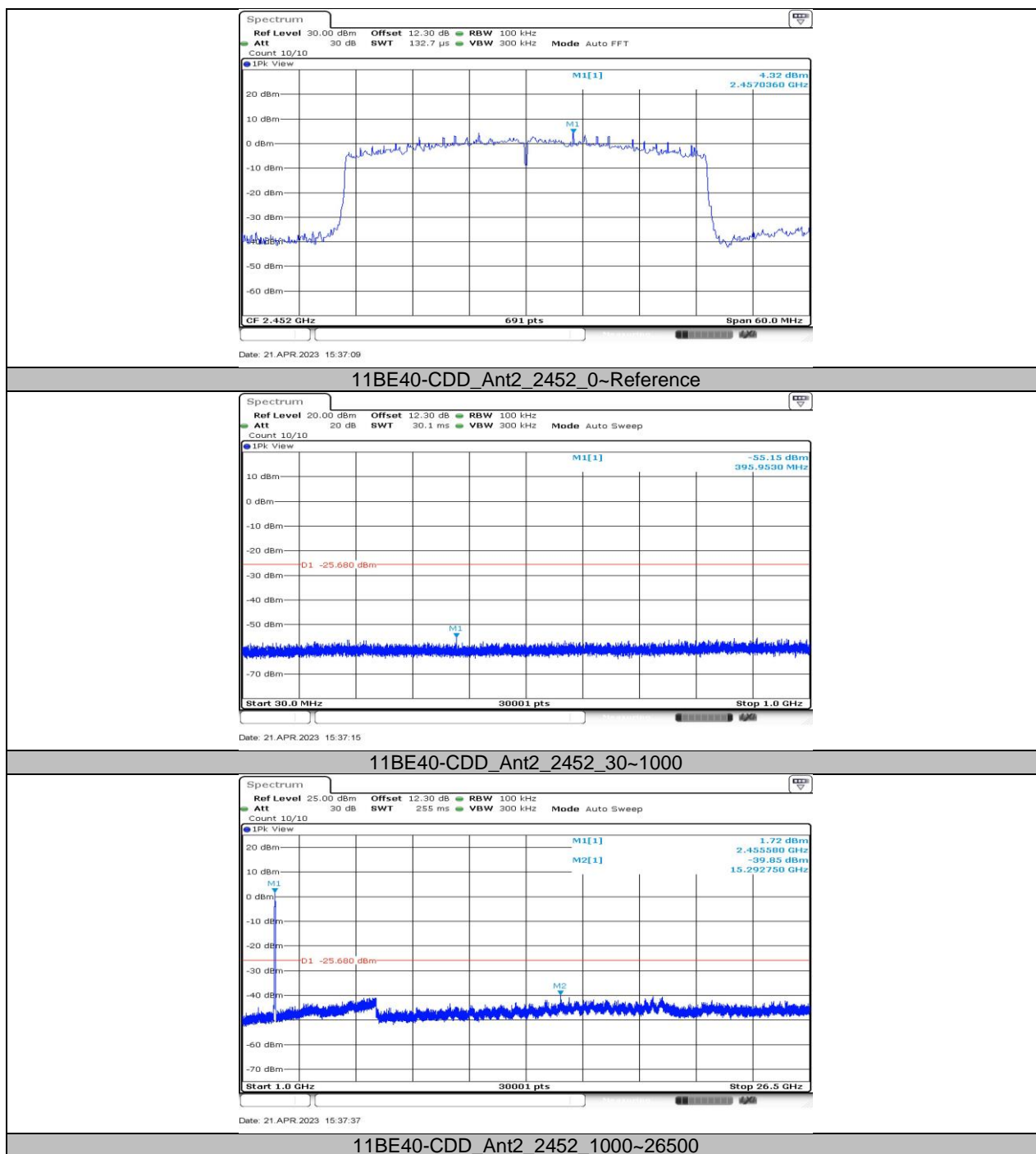
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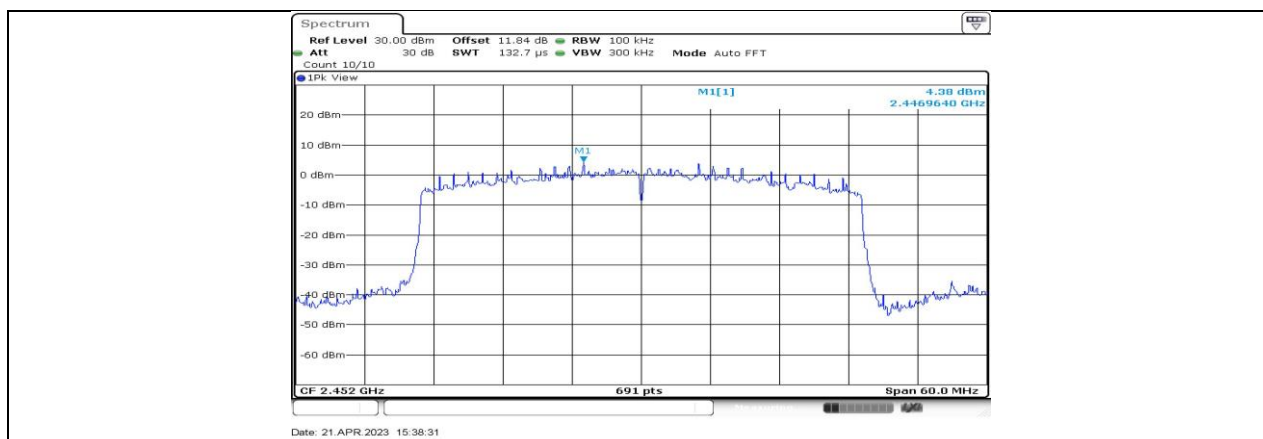


11BE40-CDD_Ant1_2452_30~1000

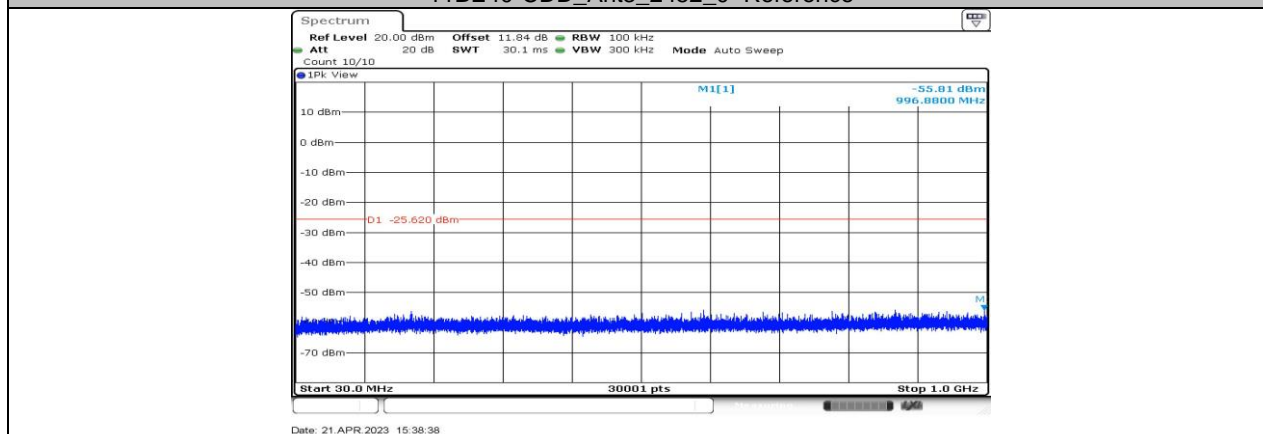


11BE40-CDD_Ant1_2452_1000~26500

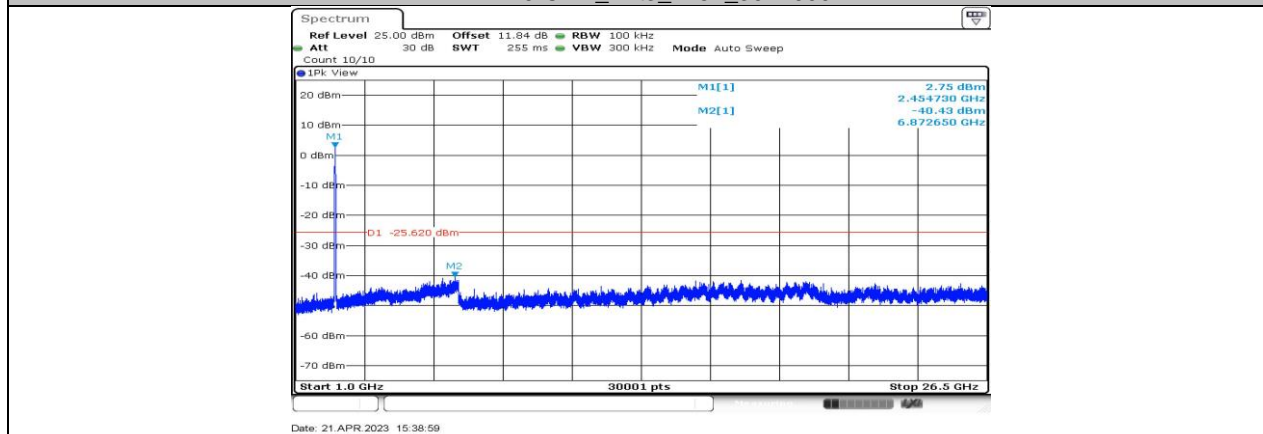




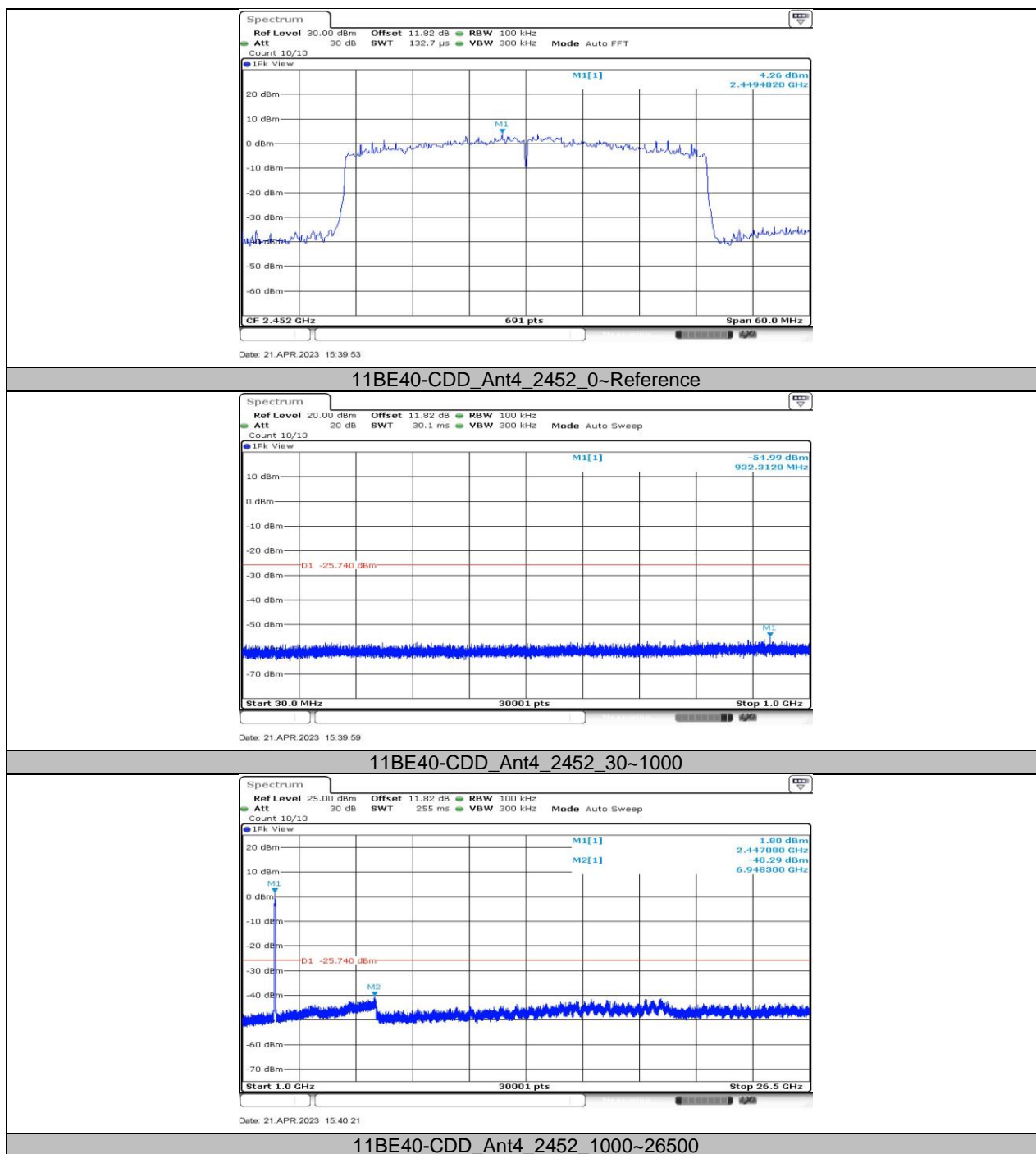
11BE40-CDD_Ant3_2452_0~Reference



11BE40-CDD_Ant3_2452_30~1000



11BE40-CDD_Ant3_2452_1000~26500



11.7. APPENDIX G: DUTY CYCLE

11.7.1. Test Result

Test Mode	On Time (msec)	Period (msec)	Duty Cycle x (Linear)	Duty Cycle (%)	Duty Cycle Correction Factor (dB)	1/T Minimum VBW (kHz)	Final setting For VBW (kHz)
11B-CDD	8.36	8.43	0.9917	99.17	0.04	0.12	0.01
11G-CDD	1.39	1.45	0.9586	95.86	0.18	0.72	1
11AX20-CDD	0.31	0.37	0.8378	83.78	0.77	3.23	4
11AX40-CDD	0.19	0.25	0.7600	76.00	1.19	5.26	6
11BE20-CDD	0.56	0.62	0.9032	90.32	0.44	1.79	2
11BE40-CDD	0.54	0.61	0.8852	88.52	0.53	1.85	2

Note:

Duty Cycle Correction Factor = $10 \log (1/x)$.

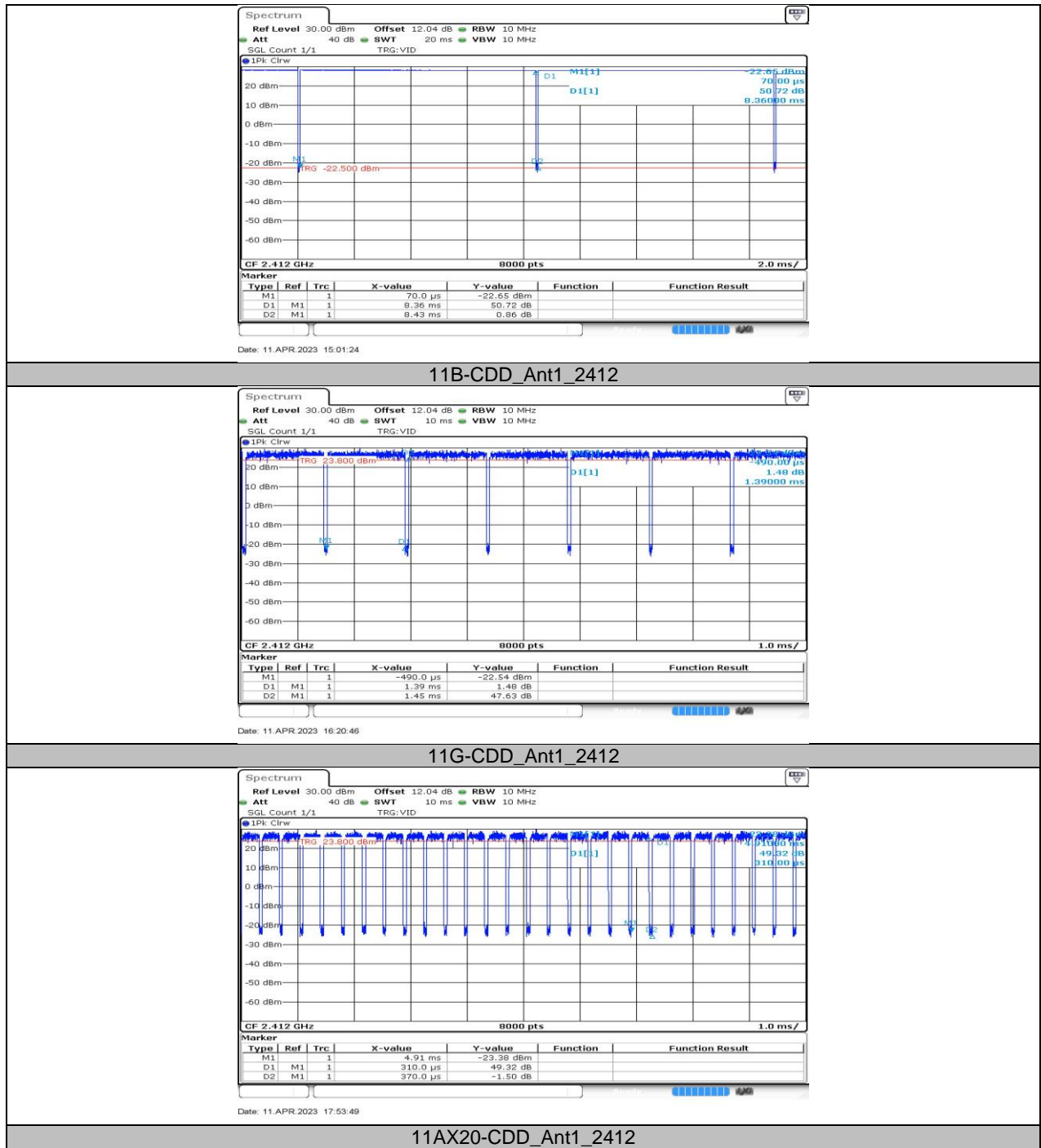
Where: x is Duty Cycle (Linear)

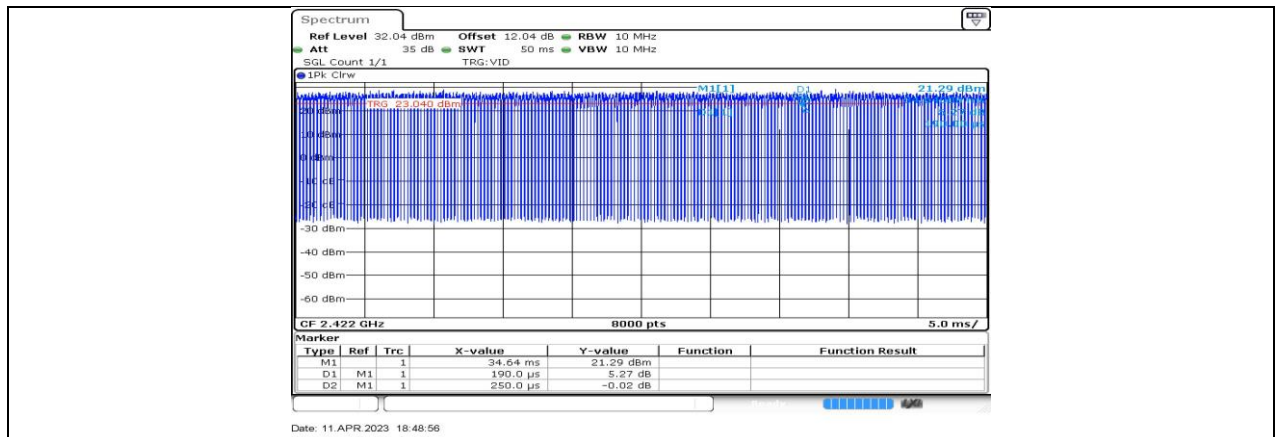
Where: T is On Time

If that calculated VBW is not available on the analyzer then the next higher value should be used.

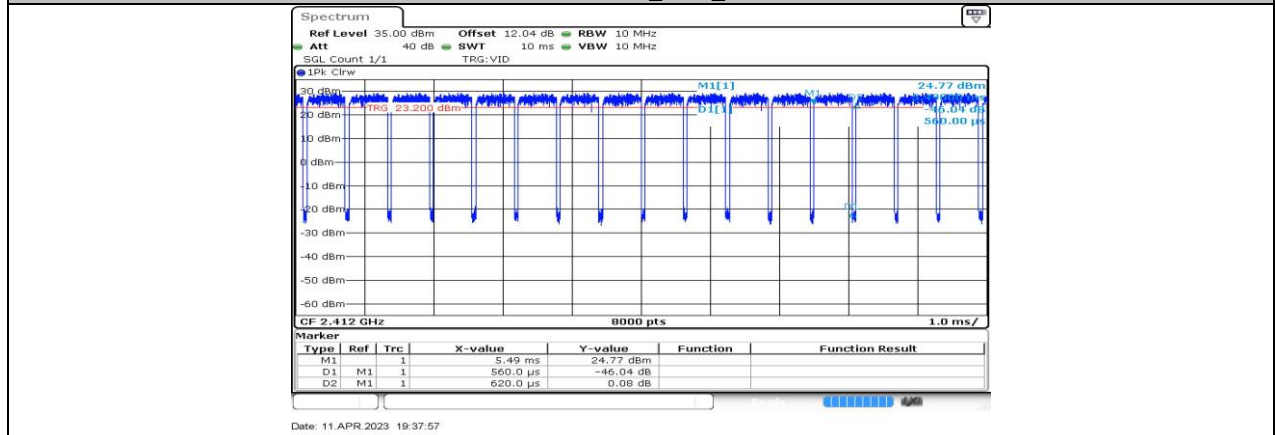
If the EUT is configured to transmit with duty cycle $\geq 98\%$, set VBW \leq RBW/100 (i.e., 10 kHz) but not less than 10 Hz.

11.7.2. Test Graphs

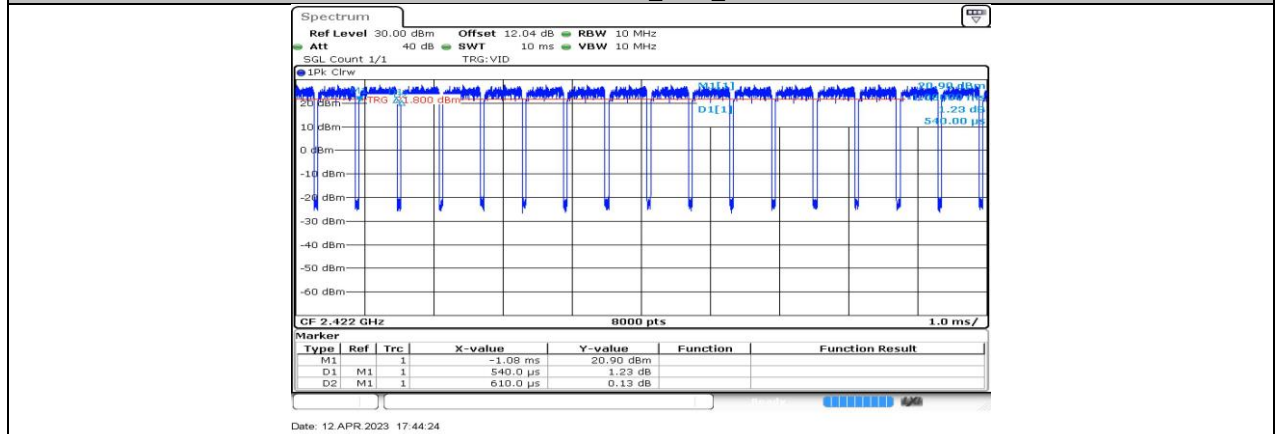




11AX40-CDD_Ant1_2422



11BE20-CDD_Ant1_2412



11BE40-CDD_Ant1_2422

END OF REPORT