

Appendix D.4: Maximum conducted output power

Test Result Channel Power

Test Mode	Antenna	Frequency[MHz]	Channel Power [dBm]	Duty Cycle [%]	DC Factor [dBm]	Result [dBm]	Limit [dBm]	Verdict
11A	Ant0	5180	10.25	68.81	1.62	11.87	≤23.98	PASS
		5200	11.29	68.63	1.63	12.92	≤23.98	PASS
		5240	11.87	68.81	1.62	13.49	≤23.98	PASS
11N20SISO	Ant0	5180	9.30	67.36	1.72	11.02	≤23.98	PASS
		5200	9.26	67.36	1.72	10.98	≤23.98	PASS
		5240	10.03	67.36	1.72	11.75	≤23.98	PASS
11N40SISO	Ant0	5190	6.32	50.78	2.94	9.26	≤23.98	PASS
		5230	7.88	49.62	3.04	10.92	≤23.98	PASS
11AC20SISO	Ant0	5180	9.18	66.84	1.75	10.93	≤23.98	PASS
		5200	9.36	66.84	1.75	11.11	≤23.98	PASS
		5240	10.17	66.84	1.75	11.92	≤23.98	PASS
11AC40SISO	Ant0	5190	6.85	51.16	2.91	9.76	≤23.98	PASS
		5230	7.38	50.78	2.94	10.32	≤23.98	PASS
11AC80SISO	Ant0	5210	6.07	33.68	4.73	10.80	≤23.98	PASS
11AX20SISO	Ant0	5180	8.04	66.50	1.77	9.81	≤23.98	PASS
		5200	7.48	66.84	1.75	9.23	≤23.98	PASS
		5240	8.27	66.84	1.75	10.02	≤23.98	PASS
11AX40SISO	Ant0	5190	6.56	51.16	2.91	9.47	≤23.98	PASS
		5230	5.45	51.16	2.91	8.36	≤23.98	PASS
11AX80SISO	Ant0	5210	4.55	33.68	4.73	9.28	≤23.98	PASS

Note: The Duty Cycle Factor is compensated in the graph.

Appendix D.5: Maximum power spectral density

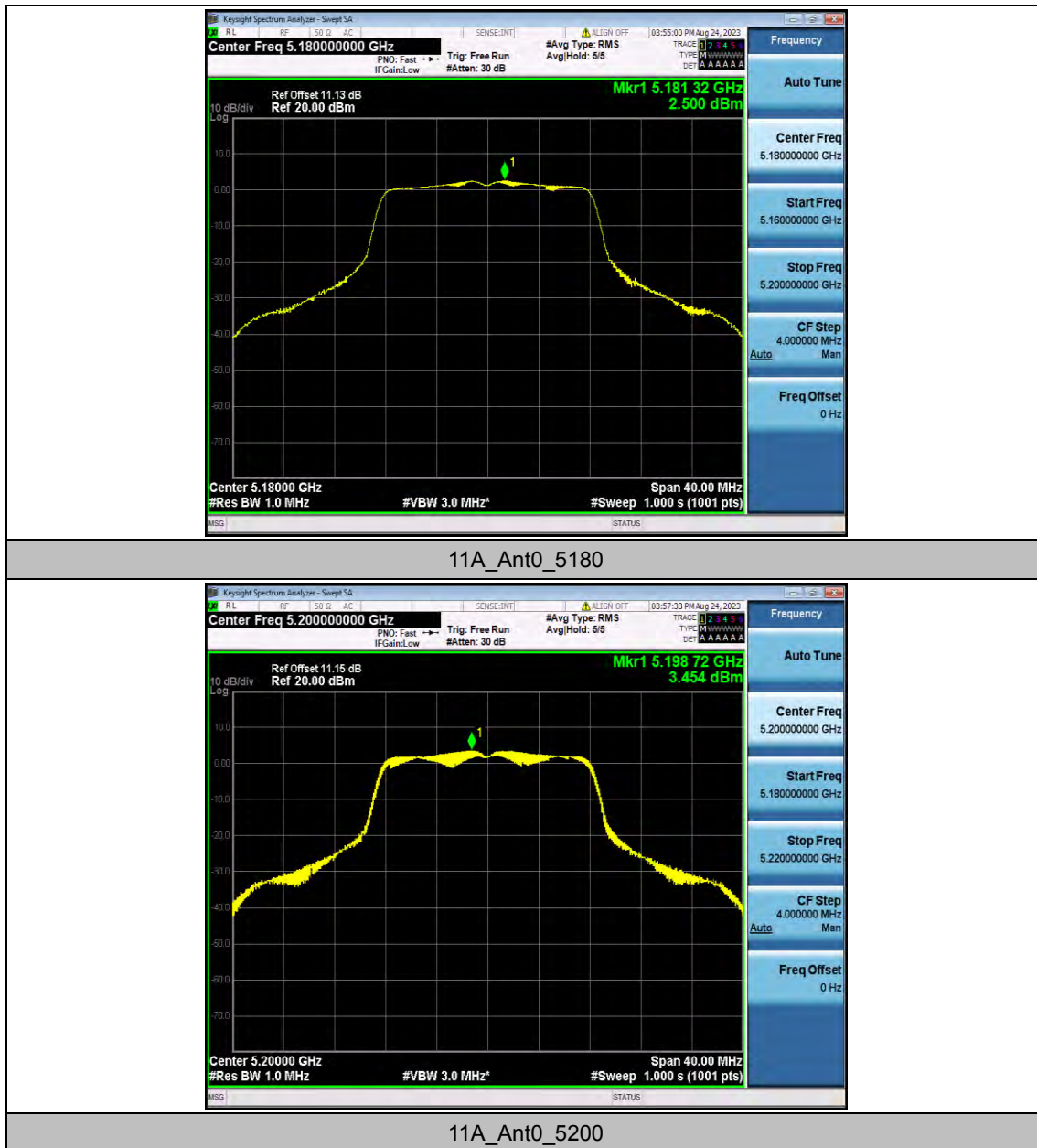
Test Result

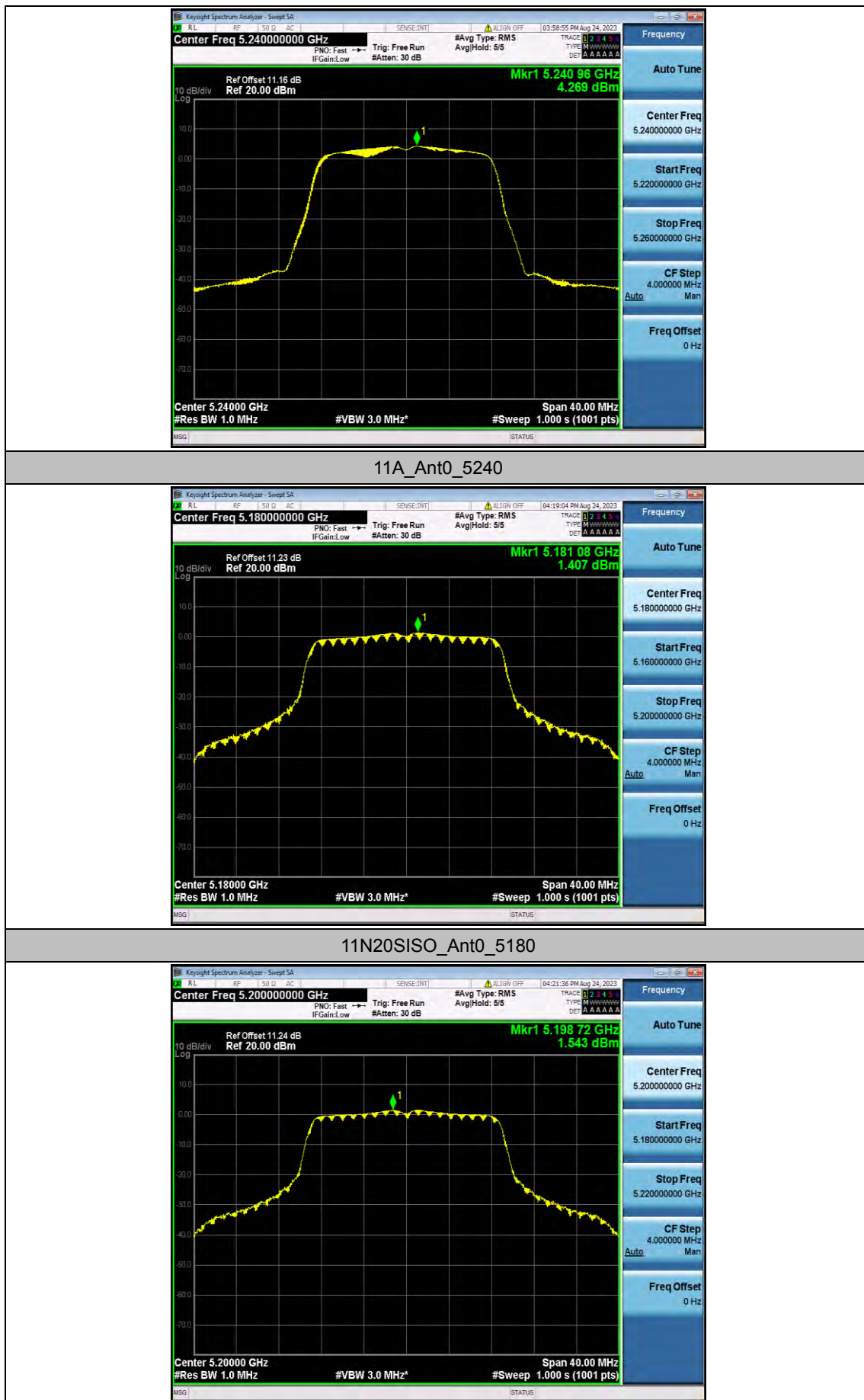
Test Mode	Antenna	Frequency[MHz]	Result [dBm/MHz]	Limit[dBm/MHz]	Verdict
11A	Ant0	5180	2.5	≤11.00	PASS
		5200	3.45	≤11.00	PASS
		5240	4.27	≤11.00	PASS
11N20SISO	Ant0	5180	1.41	≤11.00	PASS
		5200	1.54	≤11.00	PASS
		5240	2.41	≤11.00	PASS
11N40SISO	Ant0	5190	-2.64	≤11.00	PASS
		5230	-0.7	≤11.00	PASS
11AC20SISO	Ant0	5180	1.38	≤11.00	PASS
		5200	1.5	≤11.00	PASS
		5240	2.52	≤11.00	PASS
11AC40SISO	Ant0	5190	-2.4	≤11.00	PASS
		5230	-0.83	≤11.00	PASS
11AC80SISO	Ant0	5210	-5.73	≤11.00	PASS
11AX20SISO	Ant0	5180	-3.68	≤11.00	PASS
		5200	-0.36	≤11.00	PASS
		5240	0.6	≤11.00	PASS
11AX40SISO	Ant0	5190	-3.83	≤11.00	PASS
		5230	-3.44	≤11.00	PASS
11AX80SISO	Ant0	5210	-7.8	≤11.00	PASS

Note: 1.The Result and Limit Unit is dBm/500 kHz in the band 5.725–5.85 GHz.

2.The Duty Cycle Factor and RBW Factor is compensated in the graph.

Test Graphs





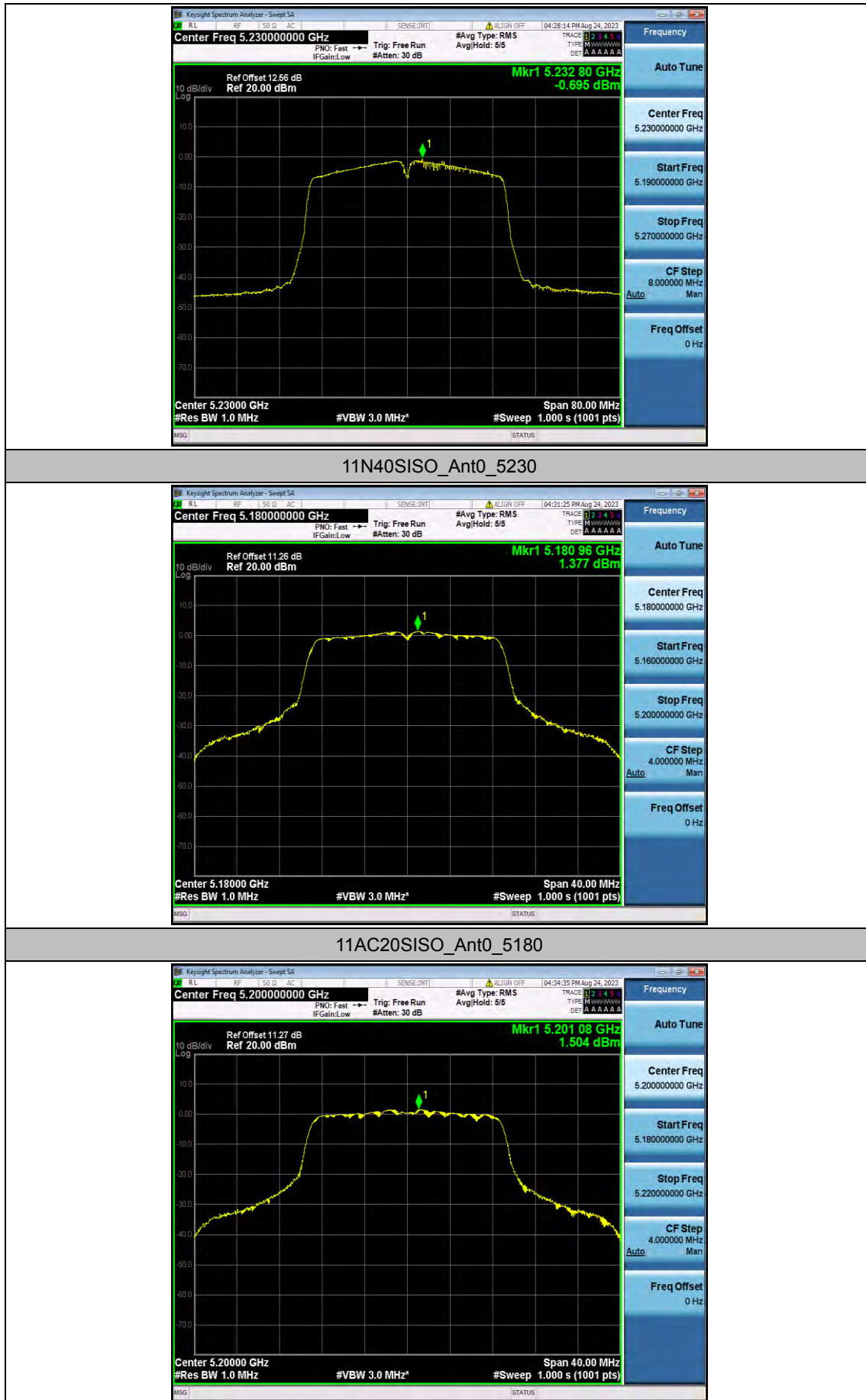
11N20SISO_Ant0_5200



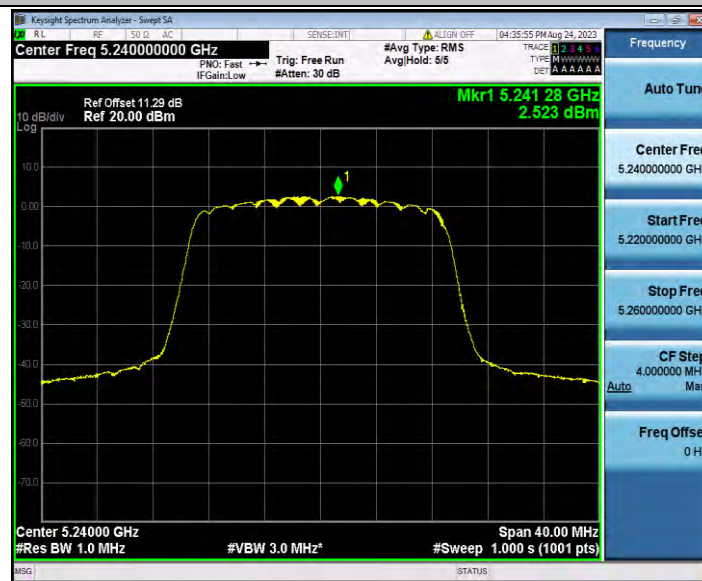
11N20SISO_Ant0_5240



11N40SISO_Ant0_5190



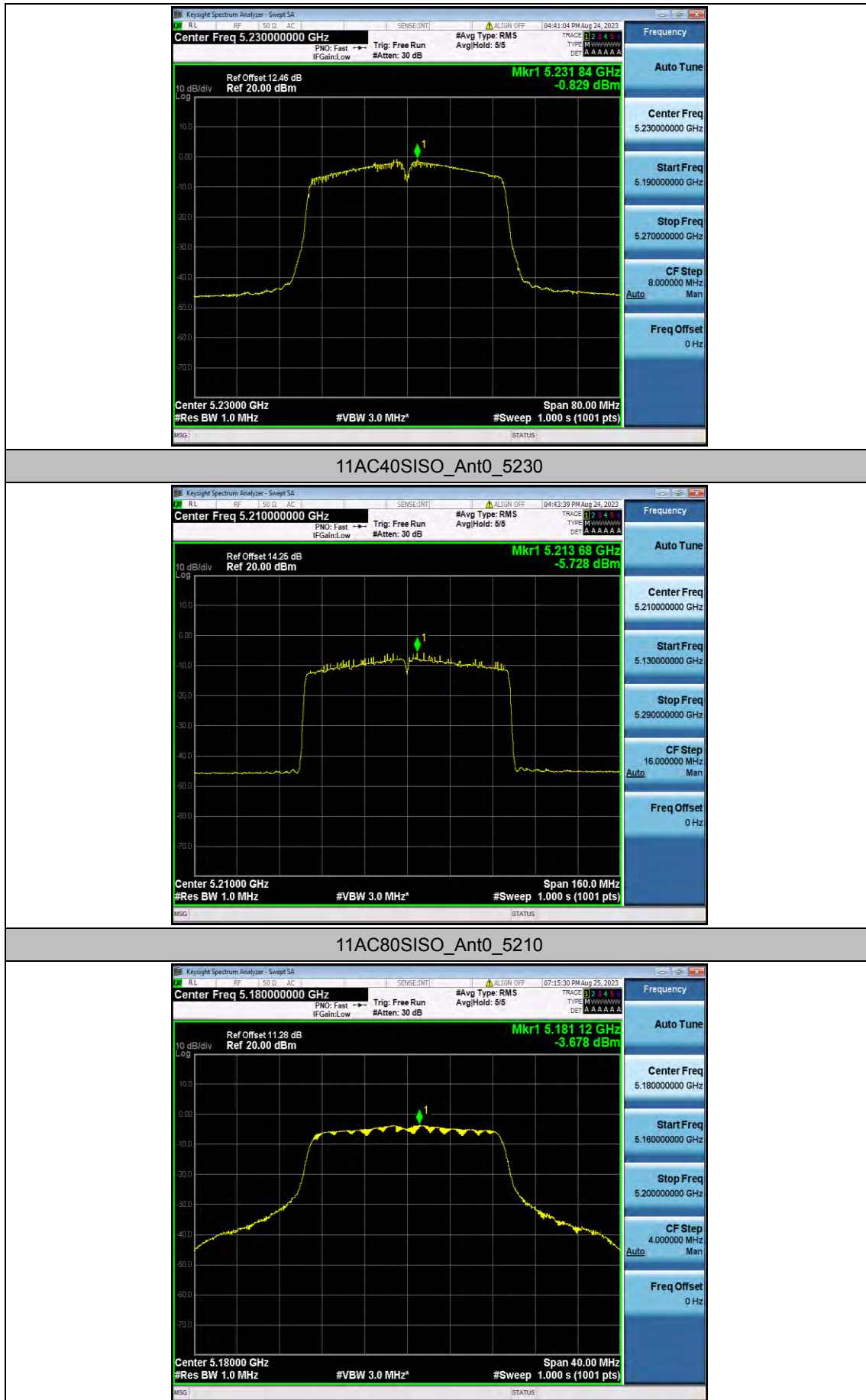
11AC20SISO_Ant0_5200



11AC20SISO_Ant0_5240



11AC40SISO_Ant0_5190



11AX20SISO_Ant0_5180



11AX20SISO_Ant0_5200



11AX20SISO_Ant0_5240



11AX40SISO_Ant0_5190



11AX40SISO_Ant0_5230



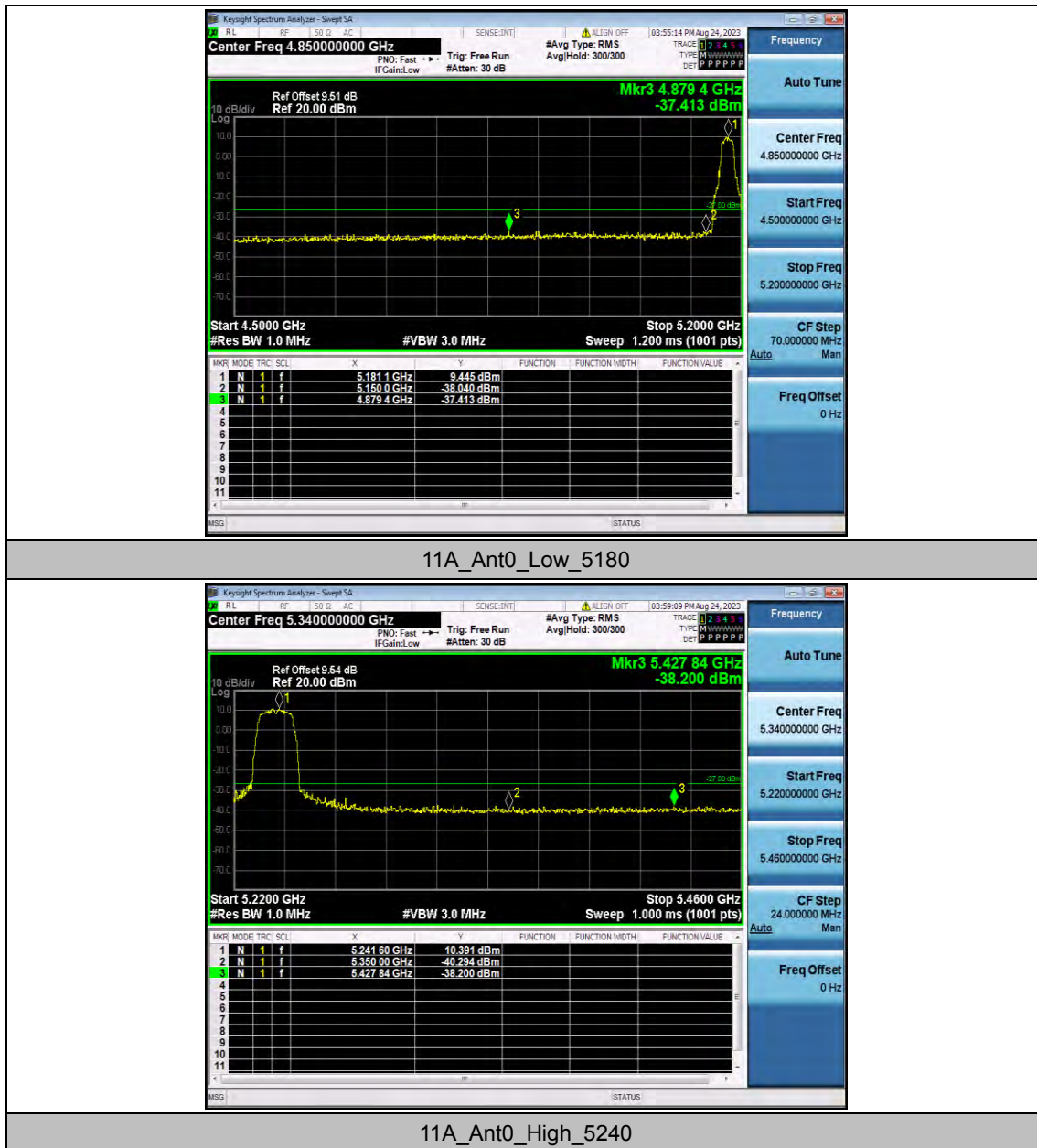
11AX80SISO_Ant0_5210

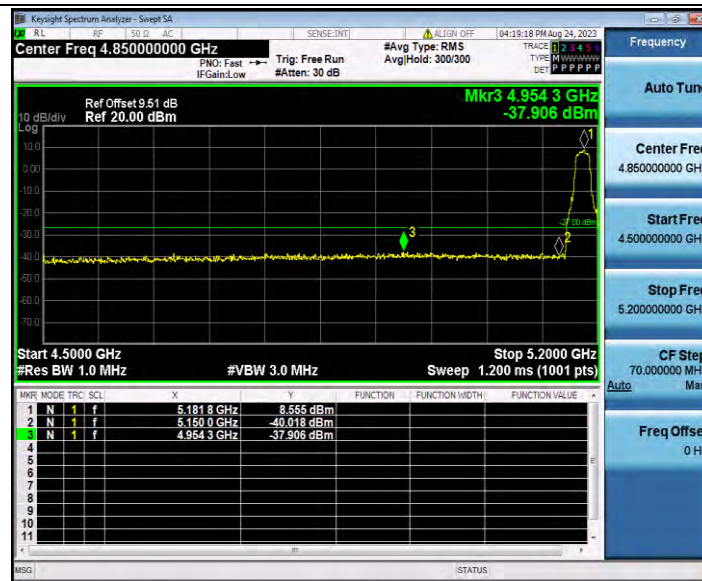
Appendix D.6: Band edge measurements

Test Result B1

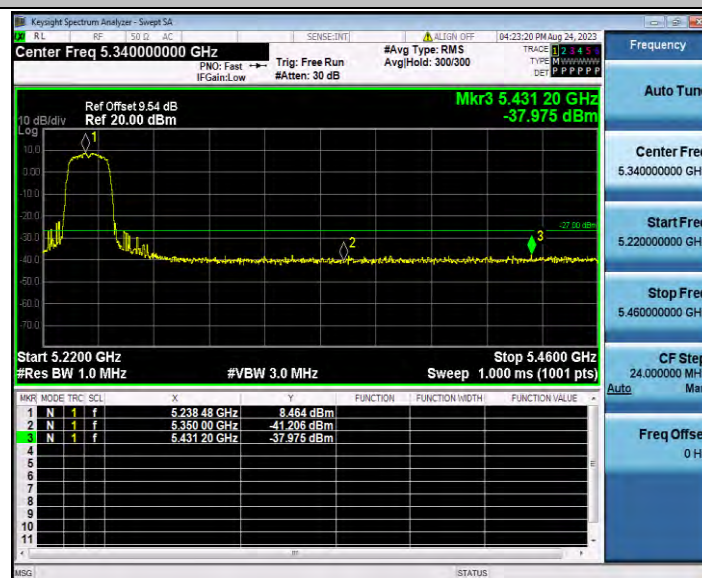
Test Mode	Antenna	ChName	Frequency[MHz]	Result[dBm]	Limit[dBm]	Verdict
11A	Ant0	Low	5180	-37.41	≤ -27	PASS
		High	5240	-38.2	≤ -27	PASS
11N20SISO	Ant0	Low	5180	-37.91	≤ -27	PASS
		High	5240	-37.98	≤ -27	PASS
11N40SISO	Ant0	Low	5190	-37.83	≤ -27	PASS
		High	5230	-37.31	≤ -27	PASS
11AC20SISO	Ant0	Low	5180	-37.53	≤ -27	PASS
		High	5240	-36.95	≤ -27	PASS
11AC40SISO	Ant0	Low	5190	-37.9	≤ -27	PASS
		High	5230	-37.75	≤ -27	PASS
11AC80SISO	Ant0	Low	5210	-37.54	≤ -27	PASS
		High	5210	-38.29	≤ -27	PASS
11AX20SISO	Ant0	Low	5180	-37.4	≤ -27	PASS
		High	5240	-37.32	≤ -27	PASS
11AX40SISO	Ant0	Low	5190	-37.59	≤ -27	PASS
		High	5230	-37.72	≤ -27	PASS
11AX80SISO	Ant0	Low	5210	-37.95	≤ -27	PASS
		High	5210	-38.11	≤ -27	PASS

Test Graphs B1

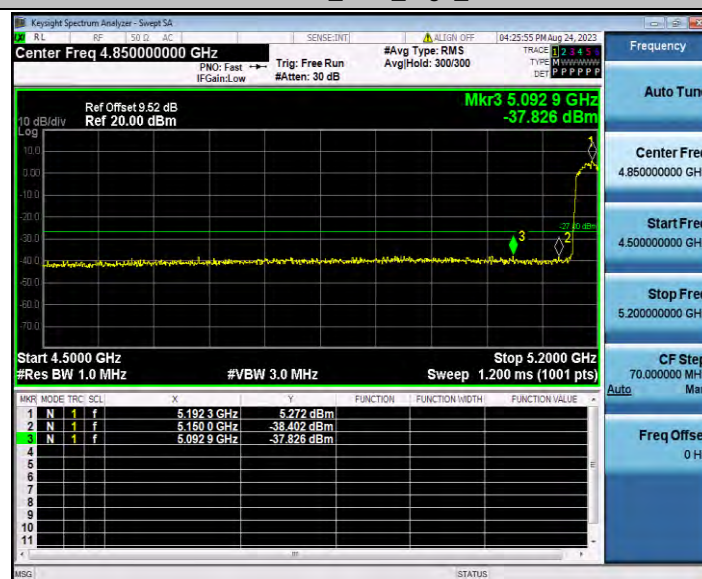




11N20SISO_Ant0_Low_5180



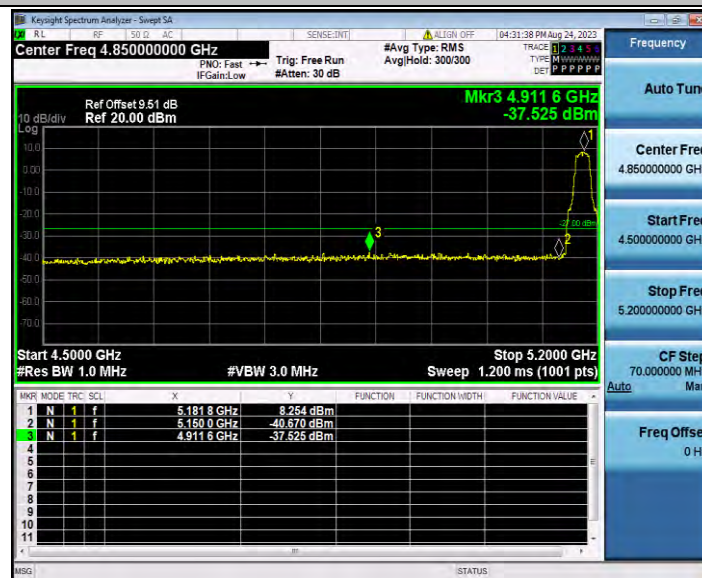
11N20SISO_Ant0_High_5240



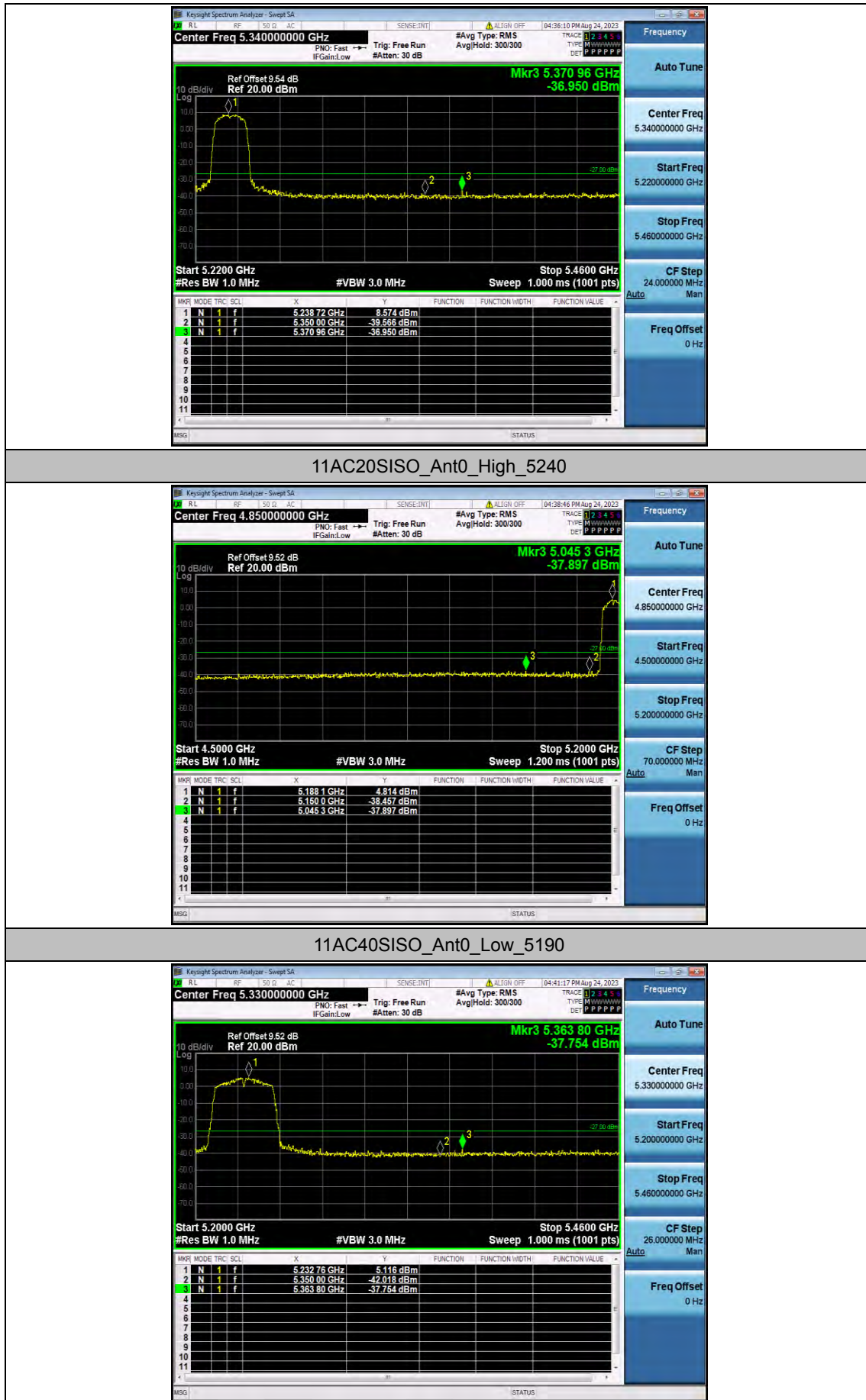
11N40SISO_Ant0_Low_5190



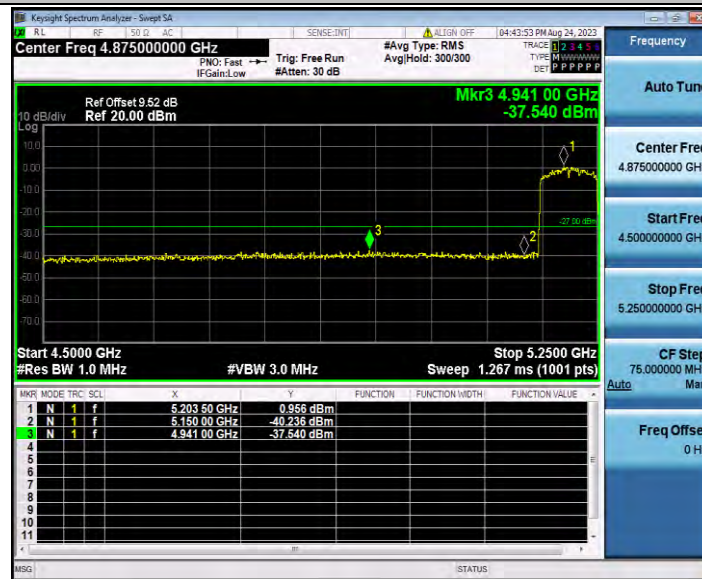
11N40SISO_Ant0_High_5230



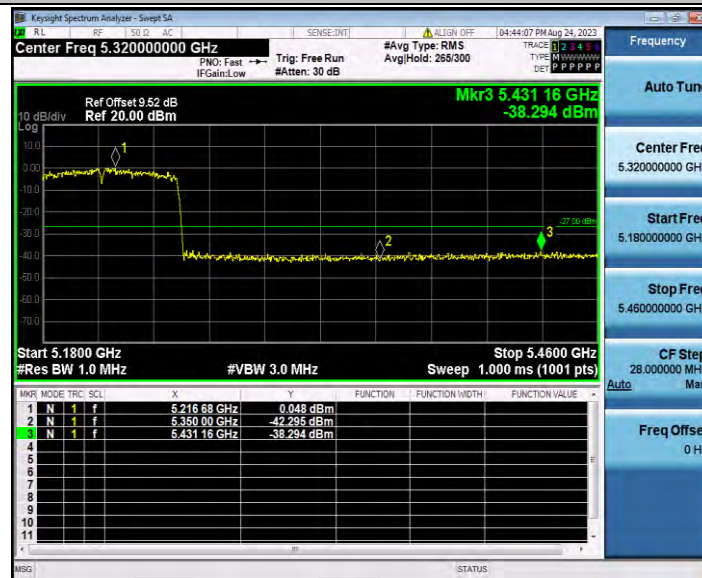
11AC20SISO_Ant0_Low_5180



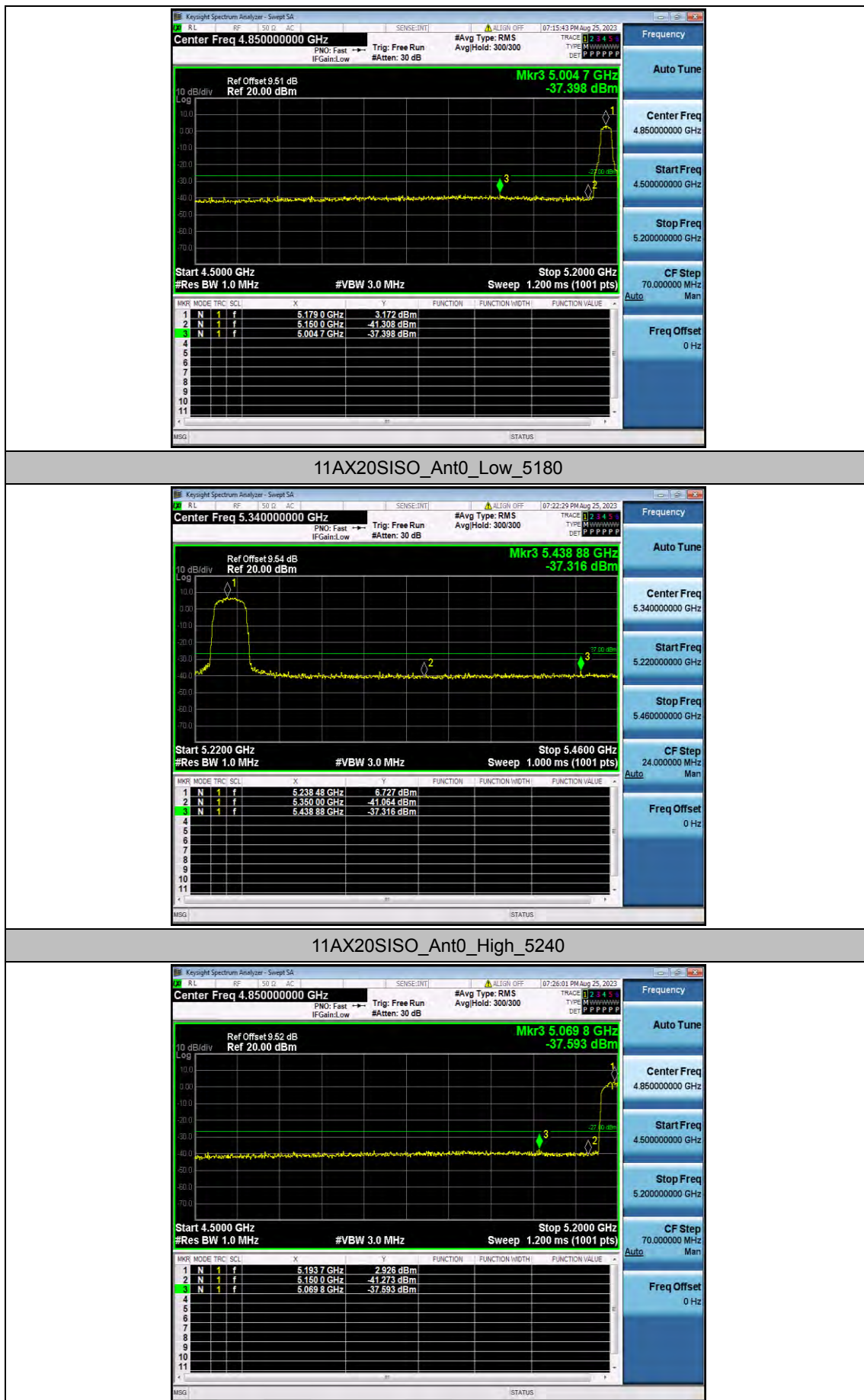
11AC40SISO_Ant0_High_5230



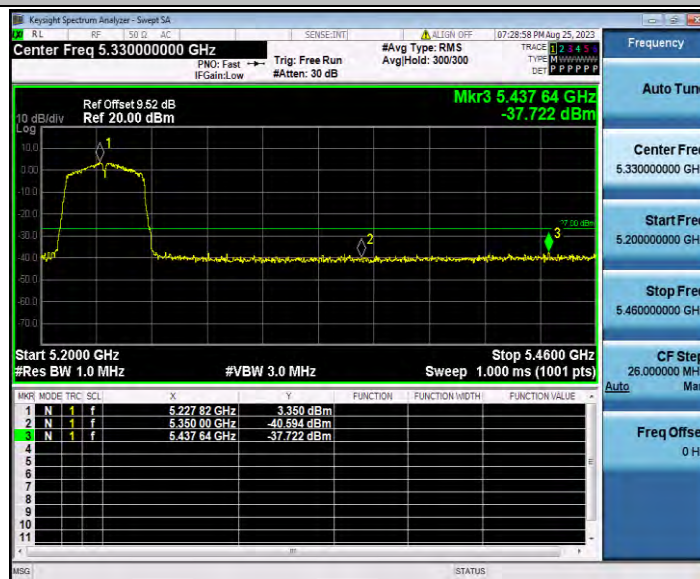
11AC80SISO_Ant0_Low_5210



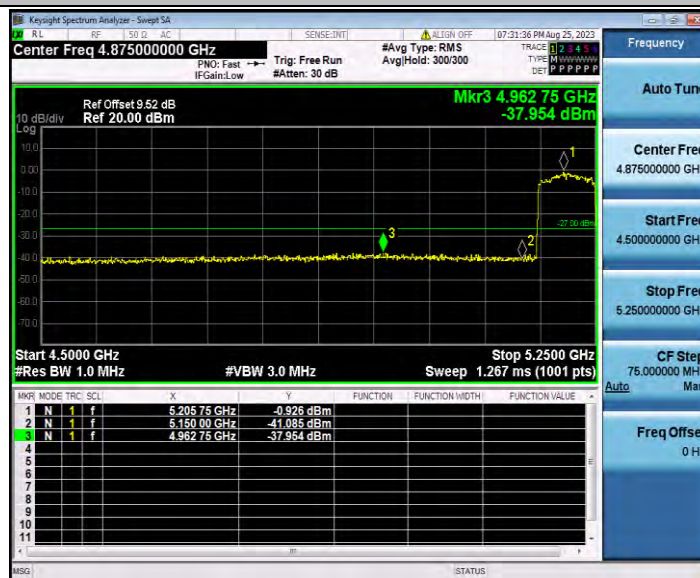
11AC80SISO_Ant0_High_5210



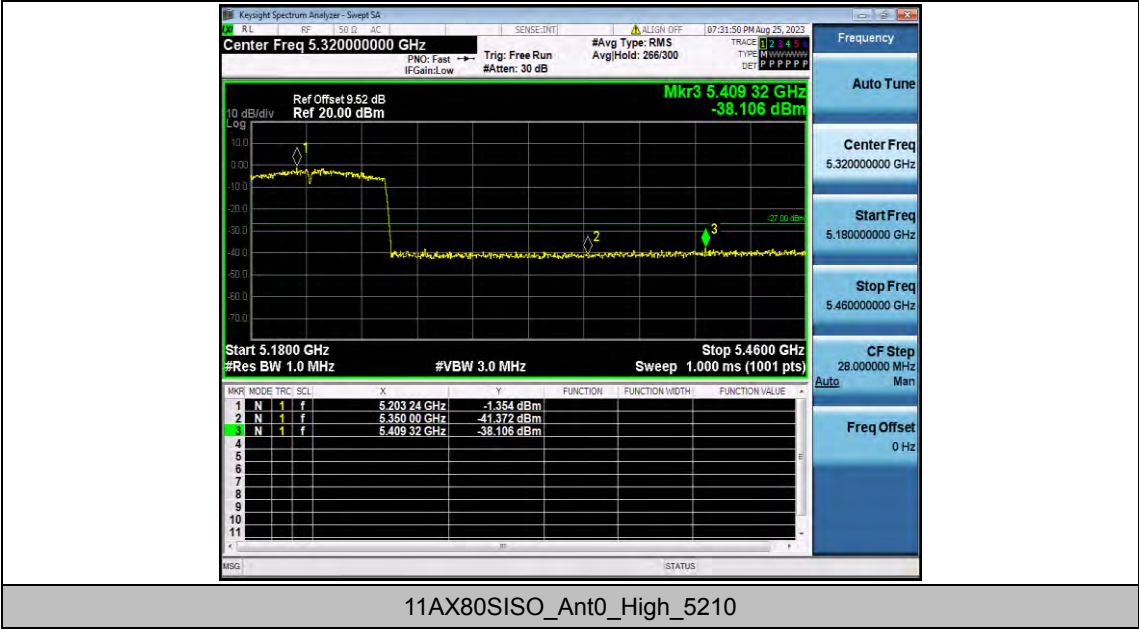
11AX40SISO_Ant0_Low_5190



11AX40SISO_Ant0_High_5230



11AX80SISO_Ant0_Low_5210



11AX80SISO_Ant0_High_5210

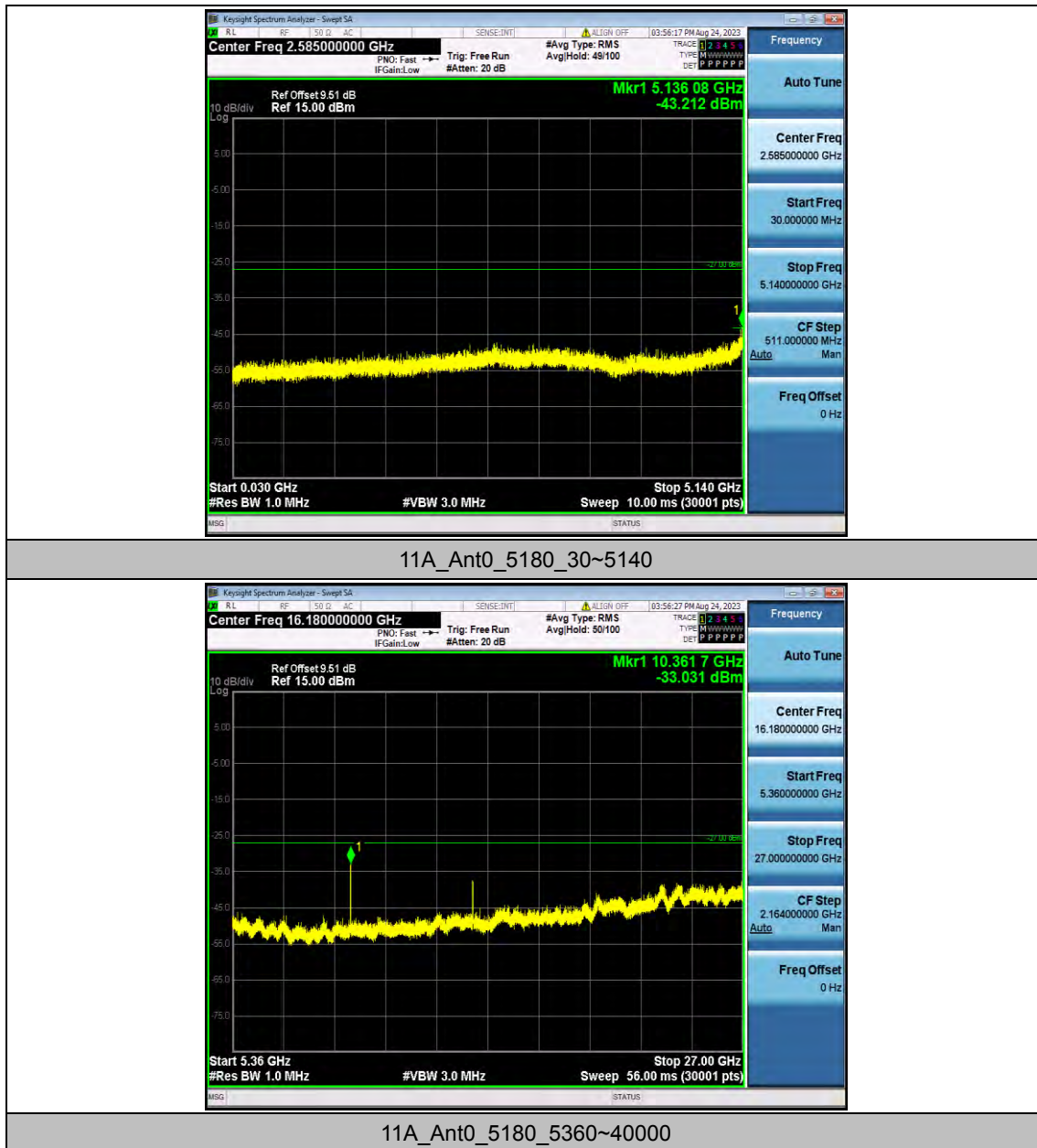
Appendix D.7: Conducted Spurious Emission

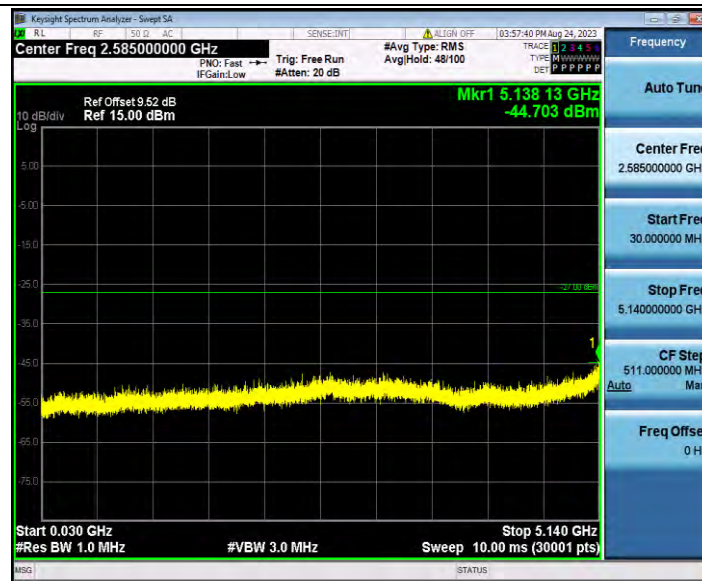
Test Result

Test Mode	Antenna	Frequency[MHz]	FreqRange [MHz]	Max. Fre [MHz]	Max. Level [dBm]	Limit [dBm]	Verdict
11A	Ant0	5180	30~5140	5136.08	-43.21	≤-27	PASS
			5360~40000	10361.73	-33.03	≤-27	PASS
		5200	30~5140	5138.13	-44.7	≤-27	PASS
			5360~40000	10402.12	-28.46	≤-27	PASS
		5240	30~5140	5112.24	-45.56	≤-27	PASS
			5360~40000	10477.86	-29.7	≤-27	PASS
11N20SISO	Ant0	5180	30~5140	5125.01	-45.87	≤-27	PASS
			5360~40000	10351.63	-32.2	≤-27	PASS
		5200	30~5140	5135.74	-45.28	≤-27	PASS
			5360~40000	10401.4	-32.92	≤-27	PASS
		5240	30~5140	2628.95	-46.7	≤-27	PASS
			5360~40000	10484.35	-31.97	≤-27	PASS
11N40SISO	Ant0	5190	30~5140	5138.64	-44.95	≤-27	PASS
			5360~40000	24274.08	-37.67	≤-27	PASS
		5230	30~5140	5133.36	-45.76	≤-27	PASS
			5360~40000	10460.55	-35.17	≤-27	PASS
11AC20SISO	Ant0	5180	30~5140	5130.46	-45.6	≤-27	PASS
			5360~40000	10361.73	-33.85	≤-27	PASS
		5200	30~5140	5133.53	-45.97	≤-27	PASS
			5360~40000	10399.96	-33.23	≤-27	PASS
		5240	30~5140	5012.93	-46.7	≤-27	PASS
			5360~40000	10480.75	-32.08	≤-27	PASS
11AC40SISO	Ant0	5190	30~5140	5135.57	-45.69	≤-27	PASS
			5360~40000	26919.93	-37.42	≤-27	PASS
		5230	30~5140	5009.52	-46.84	≤-27	PASS
			5360~40000	10451.17	-35.13	≤-27	PASS
11AC80SISO	Ant0	5210	30~5140	2722.8	-45.92	≤-27	PASS
			5360~40000	23508.75	-38.16	≤-27	PASS
11AX20SISO	Ant0	5180	30~5140	5139.66	-46.13	≤-27	PASS
			5360~40000	25646.78	-38.2	≤-27	PASS
		5200	30~5140	5131.82	-45.74	≤-27	PASS
			5360~40000	10402.84	-34.6	≤-27	PASS
		5240	30~5140	5103.72	-46.09	≤-27	PASS
			5360~40000	10480.75	-33.15	≤-27	PASS
11AX40SISO	Ant0	5190	30~5140	5058.07	-45.82	≤-27	PASS
			5360~40000	24204.83	-37.58	≤-27	PASS

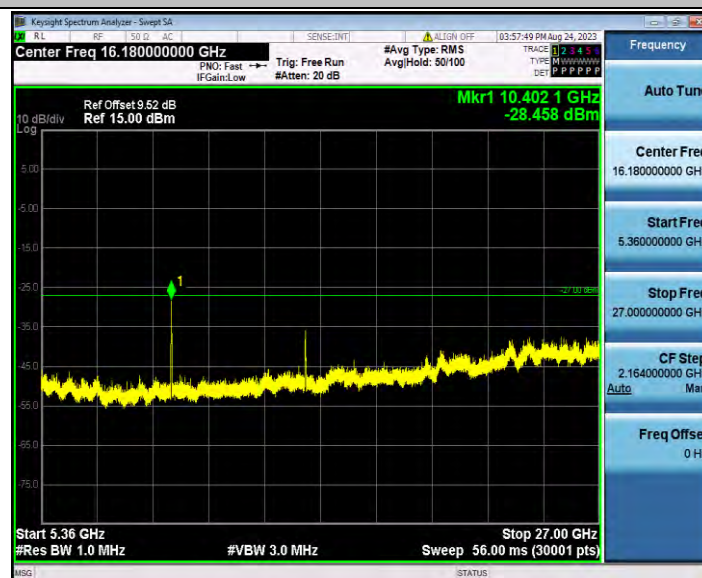
		5230	30~5140	5115.3	-46.6	≤-27	PASS
			5360~40000	26054.33	-37.5	≤-27	PASS
11AX80SISO	Ant0	5210	30~5140	5129.1	-46.77	≤-27	PASS
			5360~40000	24357.76	-37.6	≤-27	PASS

Test Graphs

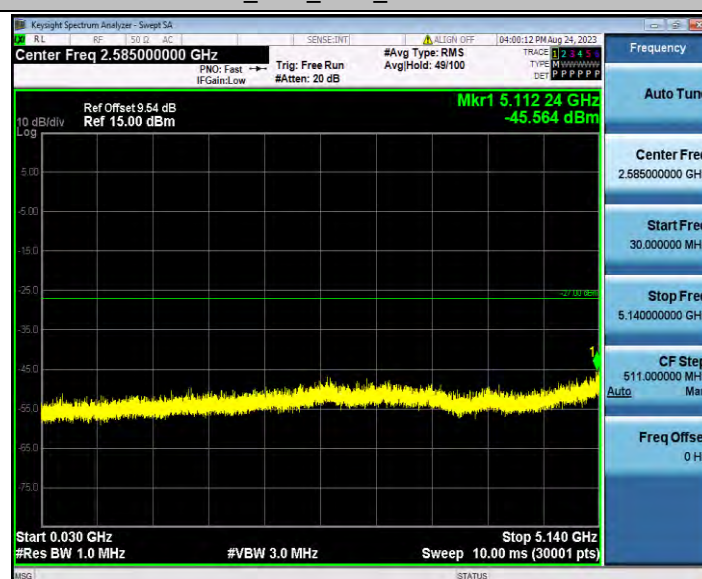




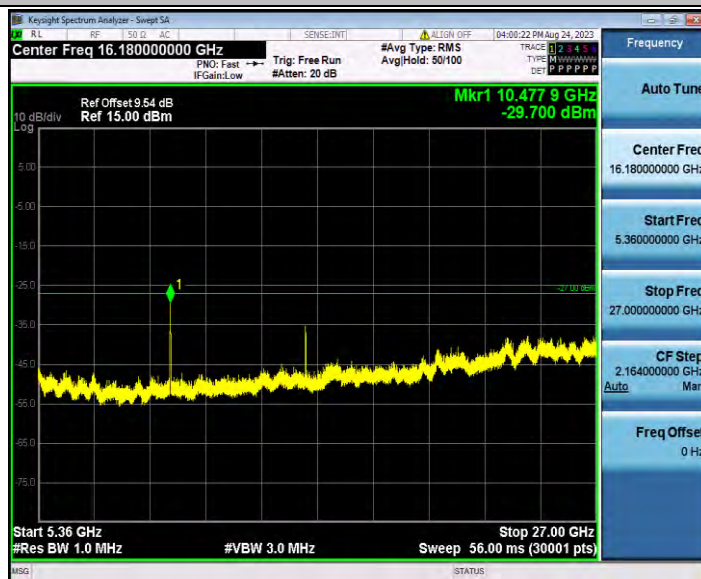
11A_Ant0_5200_30~5140



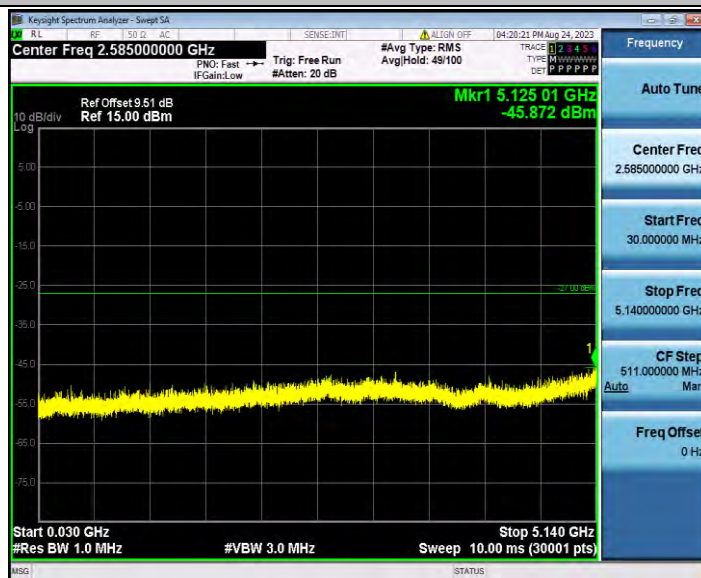
11A_Ant0_5200_5360~40000



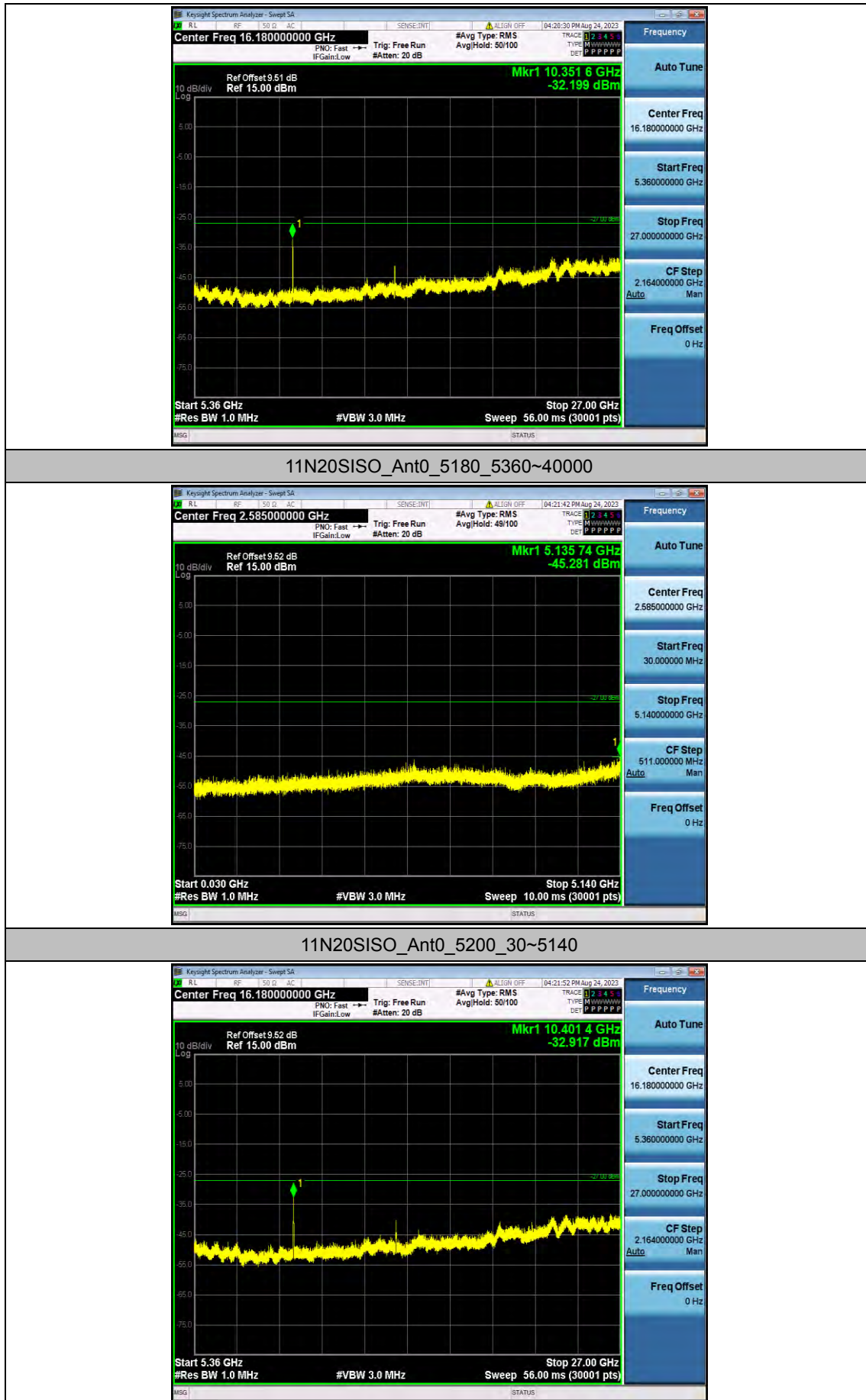
11A_Ant0_5240_30~5140



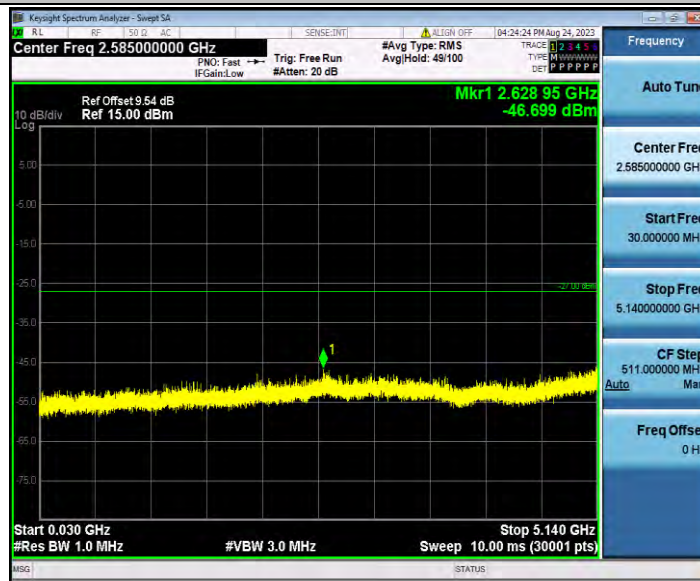
11A_Ant0_5240_5360~40000



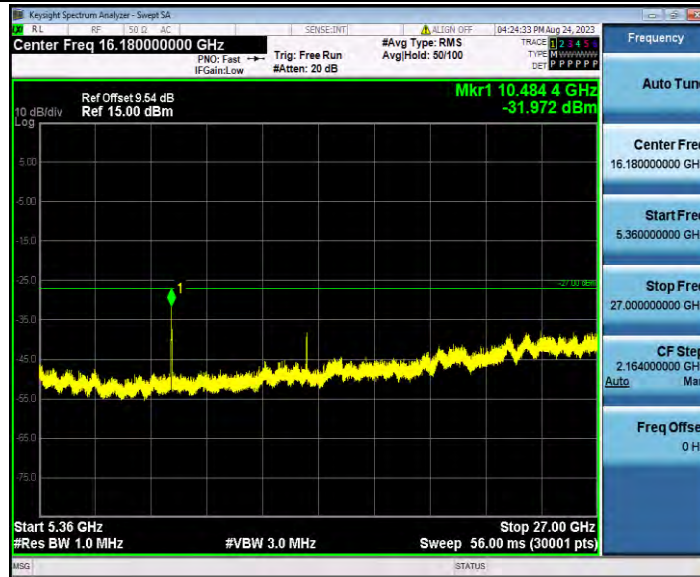
11N20SISO_Ant0_5180_30~5140



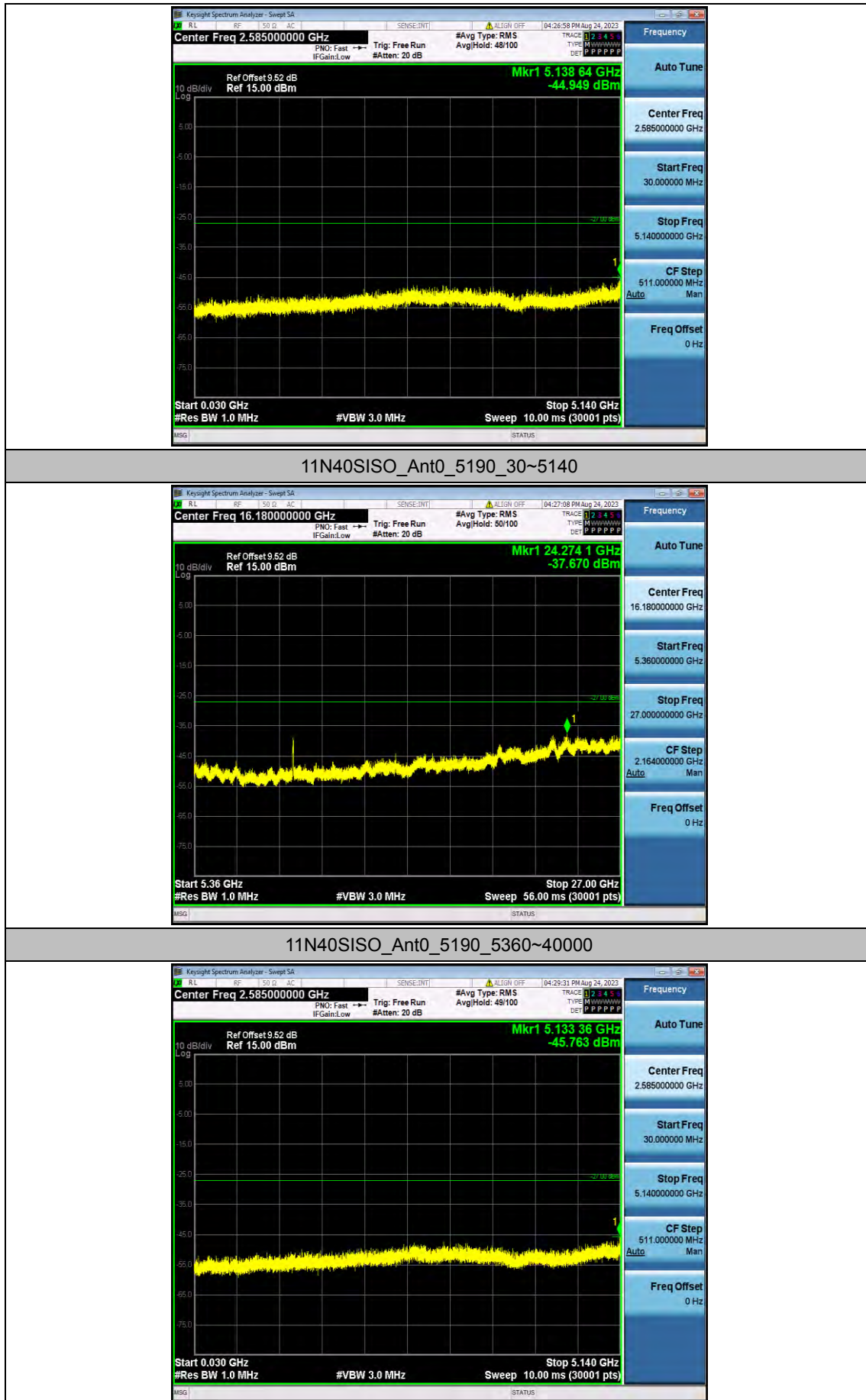
11N20SISO_Ant0_5200_5360~40000



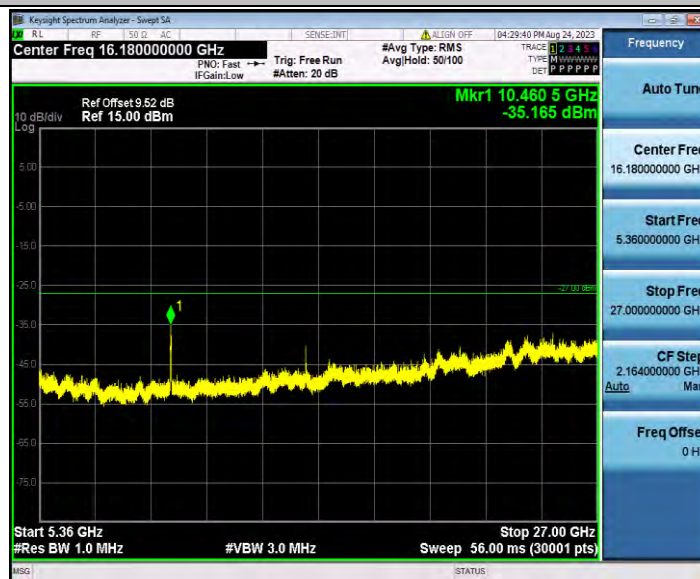
11N20SISO_Ant0_5240_30~5140



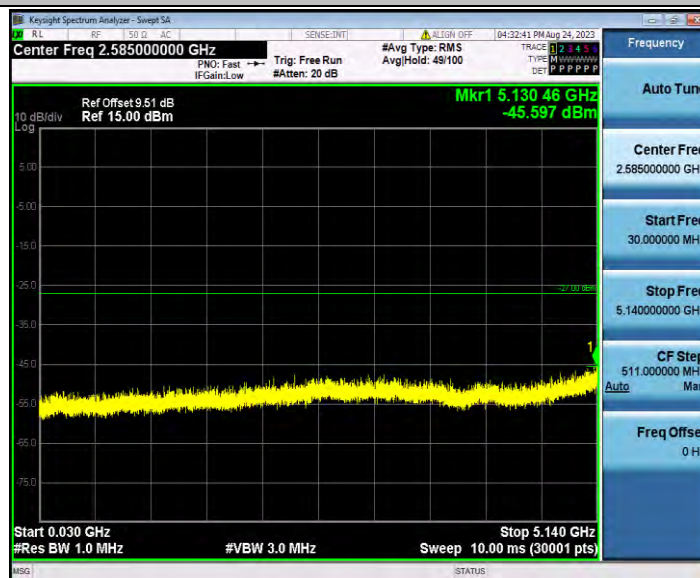
11N20SISO_Ant0_5240_5360~40000



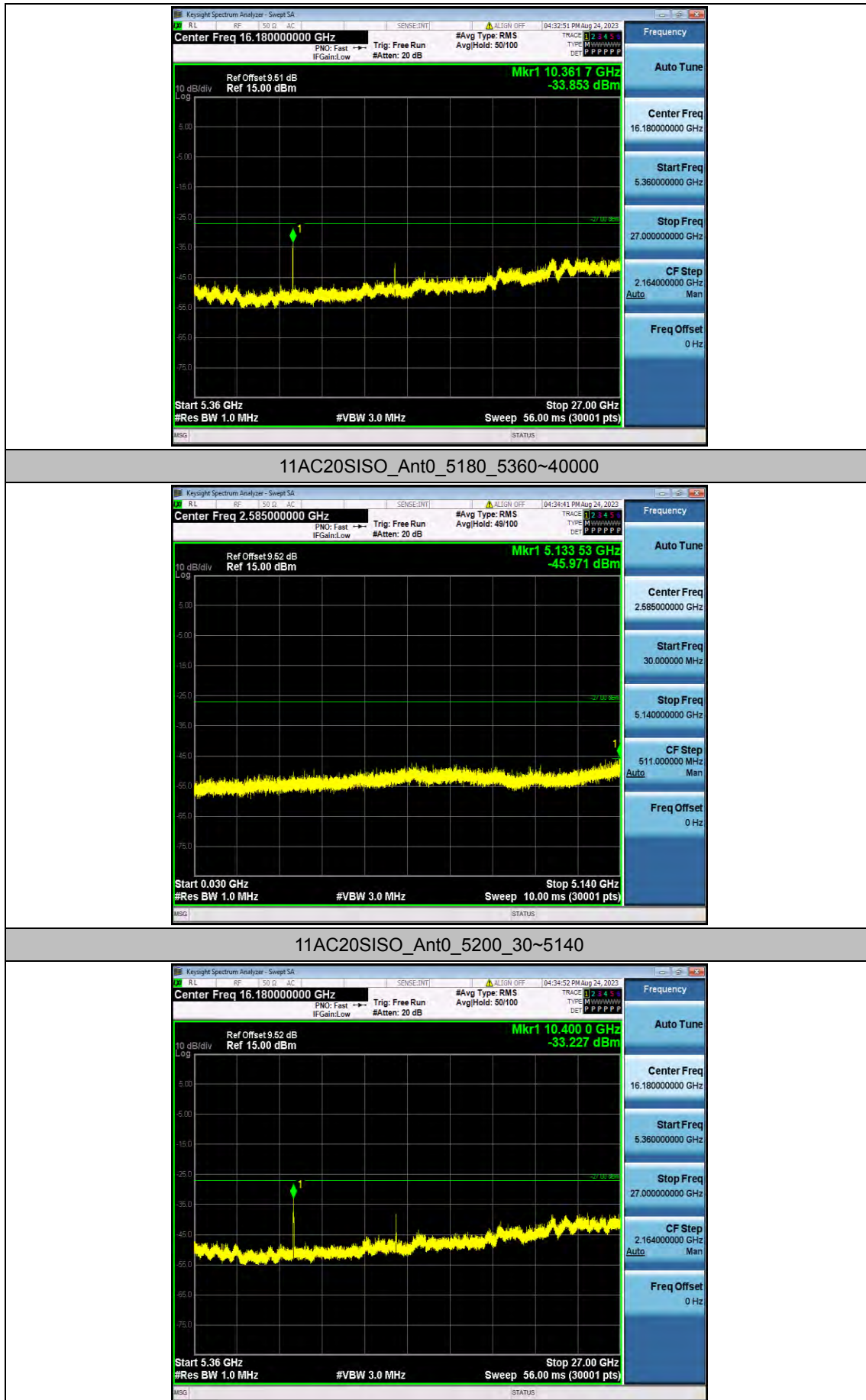
11N40SISO_Ant0_5230_30~5140



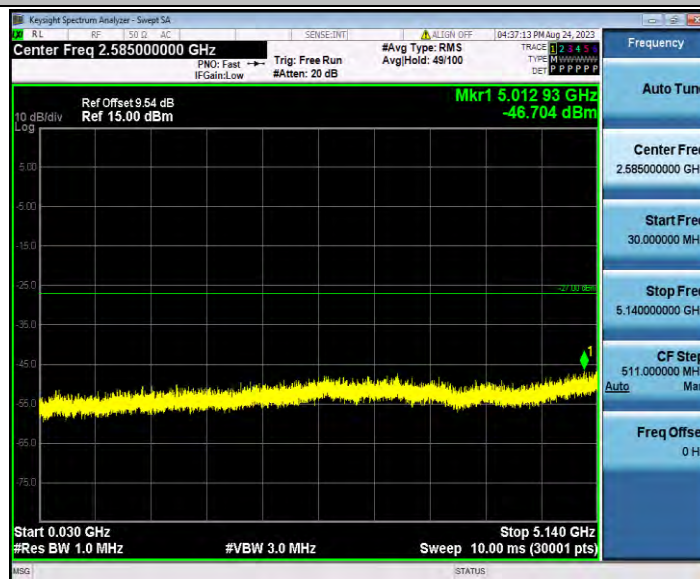
11N40SISO_Ant0_5230_5360~40000



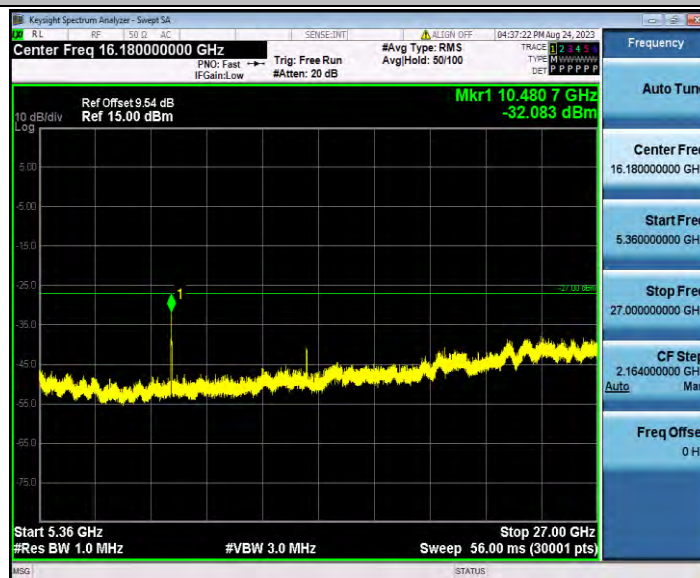
11AC20SISO_Ant0_5180_30~5140



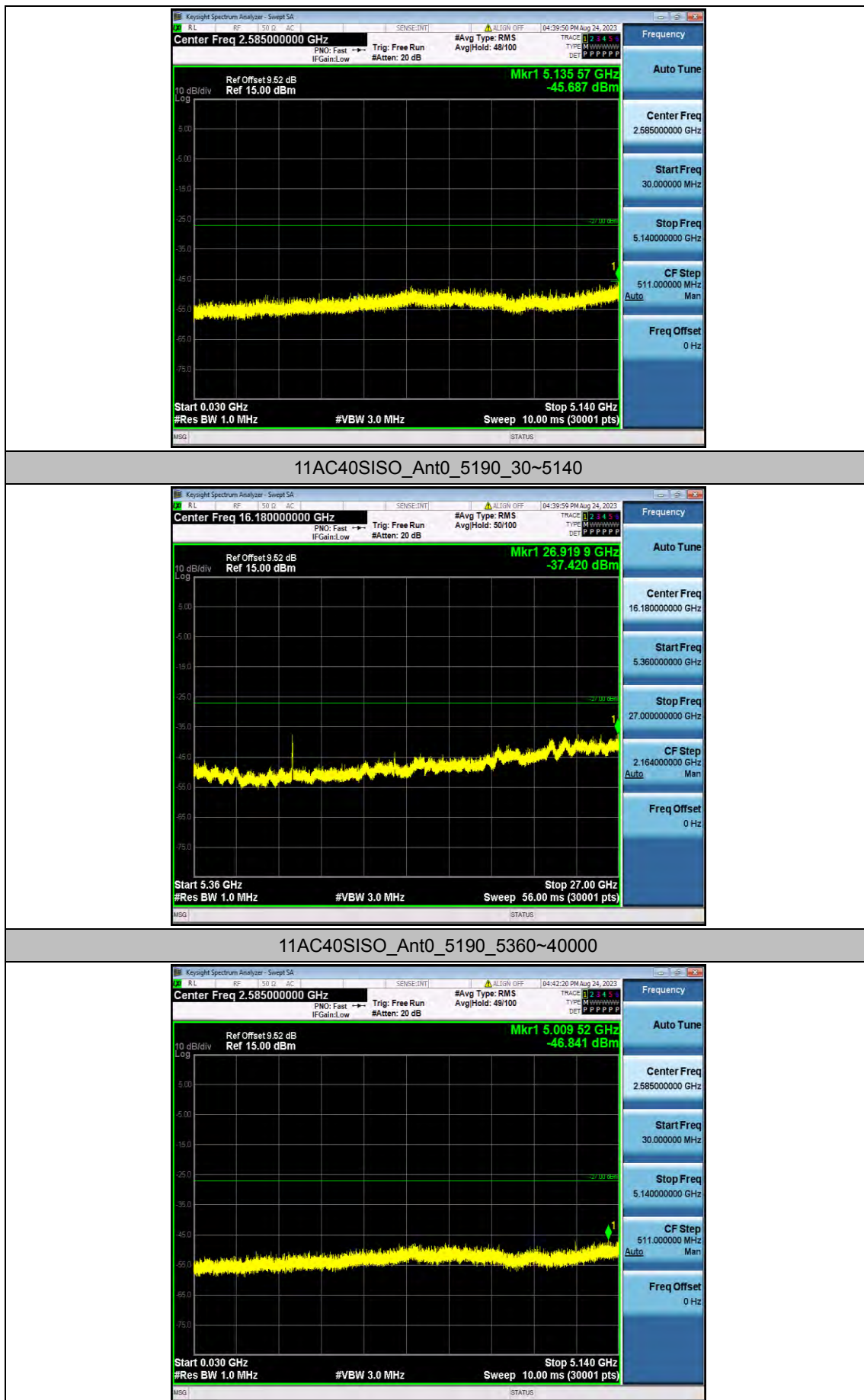
11AC20SISO_Ant0_5200_5360~40000



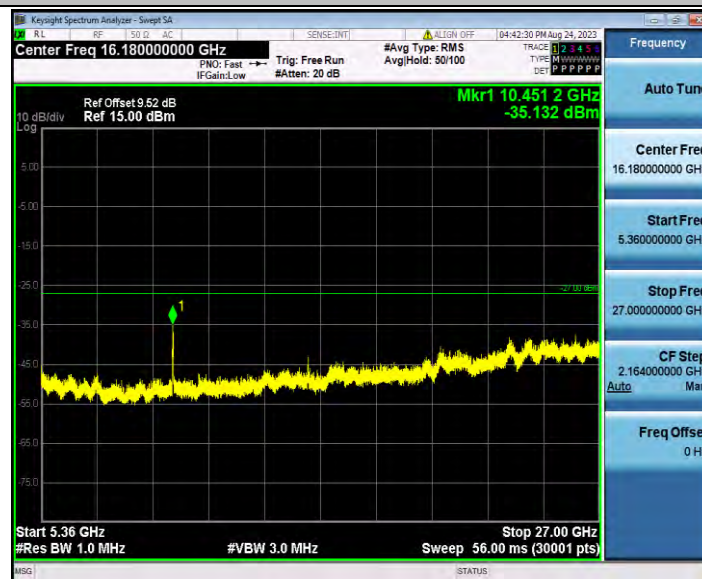
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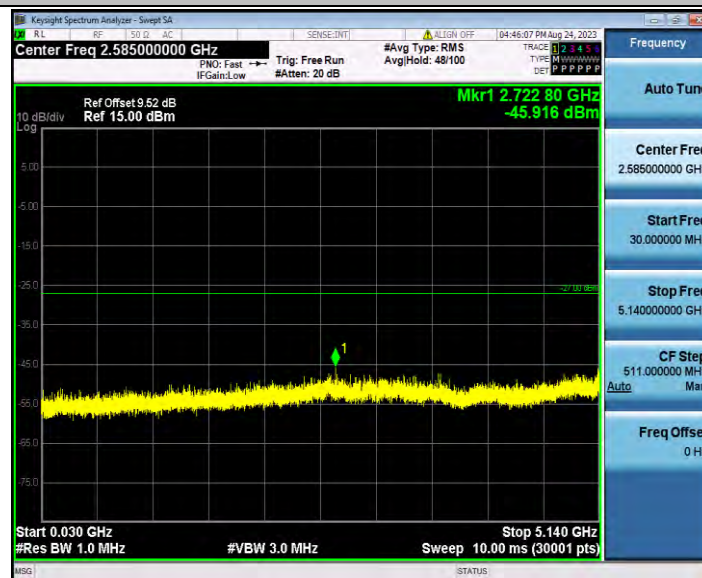
11AC20SISO_Ant0_5240_5360~40000



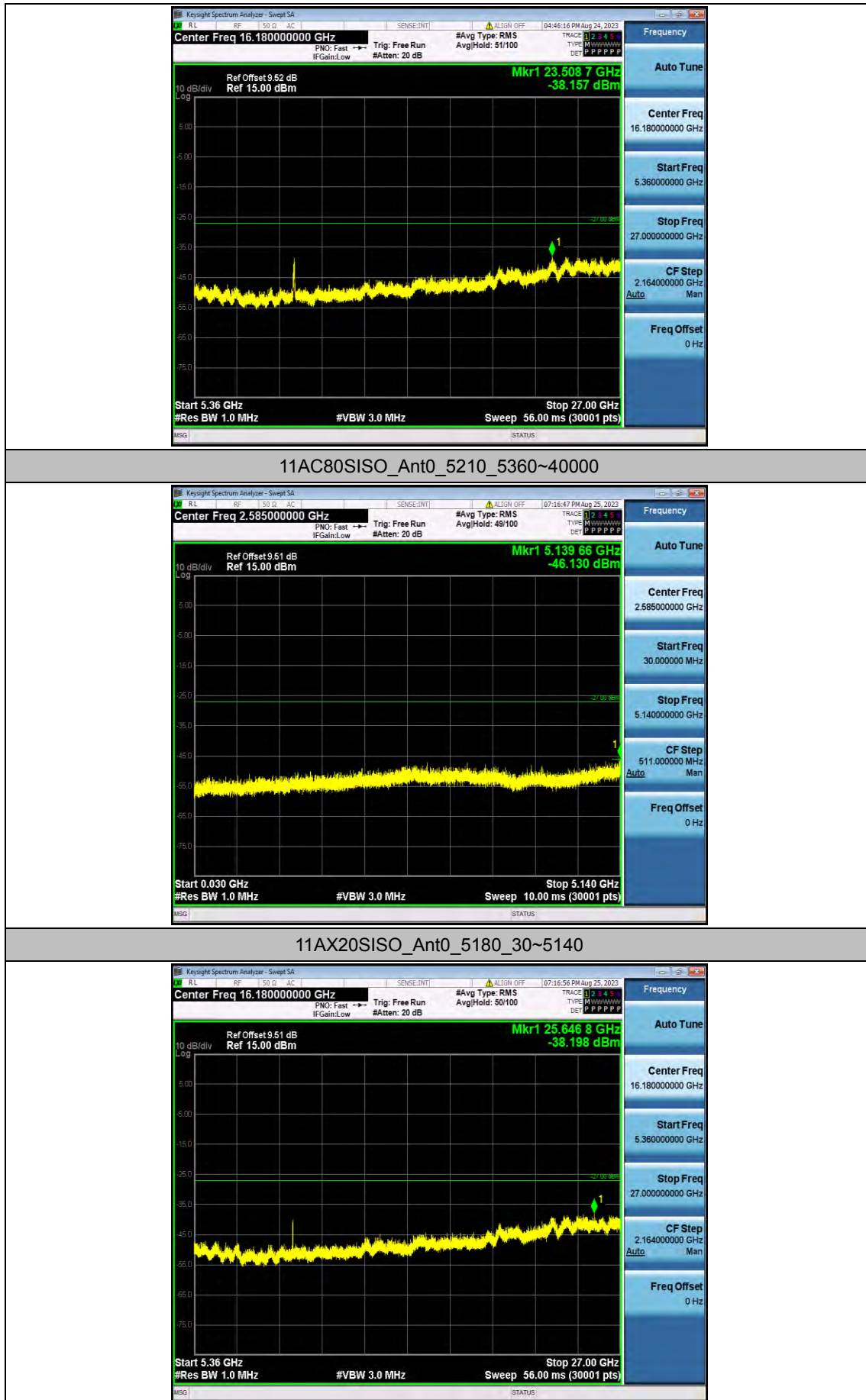
11AC40SISO_Ant0_5230_30~5140



11AC40SISO_Ant0_5230_5360~40000



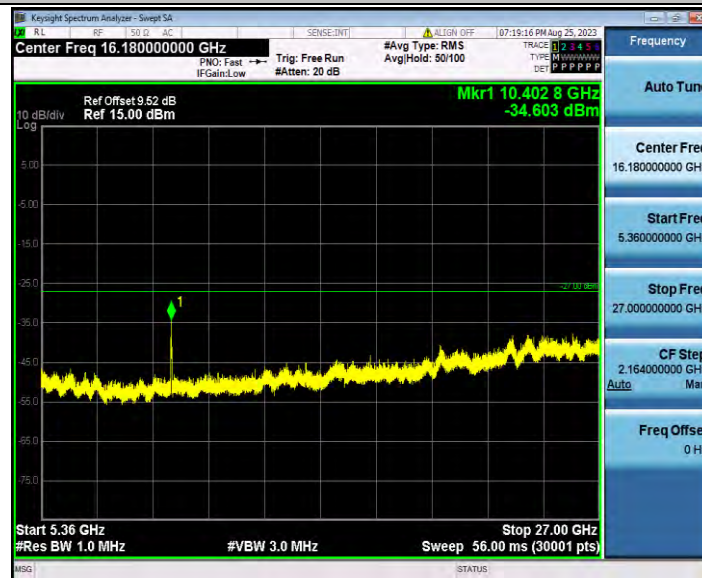
11AC80SISO_Ant0_5210_30~5140



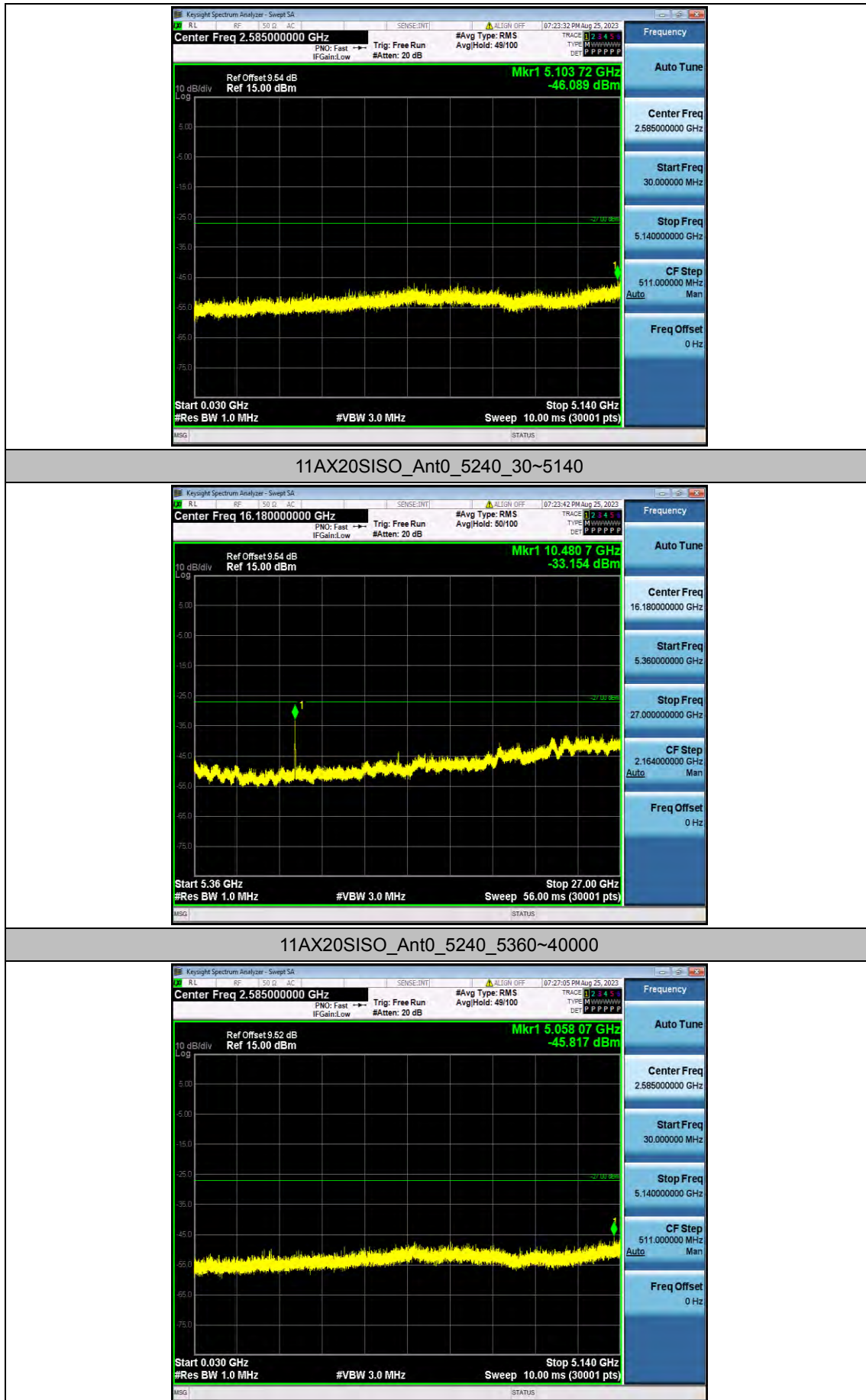
11AX20SISO_Ant0_5180_5360~40000



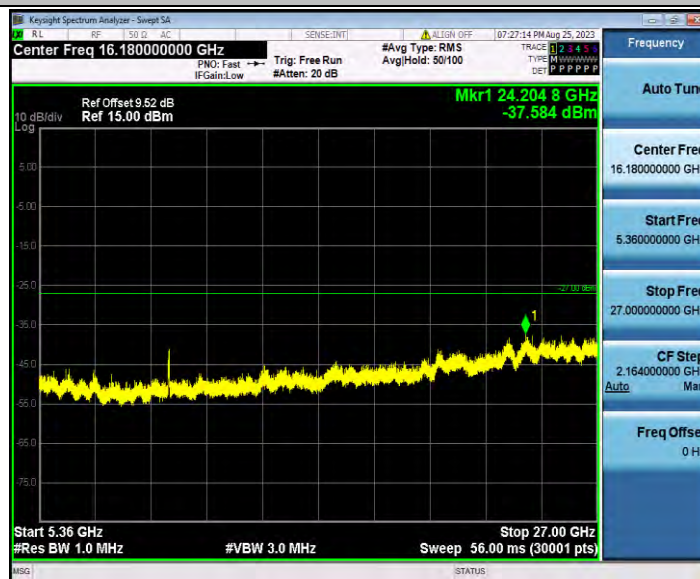
11AX20SISO_Ant0_5200_30~5140



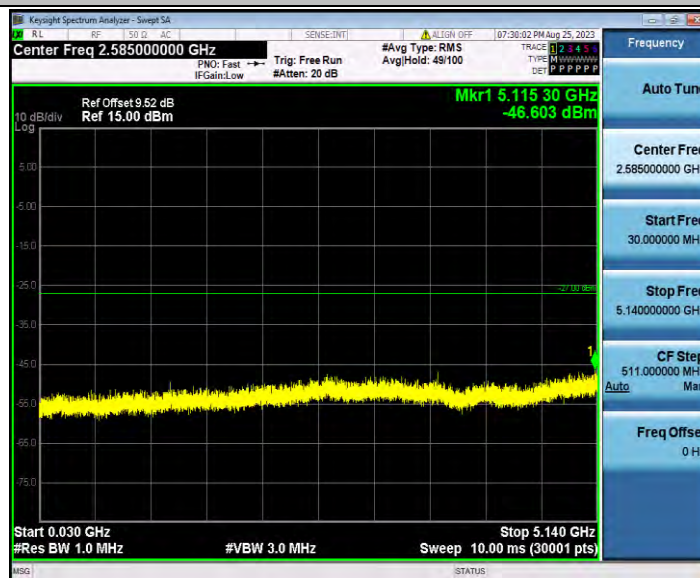
11AX20SISO_Ant0_5200_5360~40000



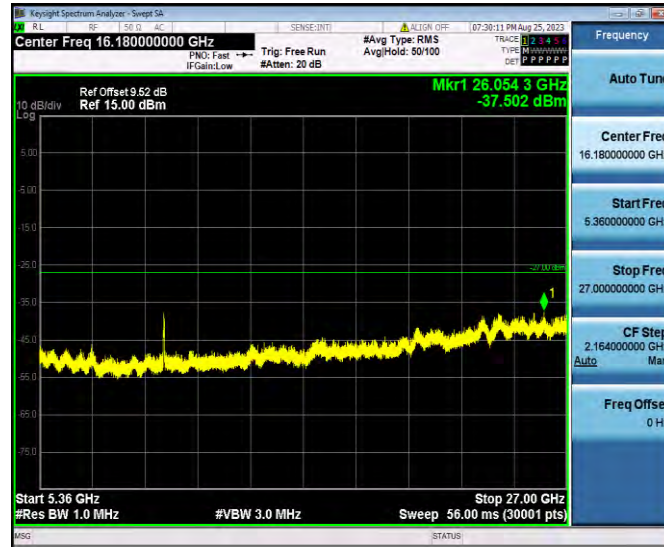
11AX40SISO_Ant0_5190_30~5140



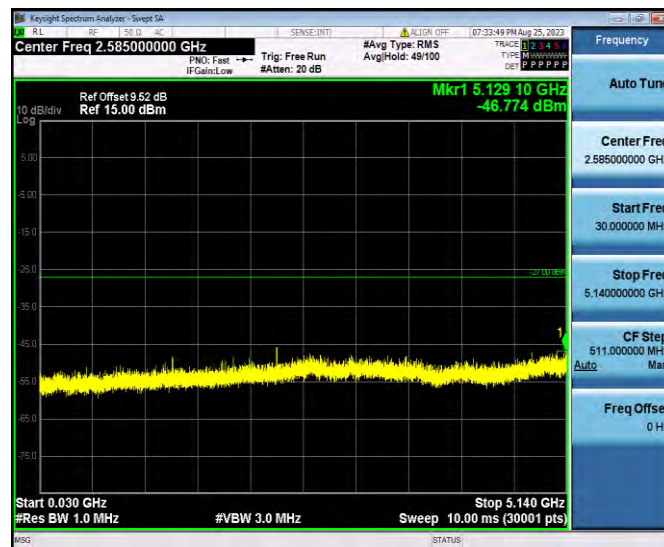
11AX40SISO_Ant0_5190_5360~40000



11AX40SISO_Ant0_5230_30~5140



11AX40SISO_Ant0_5230_5360~40000



11AX80SISO_Ant0_5210_5360~40000



11AX80SISO_Ant0_5210_5360~40000