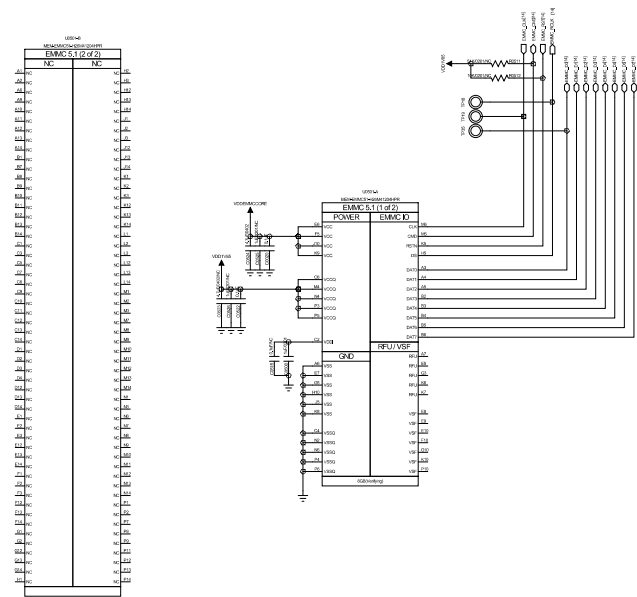
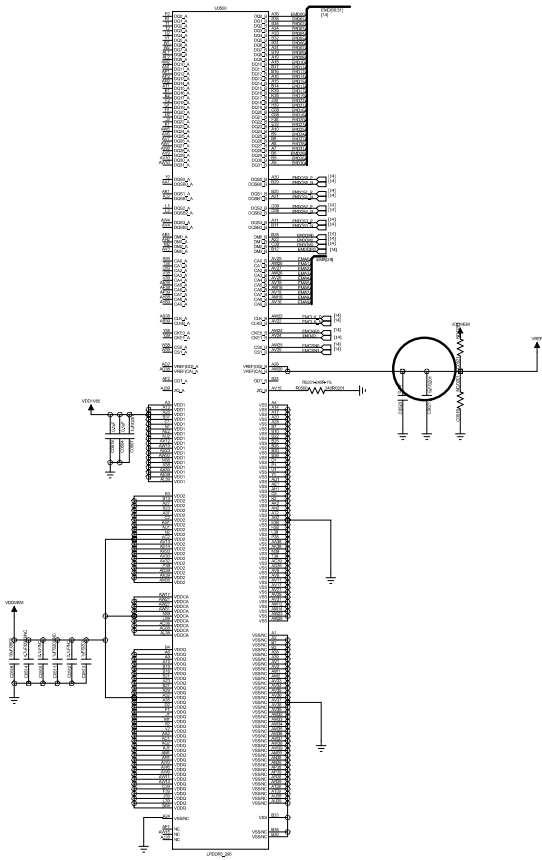


HW Revision History			
Rev	Changes	Approved	Date

eMMC

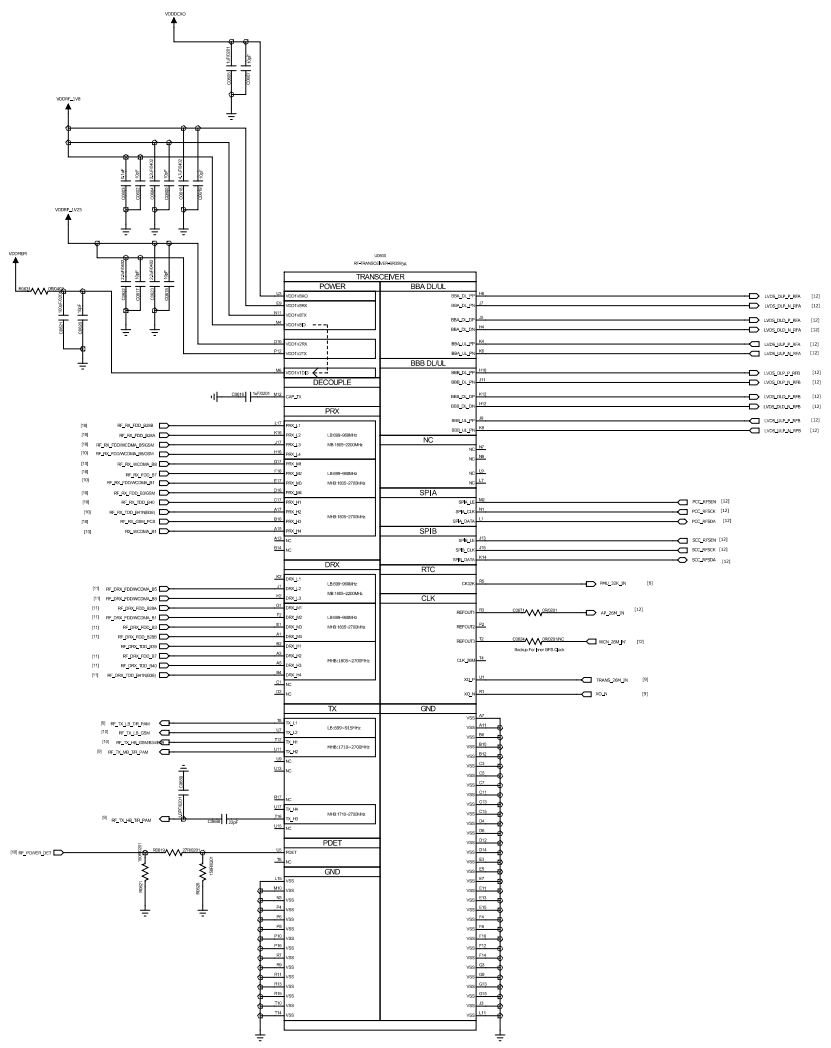
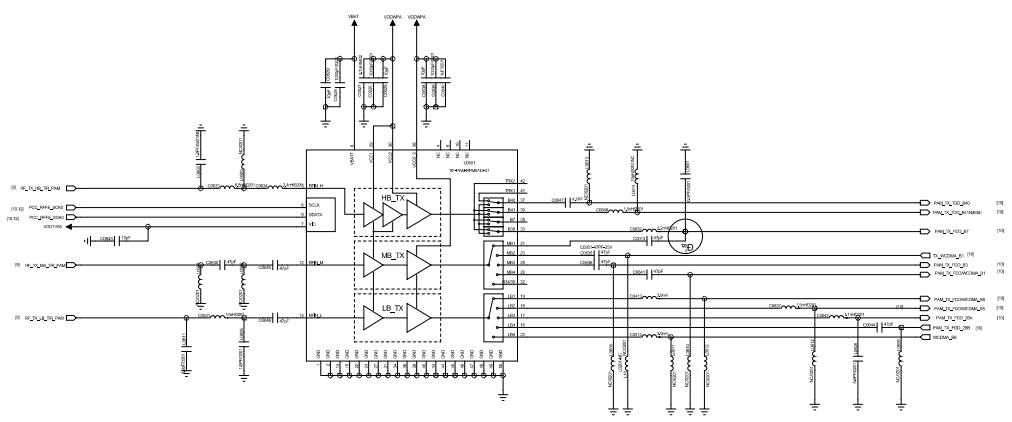


LPDDR3
296

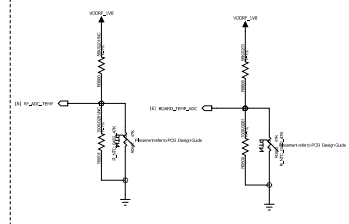


REV: 00000000			
REV	DATE	APPROVED	DATE

LTE/WCDMA PAM

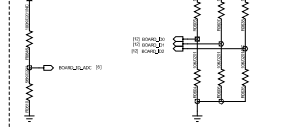


NTC

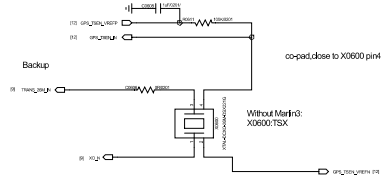


BoardID0	
Clock Mode	TSX
TCXO+32K	0
LPDDR Type	1
LPDDR3	0
LPDDR4X	1
BoardID1	
LPDDR3	0
LPDDR4X	1

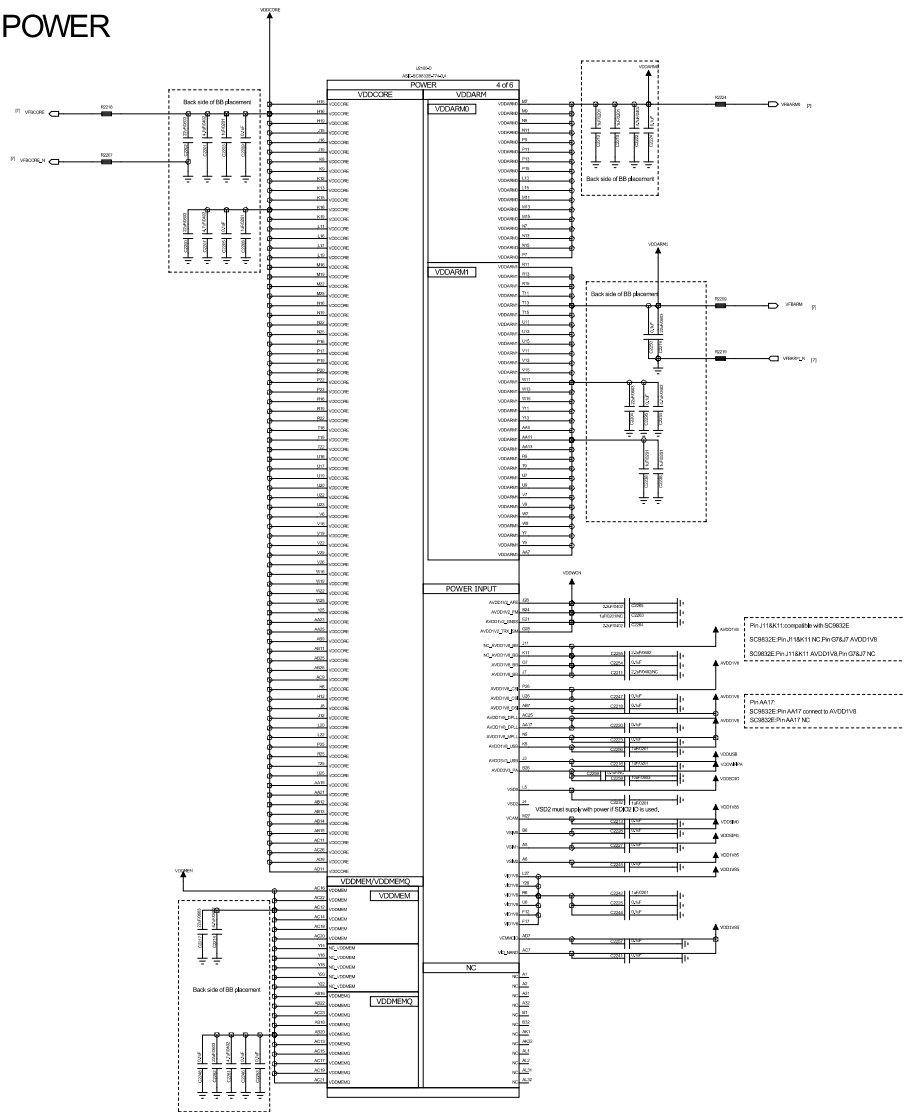
Band info



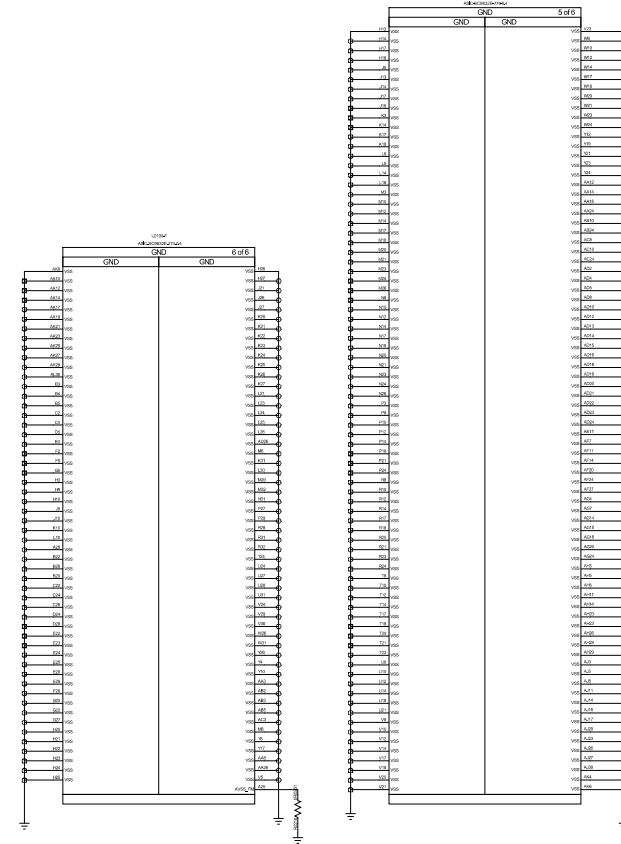
DCXO/TSX 26M



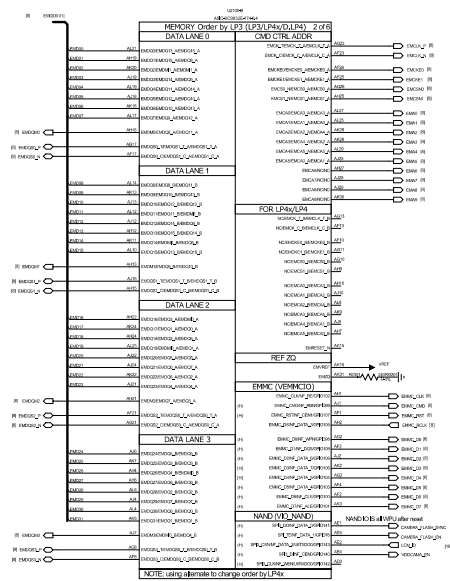
POWER



GND



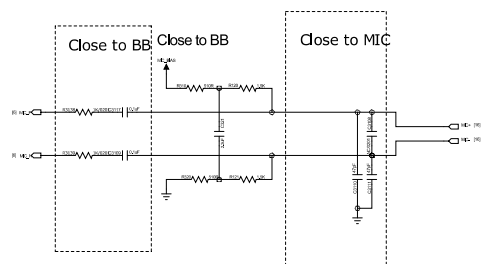
MEMORY



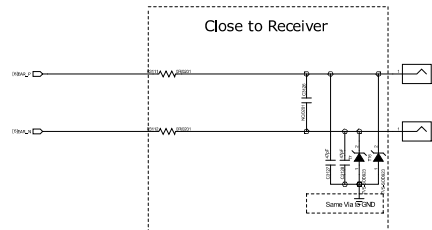
NOTE: using a **laminate** to charge order by LP4.

Integration			
LT	Q240	APPROVED	DATE

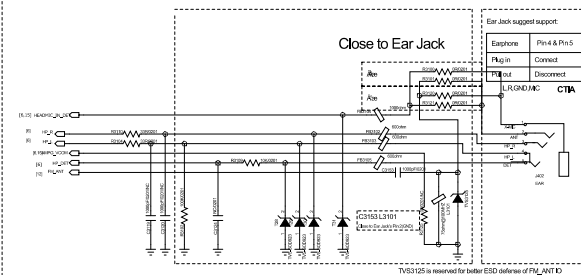
MAIN MIC



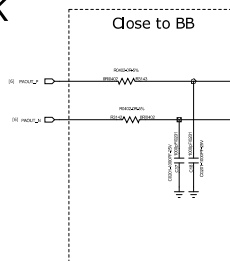
RECEIVER



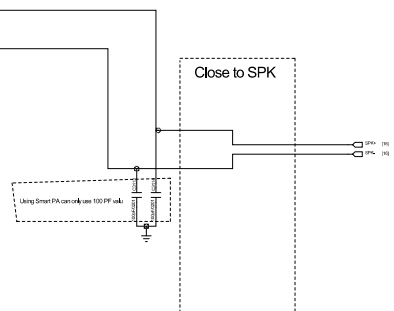
3.5mm Ear Jack



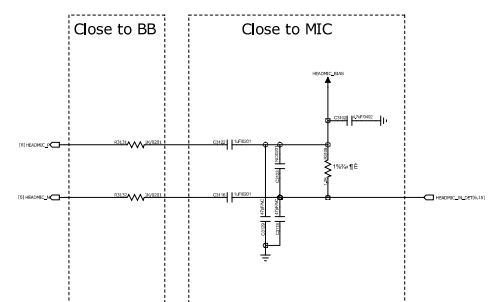
interior SPK



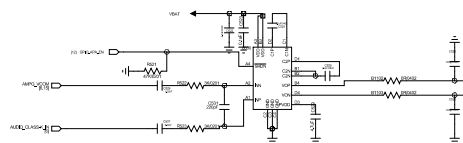
SPEAKER



HEAD MIC

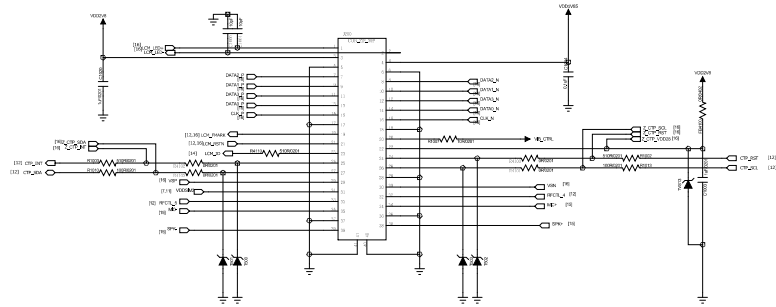


CLASS K PA

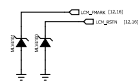
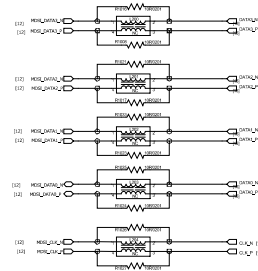


REV. 001 (2018-01-01)			
REV.	REVISION	APPROVED	DATE

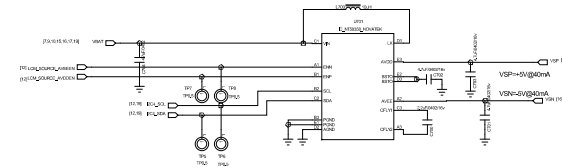
5.5"FHD LCM(4lane MIPI)



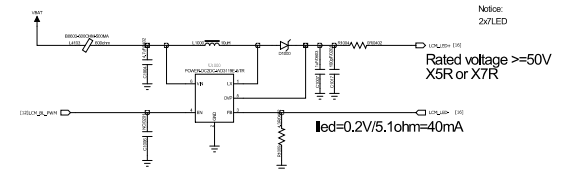
EMI



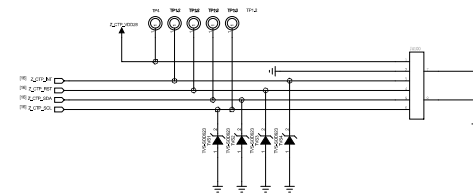
DCDC 3POWER



Back Light Driver



CTP

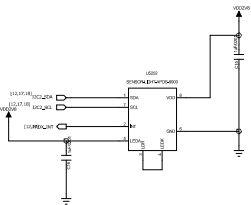


REVISIONS			
REV	REASON	APPROVED	DATE

Accelerometer&GYRO: ICM-20600

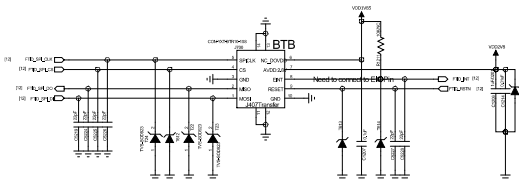
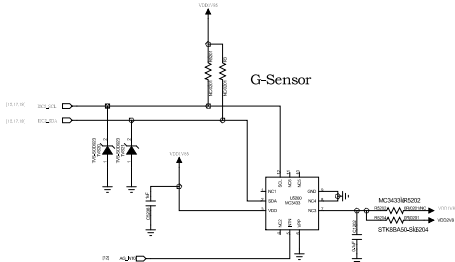
ALS & PROX

InfraRed Sensor



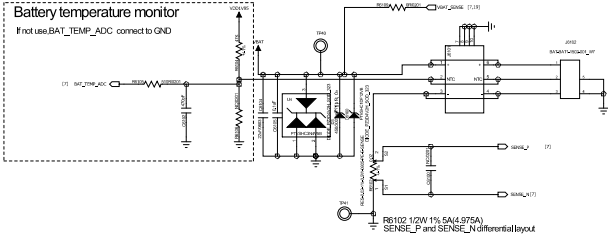
G Sensor

FingerPrint ID

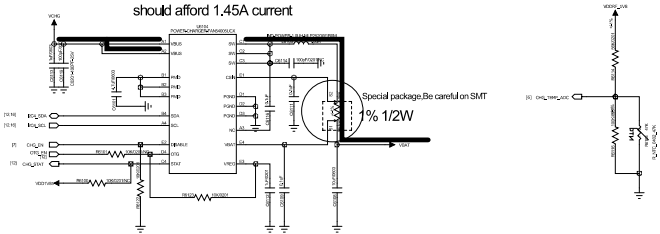


Revision History			
ITS	DESCRIPTION	APPROVED	DATE

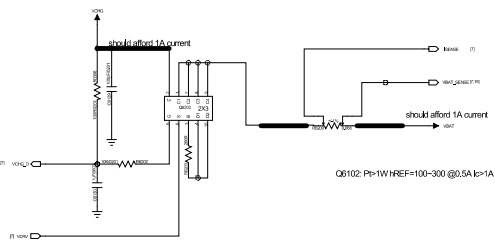
BATTERY



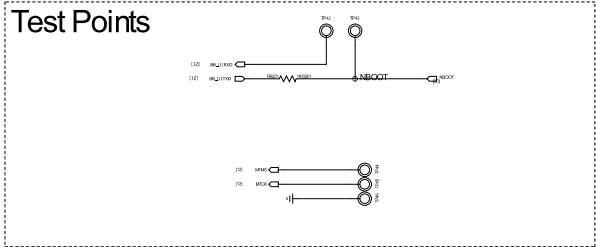
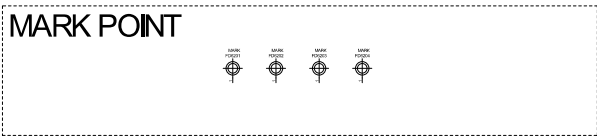
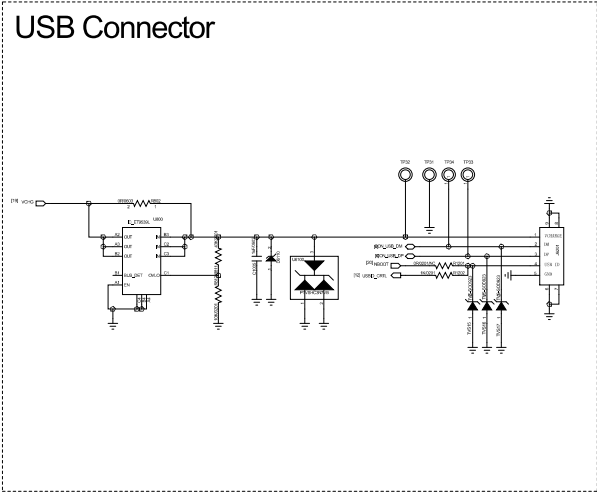
Switching charger



CHARGE



REV. 00000000			
DATE	DESCRIPTION	APPROVED	DATE



SharkL3 strapping pin

Ball name	Signal name in boot code	Value	Descriptions	Default value
UTXD	USB_ULOAD_EN	0	USB download enable	1 (set 1 up)
AUD_DAD1	ARM_BOOT_MD0	000	Reserved	111 (set 1 up)
		001	TLC NAND Toggle	
		010	MLC/SLC NAND Flash(NEF)	
		011	SD boot (no fine cut)	
AUD_DAD0	ARM_BOOT_MD1	100	MLC/SCL NAND Flash Toggle	(MSB mod)
		101	TLC NAND Flash Toggle	(LSB mod)
		110	Reserved	
ADL_BCLK	ARM_BOOT_MD2	111	ARM boot	

[illegible]

