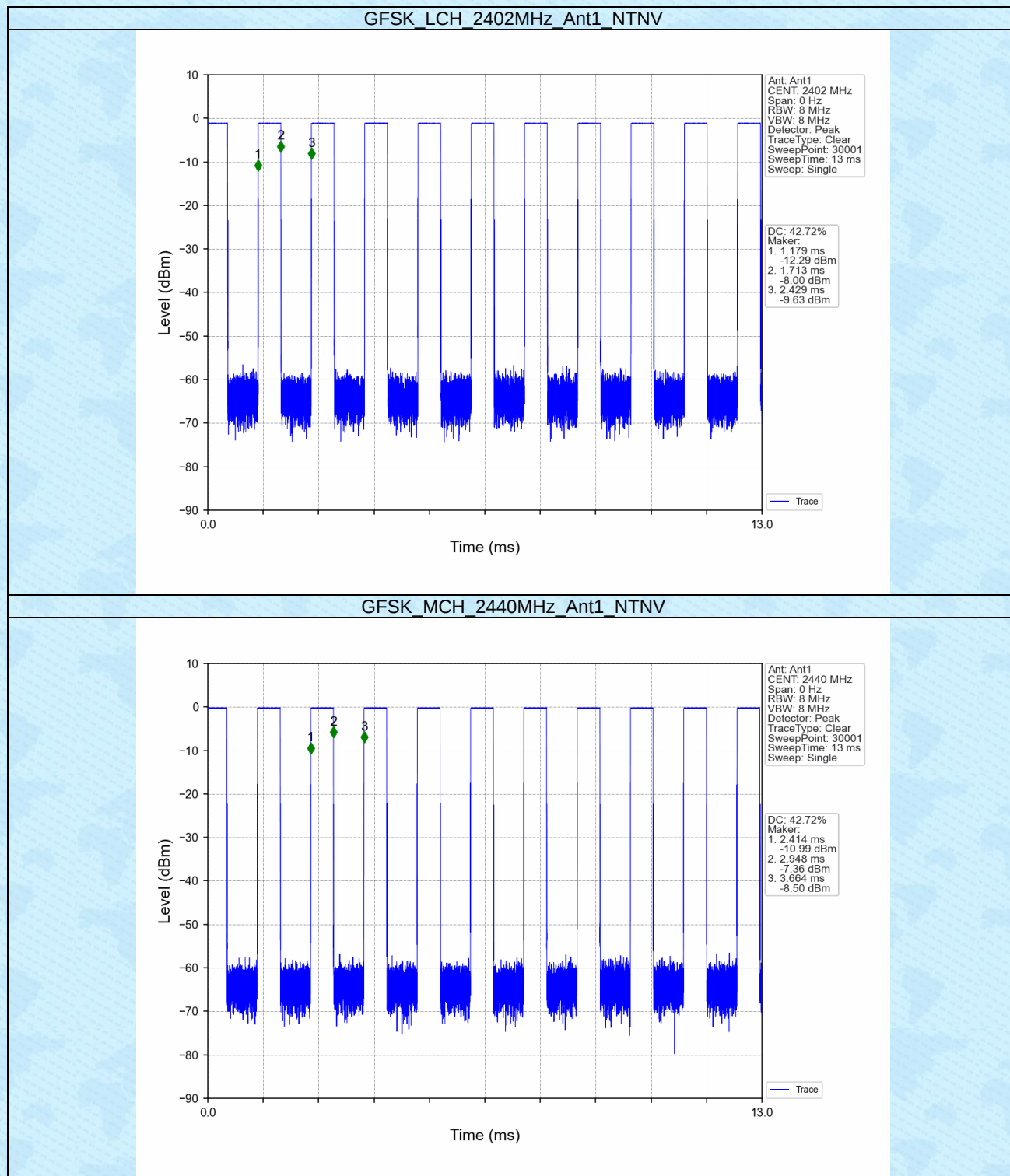


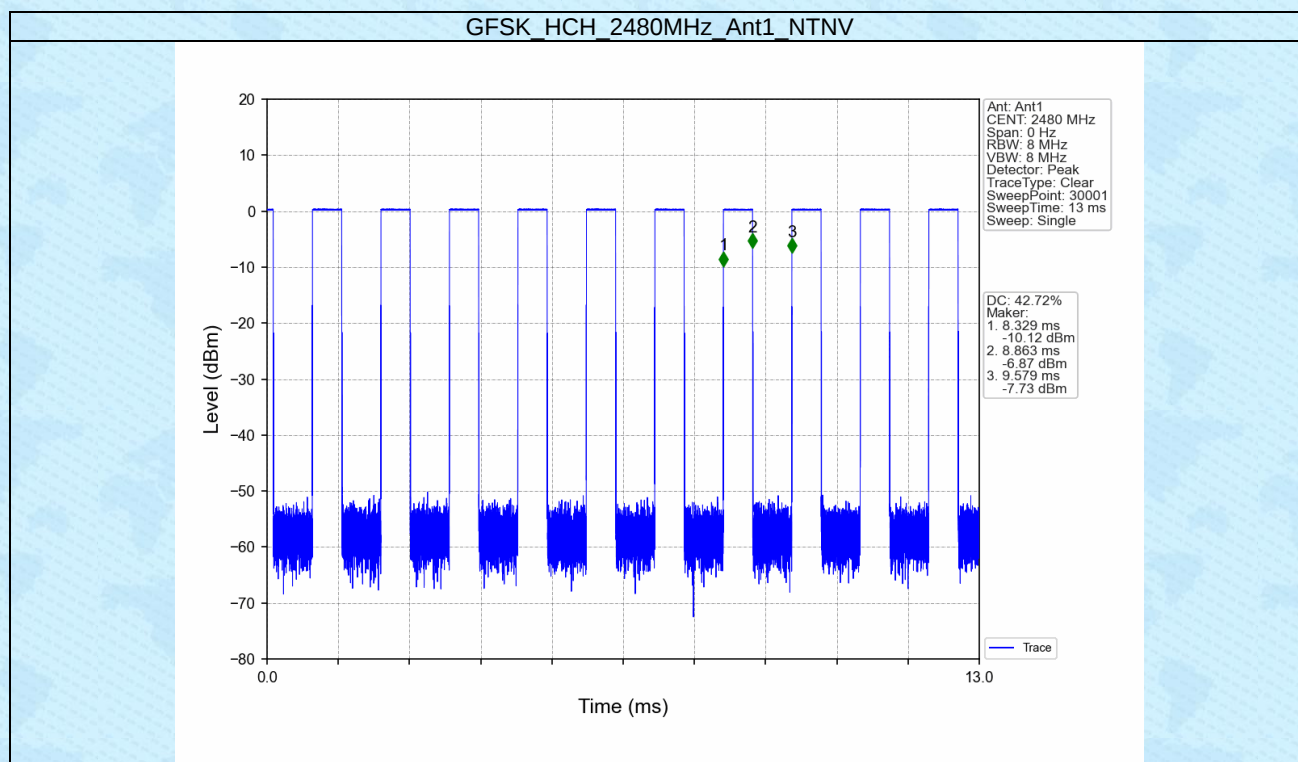
1. Duty Cycle

1.1 Test Result

Ant1							
Mode	TX Type	Frequency (MHz)	T_on (ms)	Period (ms)	Duty Cycle (%)	Duty Cycle Correction Factor (dB)	Max. DC Variation (%)
GFSK	SISO	2402	0.534	1.250	42.72	3.69	0.03
		2440	0.534	1.250	42.72	3.69	0.03
		2480	0.534	1.250	42.72	3.69	0.03

1.2 Test Graph





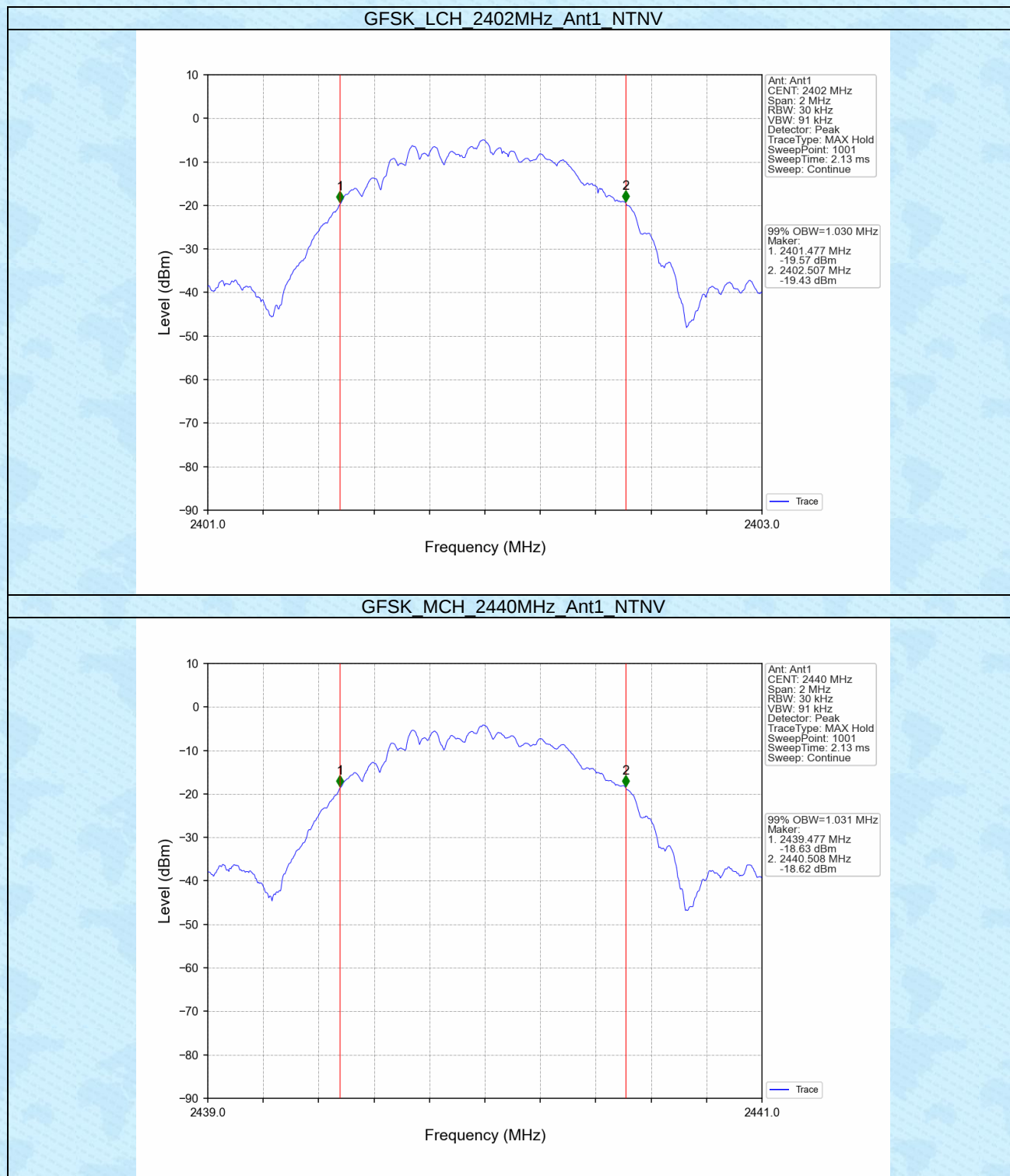
2. Bandwidth

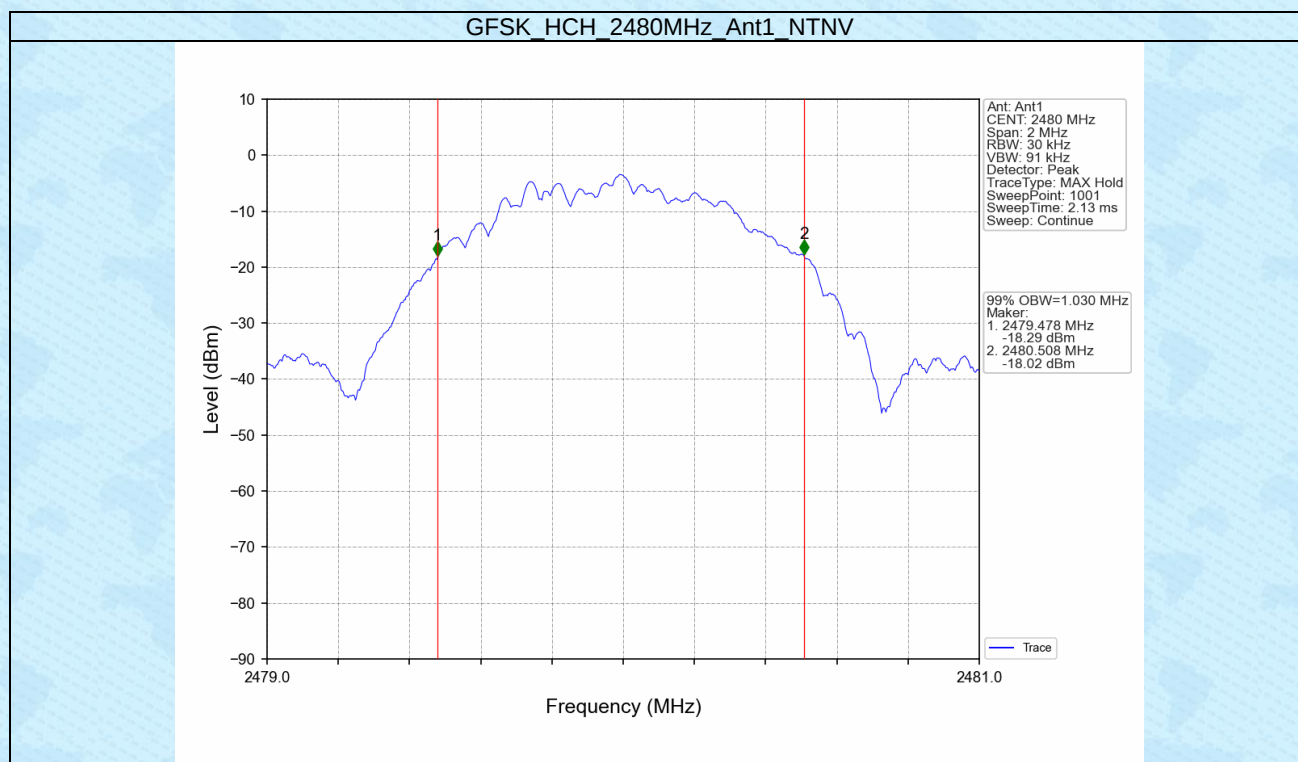
2.1 OBW

2.1.1 Test Result

Mode	TX Type	Frequency (MHz)	ANT	99% Occupied Bandwidth (MHz)		Verdict
				Result	Limit	
GFSK	SISO	2402	1	1.030	/	Pass
		2440	1	1.031	/	Pass
		2480	1	1.030	/	Pass

2.1.2 Test Graph



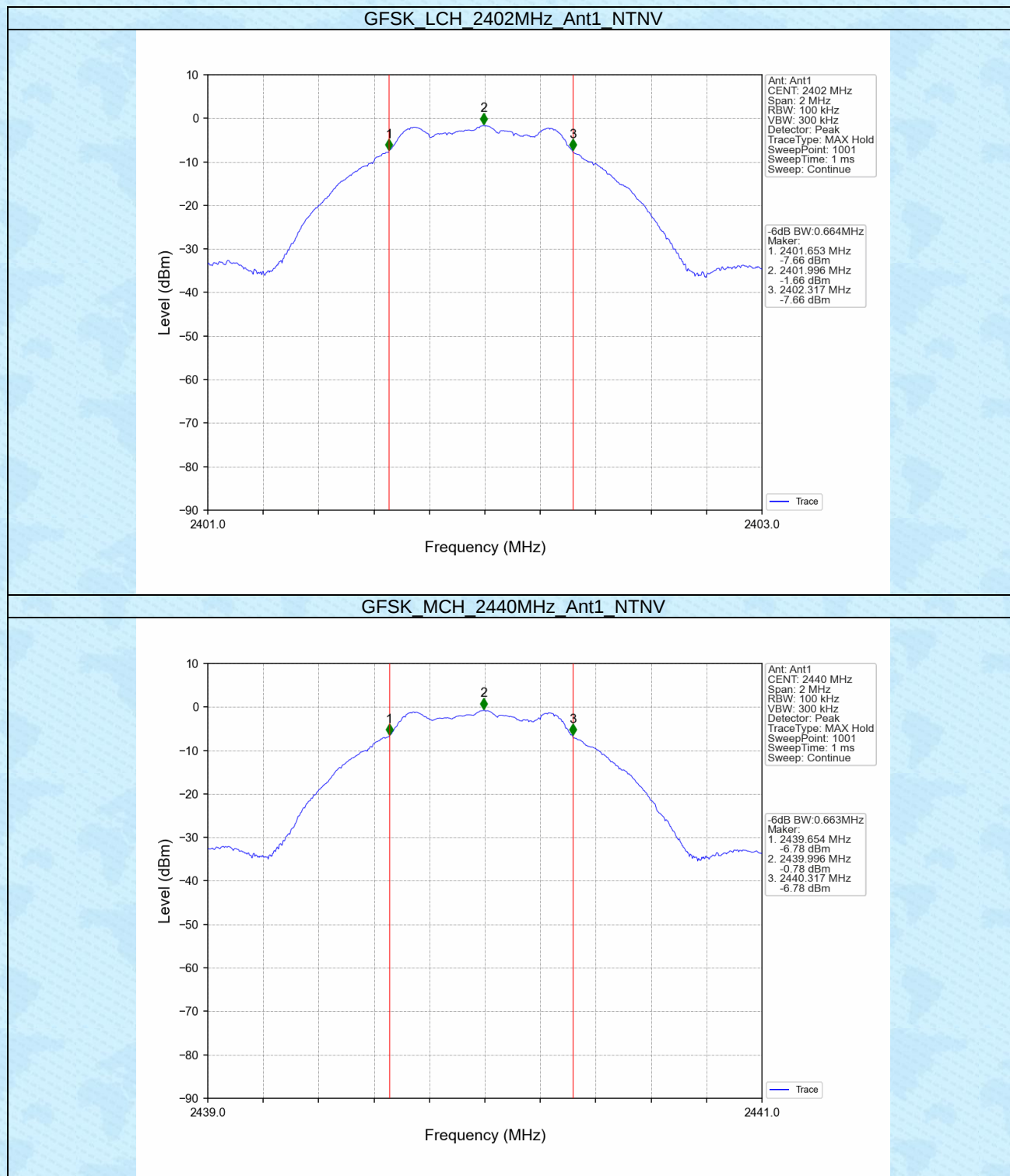


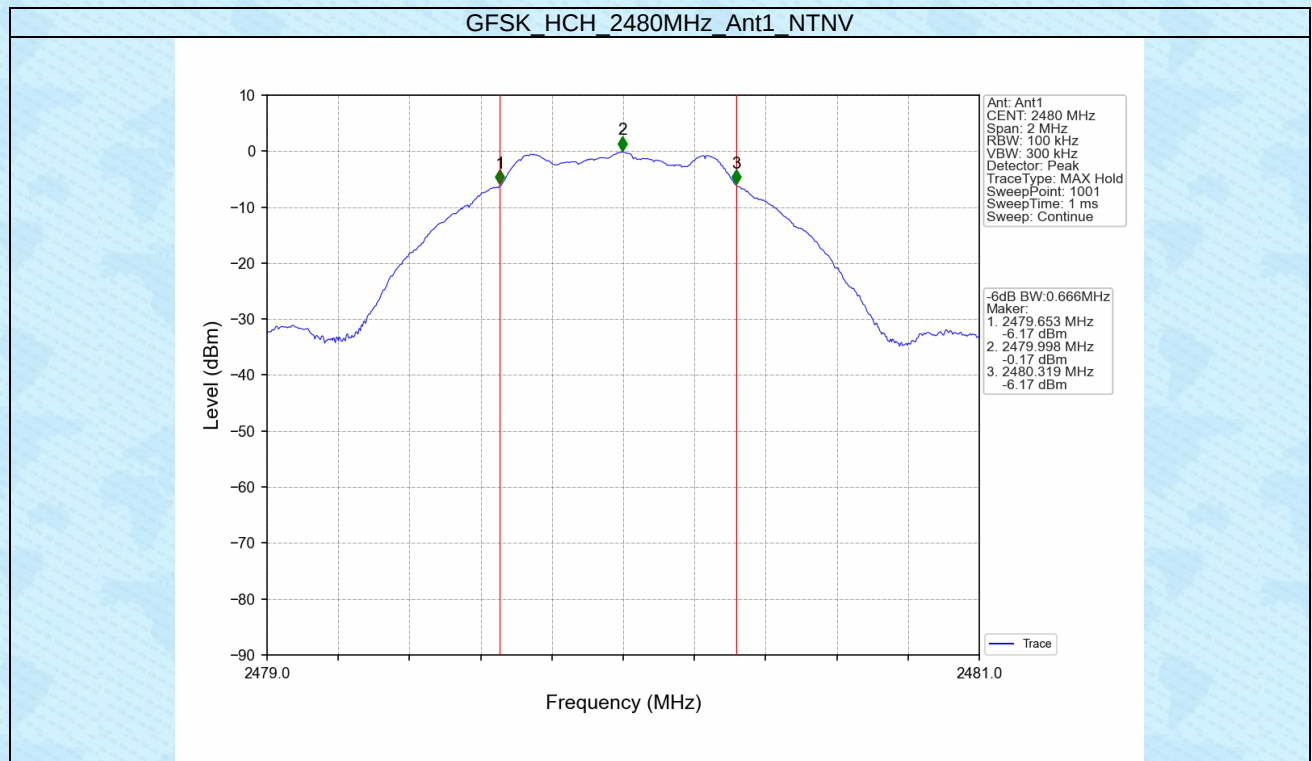
2.2 6dB BW

2.2.1 Test Result

Mode	TX Type	Frequency (MHz)	ANT	6dB Bandwidth (MHz)		Verdict
				Result	Limit	
GFSK	SISO	2402	1	0.664	≥ 0.5	Pass
		2440	1	0.663	≥ 0.5	Pass
		2480	1	0.666	≥ 0.5	Pass

2.2.2 Test Graph





3. Maximum Conducted Output Power

3.1 Test Result

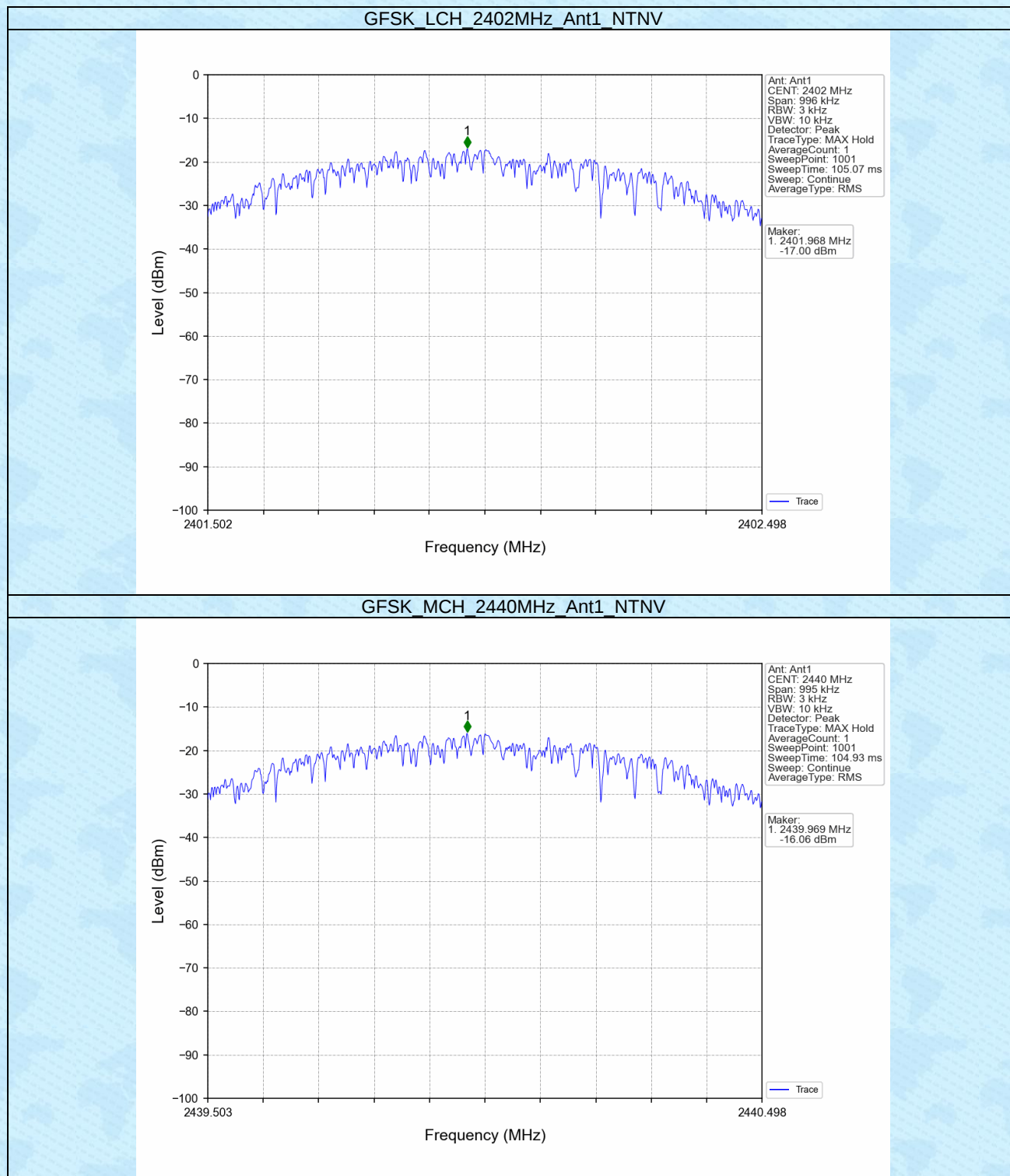
Mode	TX Type	Frequency (MHz)	Maximum Peak Conducted Output Power (dBm)		Verdict
			ANT1	Limit	
GFSK	SISO	2402	-1.13	<=30	Pass
		2440	-0.96	<=30	Pass
		2480	-0.83	<=30	Pass

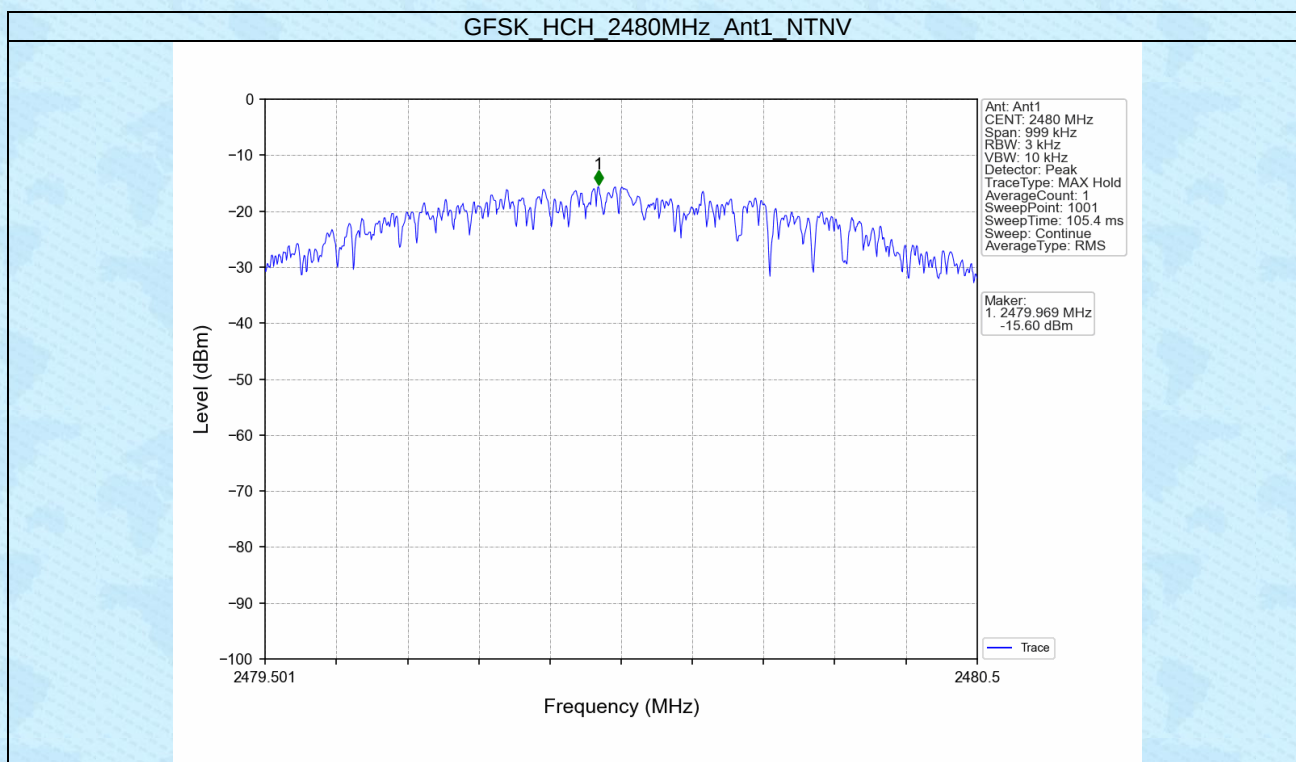
4. Maximum Power Spectral Density

4.1 Test Result

Mode	TX Type	Frequency (MHz)	Maximum PSD (dBm/3kHz)		Verdict
			ANT1	Limit	
GFSK	SISO	2402	-17.00	≤ 8	Pass
		2440	-16.06	≤ 8	Pass
		2480	-15.60	≤ 8	Pass

4.2 Test Graph





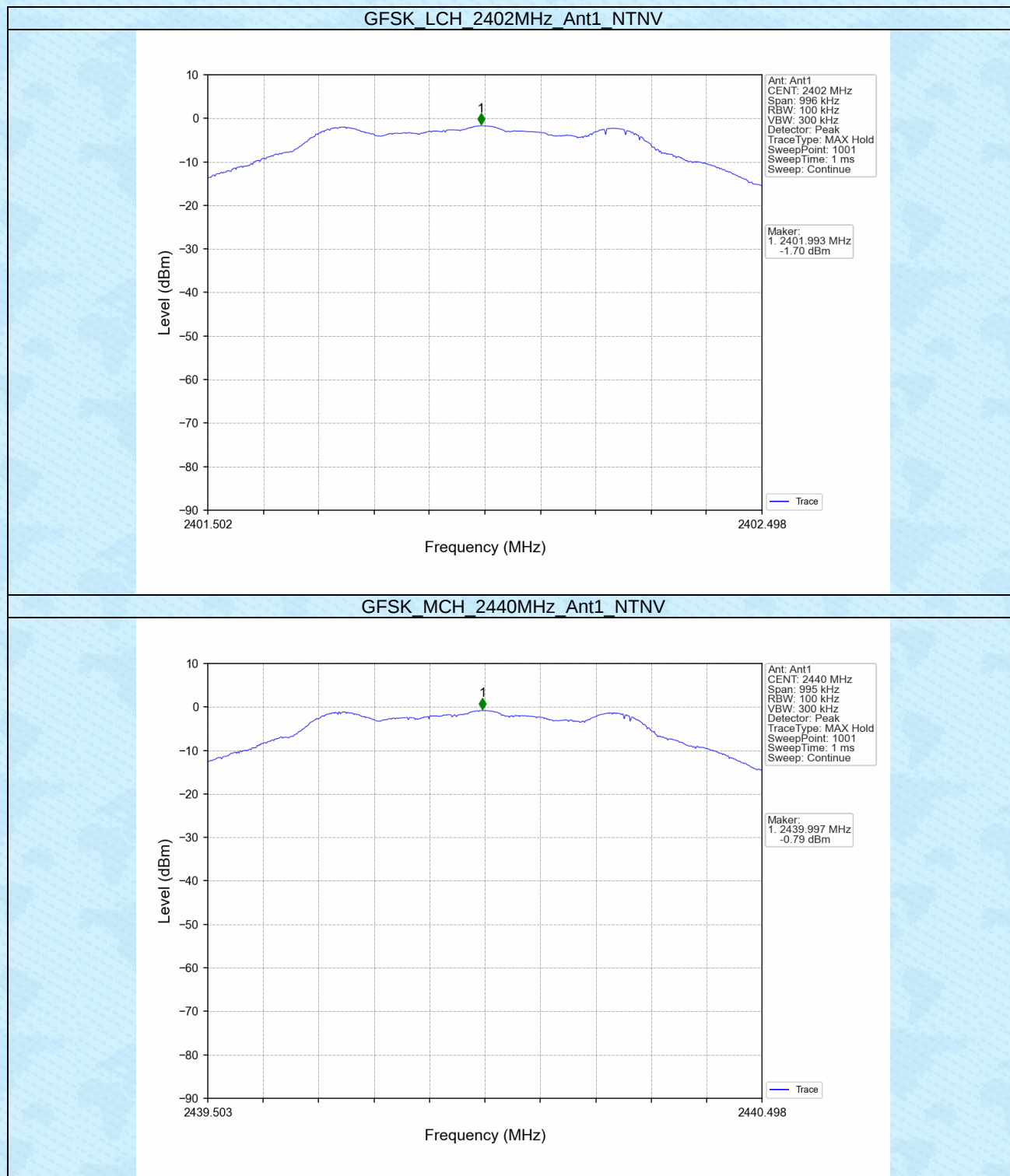
5. Unwanted Emissions In Non-restricted Frequency Bands

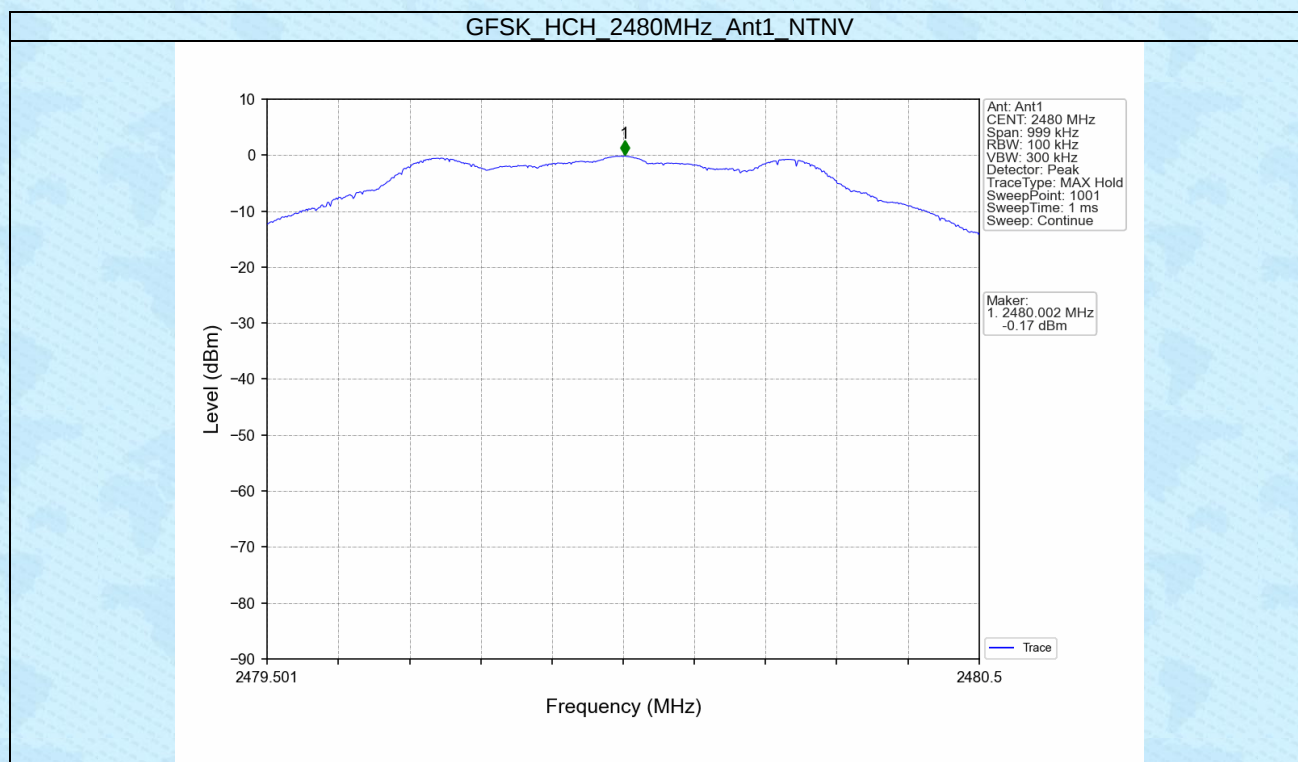
5.1 Ref

5.1.1 Test Result

Mode	TX Type	Frequency (MHz)	ANT	Level of Reference (dBm)
GFSK	SISO	2402	1	-1.70
		2440	1	-0.79
		2480	1	-0.17
Note1: Refer to FCC Part 15.247 (d) and ANSI C63.10-2013, the channel contains the maximum PSD level was used to establish the reference level.				

5.1.2 Test Graph



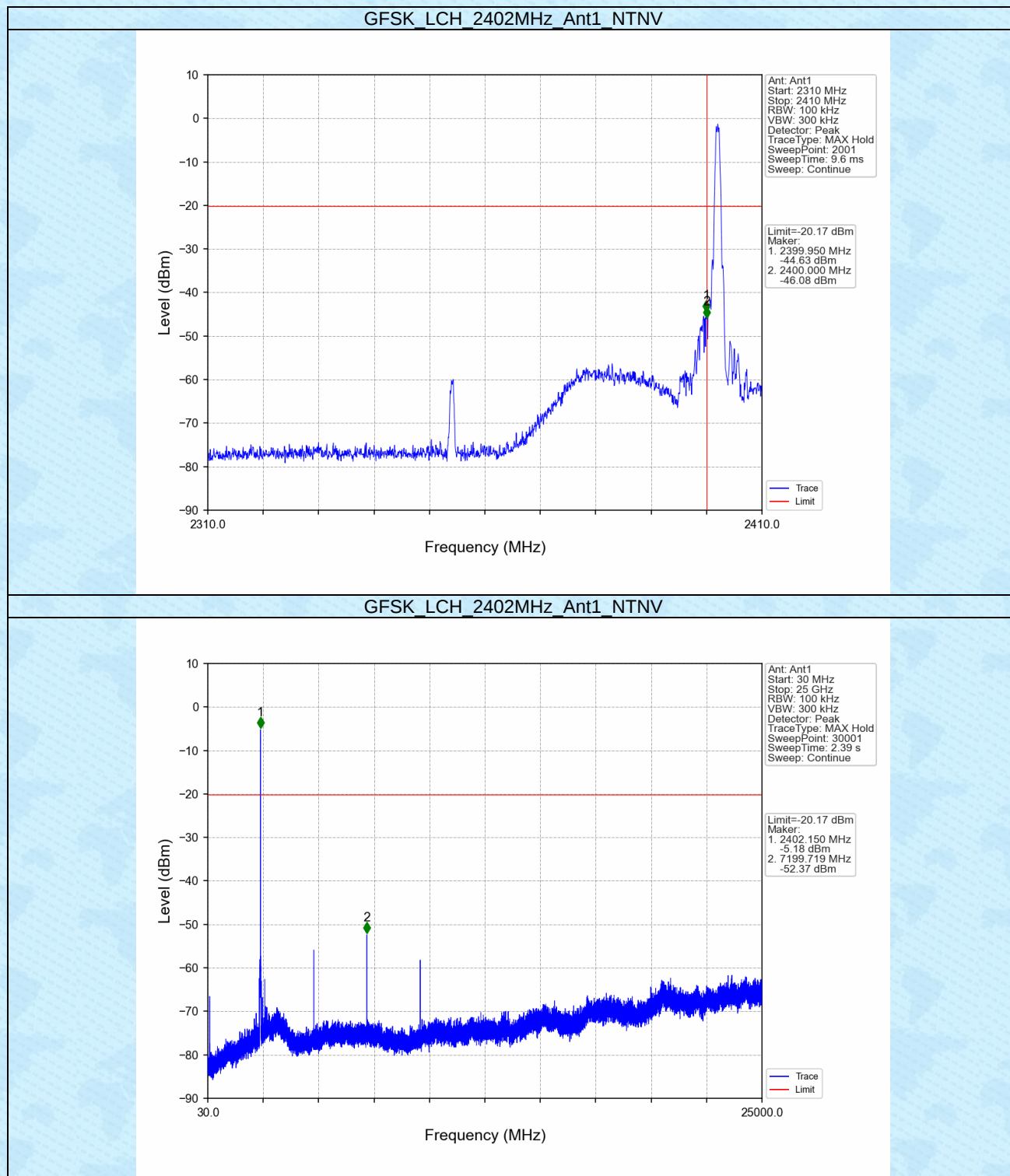


5.2 CSE

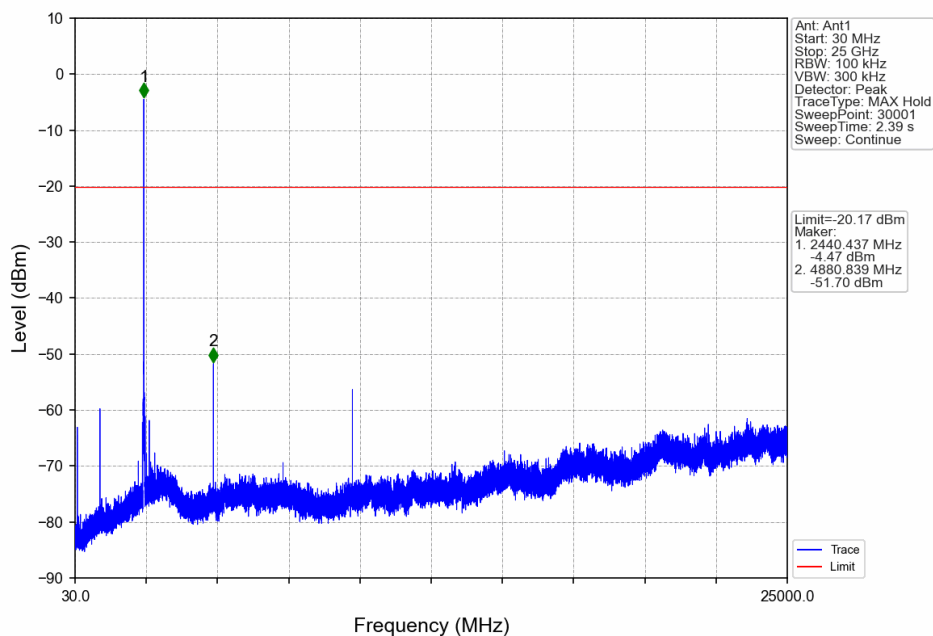
5.2.1 Test Result

Mode	TX Type	Frequency (MHz)	ANT	Level of Reference (dBm)	Limit (dBm)	Verdict
GFSK	SISO	2402	1	-0.17	-20.17	Pass
		2440	1	-0.17	-20.17	Pass
		2480	1	-0.17	-20.17	Pass
Note1: Refer to FCC Part 15.247 (d) and ANSI C63.10-2013, the channel contains the maximum PSD level was used to establish the reference level.						

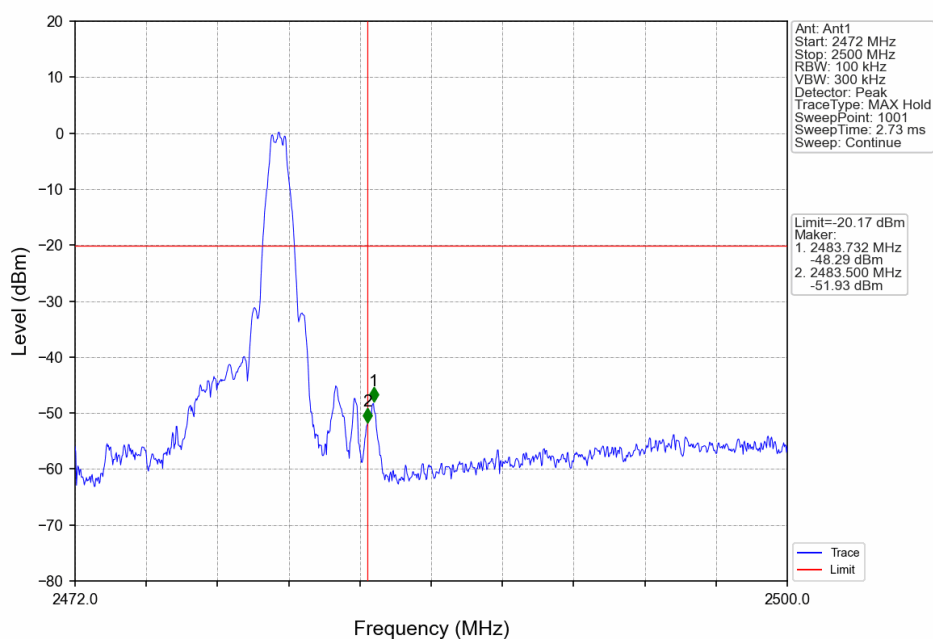
5.2.2 Test Graph

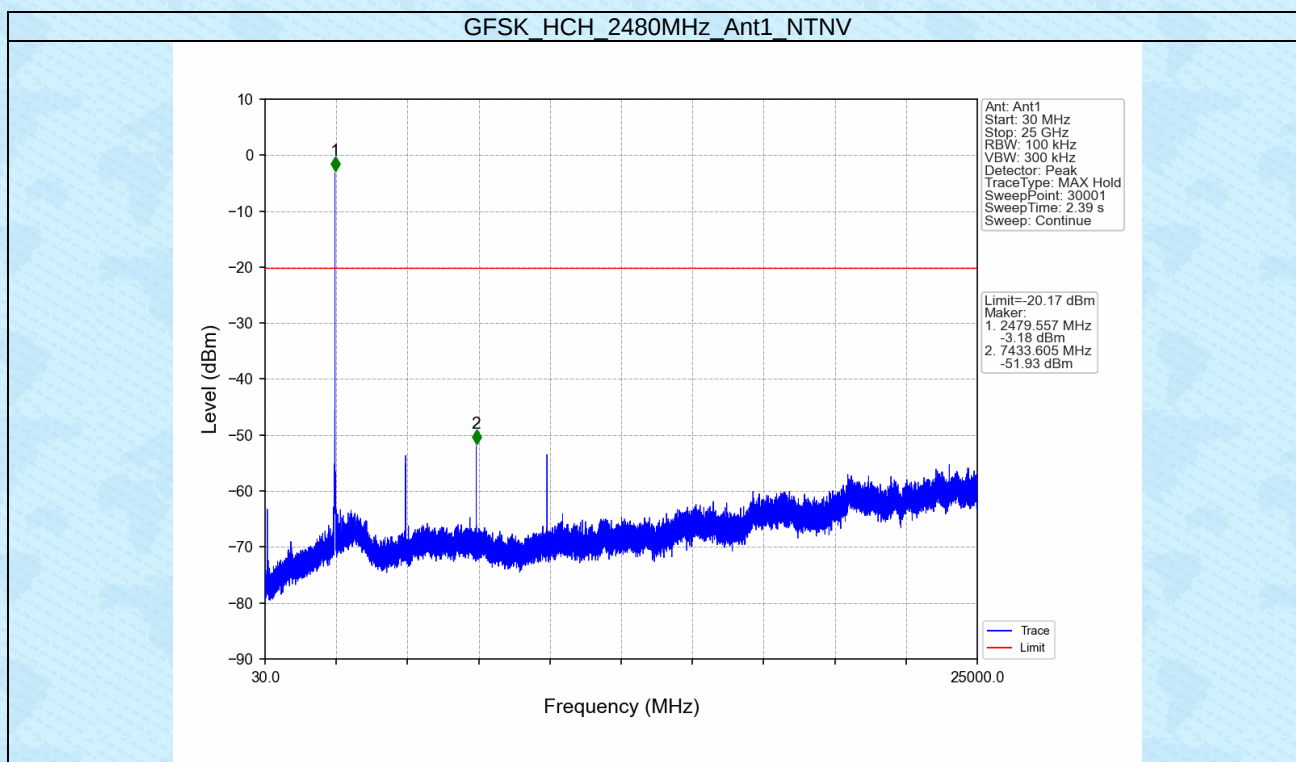


GFSK_MCH_2440MHz_Ant1_NTNV



GFSK_HCH_2480MHz_Ant1_NTNV





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