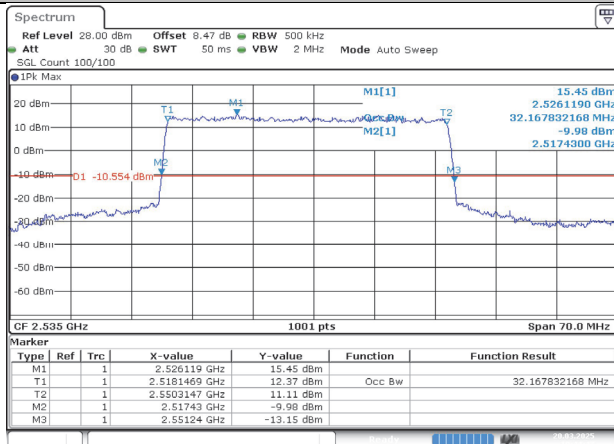


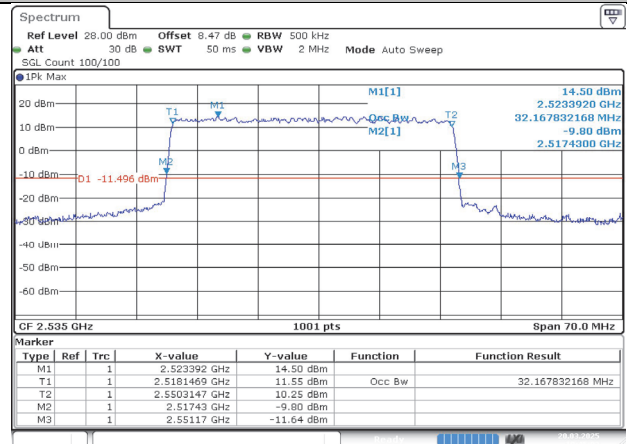
n7A / 15KHz / 35MHz

MCH / DFT-Pi2BPSK / Outer_Full



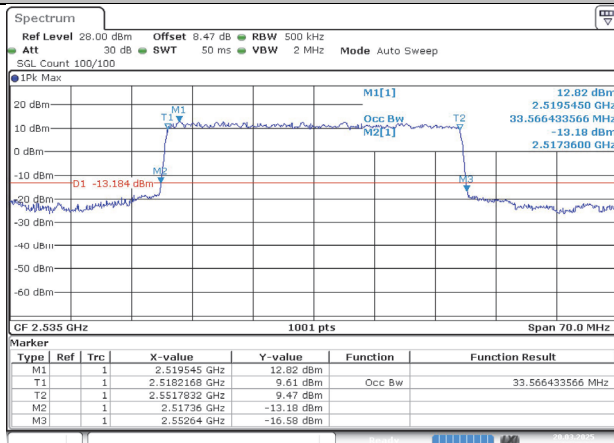
Date: 20 MAR 2025 10:44:47

MCH / DFT-QPSK / Outer_Full



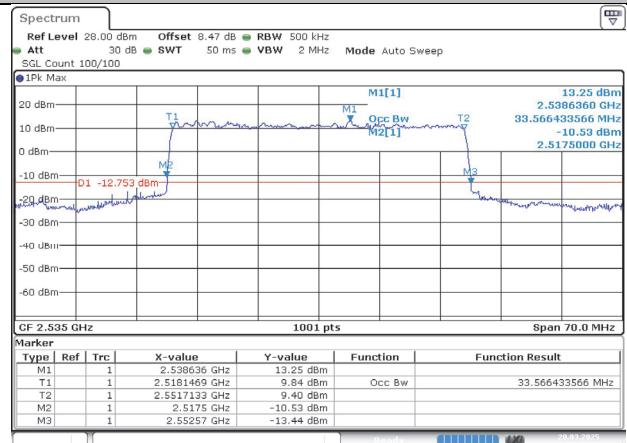
Date: 20 MAR 2025 10:45:03

MCH / CP-QPSK / Outer_Full



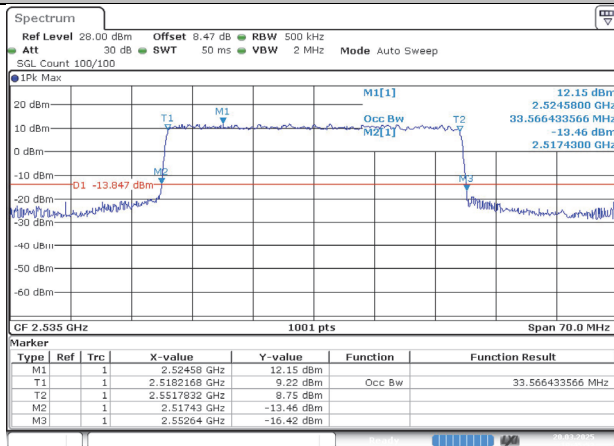
Date: 20 MAR 2025 10:45:38

MCH / CP-16QAM / Outer_Full



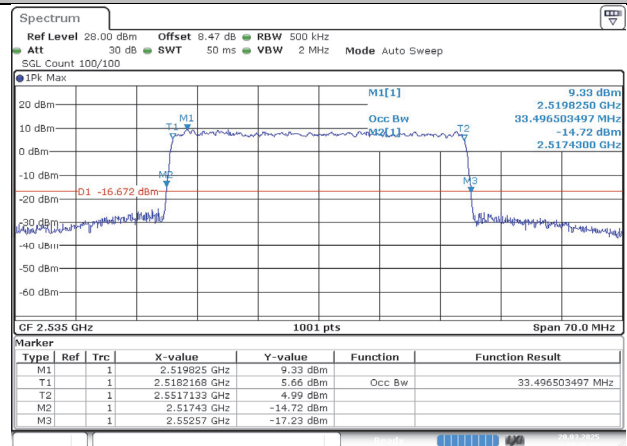
Date: 20 MAR 2025 10:45:46

MCH / CP-64QAM / Outer_Full



Date: 20 MAR 2025 10:45:53

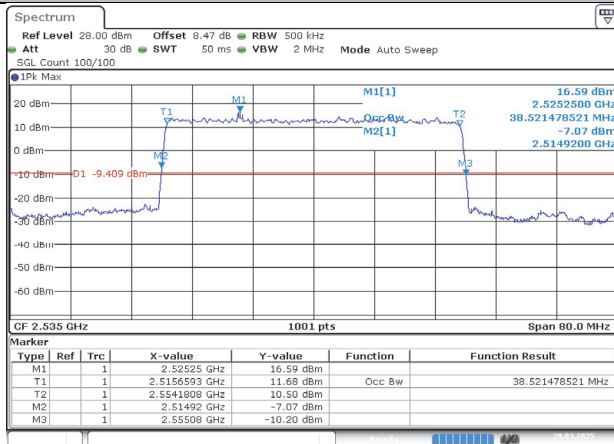
MCH / CP-256QAM / Outer_Full



Date: 20 MAR 2025 10:46:03

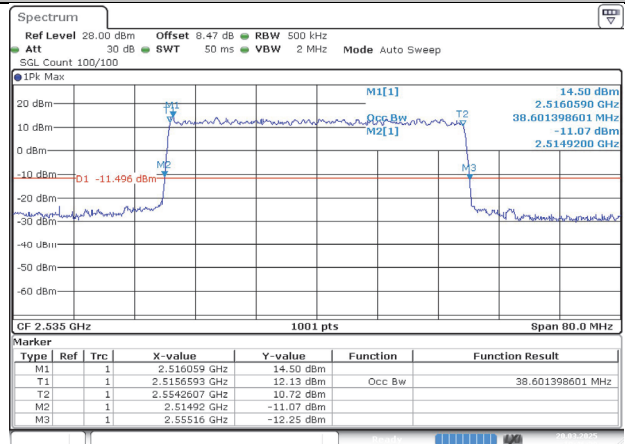
n7A / 15KHz / 40MHz

MCH / DFT-Pi2BPSK / Outer_Full



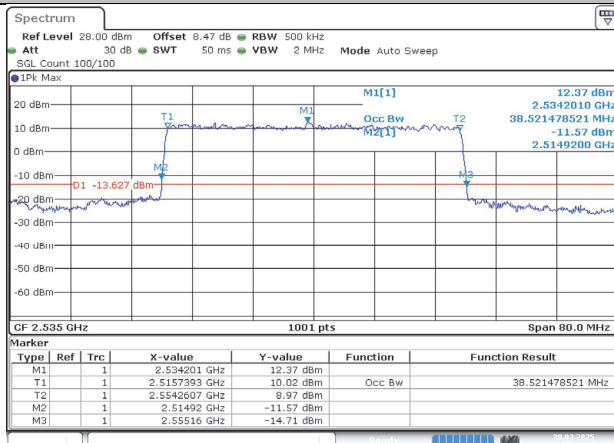
Date: 20 MAR 2025 10:46:23

MCH / DFT-QPSK / Outer_Full



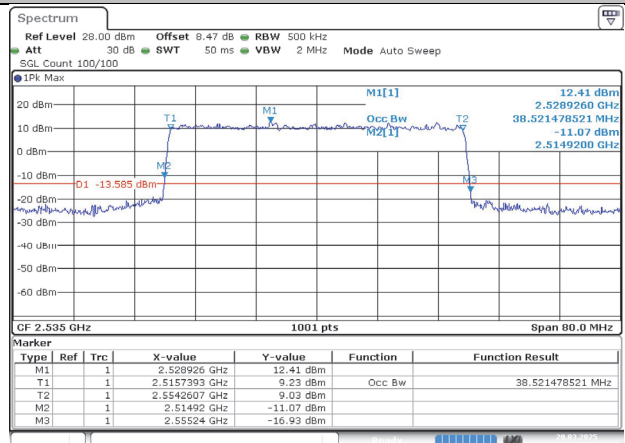
Date: 20 MAR 2025 10:46:39

MCH / CP-QPSK / Outer_Full



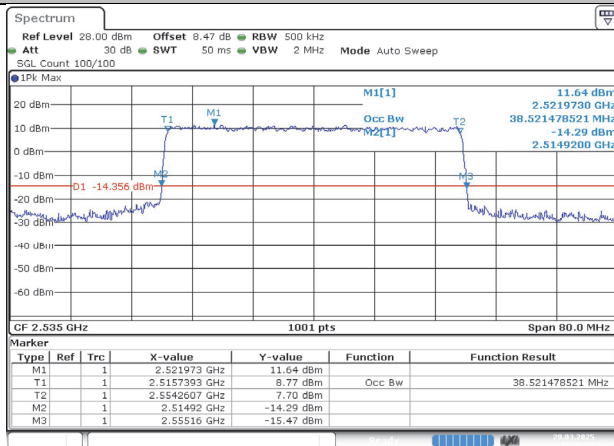
Date: 20 MAR 2025 10:47:14

MCH / CP-16QAM / Outer_Full



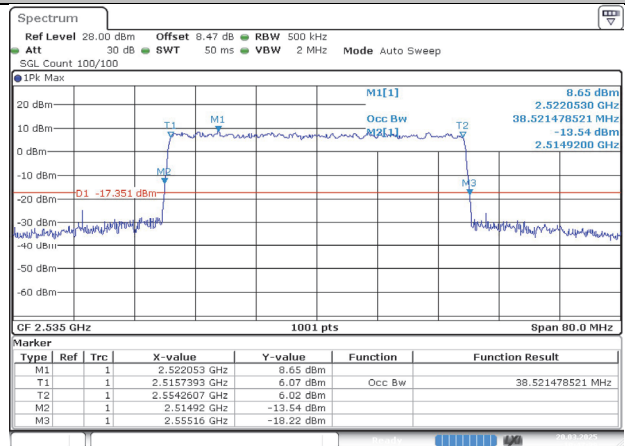
Date: 20 MAR 2025 10:47:22

MCH / CP-64QAM / Outer_Full



Date: 20 MAR 2025 10:47:29

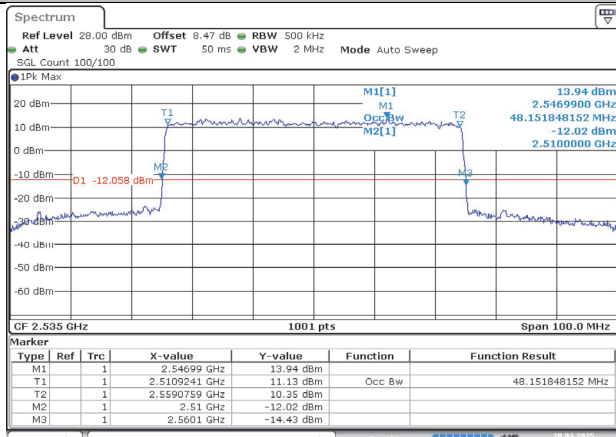
MCH / CP-256QAM / Outer_Full



Date: 20 MAR 2025 10:47:39

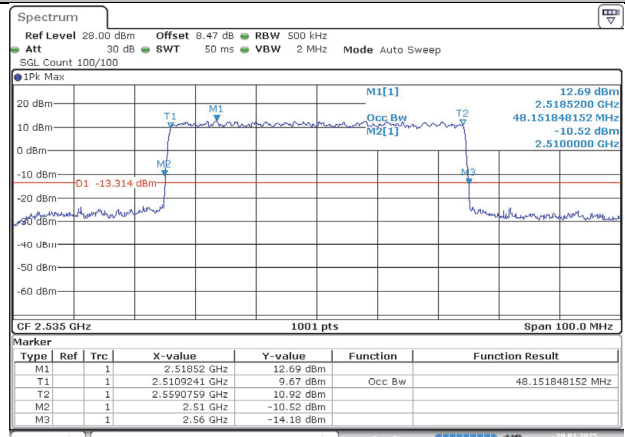
n7A / 15KHz / 50MHz

MCH / DFT-Pi2BPSK / Outer_Full



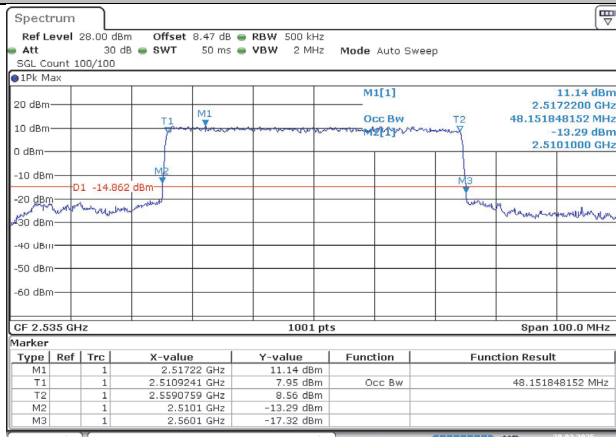
Date: 20 MAR 2025 10:47:59

MCH / DFT-QPSK / Outer_Full



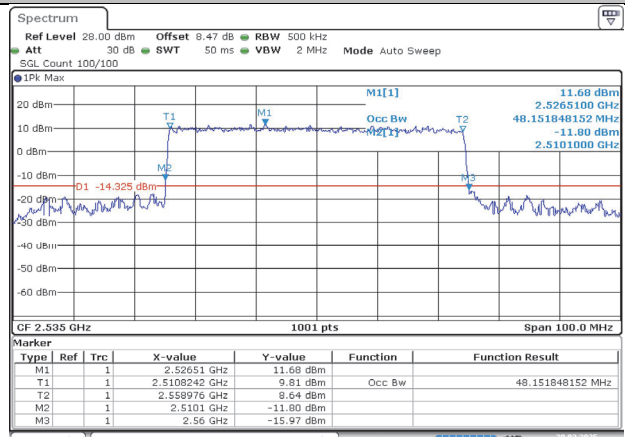
Date: 20 MAR 2025 10:48:21

MCH / CP-QPSK / Outer_Full



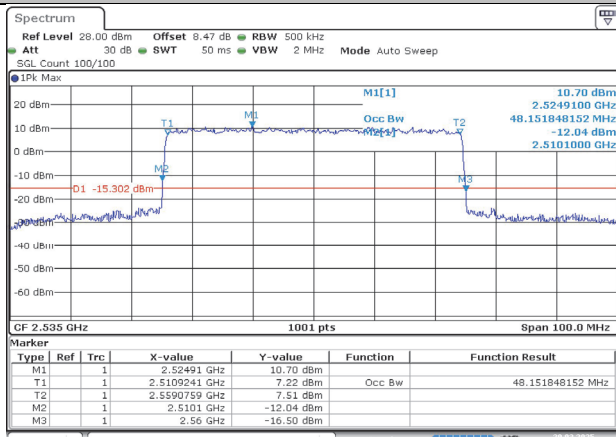
Date: 20 MAR 2025 10:48:56

MCH / CP-16QAM / Outer_Full



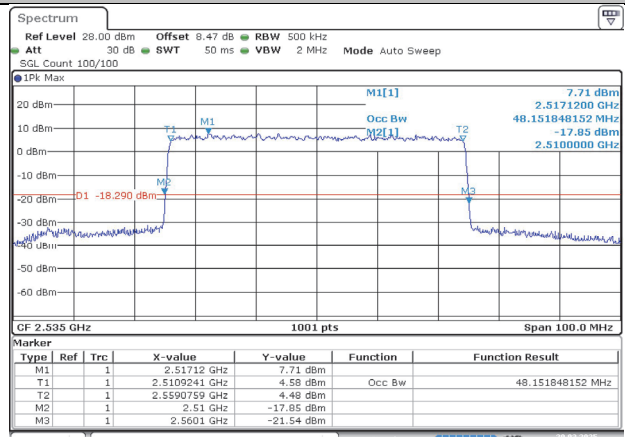
Date: 20 MAR 2025 10:49:04

MCH / CP-64QAM / Outer_Full



Date: 20 MAR 2025 10:49:11

MCH / CP-256QAM / Outer_Full



Date: 20 MAR 2025 10:49:21

5. Conducted Band Edges

5.1. Test Plots for SCS=15KHz

