

FUNCTIONAL DESCRIPTION

The CFS8DL6128RF-2 is identical in operation to the CFS8DL6128RF. The need to re-layout the board was precipitated by the fact that the previous receiver chip is now obsolete. In Receive, RF enters the DIVERSITY ANTENNAS (ANT1 & ANT2) the diversity antennas are selected by ANTENNA SELECTOR DIODES (CR4 & CR5) FILTER (FL1) performs a band-pass function and the TX/ RX SWITCH (SW2) selects either the transmitter or the receiver. LNA (U14) improves the receivers sensitivity. RECEIVER CHIP (U5) performs all basic receiver functions including a PLL L.O. whose FDE is CRYSTAL (YL3). I.F. Band-passing is performed by FILTER (FL2) base-band signal processing (raw data) is done by U1 & U4 etc., The transmitter is a PLL chip (U15) etc., whose FDE is YL4. The output power level is attained by PA (U6) etc., All of these functions are controlled by MICROPROCESSOR (U2). The second microprocessor (U9) handles all other functions such as communicating with the keypad and LCD display.