



Shenzhen CTL Testing Technology Co., Ltd.

Tel: +86-755-89486194 E-mail: ctl@ctl-lab.com

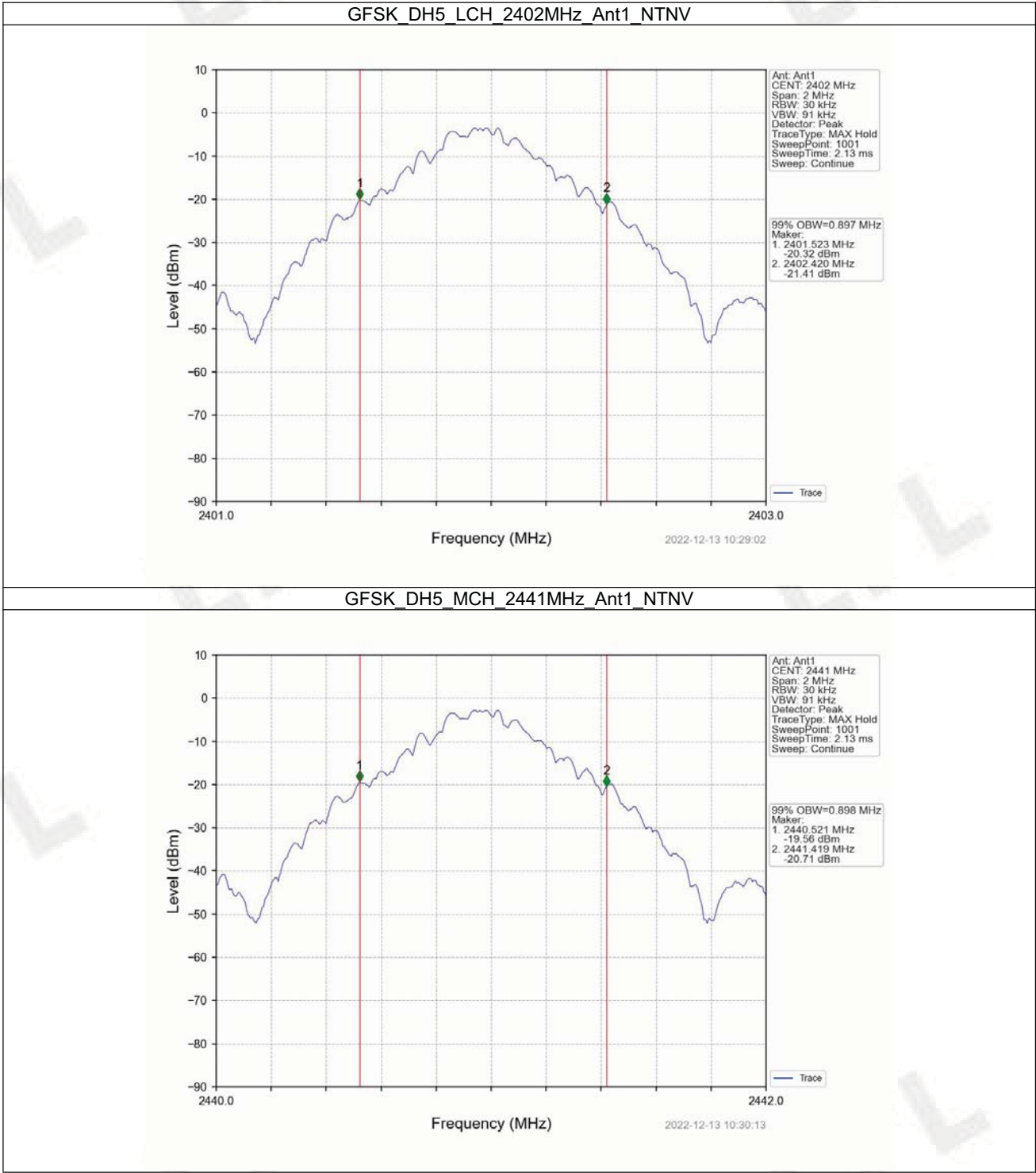
1. Bandwidth

1.1 OBW

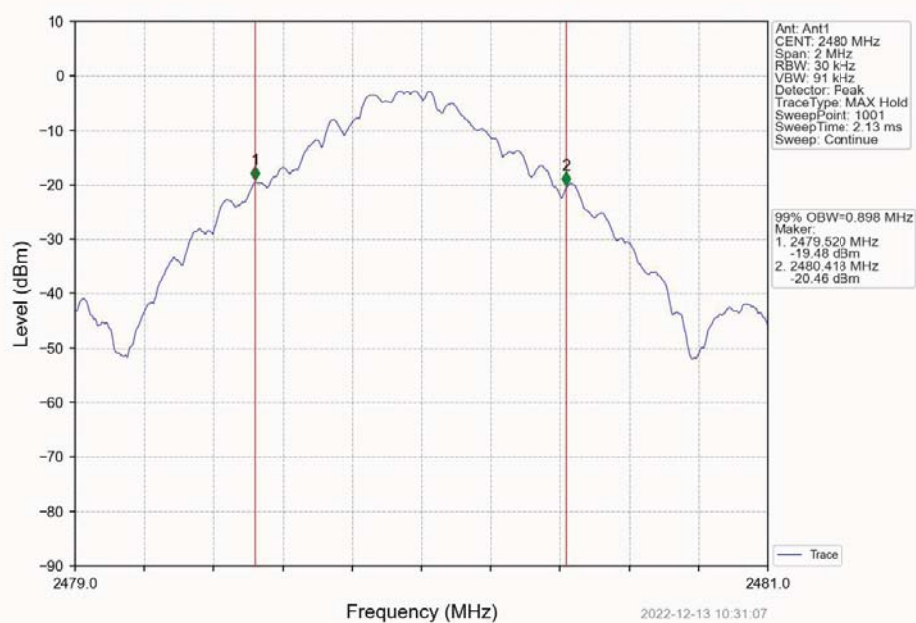
1.1.1 Test Result

Mode	TX Type	Frequency (MHz)	Packet Type	ANT	99% Occupied Bandwidth (MHz)	Verdict
					Result	
GFSK	SISO	2402	DH5	1	0.897	Pass
		2441	DH5	1	0.898	Pass
		2480	DH5	1	0.898	Pass
Pi/4DQPSK	SISO	2402	2DH5	1	1.201	Pass
		2441	2DH5	1	1.201	Pass
		2480	2DH5	1	1.200	Pass
8DPSK	SISO	2402	3DH5	1	1.206	Pass
		2441	3DH5	1	1.207	Pass
		2480	3DH5	1	1.207	Pass

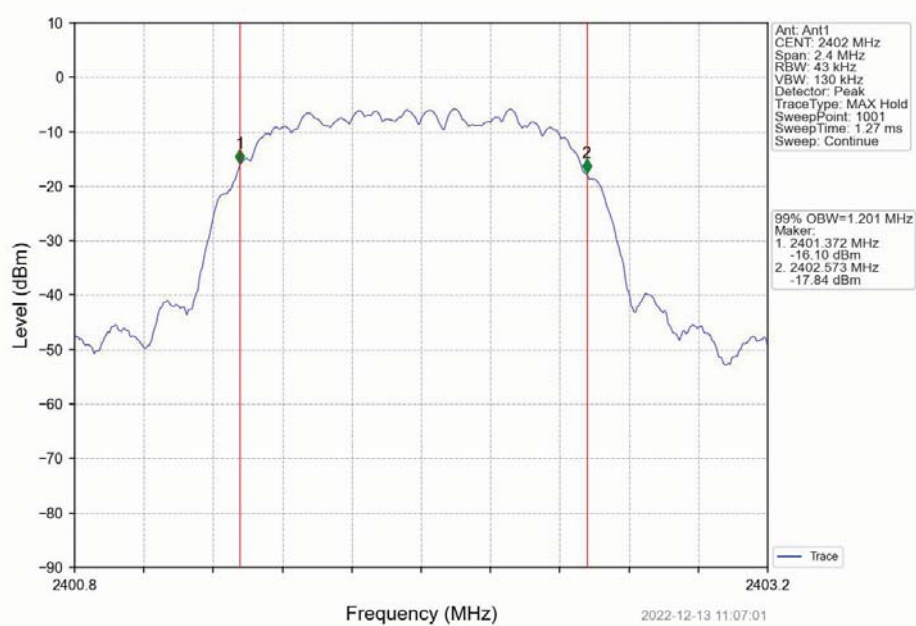
1.1.2 Test Graph



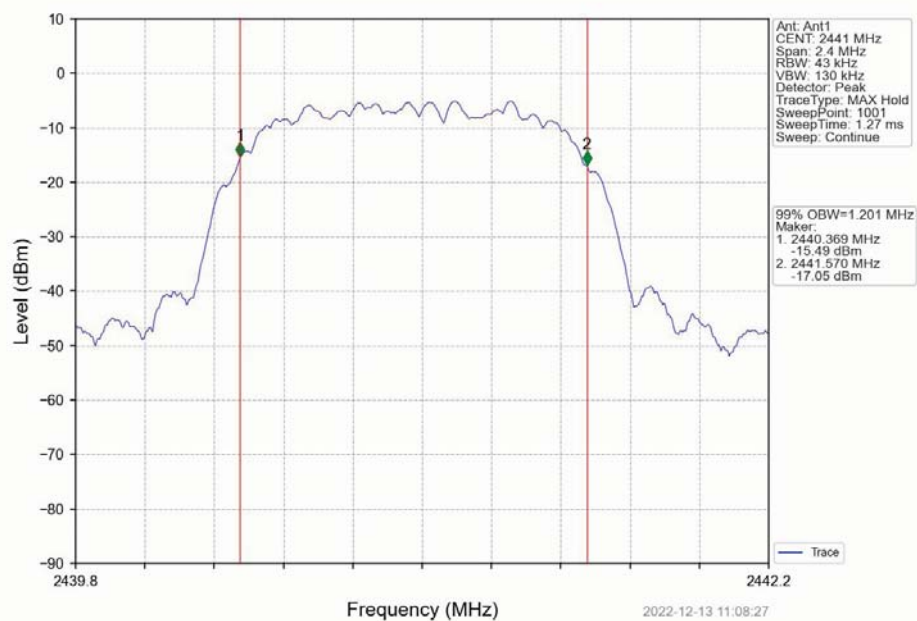
GFSK_DH5_HCH_2480MHz_Ant1_NTNV



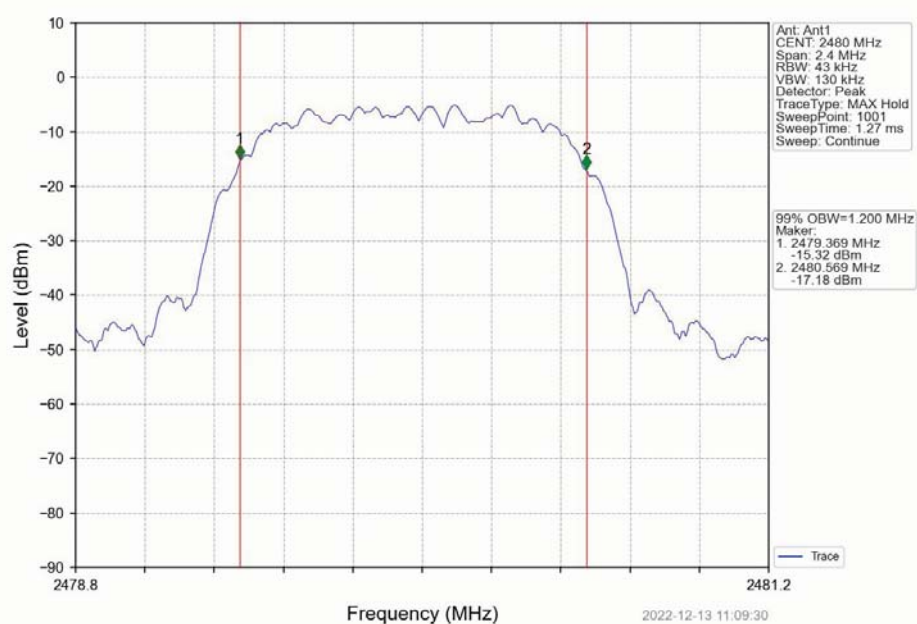
Pi/4DQPSK_2DH5_LCH_2402MHz_Ant1_NTNV



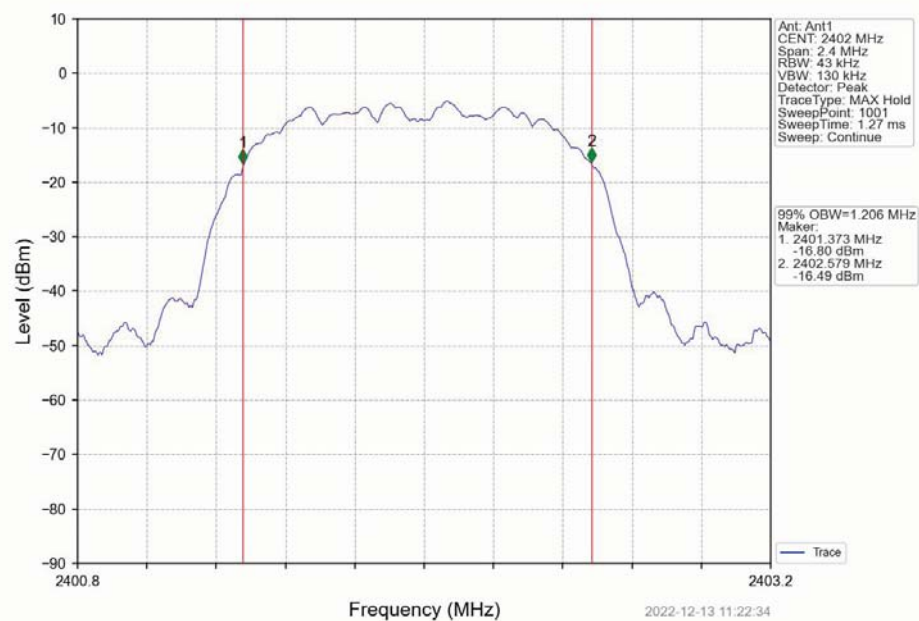
Pi/4DQPSK_2DH5_MCH_2441MHz_Ant1_NTNV



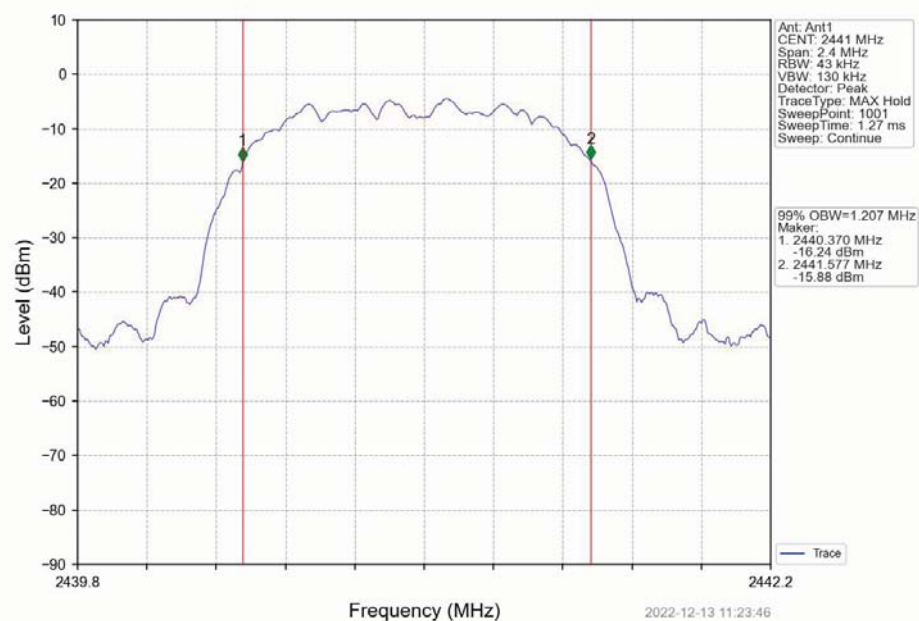
Pi/4DQPSK_2DH5_HCH_2480MHz_Ant1_NTNV

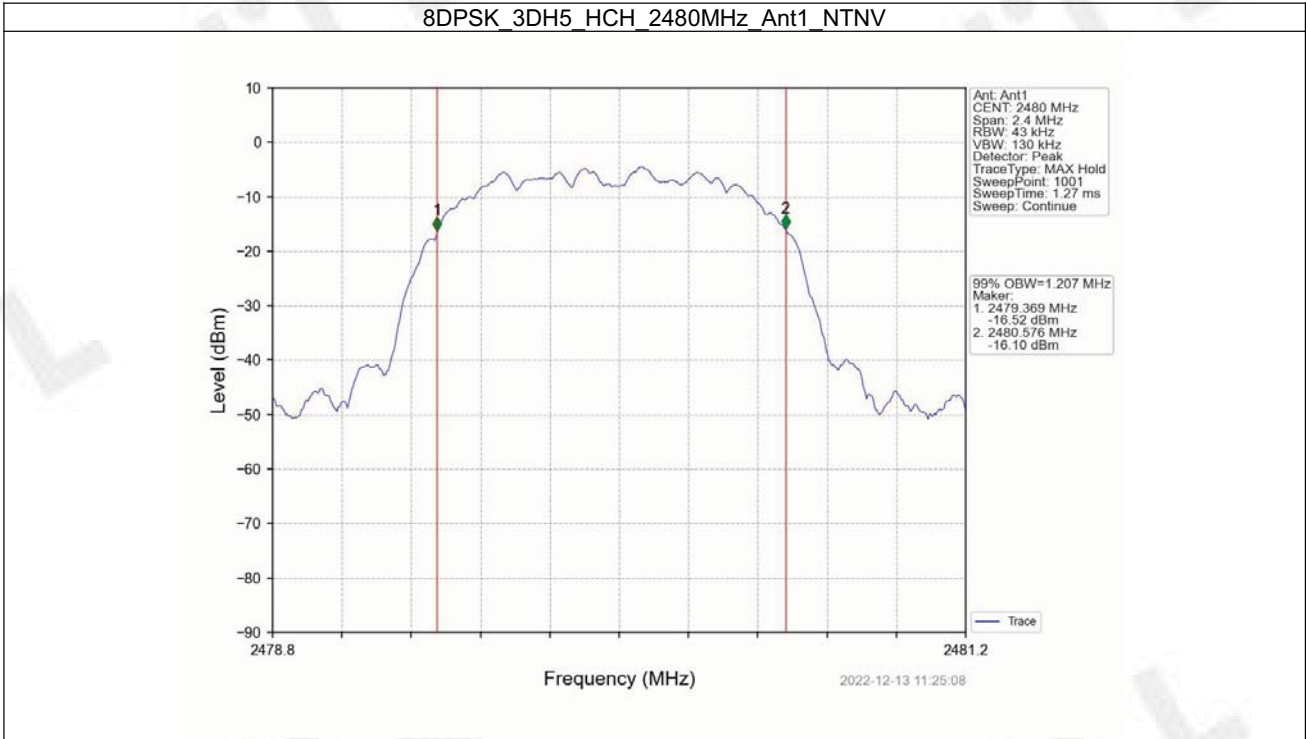


8DPSK_3DH5_LCH_2402MHz_Ant1_NTNV



8DPSK_3DH5_MCH_2441MHz_Ant1_NTNV



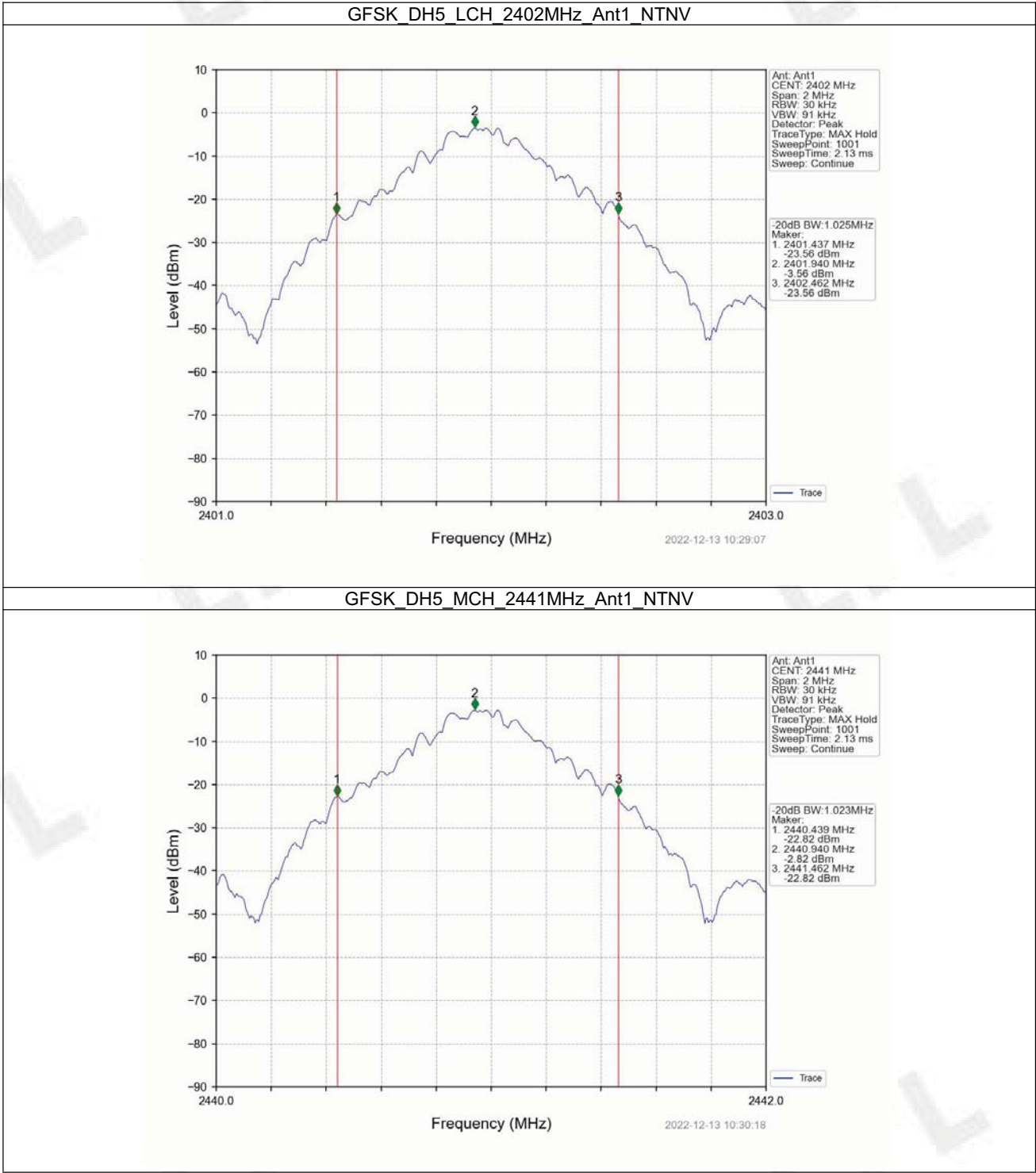


1.2 20dB BW

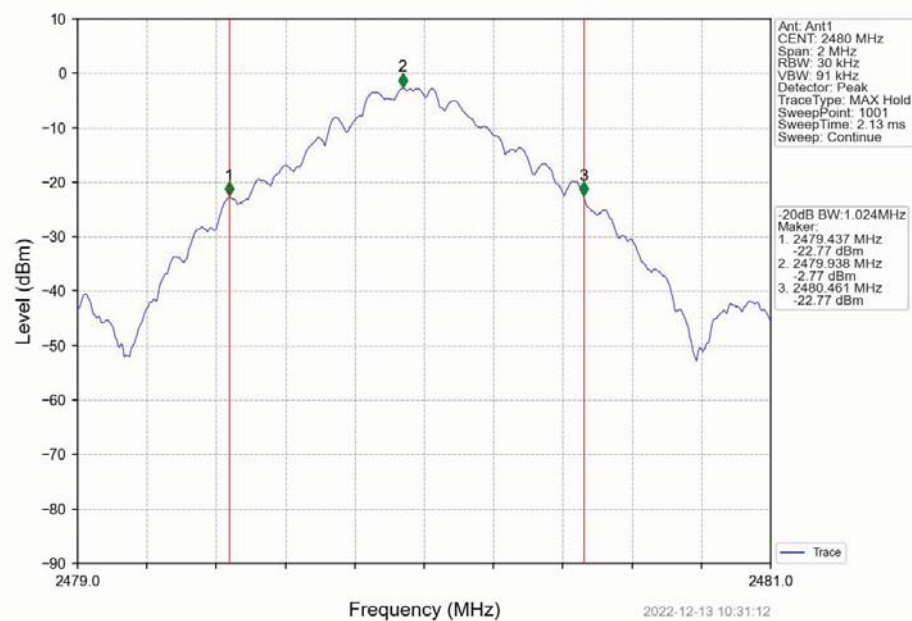
1.2.1 Test Result

Mode	TX Type	Frequency (MHz)	Packet Type	ANT	20dB Bandwidth (MHz)	Verdict
					Result	
GFSK	SISO	2402	DH5	1	1.025	Pass
		2441	DH5	1	1.023	Pass
		2480	DH5	1	1.024	Pass
Pi/4DQPSK	SISO	2402	2DH5	1	1.380	Pass
		2441	2DH5	1	1.379	Pass
		2480	2DH5	1	1.379	Pass
8DPSK	SISO	2402	3DH5	1	1.357	Pass
		2441	3DH5	1	1.361	Pass
		2480	3DH5	1	1.358	Pass

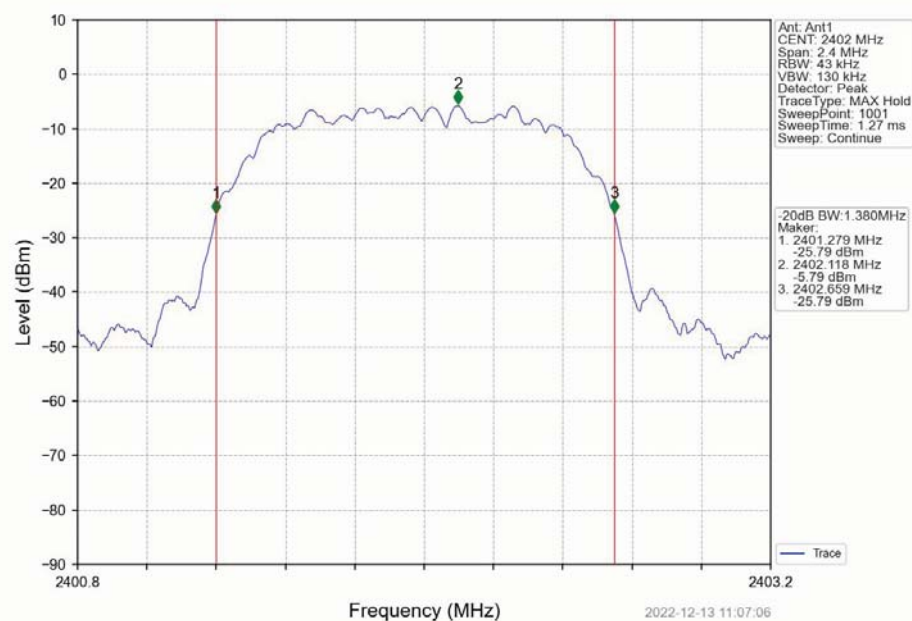
1.2.2 Test Graph



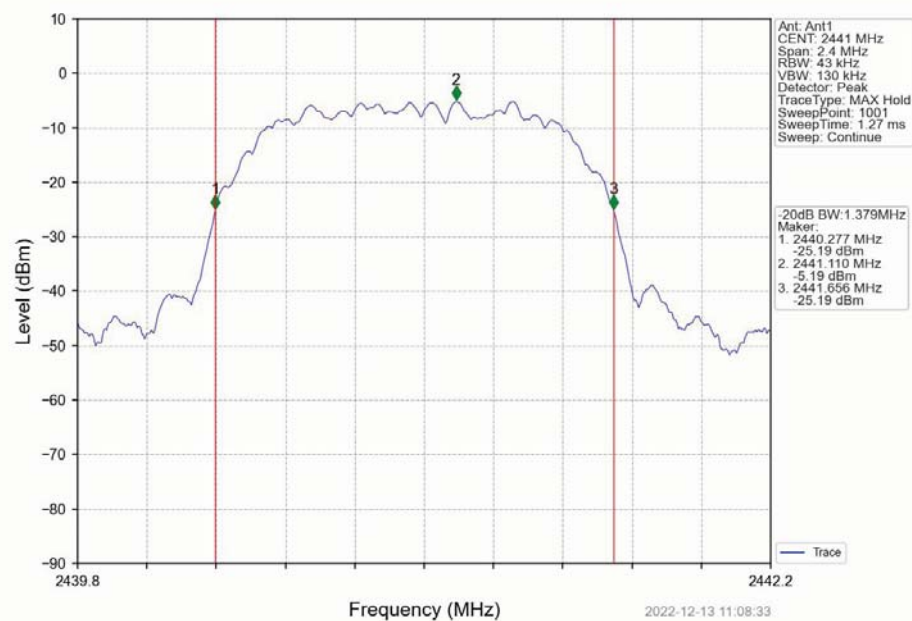
GFSK_DH5_HCH_2480MHz_Ant1_NTNV



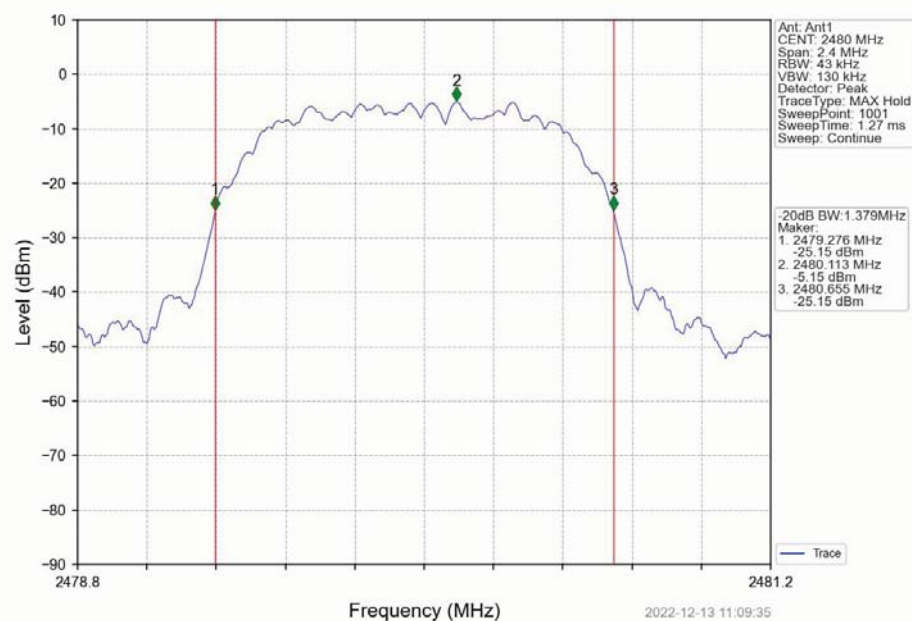
Pi/4DQPSK_2DH5_LCH_2402MHz_Ant1_NTNV



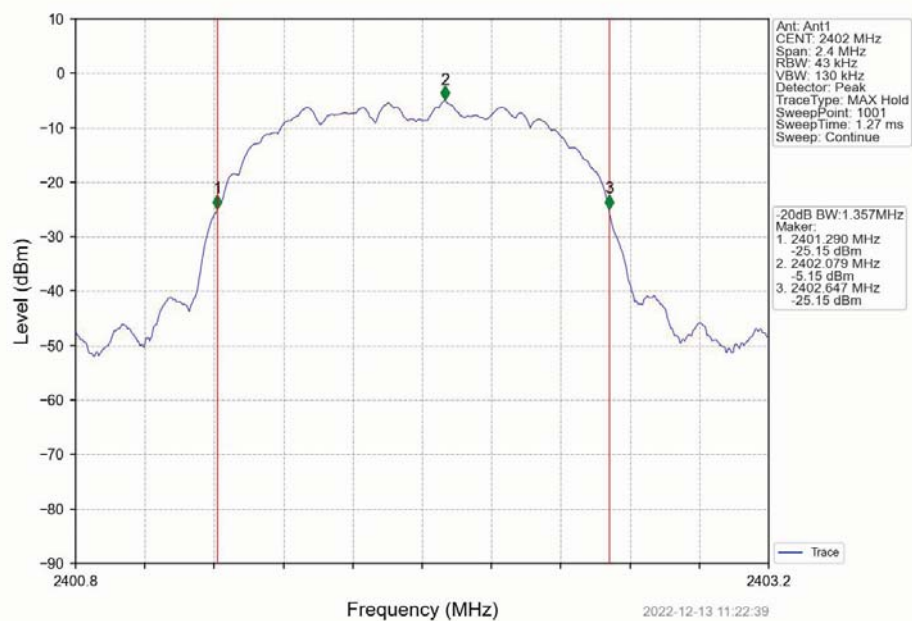
Pi/4DQPSK_2DH5_MCH_2441MHz_Ant1_NTNV



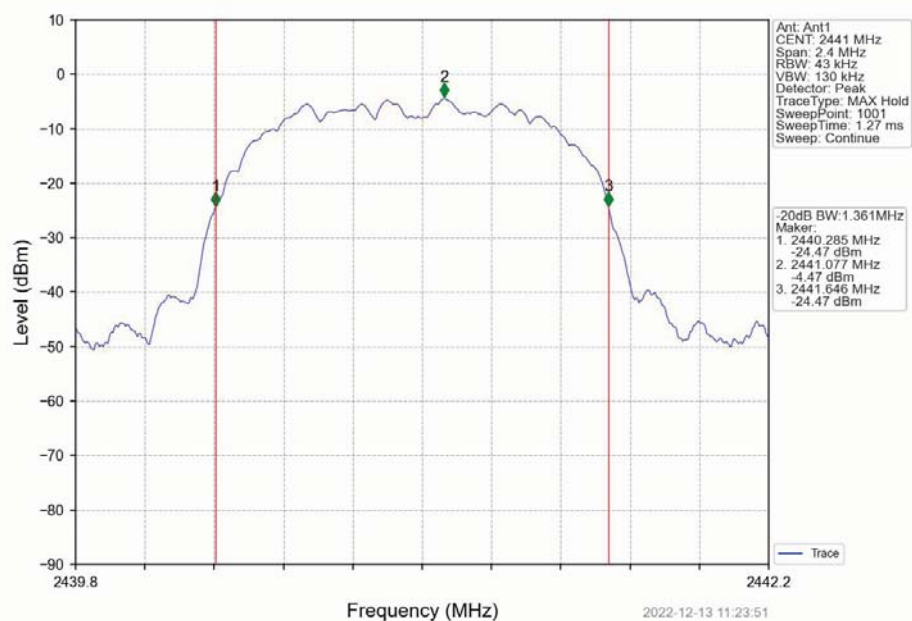
Pi/4DQPSK_2DH5_HCH_2480MHz_Ant1_NTNV

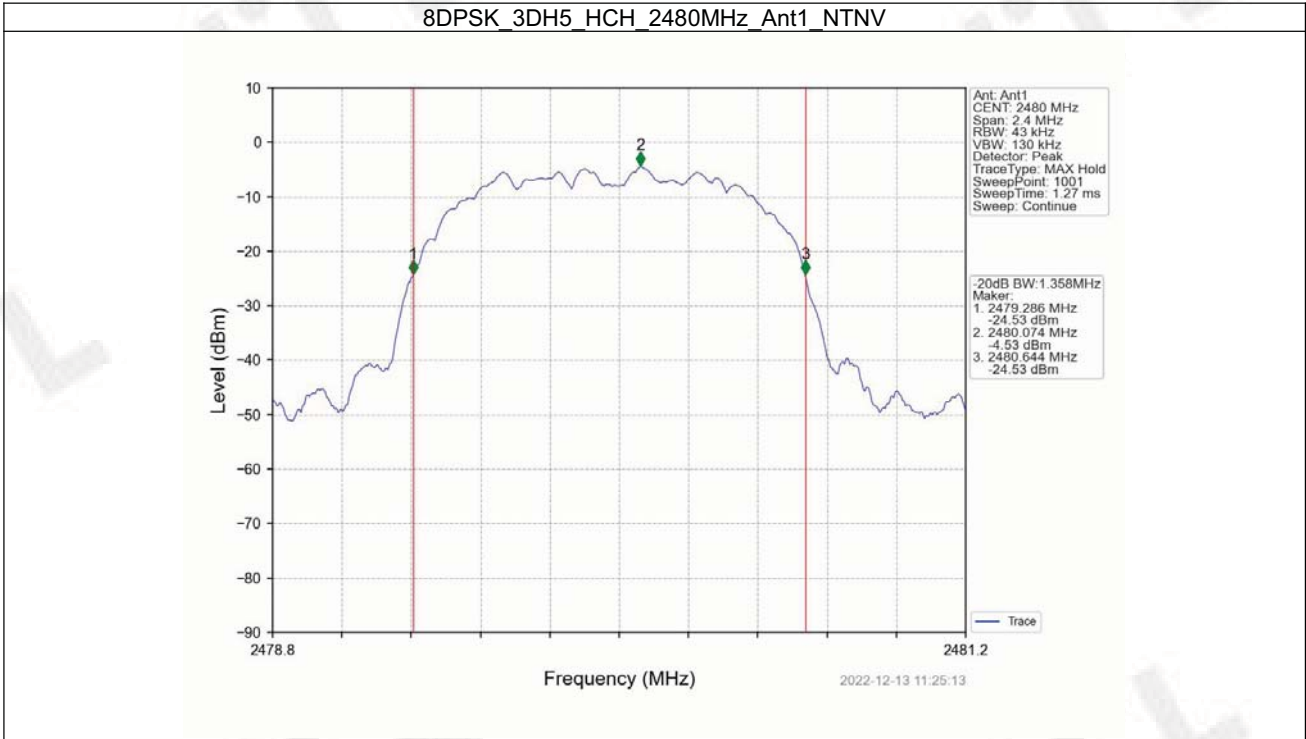


8DPSK_3DH5_LCH_2402MHz_Ant1_NTNV



8DPSK_3DH5_MCH_2441MHz_Ant1_NTNV





2. Maximum Conducted Output Power

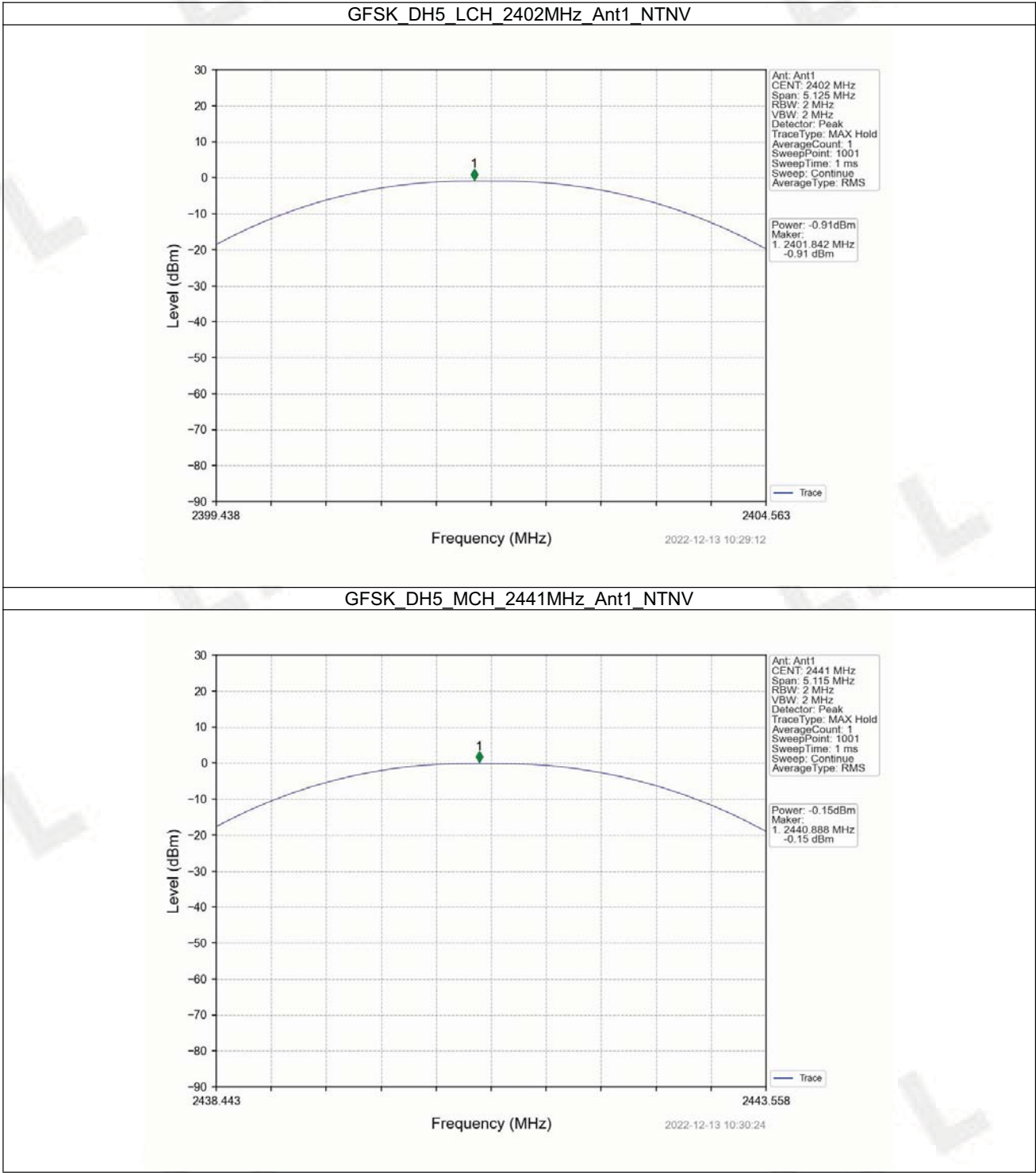
2.1 Power

2.1.1 Test Result

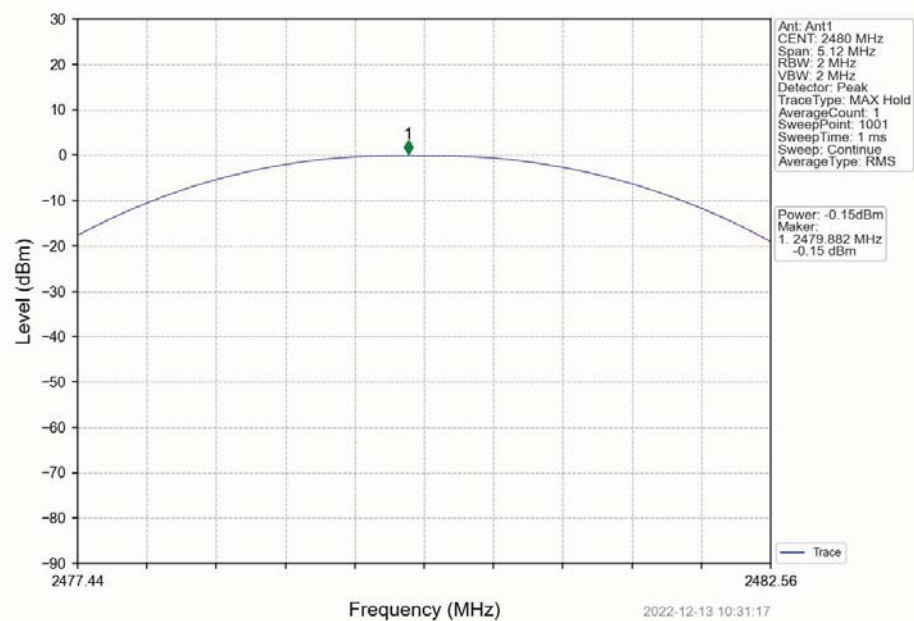
Mode	TX Type	Frequency (MHz)	Packet Type	Maximum Peak Conducted Output Power (dBm)		Verdict
				ANT1	Limit	
GFSK	SISO	2402	DH5	-0.91	<=20.97	Pass
		2441	DH5	-0.15	<=20.97	Pass
		2480	DH5	-0.15	<=20.97	Pass
Pi/4DQPSK	SISO	2402	2DH5	1.38	<=20.97	Pass
		2441	2DH5	2.07	<=20.97	Pass
		2480	2DH5	1.98	<=20.97	Pass
8DPSK	SISO	2402	3DH5	1.76	<=20.97	Pass
		2441	3DH5	2.52	<=20.97	Pass
		2480	3DH5	2.44	<=20.97	Pass

Note1: Antenna Gain: Ant1: 4.34dBi;

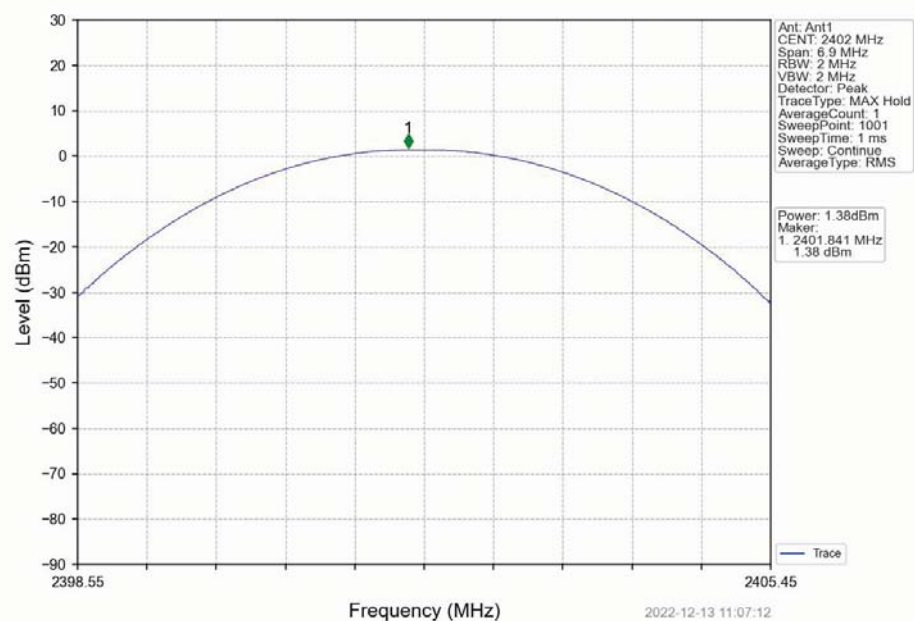
2.1.2 Test Graph



GFSK_DH5_HCH_2480MHz_Ant1_NTNV



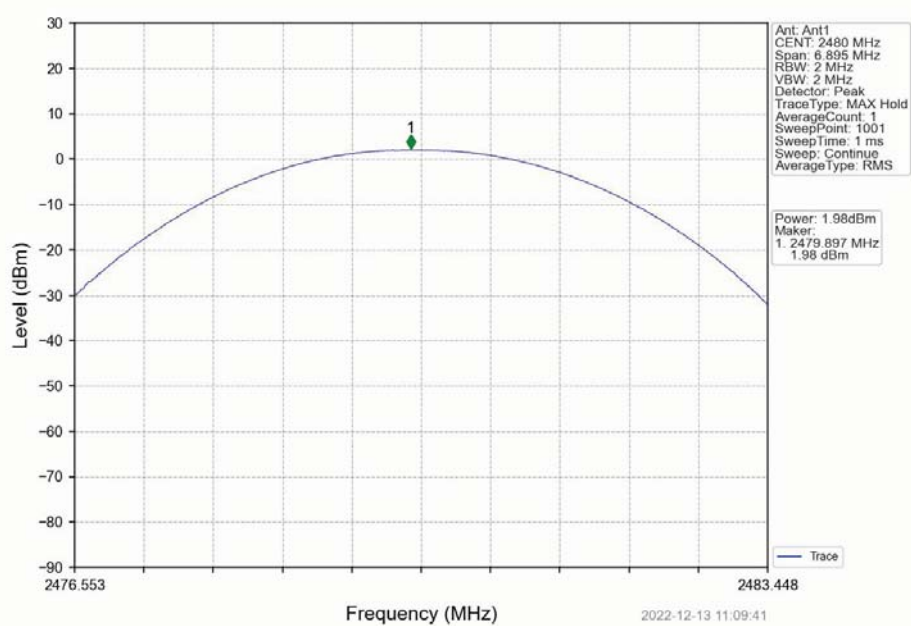
Pi/4DQPSK_2DH5_LCH_2402MHz_Ant1_NTNV



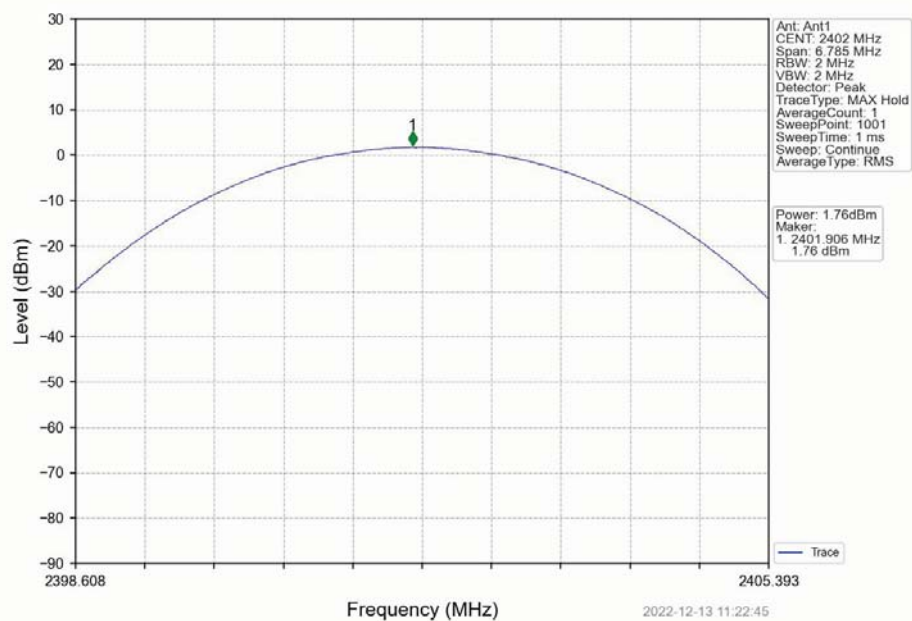
Pi/4DQPSK_2DH5_MCH_2441MHz_Ant1_NTNV



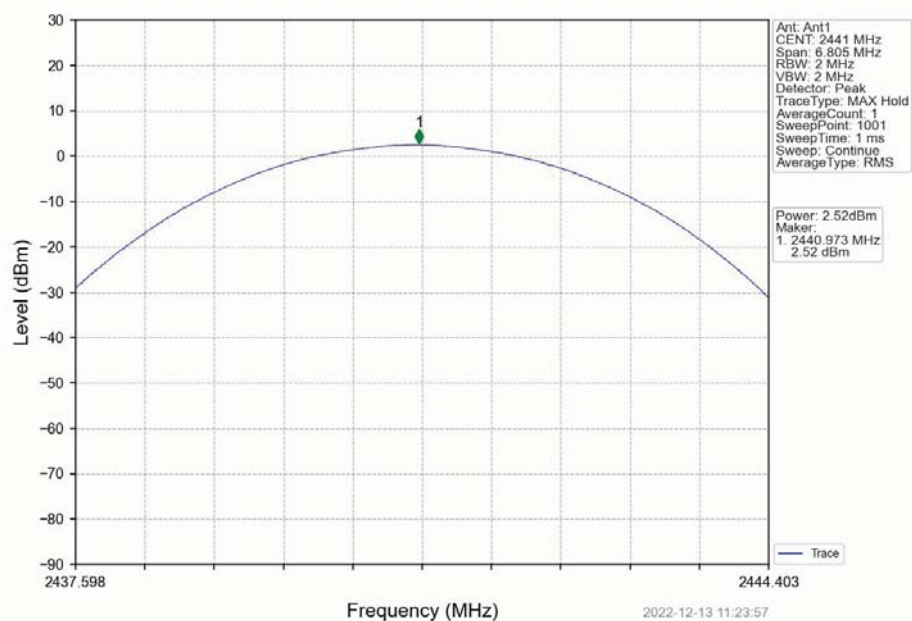
Pi/4DQPSK_2DH5_HCH_2480MHz_Ant1_NTNV

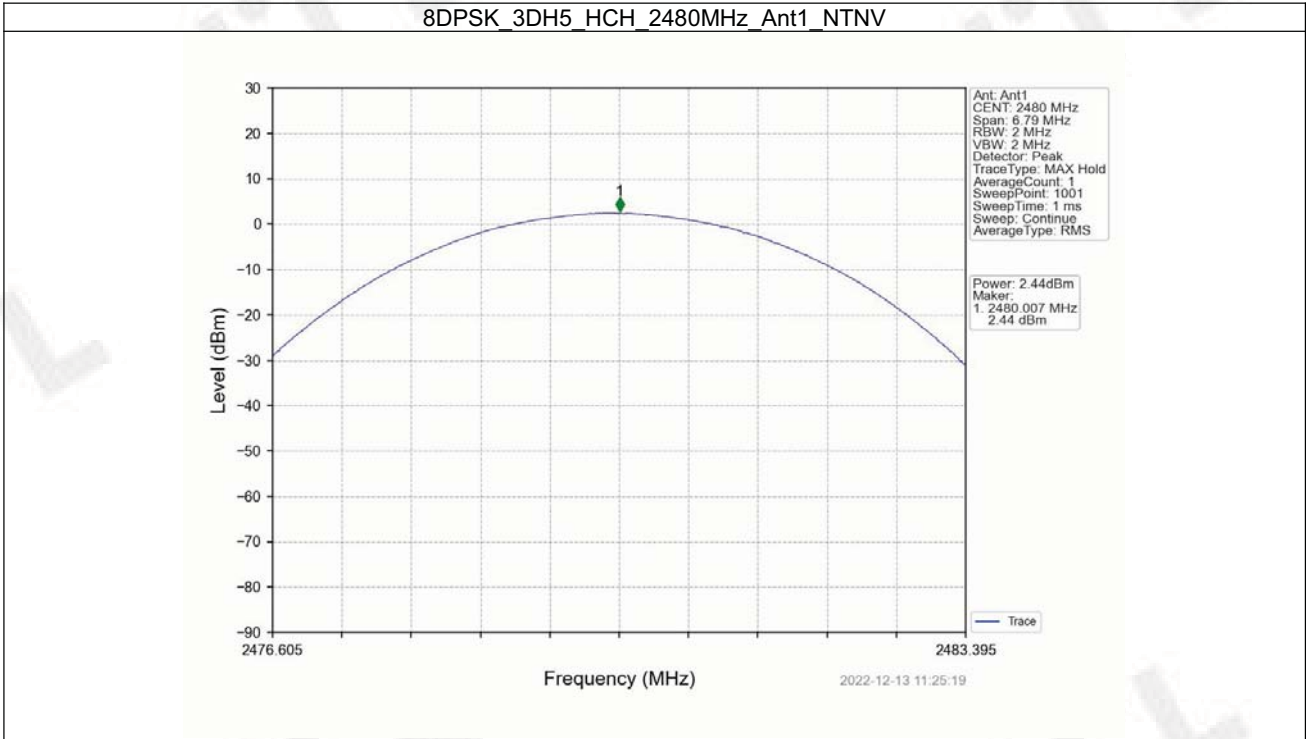


8DPSK_3DH5_LCH_2402MHz_Ant1_NTNV



8DPSK_3DH5_MCH_2441MHz_Ant1_NTNV





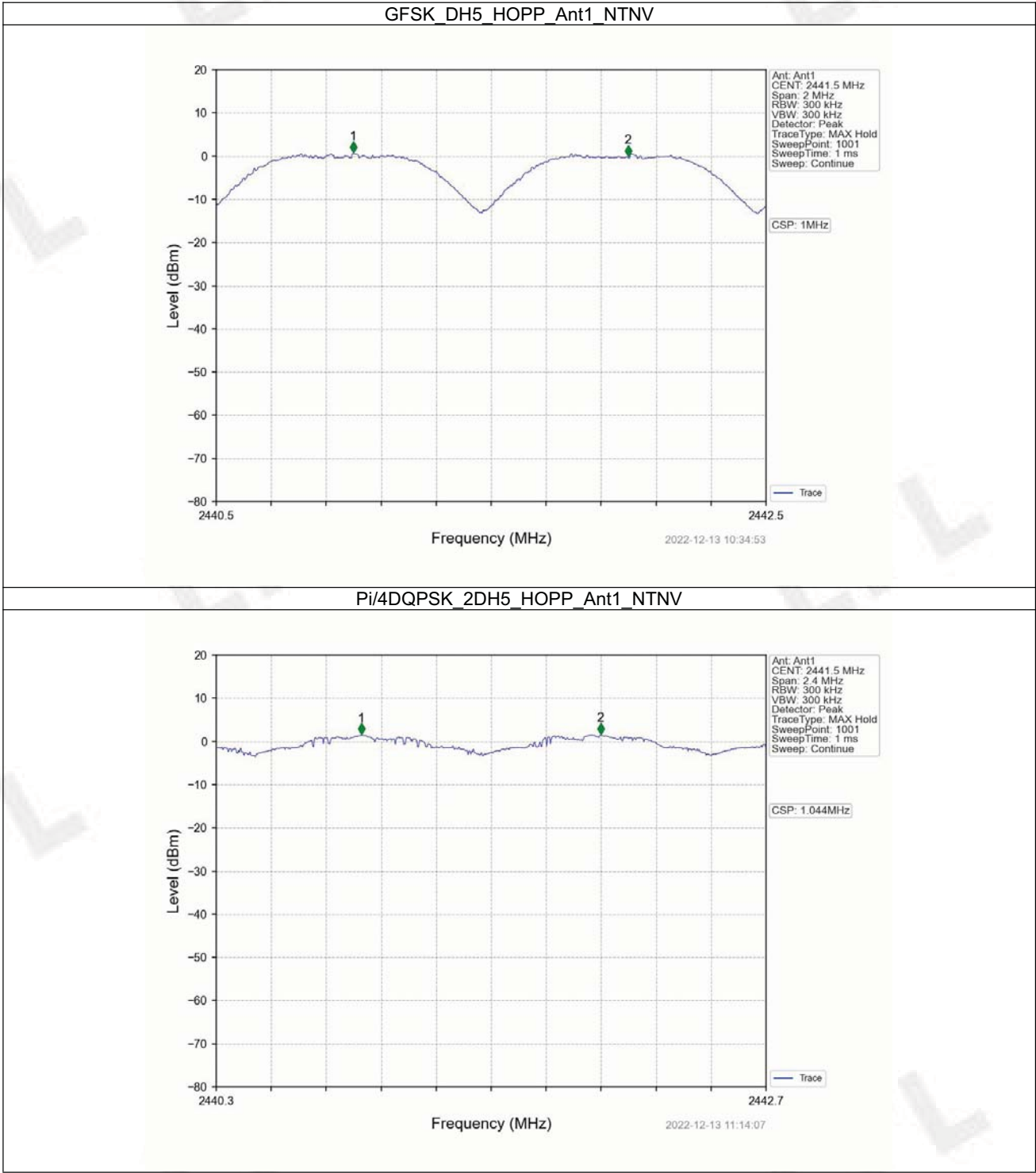
3. Carrier Frequency Separation

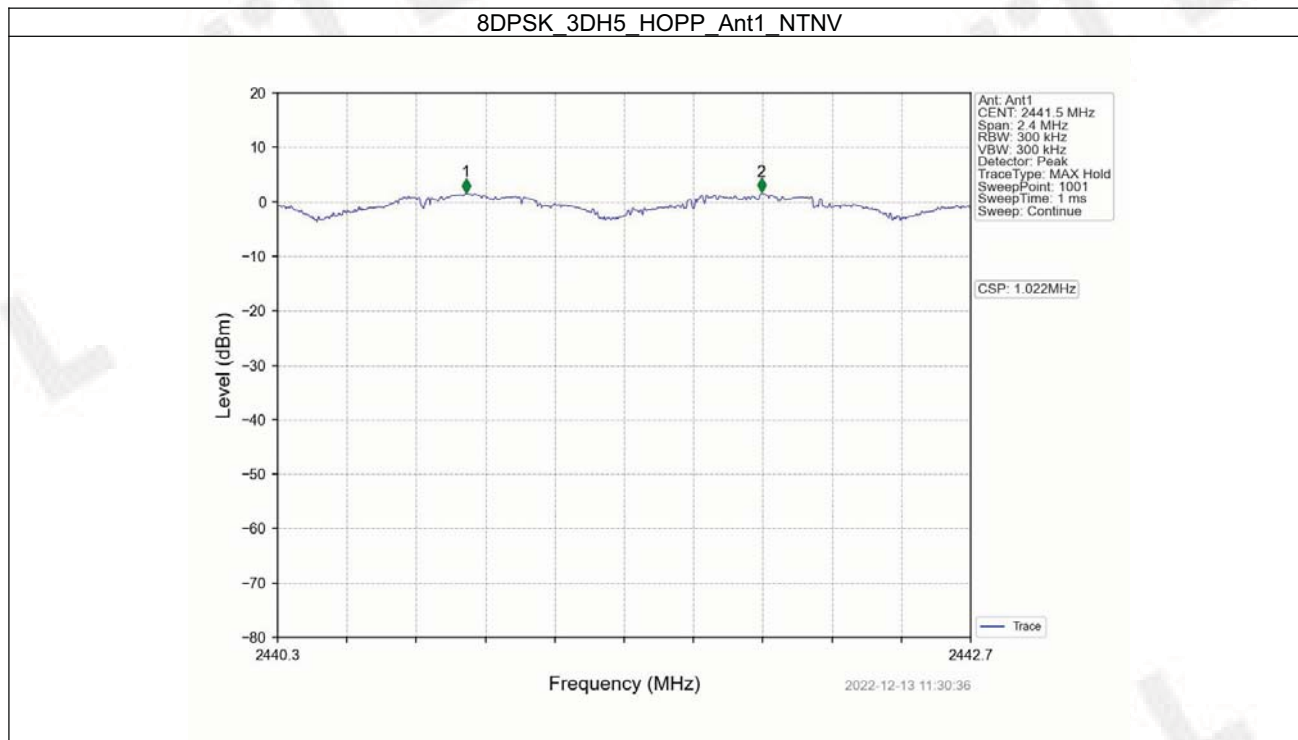
3.1 Ant1

3.1.1 Test Result

Ant1							
Mode	TX Type	Frequency (MHz)	Packet Type	Channel Separation (MHz)	20dB Bandwidth (MHz)	Limit (MHz)	Verdict
GFSK	SISO	HOPP	DH5	1.000	1.025	≥ 0.683	Pass
Pi/4DQPSK	SISO	HOPP	2DH5	1.044	1.380	≥ 0.92	Pass
8DPSK	SISO	HOPP	3DH5	1.022	1.361	≥ 0.907	Pass

3.1.2 Test Graph





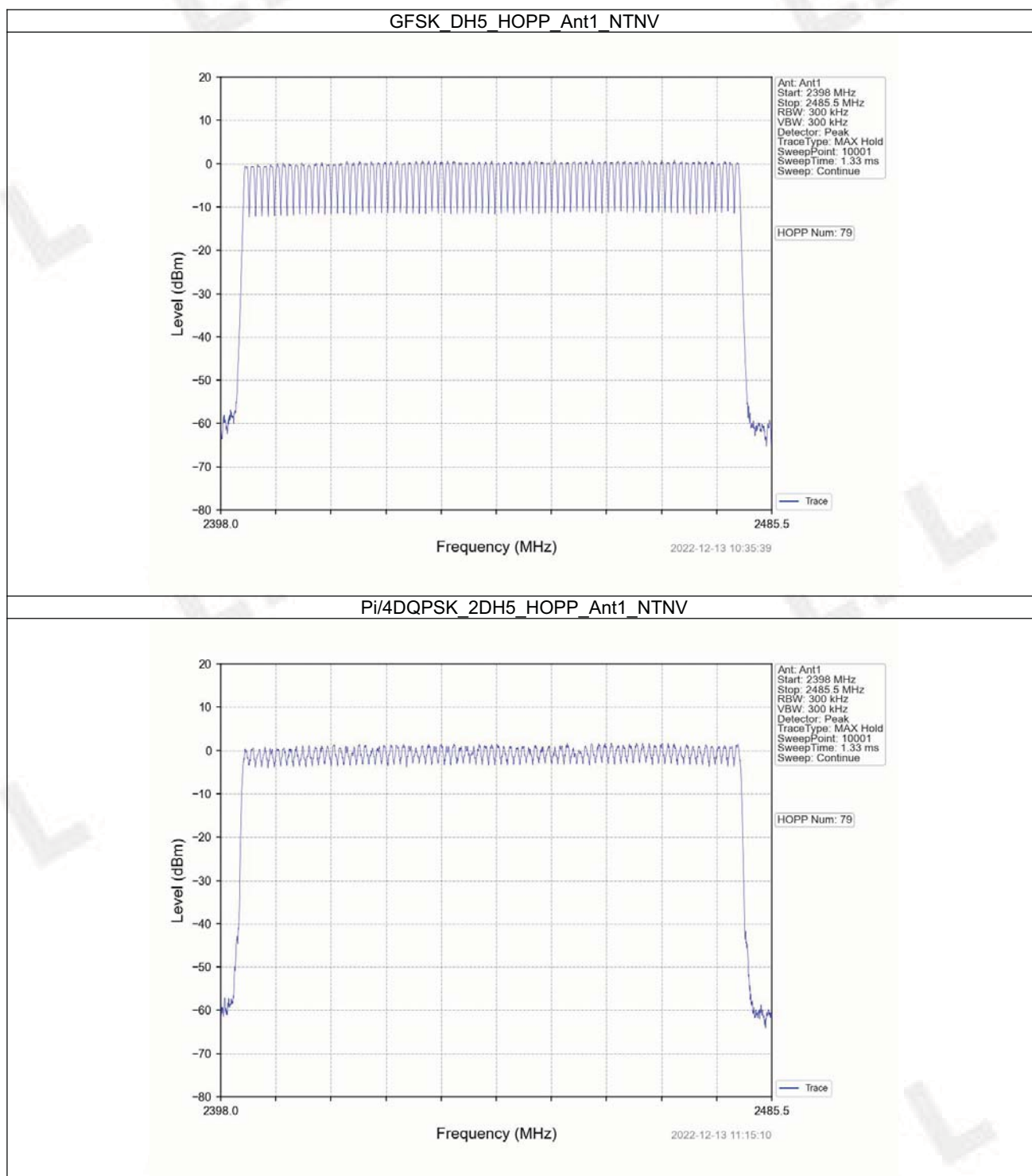
4. Number of Hopping Frequencies

4.1 HoppNum

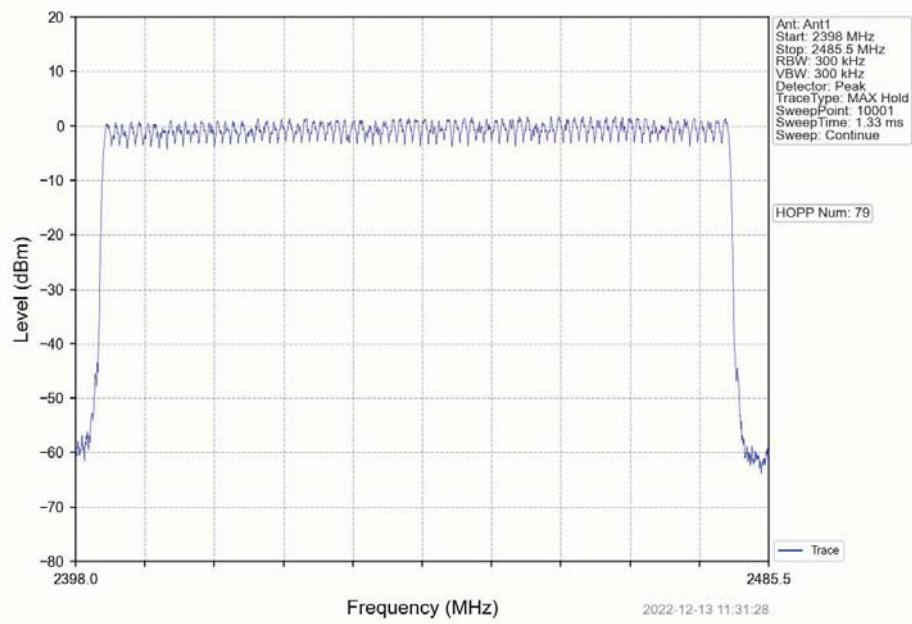
4.1.1 Test Result

Mode	TX Type	Frequency (MHz)	Packet Type	Num of Hopping Frequencies		Verdict
				ANT1	Limit	
GFSK	SISO	HOPP	DH5	79	≥ 15	Pass
Pi/4DQPSK	SISO	HOPP	2DH5	79	≥ 15	Pass
8DPSK	SISO	HOPP	3DH5	79	≥ 15	Pass

4.1.2 Test Graph



8DPSK_3DH5_HOPP_Ant1_NTNV



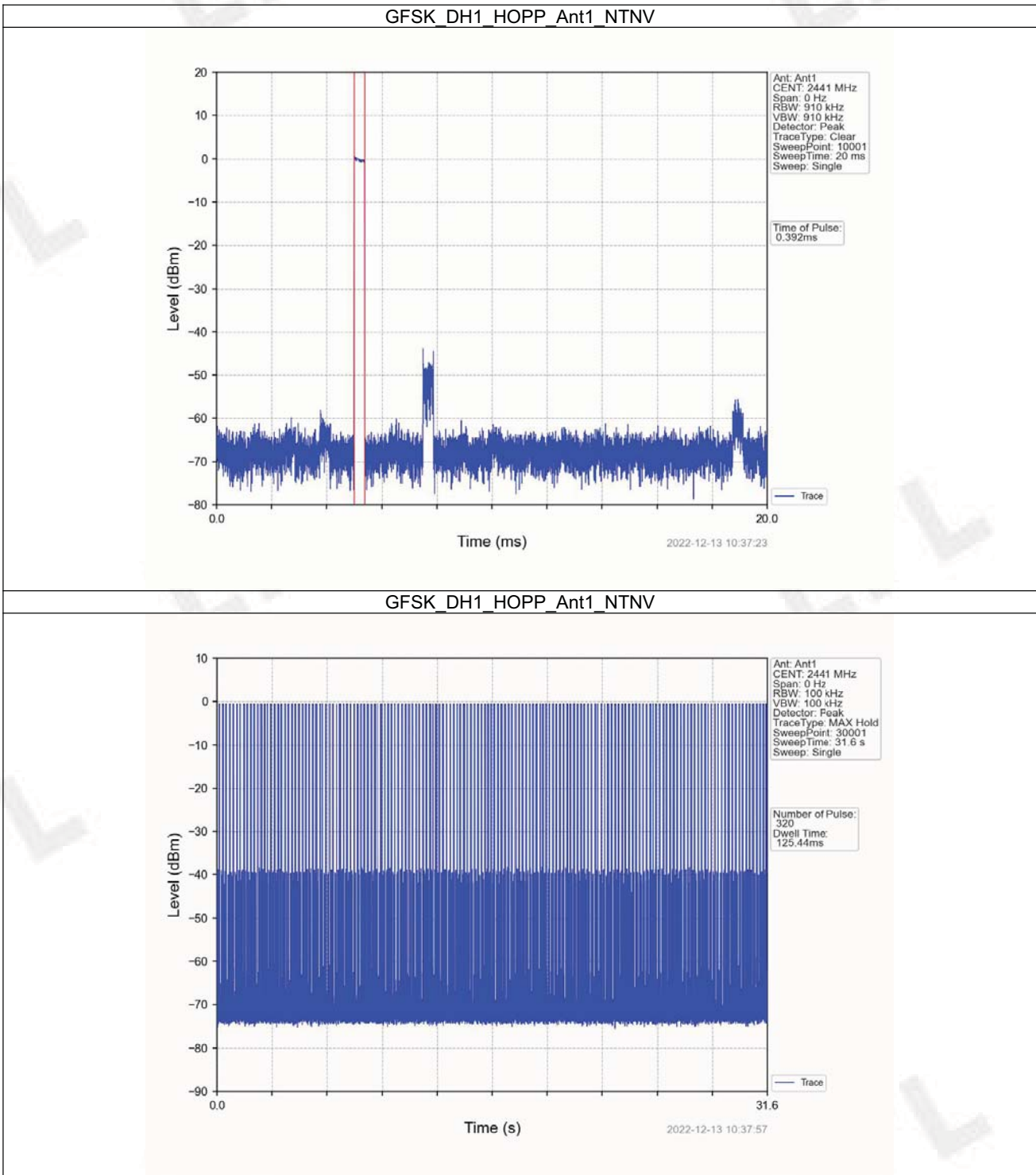
5. Time of Occupancy (Dwell Time)

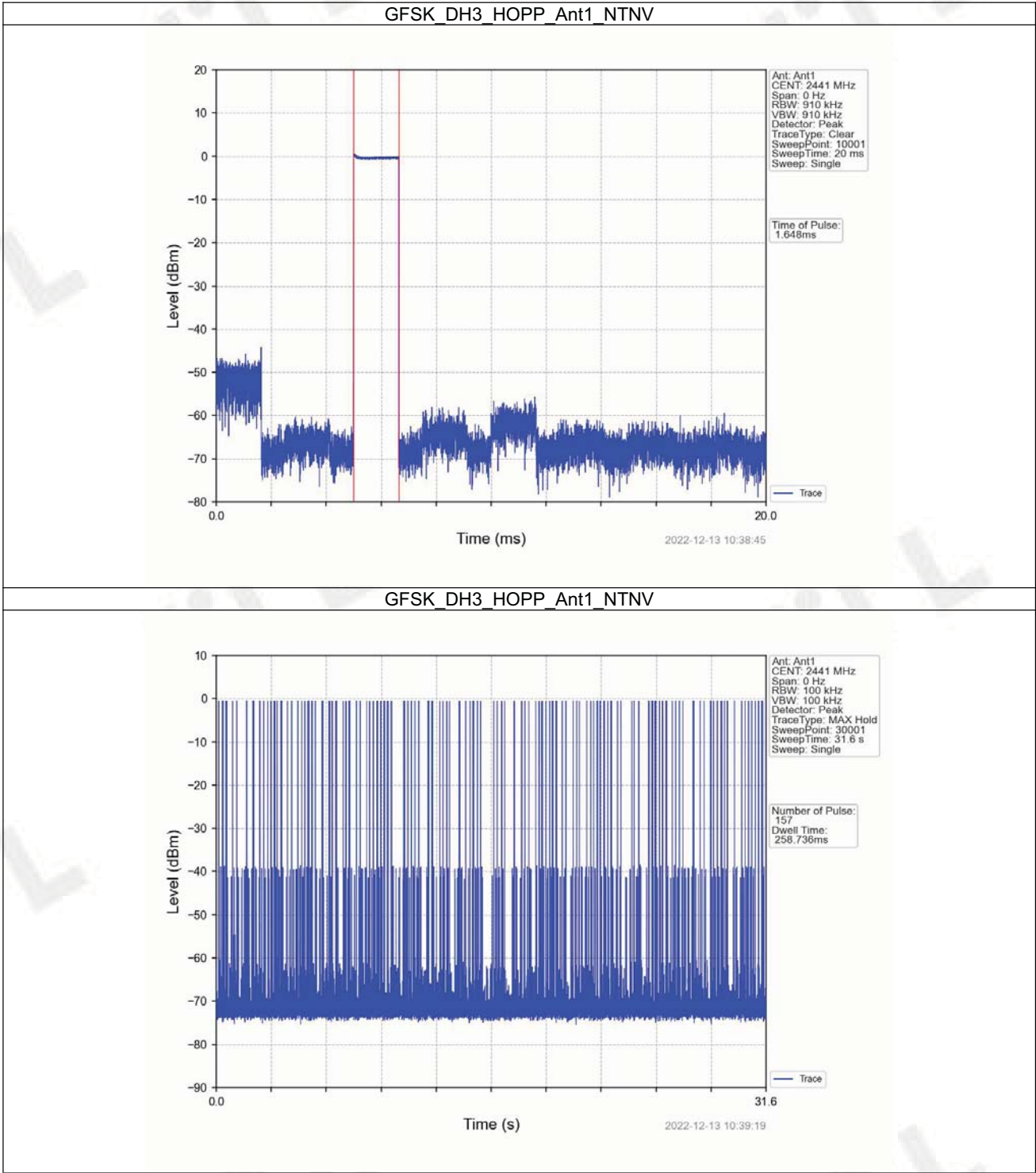
5.1 Ant1

5.1.1 Test Result

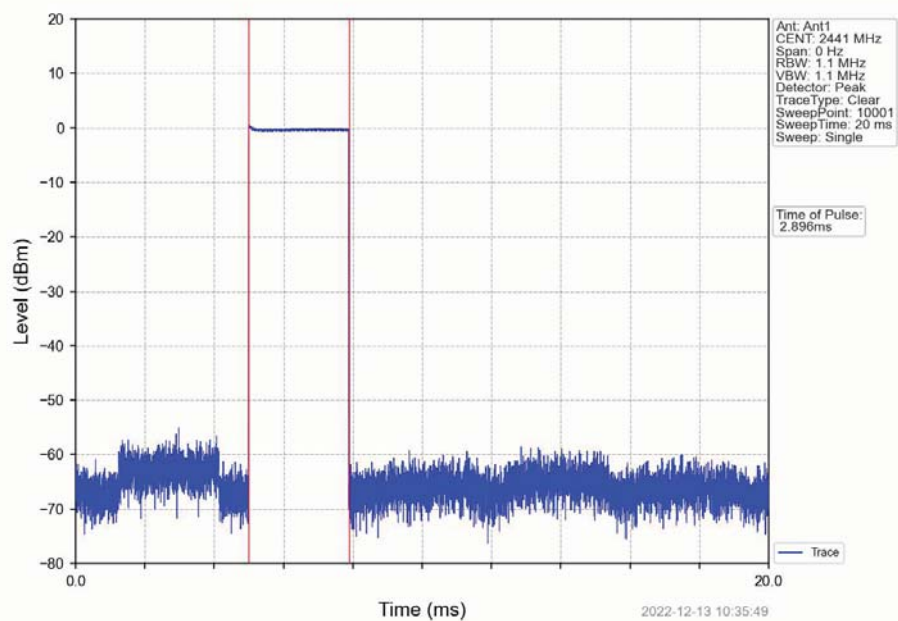
Ant1									
Mode	Tx Type	Frequency (MHz)	Packet Type	Duration of Single Pulse (ms)	Observation Period (s)	Num of Pulse in Observation Period	Dwell Time (ms)	Limit (ms)	Verdict
GFSK	SISO	HOPP	DH1	0.392	31.600	320	125.440	<=400	Pass
			DH3	1.648	31.600	157	258.736	<=400	Pass
			DH5	2.896	31.600	102	295.392	<=400	Pass
Pi/4DQPSK	SISO	HOPP	2DH1	0.396	31.600	320	126.720	<=400	Pass
			2DH3	1.656	31.600	156	258.336	<=400	Pass
			2DH5	2.904	31.600	115	333.960	<=400	Pass
8DPSK	SISO	HOPP	3DH1	0.394	31.600	320	126.080	<=400	Pass
			3DH3	1.654	31.600	154	254.716	<=400	Pass
			3DH5	2.906	31.600	106	308.036	<=400	Pass

5.1.2 Test Graph

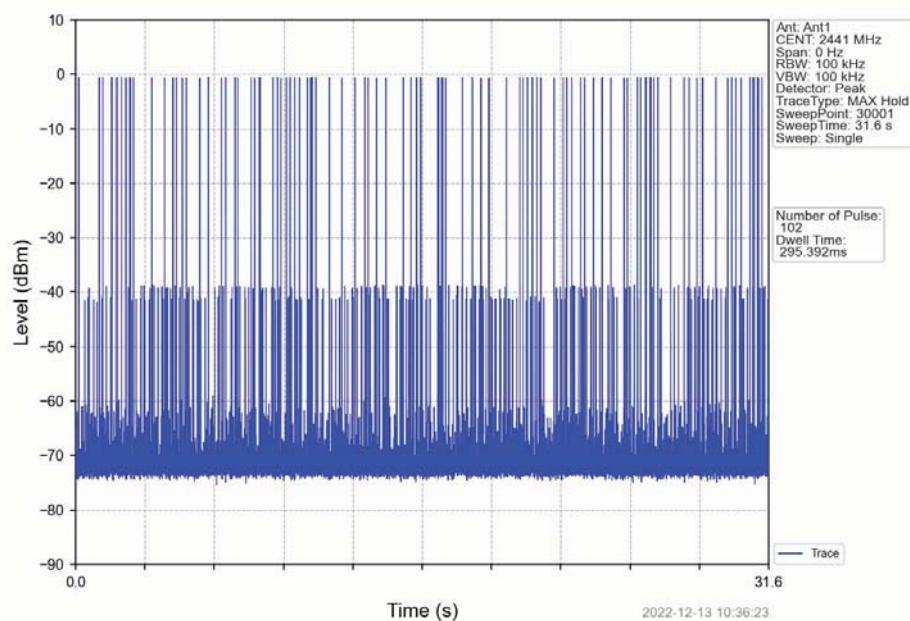




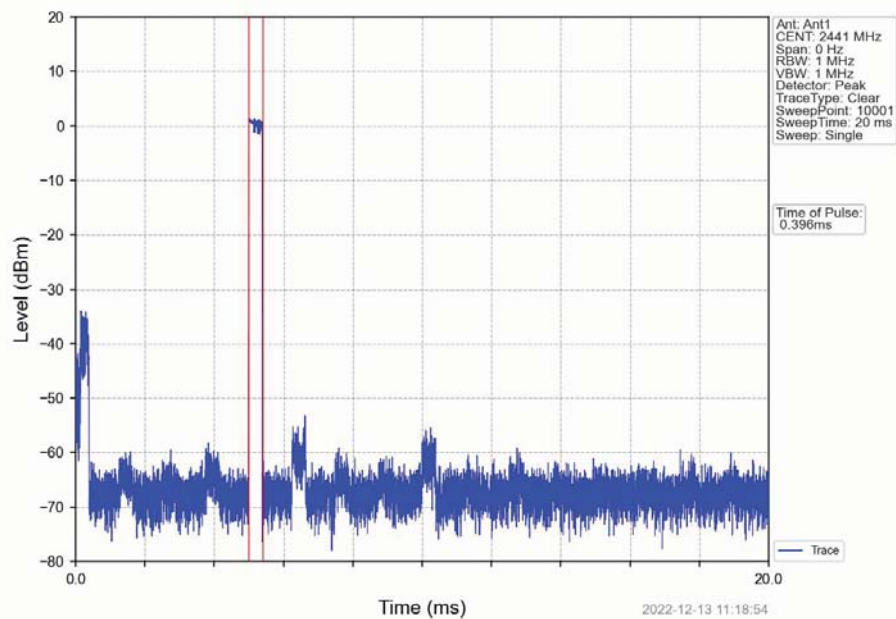
GFSK_DH5_HOPP_Ant1_NTNV



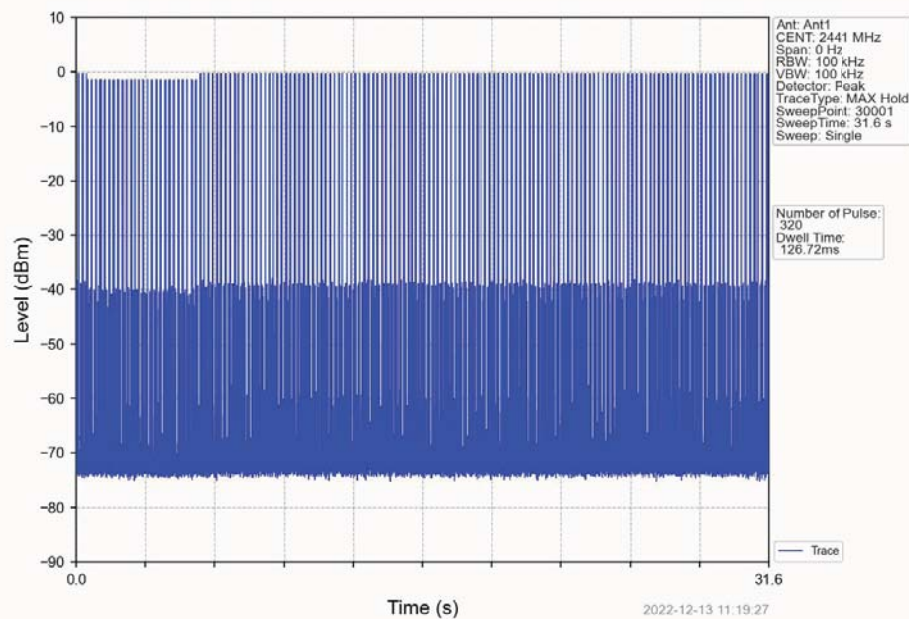
GFSK_DH5_HOPP_Ant1_NTNV



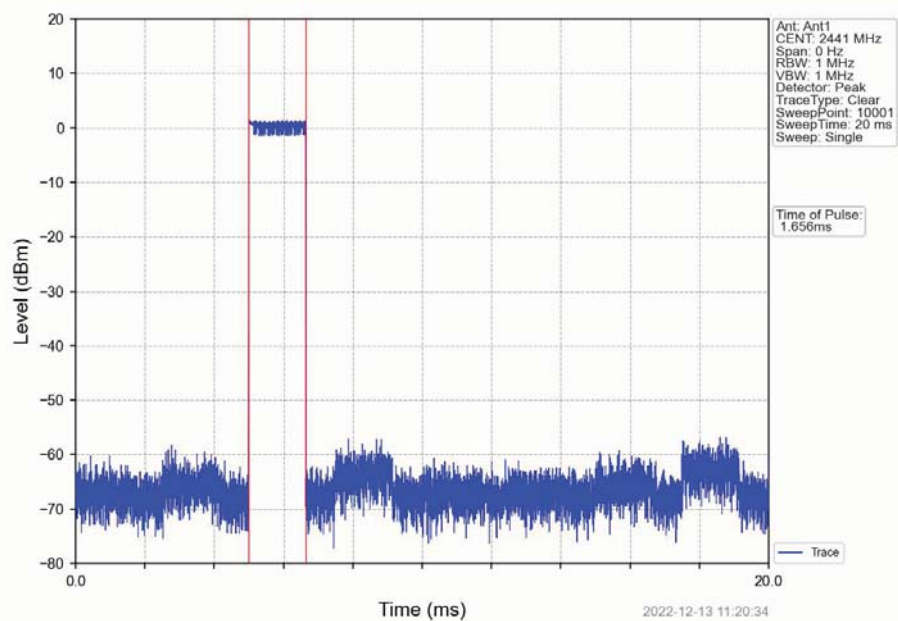
Pi/4DQPSK_2DH1_HOPP_Ant1_NTNV



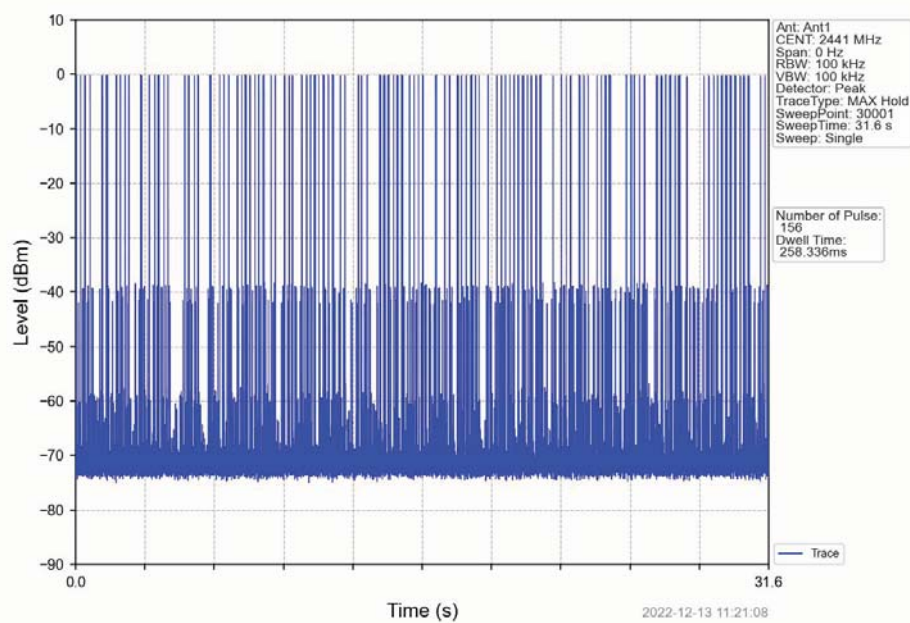
Pi/4DQPSK_2DH1_HOPP_Ant1_NTNV



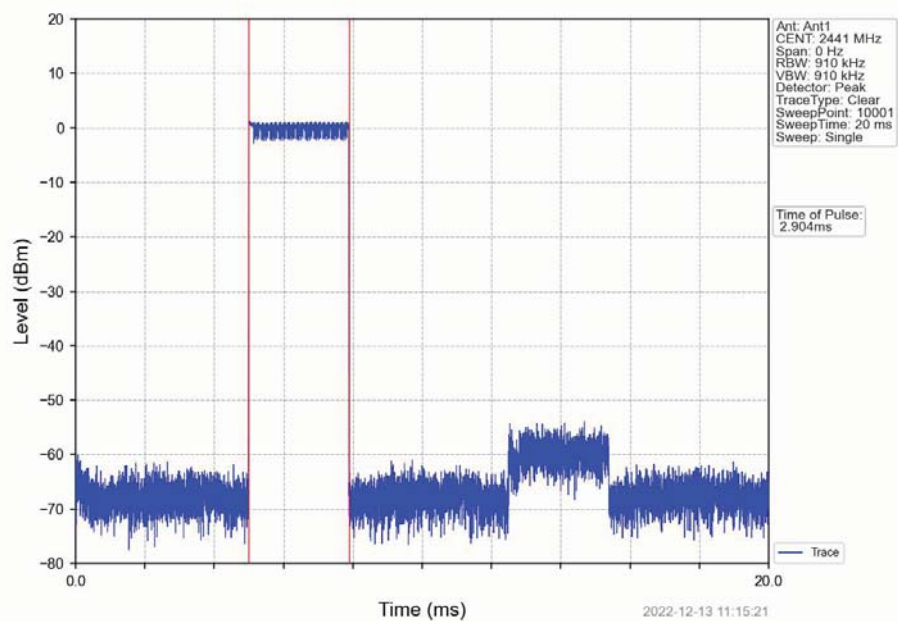
Pi/4DQPSK_2DH3_HOPP_Ant1_NTNV



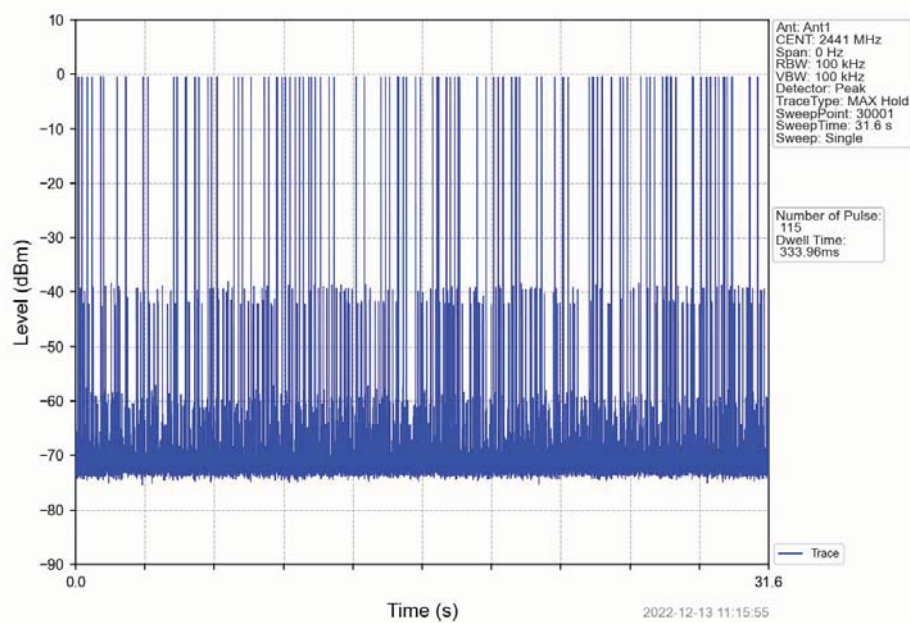
Pi/4DQPSK_2DH3_HOPP_Ant1_NTNV



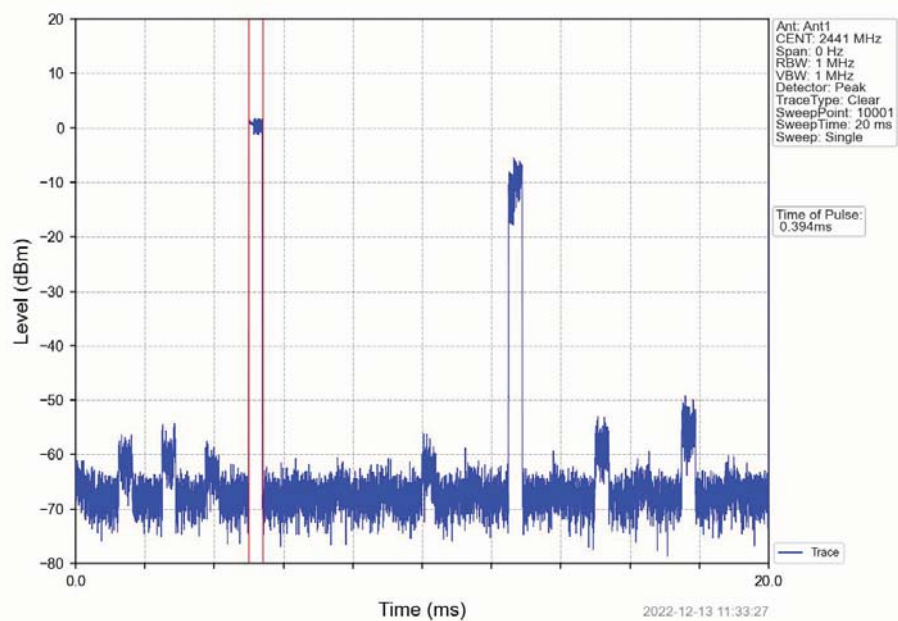
Pi/4DQPSK_2DH5_HOPP_Ant1_NTNV



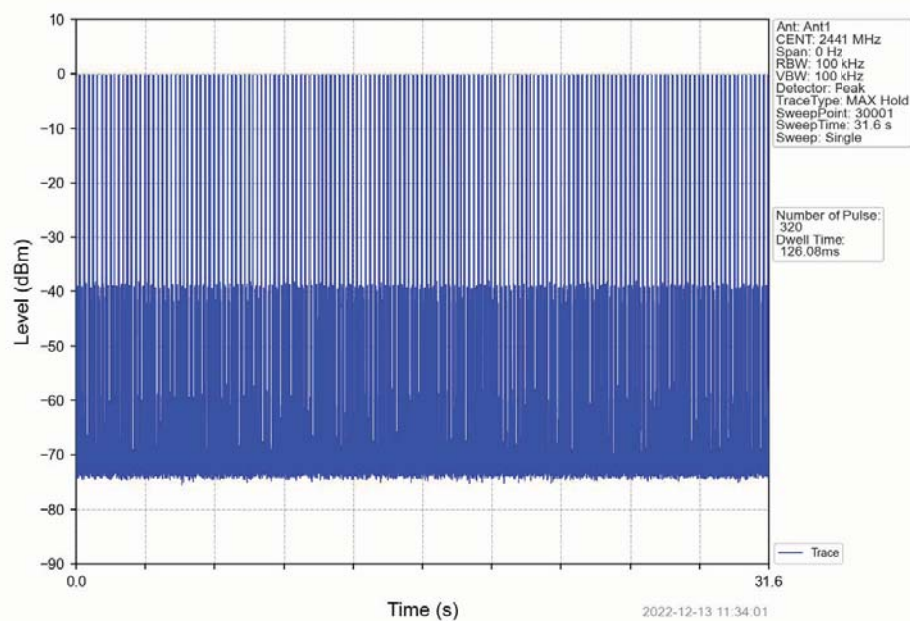
Pi/4DQPSK_2DH5_HOPP_Ant1_NTNV



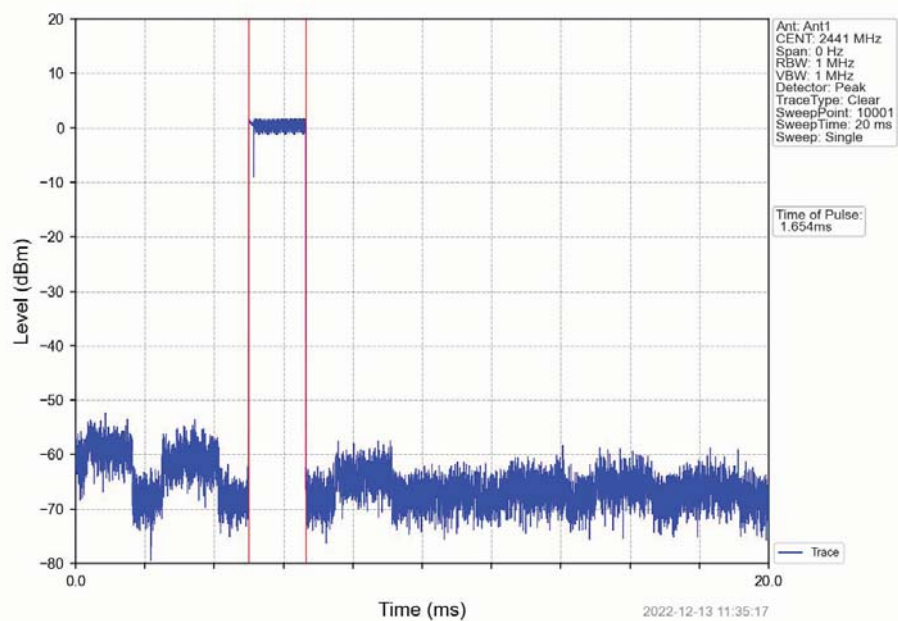
8DPSK_3DH1_HOPP_Ant1_NTNV



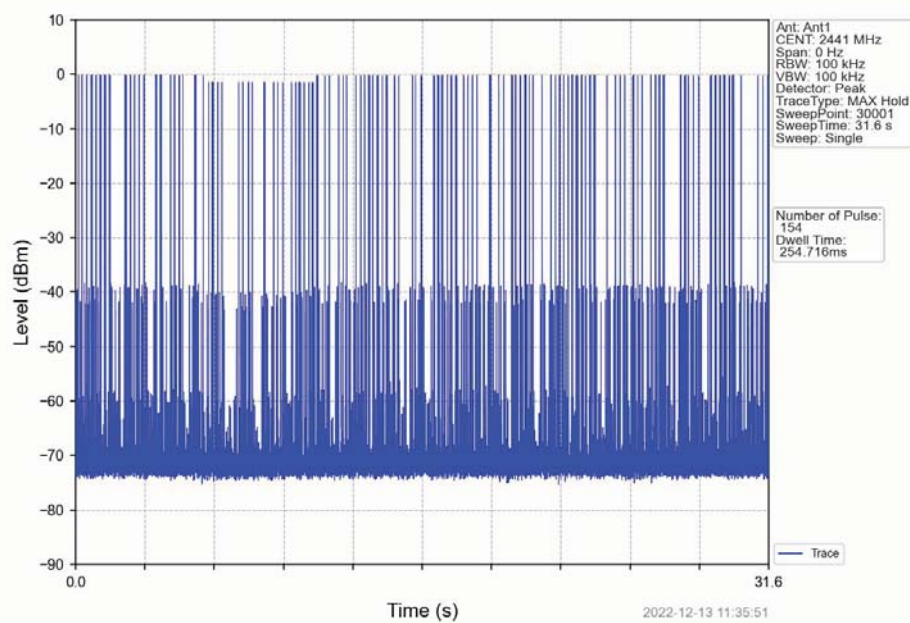
8DPSK_3DH1_HOPP_Ant1_NTNV



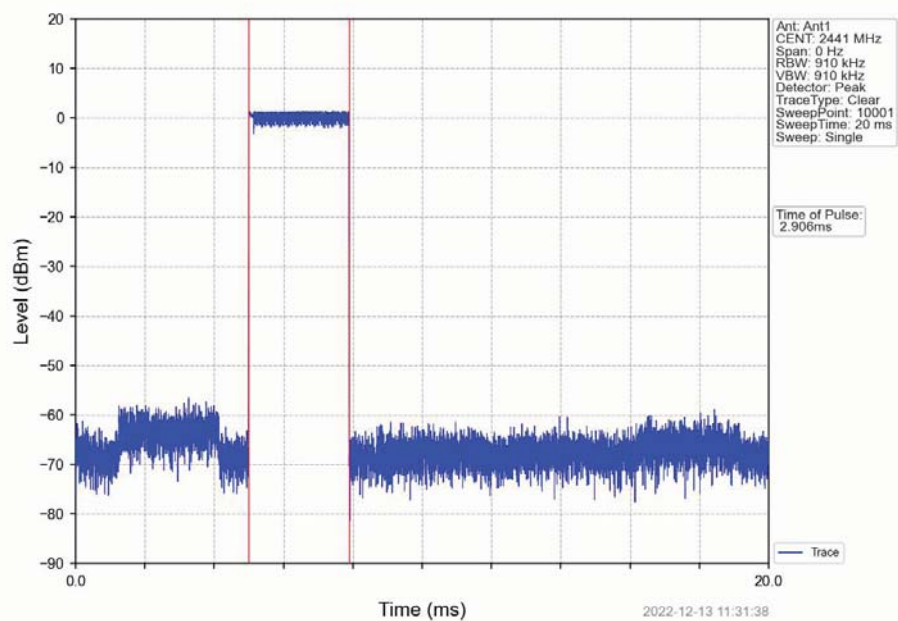
8DPSK_3DH3_HOPP_Ant1_NTNV



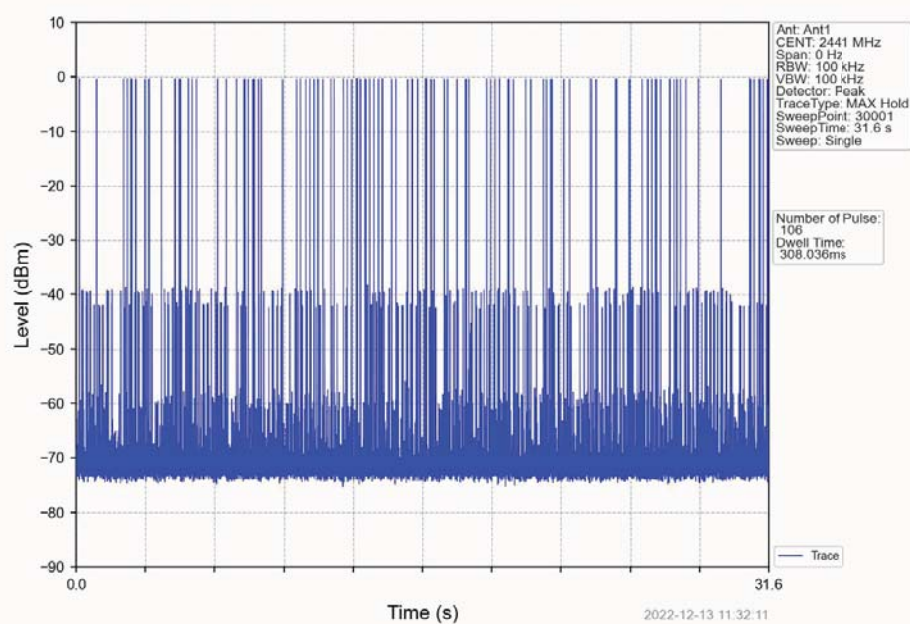
8DPSK_3DH3_HOPP_Ant1_NTNV



8DPSK_3DH5_HOPP_Ant1_NTNV



8DPSK_3DH5_HOPP_Ant1_NTNV



6. Unwanted Emissions In Non-restricted Frequency Bands

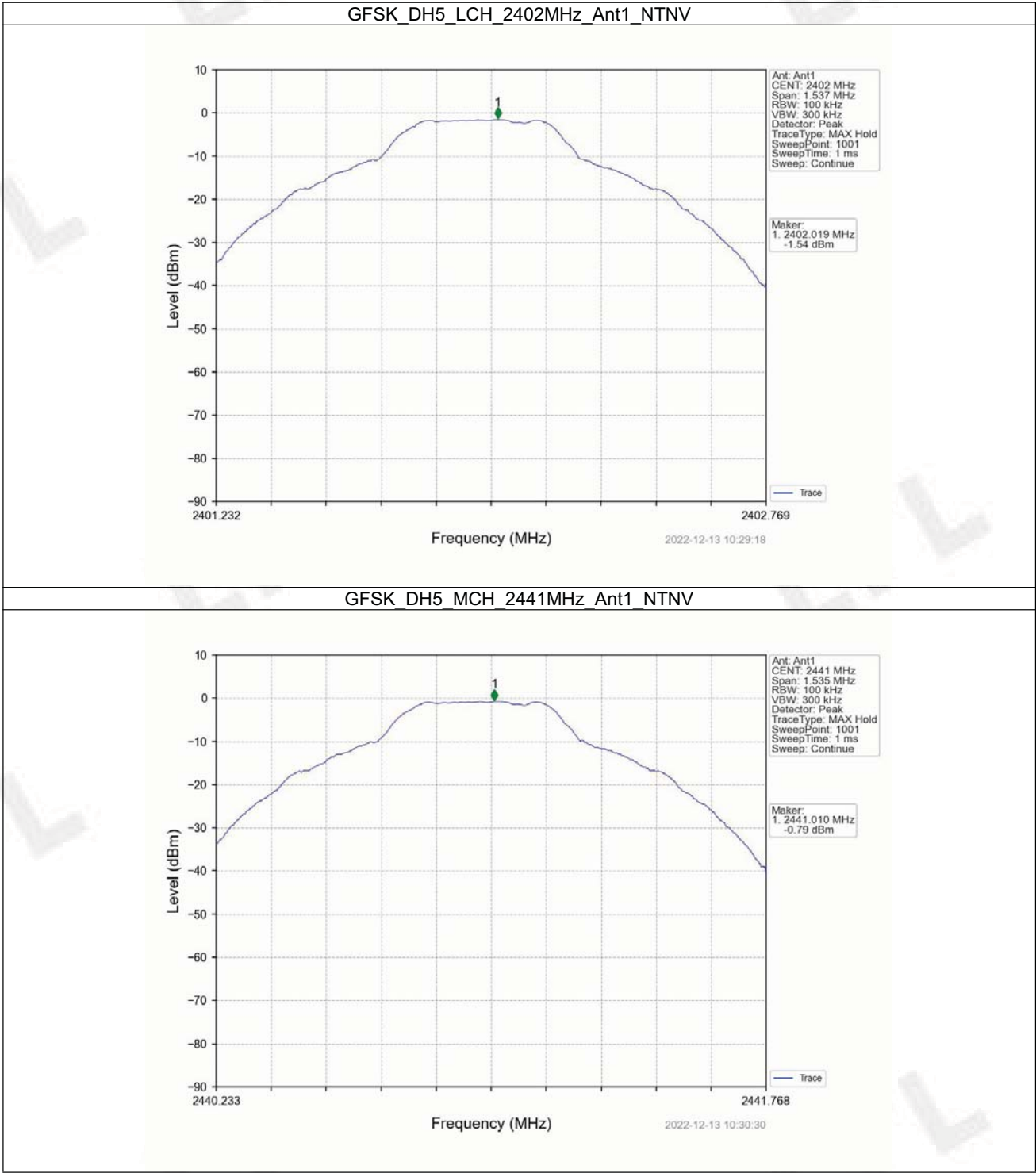
6.1 Ref

6.1.1 Test Result

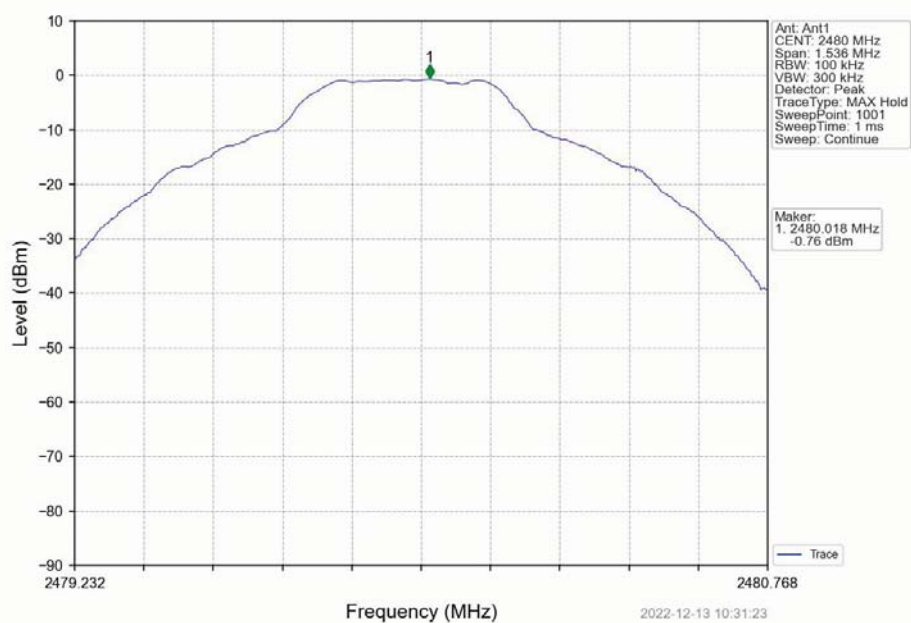
Mode	TX Type	Frequency (MHz)	Packet Type	ANT	Level of Reference (dBm)
GFSK	SISO	2402	DH5	1	-1.54
		2441	DH5	1	-0.79
		2480	DH5	1	-0.76
Pi/4DQPSK	SISO	2402	2DH5	1	-2.71
		2441	2DH5	1	-2.01
		2480	2DH5	1	-2.05
8DPSK	SISO	2402	3DH5	1	-2.92
		2441	3DH5	1	-2.11
		2480	3DH5	1	-2.22

Note1: Refer to FCC Part 15.247 (d) and ANSI C63.10-2013, the channel contains the maximum PSD level was used to establish the reference level.

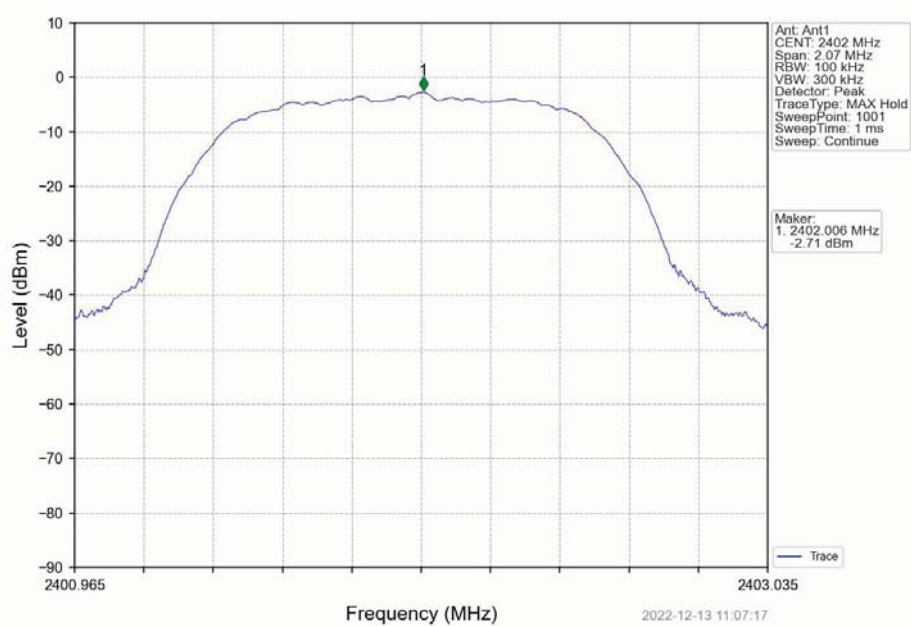
6.1.2 Test Graph



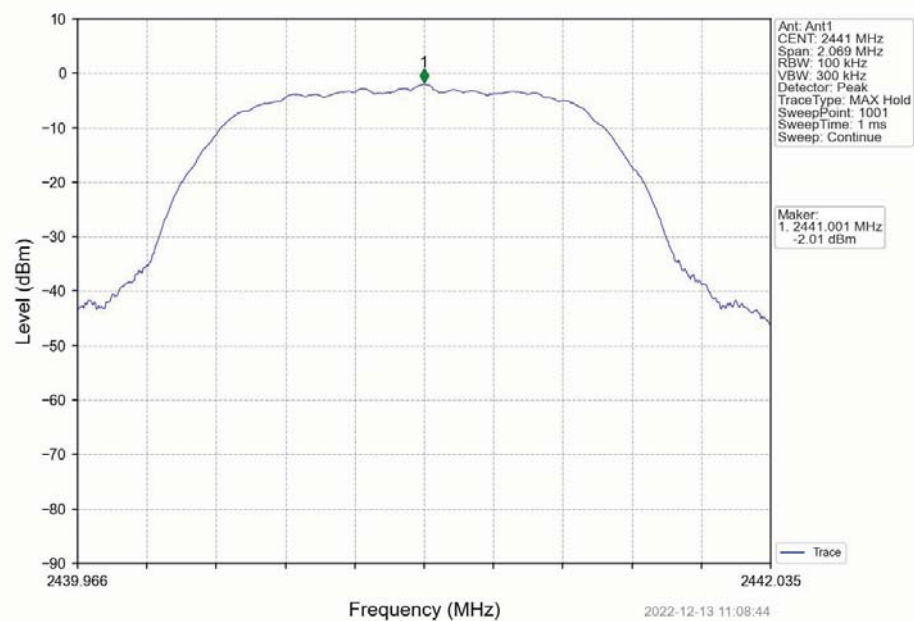
GFSK_DH5_HCH_2480MHz_Ant1_NTNV



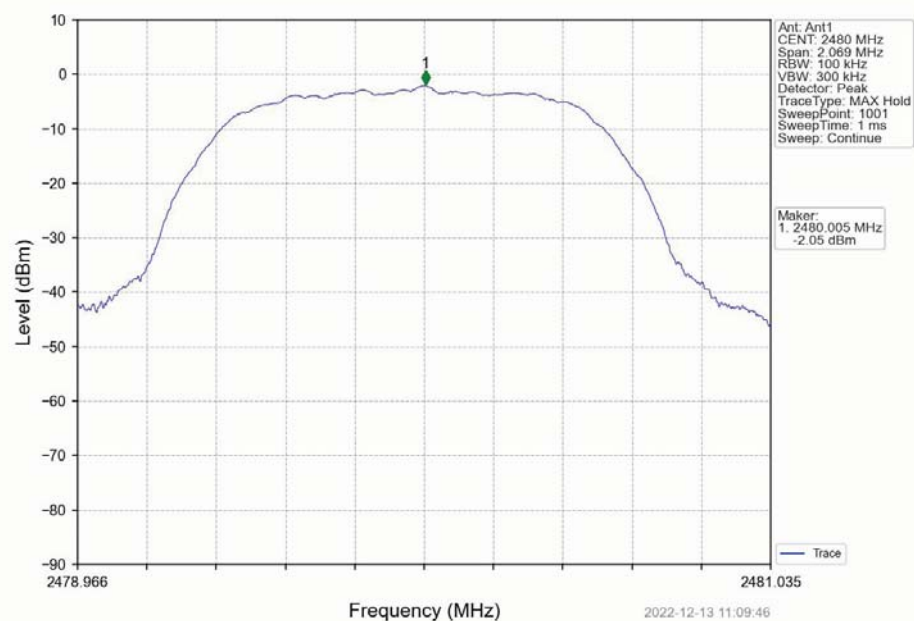
Pi/4DQPSK_2DH5_LCH_2402MHz_Ant1_NTNV



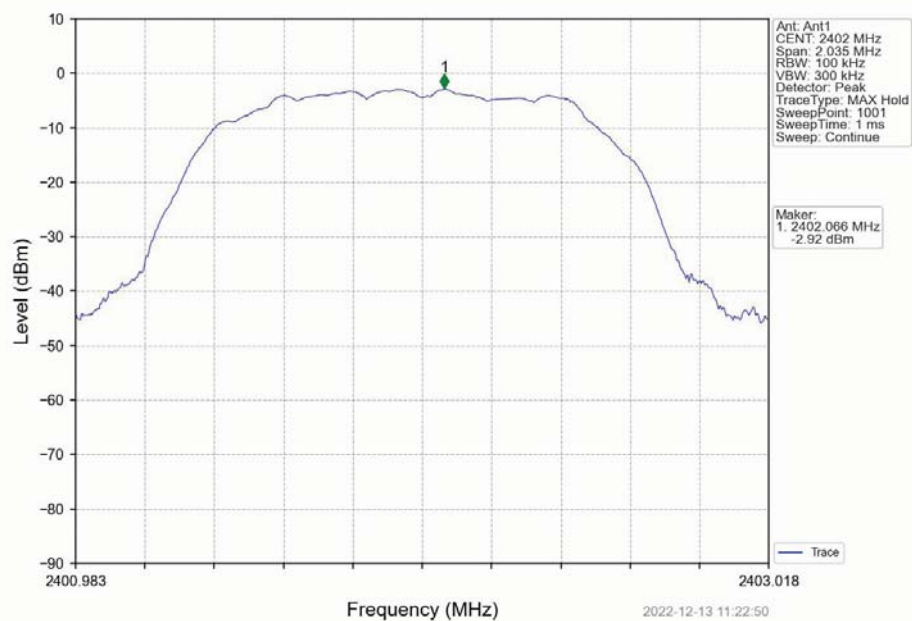
Pi/4DQPSK_2DH5_MCH_2441MHz_Ant1_NTNV



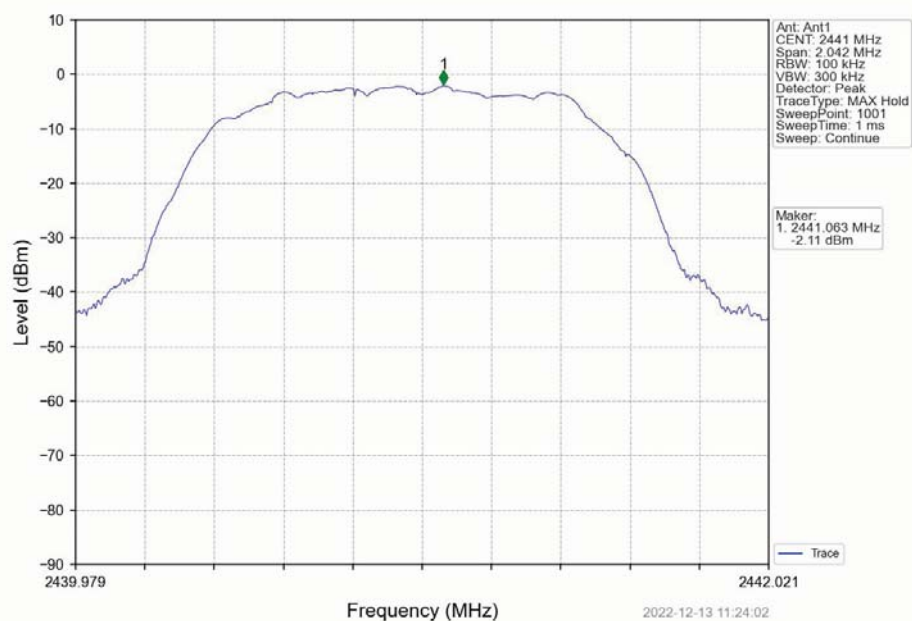
Pi/4DQPSK_2DH5_HCH_2480MHz_Ant1_NTNV



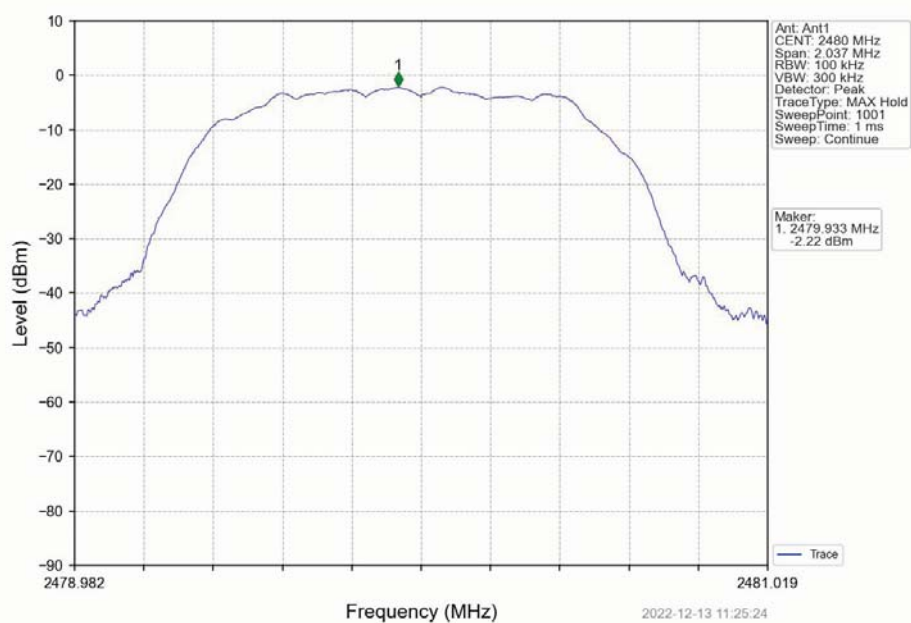
8DPSK_3DH5_LCH_2402MHz_Ant1_NTNV



8DPSK_3DH5_MCH_2441MHz_Ant1_NTNV



8DPSK_3DH5_HCH_2480MHz_Ant1_NTNV



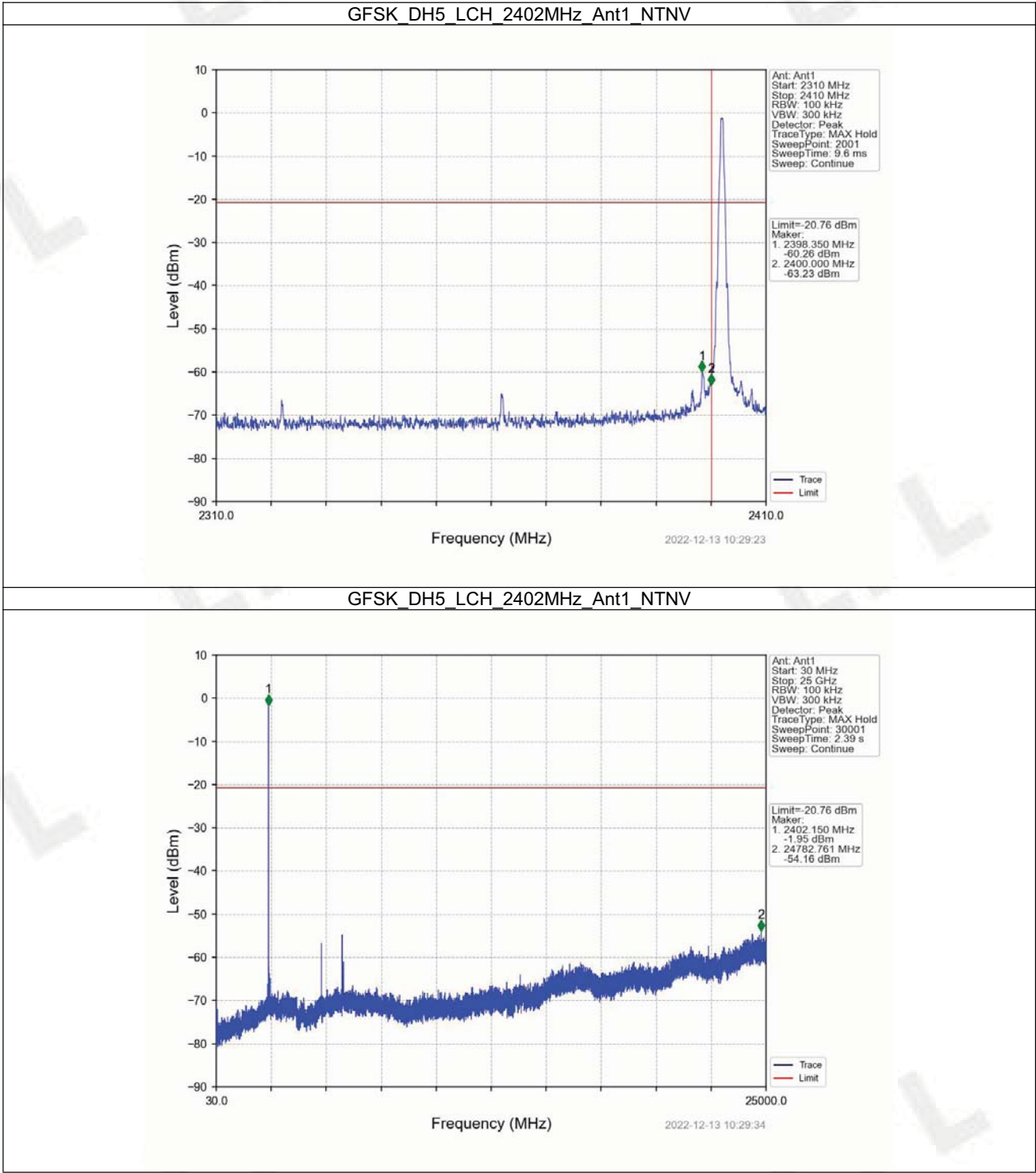
6.2 CSE

6.2.1 Test Result

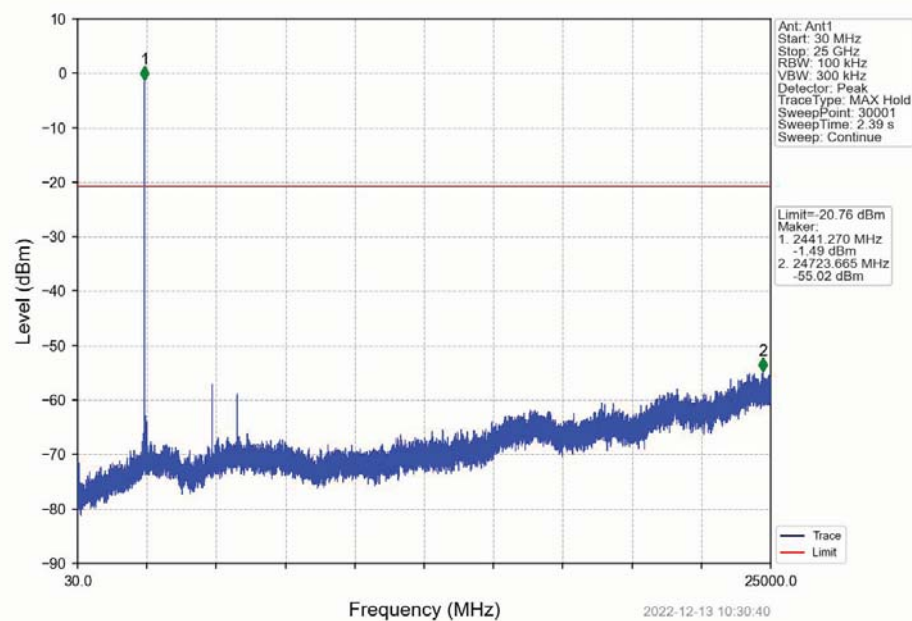
Mode	TX Type	Frequency (MHz)	Packet Type	ANT	Level of Reference (dBm)	Limit (dBm)	Verdict
GFSK	SISO	2402	DH5	1	-0.76	-20.76	Pass
		2441	DH5	1	-0.76	-20.76	Pass
		2480	DH5	1	-0.76	-20.76	Pass
		HOPP	DH5	1	-0.76	-20.76	Pass
Pi/4DQPSK	SISO	2402	2DH5	1	-2.01	-22.01	Pass
		2441	2DH5	1	-2.01	-22.01	Pass
		2480	2DH5	1	-2.01	-22.01	Pass
		HOPP	2DH5	1	-2.01	-22.01	Pass
8DPSK	SISO	2402	3DH5	1	-2.11	-22.11	Pass
		2441	3DH5	1	-2.11	-22.11	Pass
		2480	3DH5	1	-2.11	-22.11	Pass
		HOPP	3DH5	1	-2.11	-22.11	Pass

Note1: Refer to FCC Part 15.247 (d) and ANSI C63.10-2013, the channel contains the maximum PSD level was used to establish the reference level.

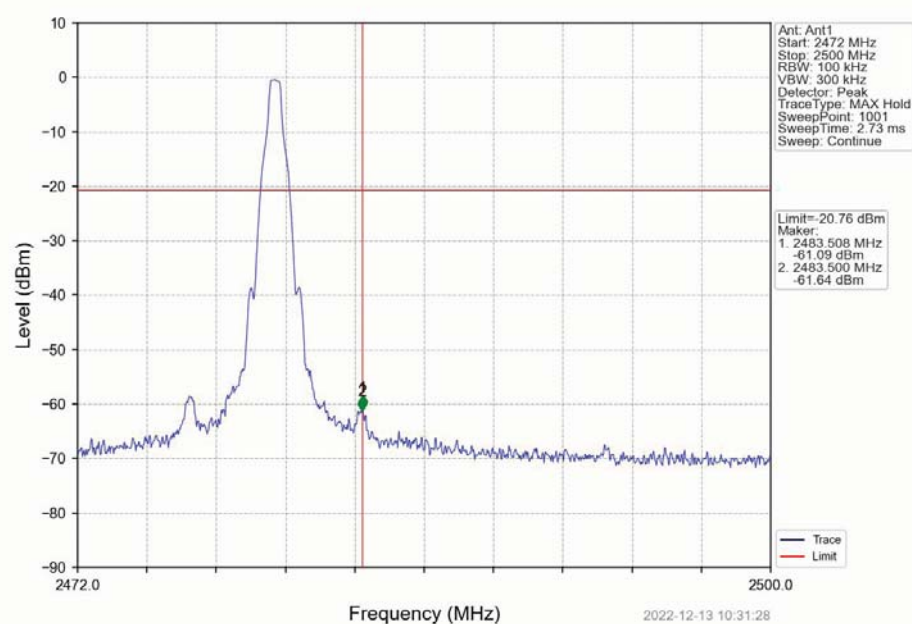
6.2.2 Test Graph



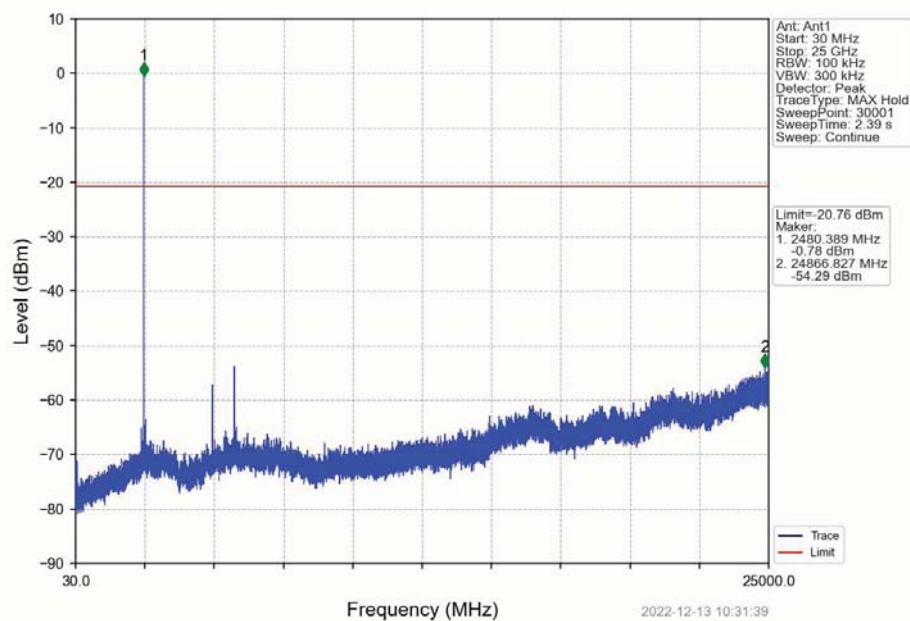
GFSK_DH5_MCH_2441MHz_Ant1_NTNV



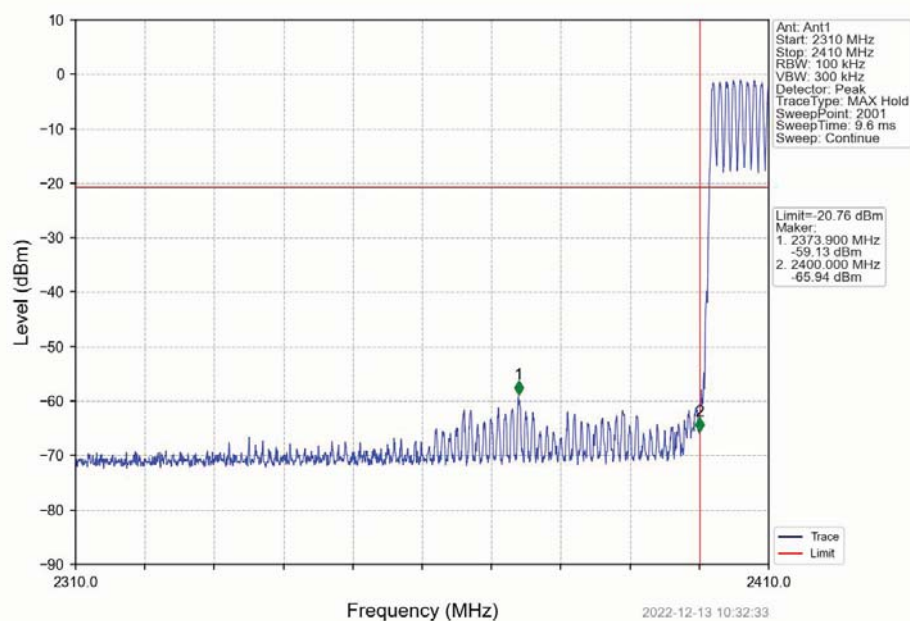
GFSK_DH5_HCH_2480MHz_Ant1_NTNV

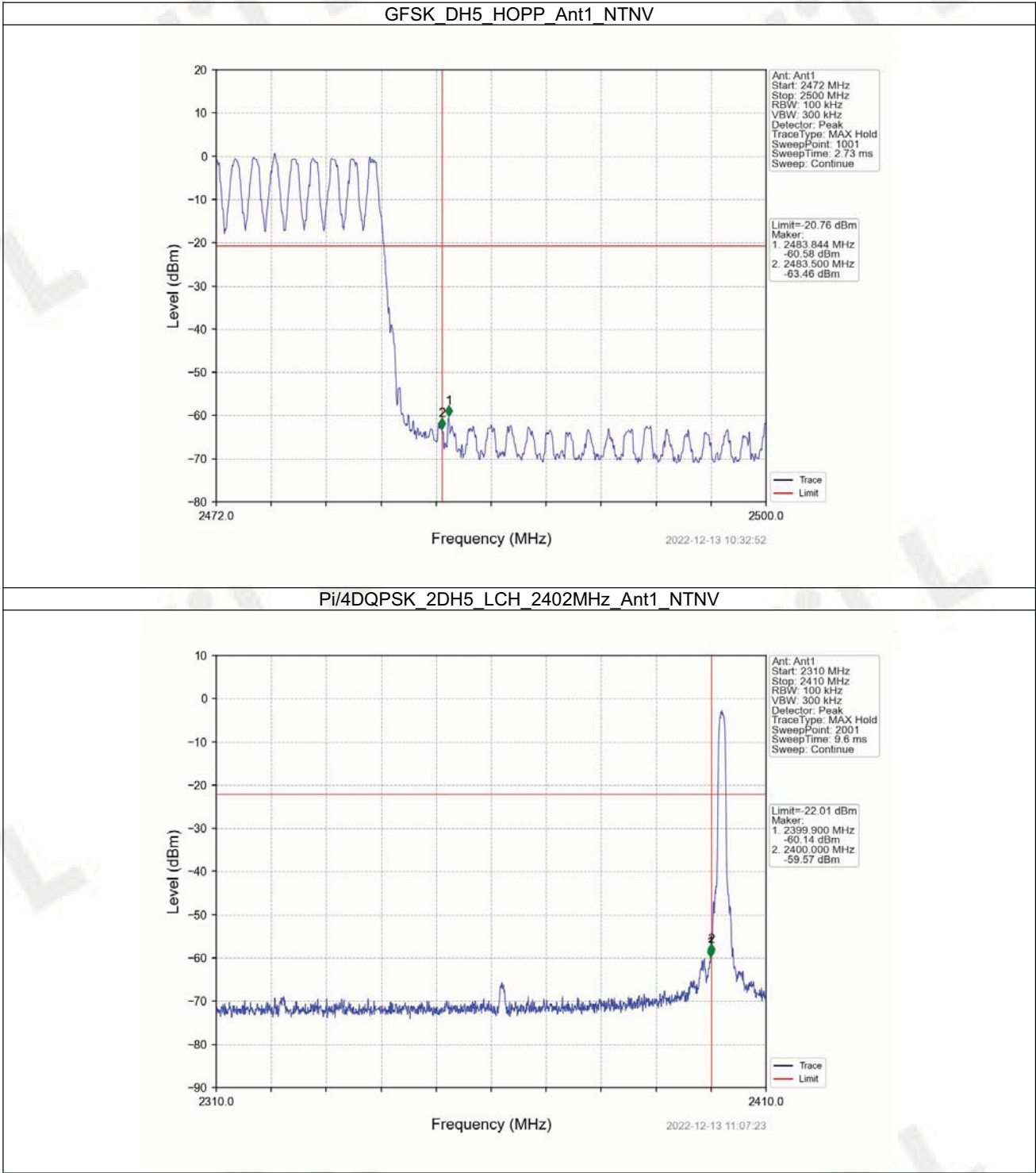


GFSK_DH5_HCH_2480MHz_Ant1_NTNV

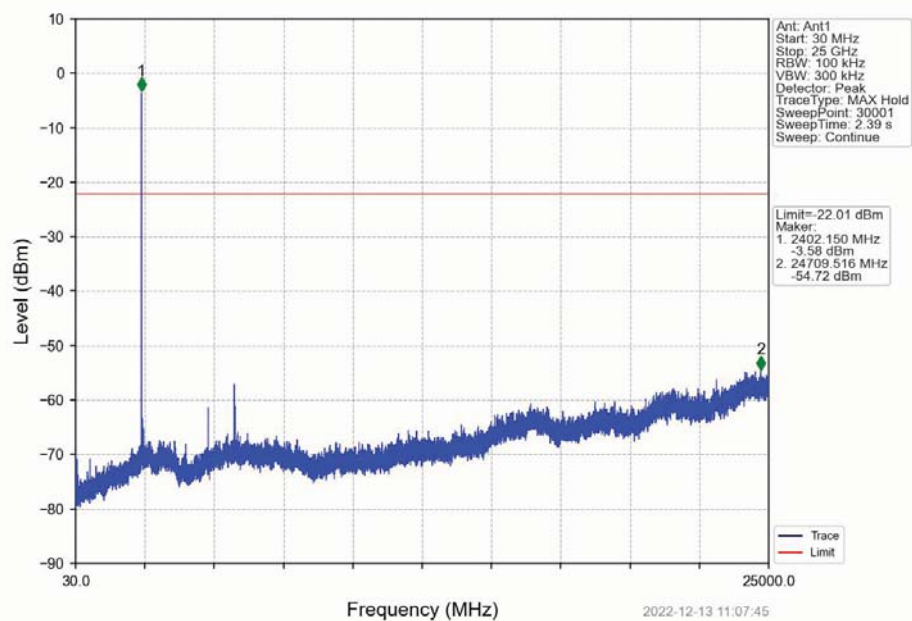


GFSK_DH5_HOPP_Ant1_NTNV

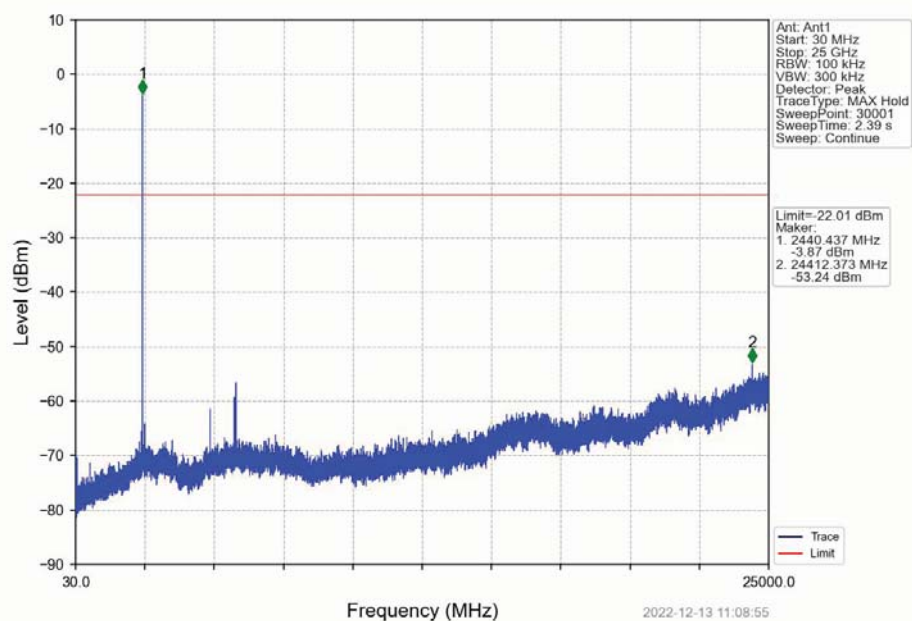




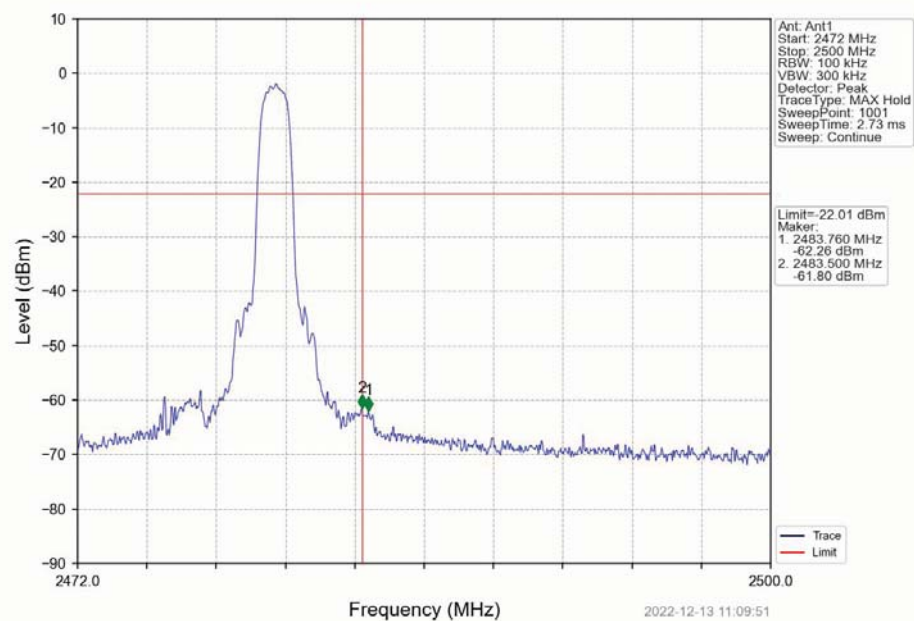
Pi/4DQPSK_2DH5_LCH_2402MHz_Ant1_NTNV



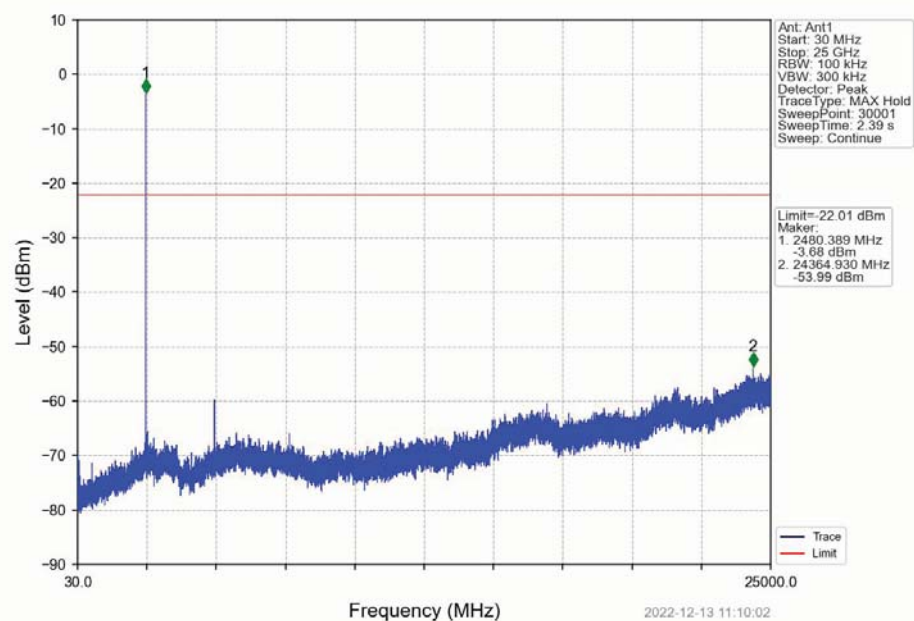
Pi/4DQPSK_2DH5_MCH_2441MHz_Ant1_NTNV



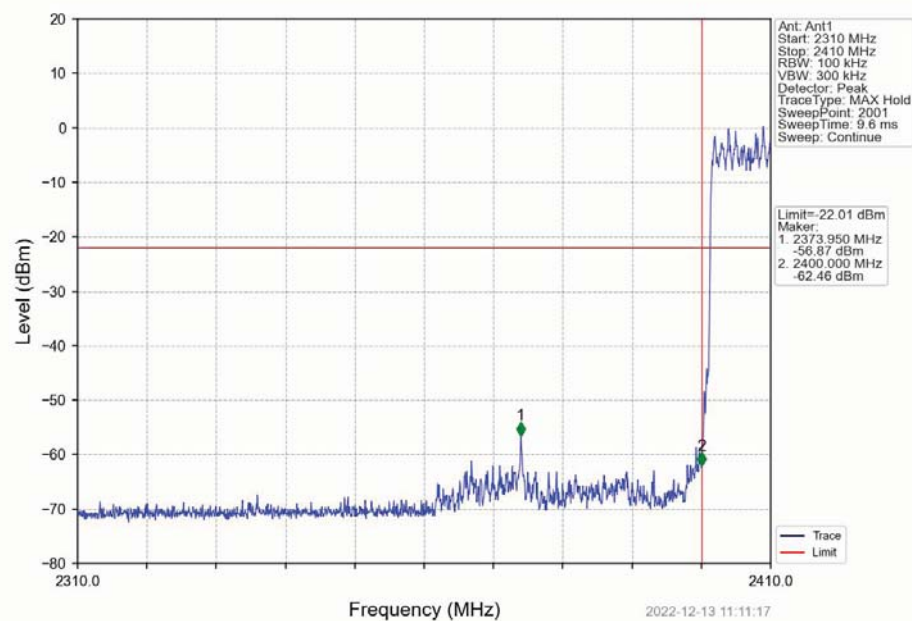
Pi/4DQPSK_2DH5_HCH_2480MHz_Ant1_NTNV



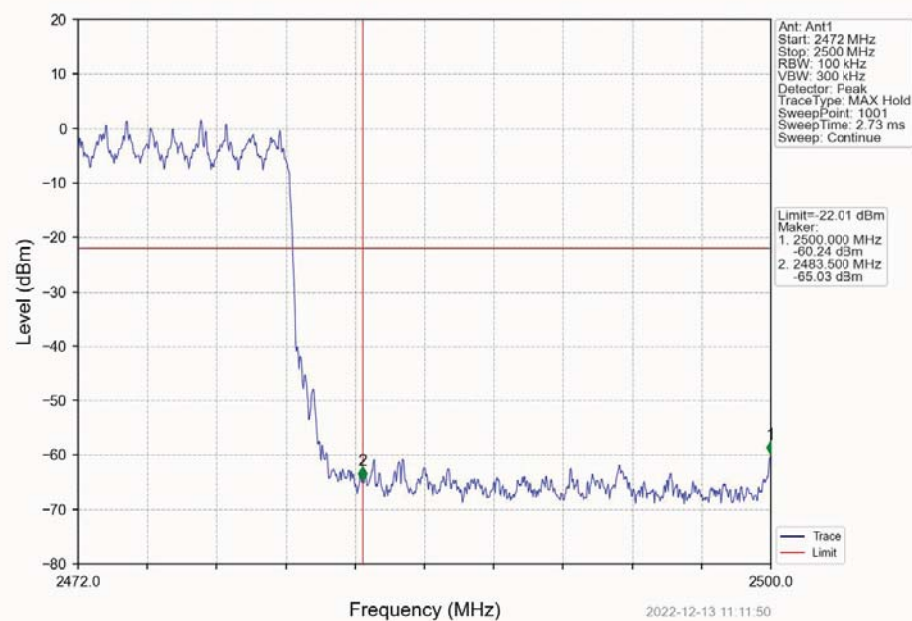
Pi/4DQPSK_2DH5_HCH_2480MHz_Ant1_NTNV



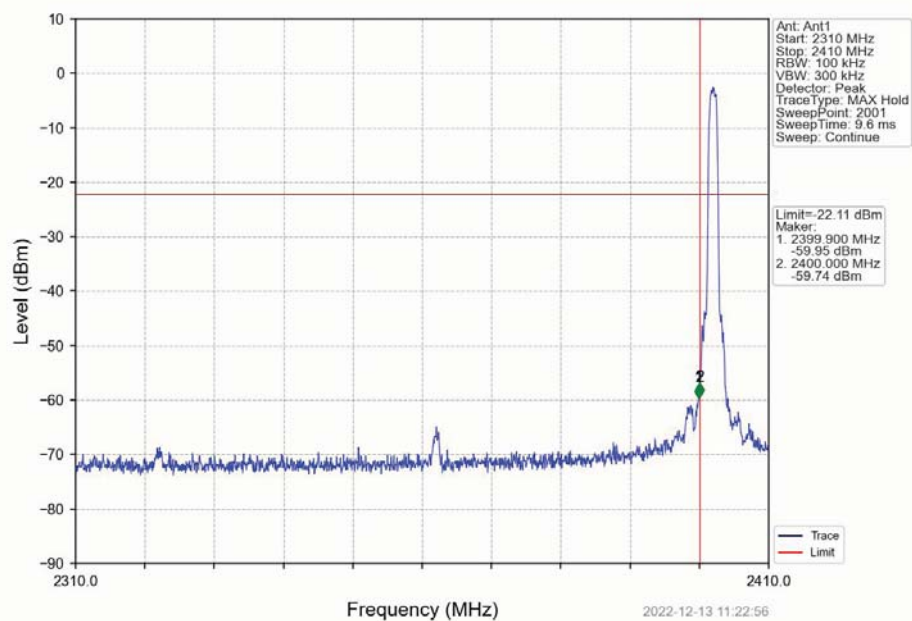
Pi/4DQPSK_2DH5_HOPP_Ant1_NTNV



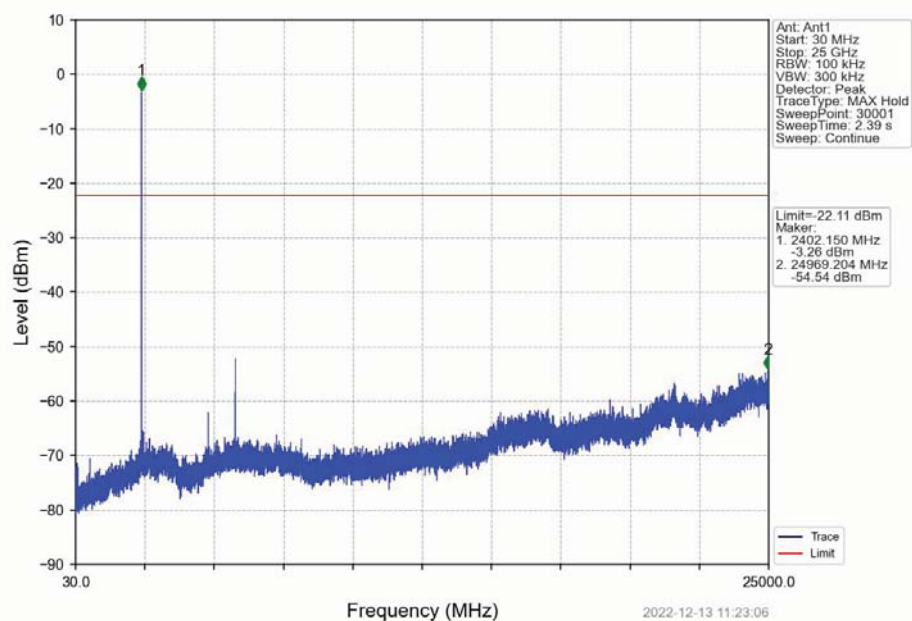
Pi/4DQPSK_2DH5_HOPP_Ant1_NTNV



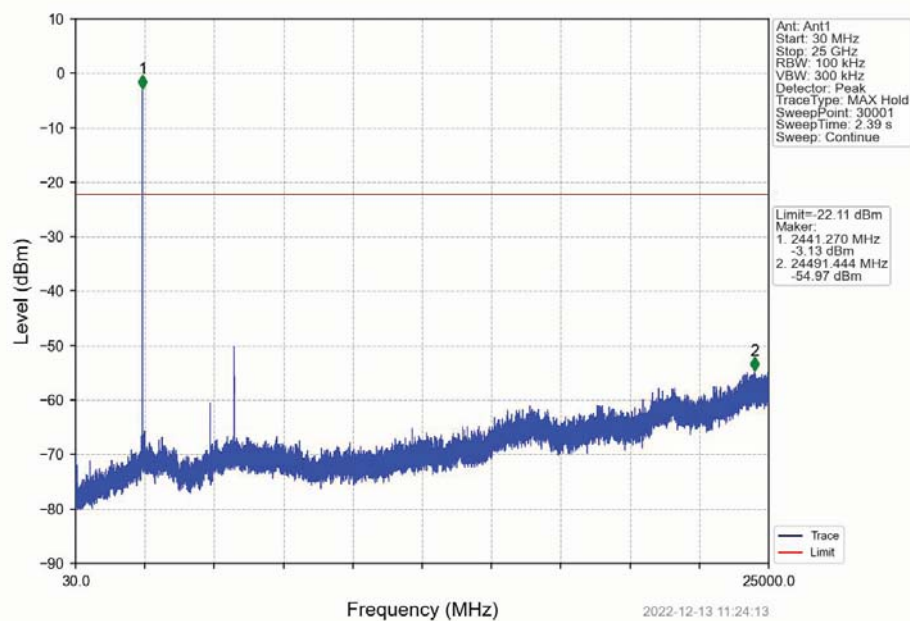
8DPSK_3DH5_LCH_2402MHz_Ant1_NTNV



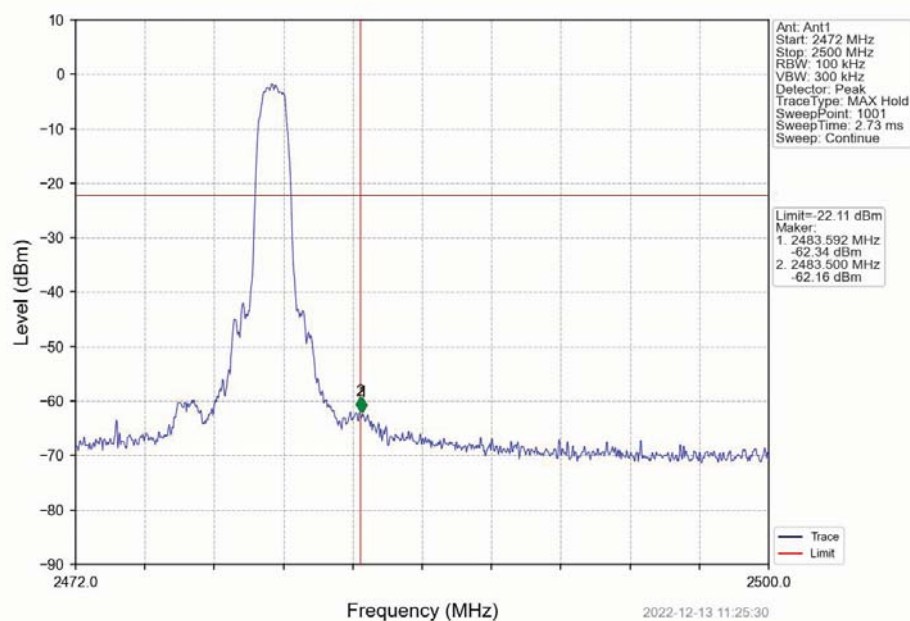
8DPSK_3DH5_LCH_2402MHz_Ant1_NTNV



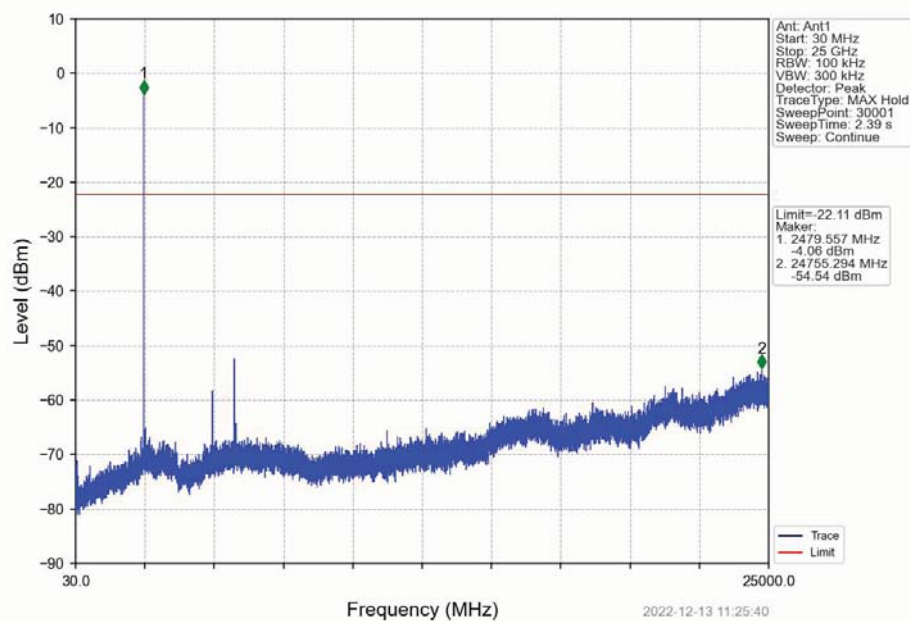
8DPSK_3DH5_MCH_2441MHz_Ant1_NTNV



8DPSK_3DH5_HCH_2480MHz_Ant1_NTNV



8DPSK_3DH5_HCH_2480MHz_Ant1_NTNV



8DPSK_3DH5_HOPP_Ant1_NTNV

