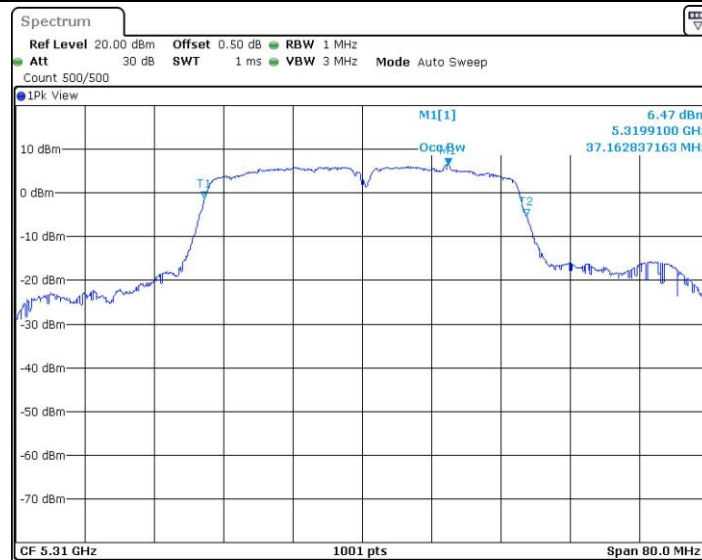




11N40SISO_Ant2_5270



11N40SISO_Ant1_5310



11N40SISO_Ant2_5310



11N40SISO_Ant1_5510



11N40SISO_Ant2_5510



11N40SISO_Ant1_5550



11N40SISO_Ant2_5550



11N40SISO_Ant1_5670



11N40SISO_Ant2_5670



11N40SISO_Ant1_5755



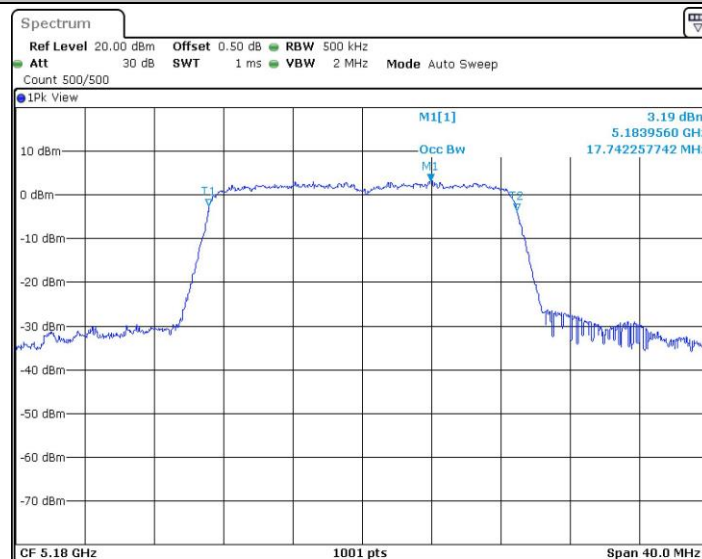
11N40SISO_Ant2_5755



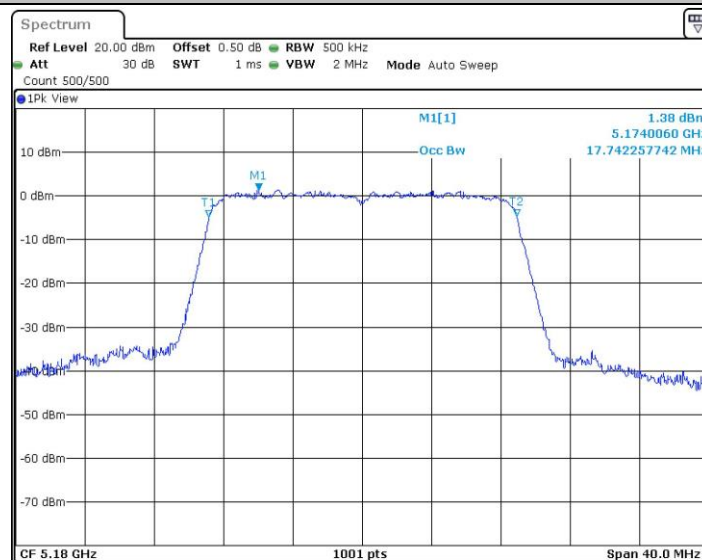
11N40SISO_Ant1_5795



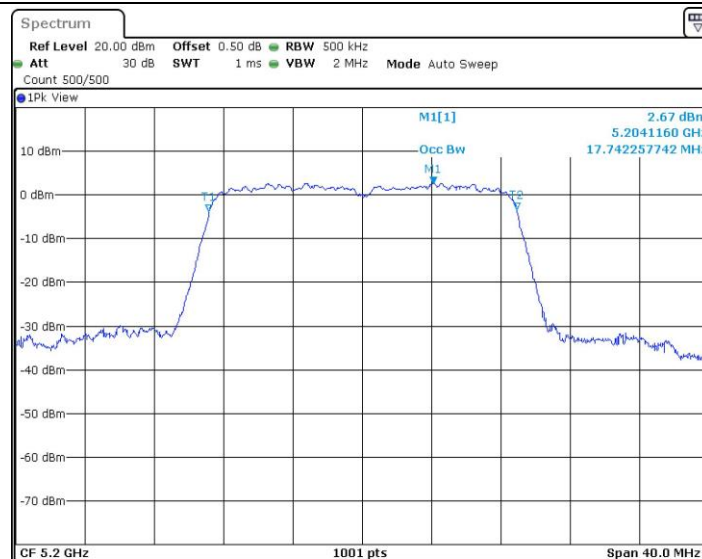
11N40SISO_Ant2_5795



11AC20SISO_Ant1_5180



11AC20SISO_Ant2_5180



11AC20SISO_Ant1_5200



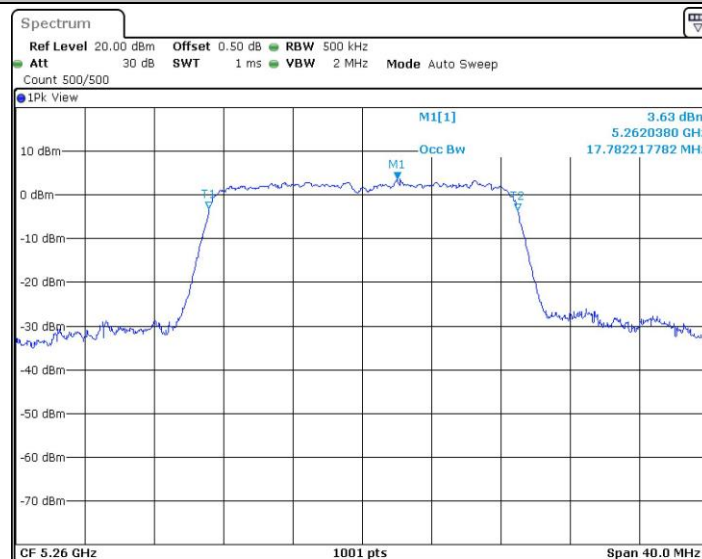
11AC20SISO_Ant2_5200



11AC20SISO_Ant1_5240



11AC20SISO_Ant2_5240



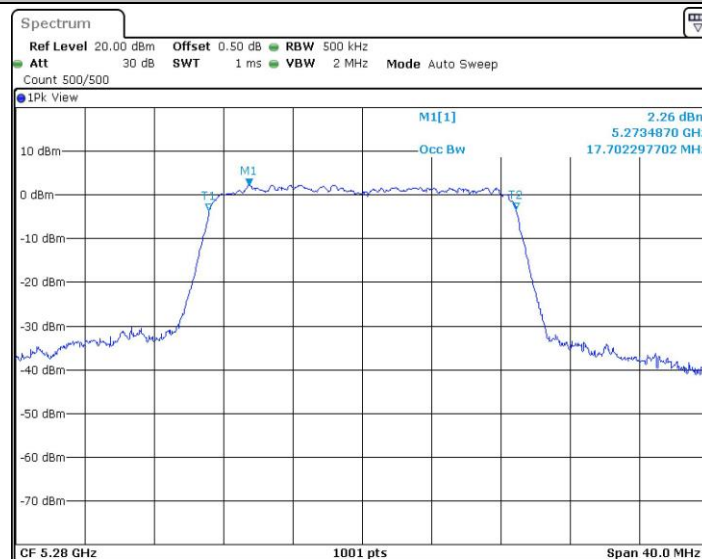
11AC20SISO_Ant1_5260



11AC20SISO_Ant2_5260



11AC20SISO_Ant1_5280



11AC20SISO_Ant2_5280



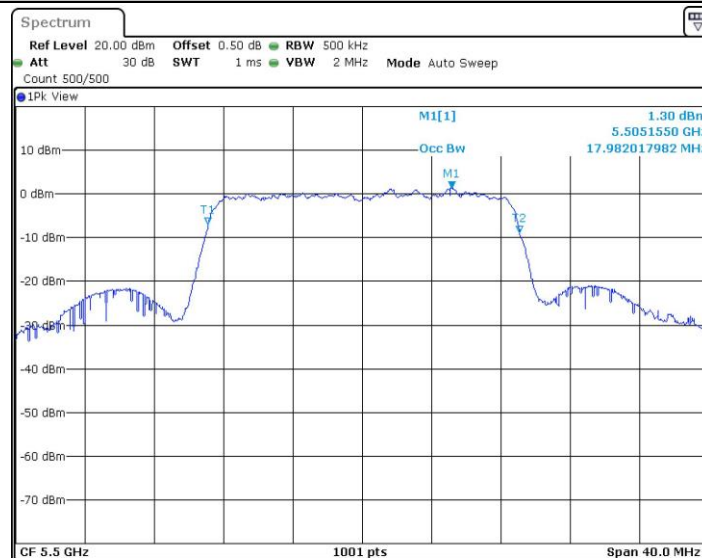
11AC20SISO_Ant1_5320



11AC20SISO_Ant2_5320



11AC20SISO_Ant1_5500



11AC20SISO_Ant2_5500



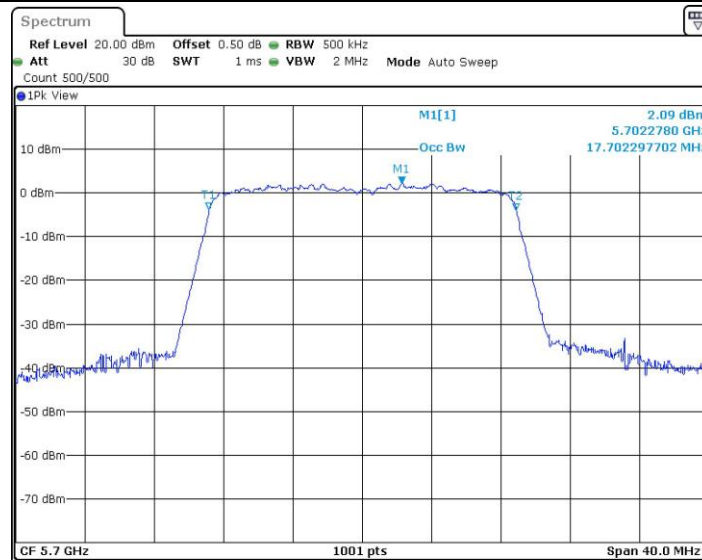
11AC20SISO_Ant1_5580



11AC20SISO_Ant2_5580



11AC20SISO_Ant1_5700



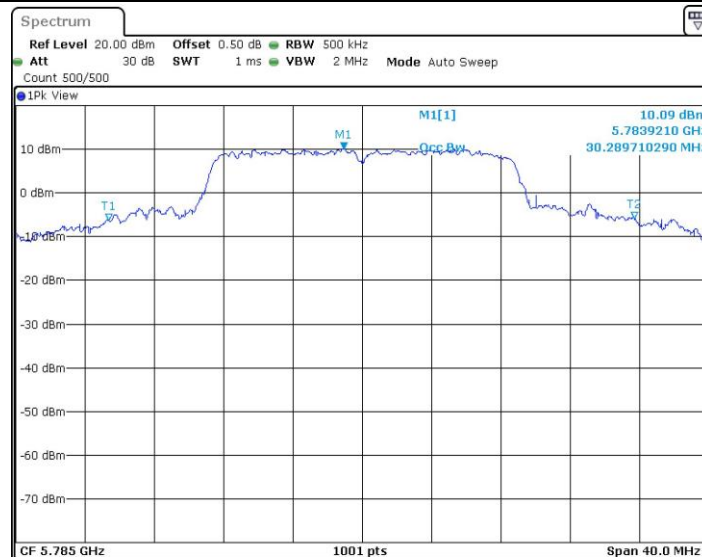
11AC20SISO_Ant2_5700



11AC20SISO_Ant1_5745



11AC20SISO_Ant2_5745



11AC20SISO_Ant1_5785



11AC20SISO_Ant2_5785



11AC20SISO_Ant1_5825



11AC20SISO_Ant2_5825



11AC40SISO_Ant1_5190



11AC40SISO_Ant2_5190



11AC40SISO_Ant1_5230



11AC40SISO_Ant2_5230



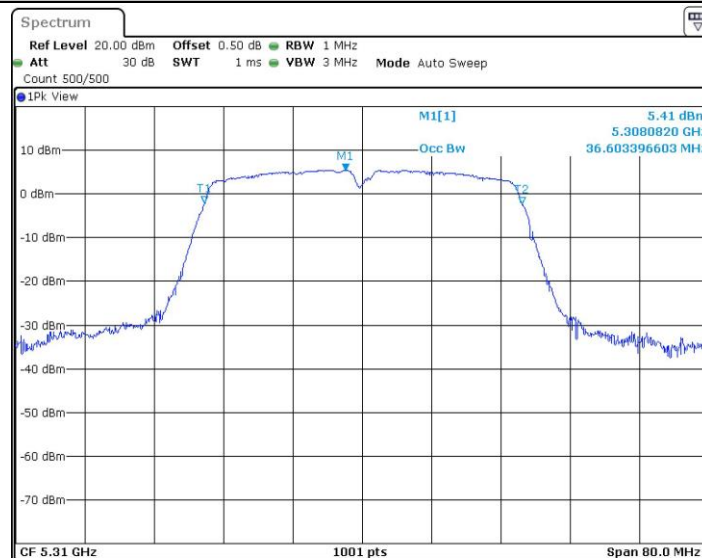
11AC40SISO_Ant1_5270



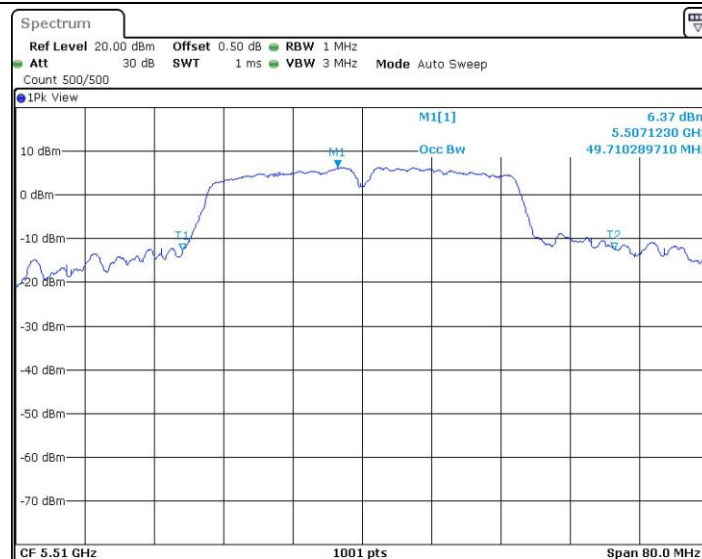
11AC40SISO_Ant2_5270



11AC40SISO_Ant1_5310



11AC40SISO_Ant2_5310



11AC40SISO_Ant1_5510



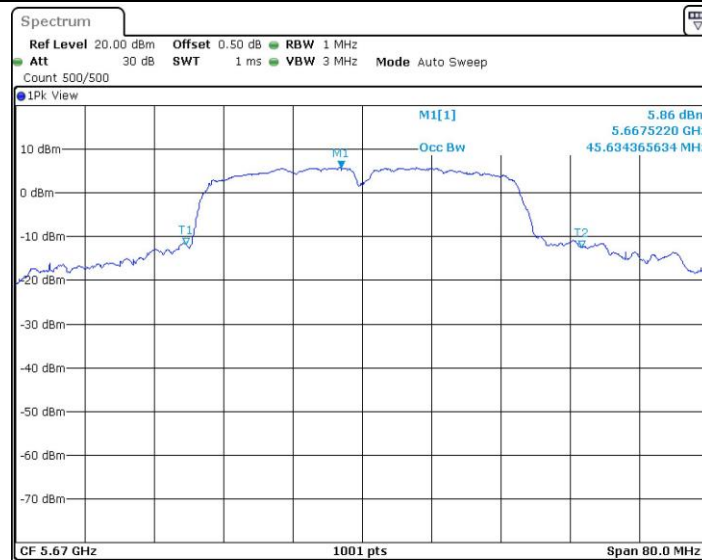
11AC40SISO_Ant2_5510



11AC40SISO_Ant1_5550



11AC40SISO_Ant2_5550



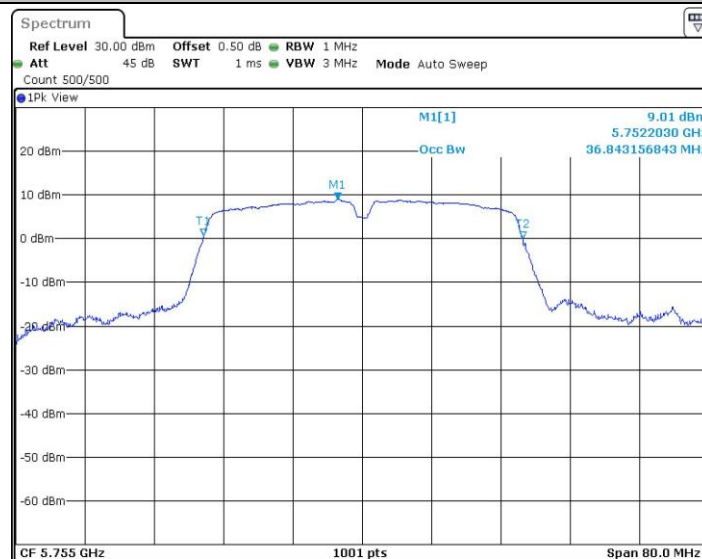
11AC40SISO_Ant1_5670



11AC40SISO_Ant2_5670



11AC40SISO_Ant1_5755



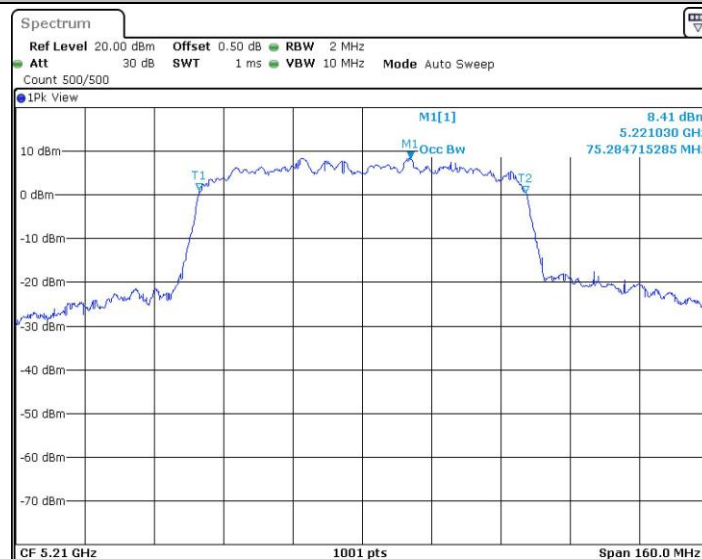
11AC40SISO_Ant2_5755



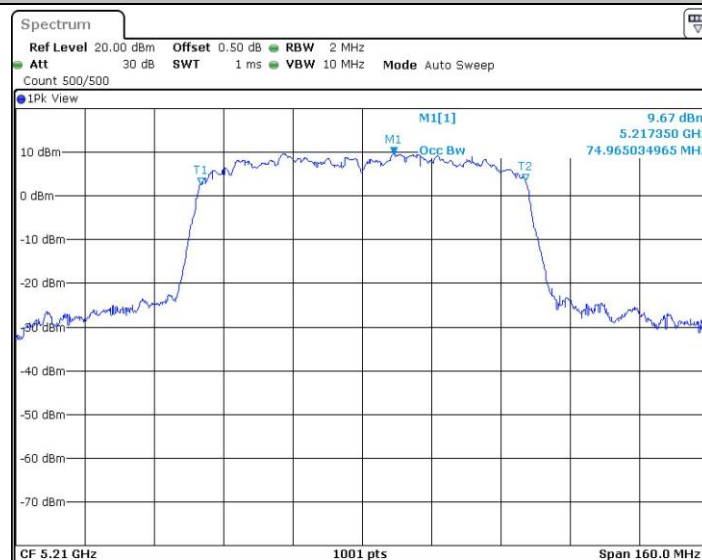
11AC40SISO_Ant1_5795



11AC40SISO_Ant2_5795



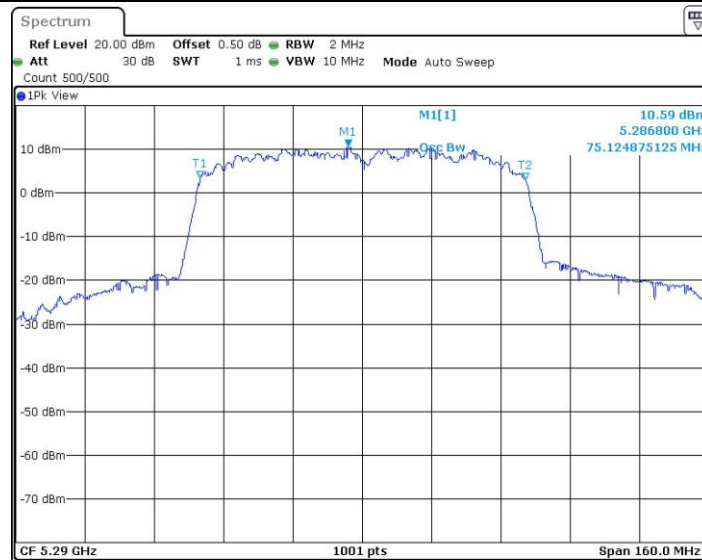
11AC80SISO_Ant1_5210



11AC80SISO_Ant2_5210



11AC80SISO_Ant1_5290



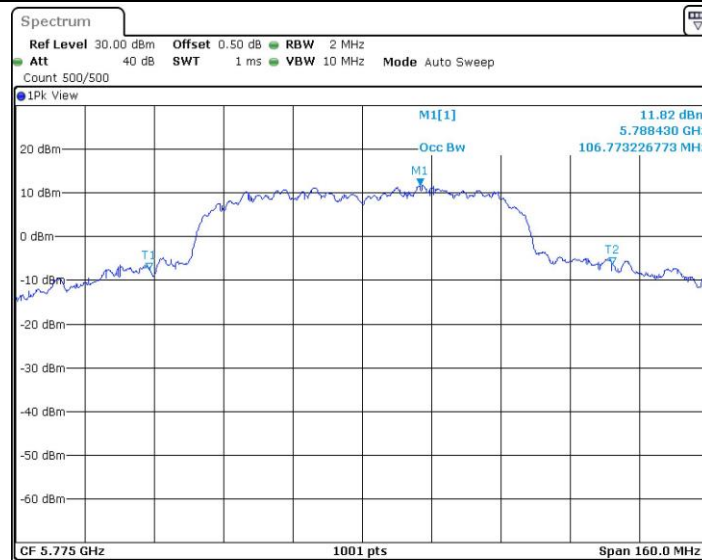
11AC80SISO_Ant2_5290



11AC80SISO_Ant1_5530



11AC80SISO_Ant2_5530



11AC80SISO_Ant1_5775



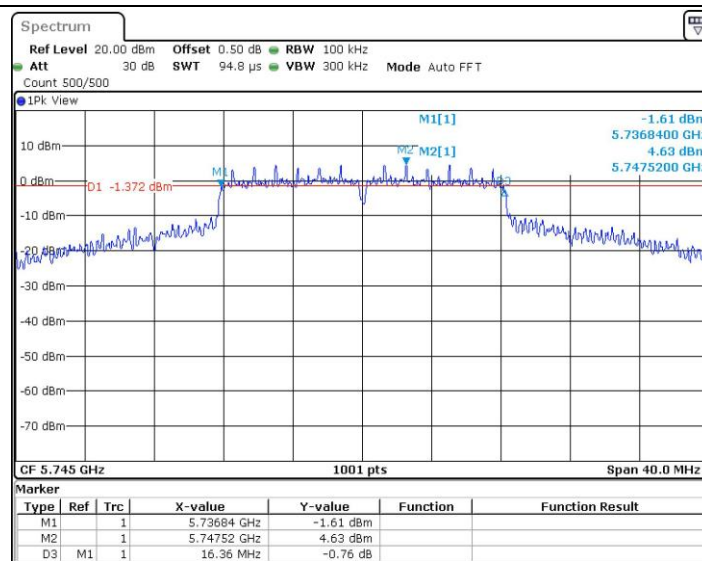
11AC80SISO_Ant2_5775

Appendix A3: Min emission bandwidth

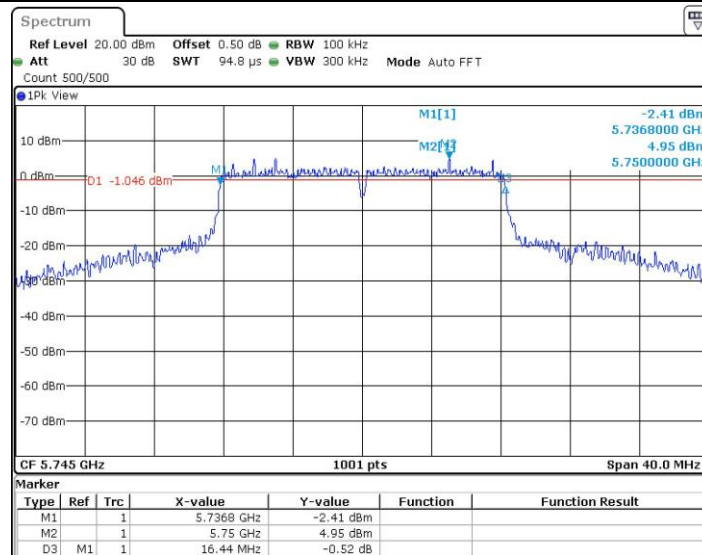
Test Result

TestMode	Antenna	Channel	6db EBW [MHz]	FL[MHz]	FH[MHz]	Limit[MHz]	Verdict
11A	Ant1	5745	16.360	5736.840	5753.200	0.5	PASS
	Ant2	5745	16.440	5736.800	5753.240	0.5	PASS
	Ant1	5785	16.440	5776.800	5793.240	0.5	PASS
	Ant2	5785	16.400	5776.800	5793.200	0.5	PASS
	Ant1	5825	16.360	5816.840	5833.200	0.5	PASS
	Ant2	5825	16.400	5816.800	5833.200	0.5	PASS
11N20SISO	Ant1	5745	17.040	5736.560	5753.600	0.5	PASS
	Ant2	5745	17.400	5736.440	5753.840	0.5	PASS
	Ant1	5785	17.640	5776.200	5793.840	0.5	PASS
	Ant2	5785	17.640	5776.200	5793.840	0.5	PASS
	Ant1	5825	17.640	5816.200	5833.840	0.5	PASS
	Ant2	5825	17.640	5816.200	5833.840	0.5	PASS
11N40SISO	Ant1	5755	35.280	5737.400	5772.680	0.5	PASS
	Ant2	5755	35.280	5737.400	5772.680	0.5	PASS
	Ant1	5795	35.280	5777.400	5812.680	0.5	PASS
	Ant2	5795	35.280	5777.400	5812.680	0.5	PASS
11AC20SISO	Ant1	5745	17.040	5736.560	5753.600	0.5	PASS
	Ant2	5745	17.640	5736.200	5753.840	0.5	PASS
	Ant1	5785	17.400	5776.440	5793.840	0.5	PASS
	Ant2	5785	17.640	5776.200	5793.840	0.5	PASS
	Ant1	5825	17.600	5816.200	5833.800	0.5	PASS
	Ant2	5825	17.640	5816.200	5833.840	0.5	PASS
11AC40SISO	Ant1	5755	35.280	5737.400	5772.680	0.5	PASS
	Ant2	5755	35.280	5737.400	5772.680	0.5	PASS
	Ant1	5795	35.280	5777.400	5812.680	0.5	PASS
	Ant2	5795	35.280	5777.400	5812.680	0.5	PASS
11AC80SISO	Ant1	5775	60.480	5746.040	5806.520	0.5	PASS
	Ant2	5775	75.520	5737.240	5812.760	0.5	PASS

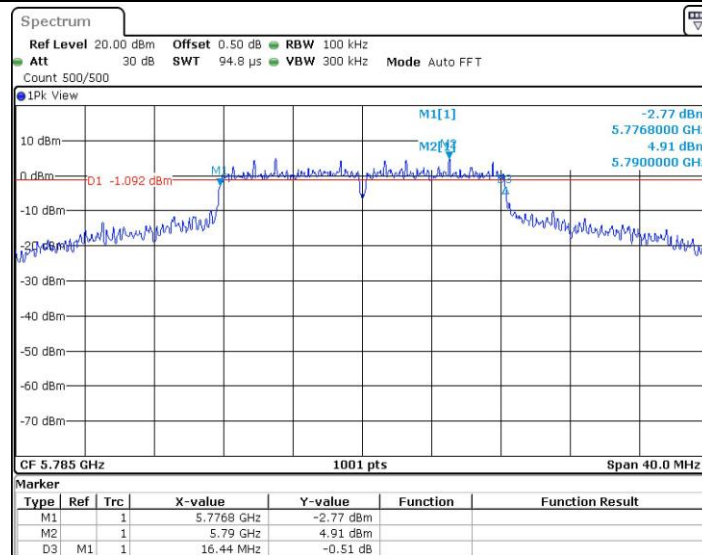
Test Graphs



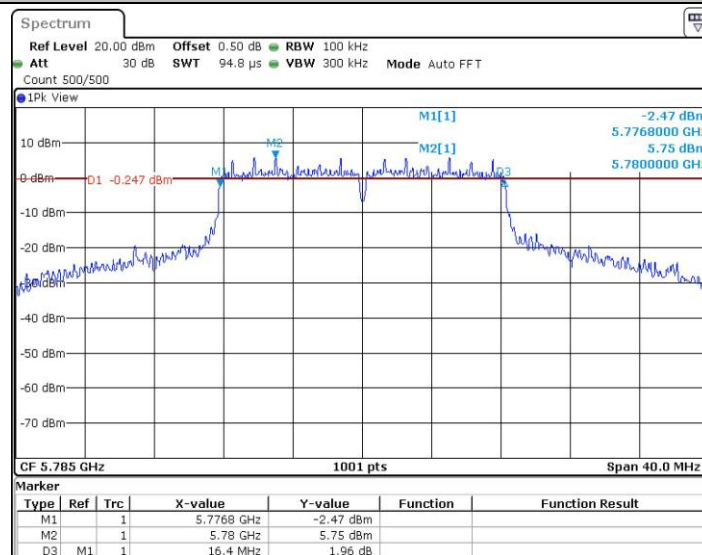
11A_Ant1_5745



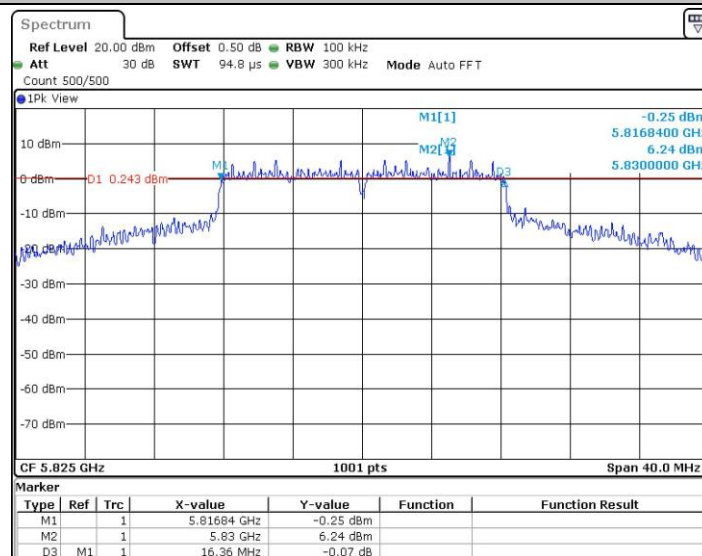
11A_Ant2_5745



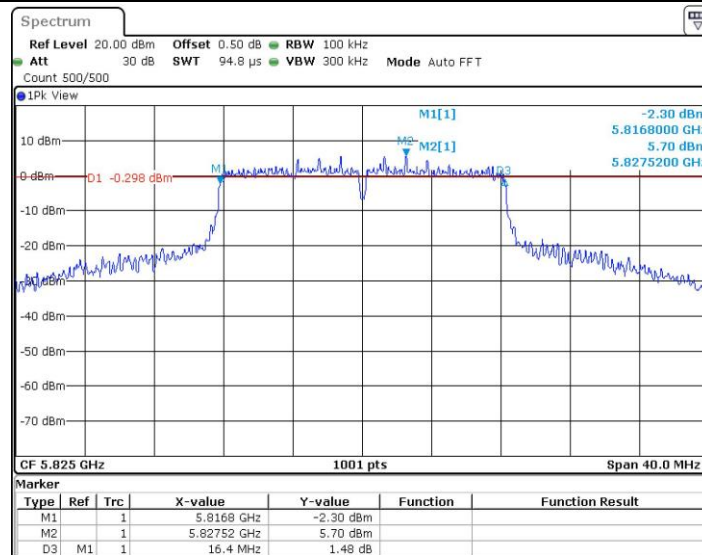
11A_Ant1_5785



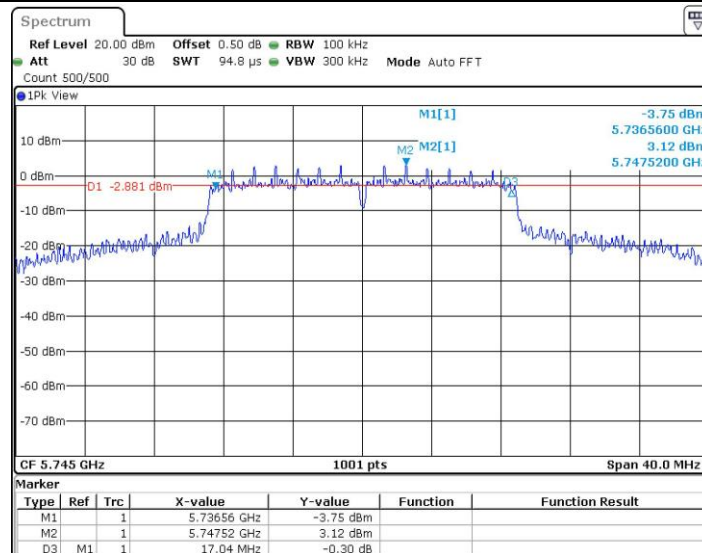
11A_Ant2_5785



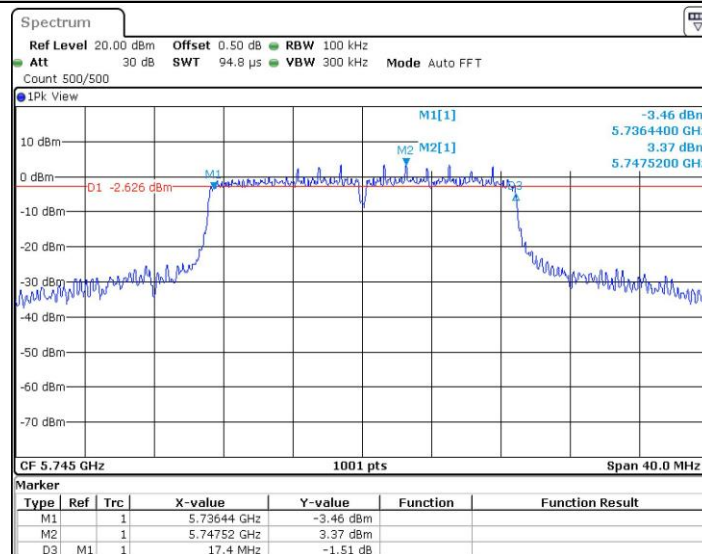
11A_Ant1_5825



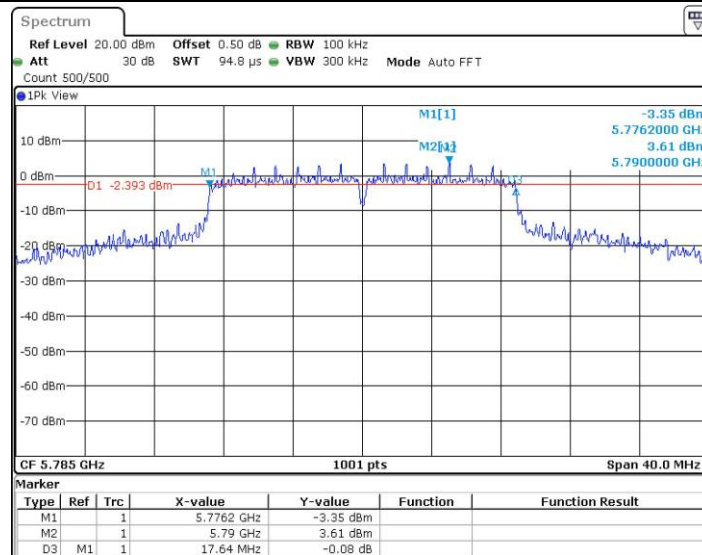
11A_Ant2_5825



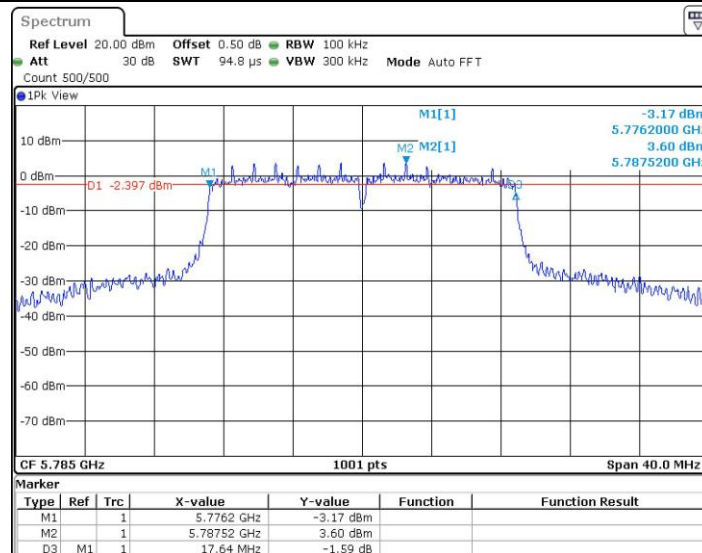
11N20SISO_Ant1_5745



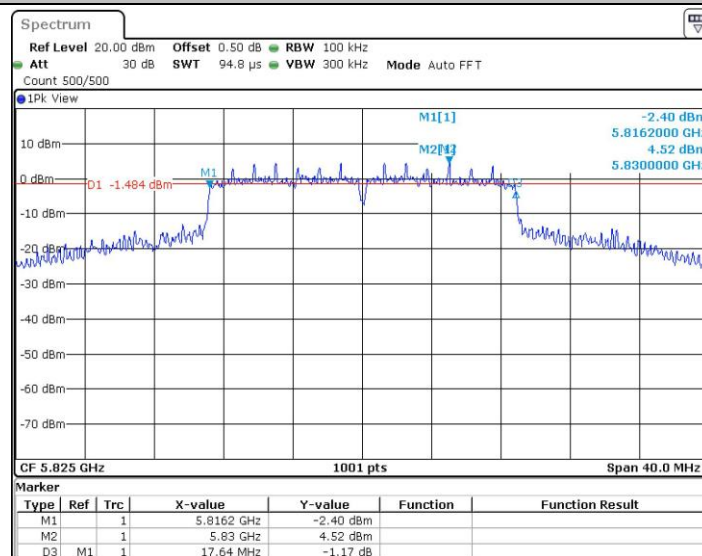
11N20SISO_Ant2_5745



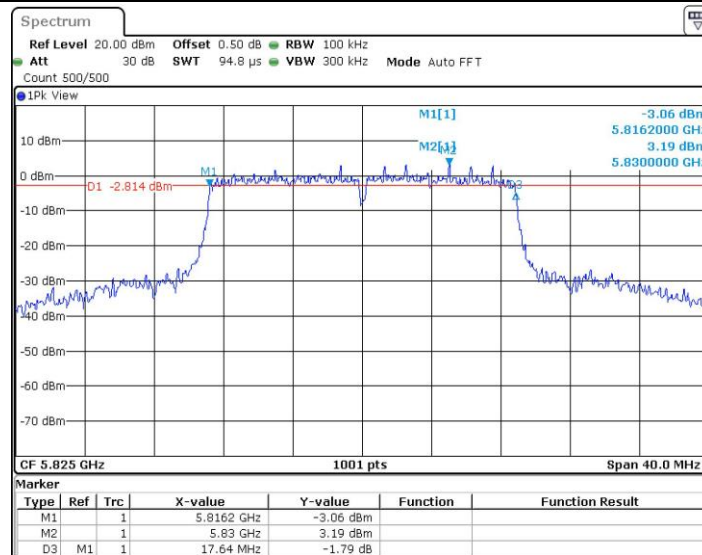
11N20SISO_Ant1_5785



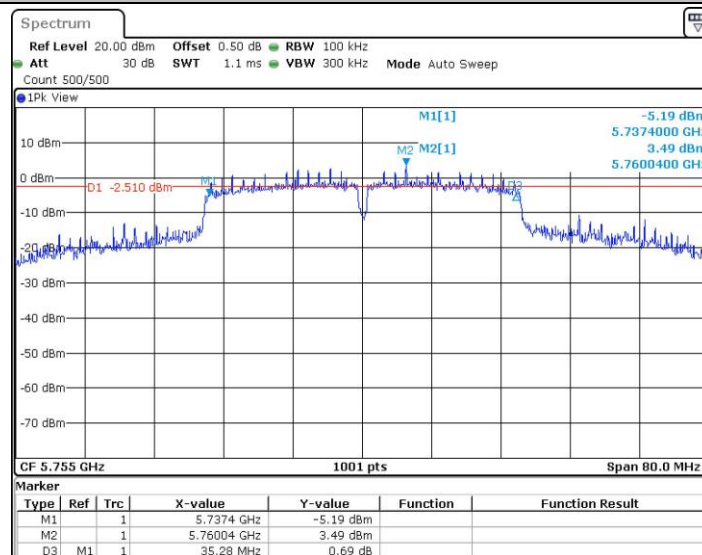
11N20SISO_Ant2_5785



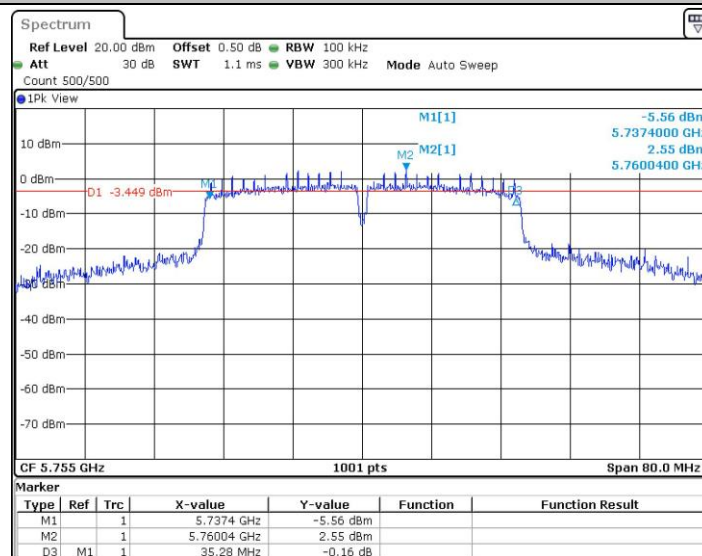
11N20SISO_Ant1_5825



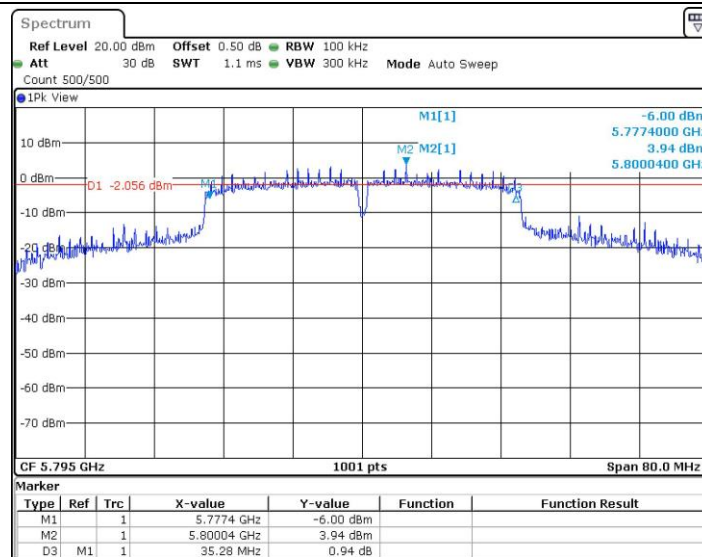
11N20SISO_Ant2_5825



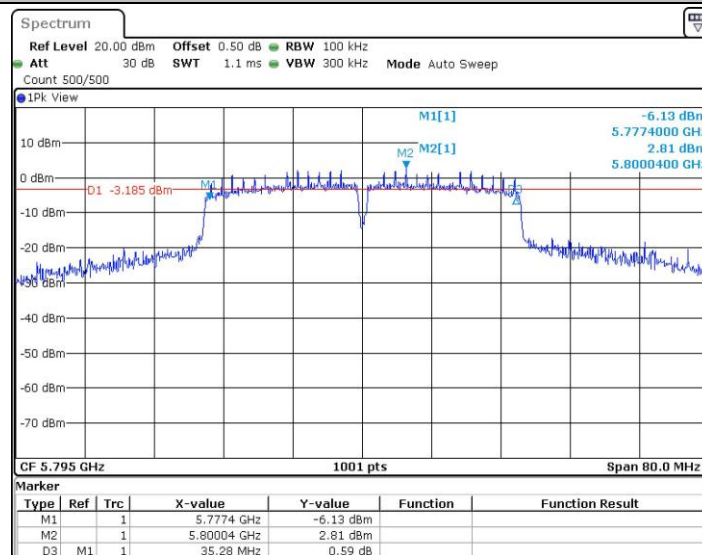
11N40SISO_Ant1_5755



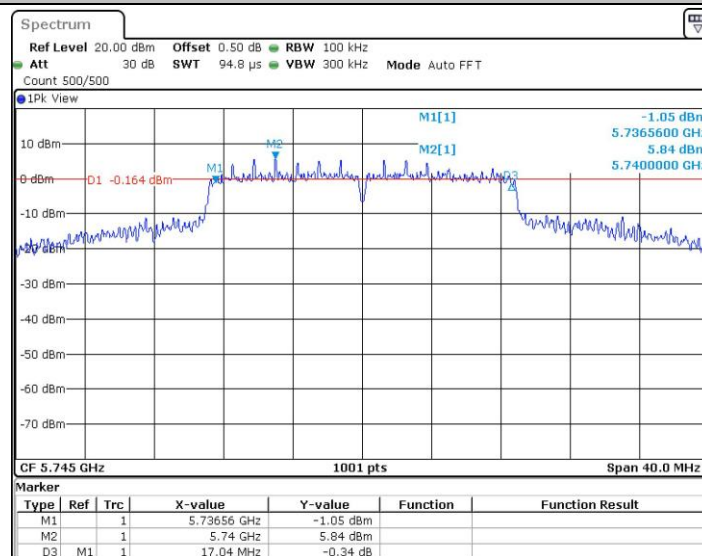
11N40SISO_Ant2_5755



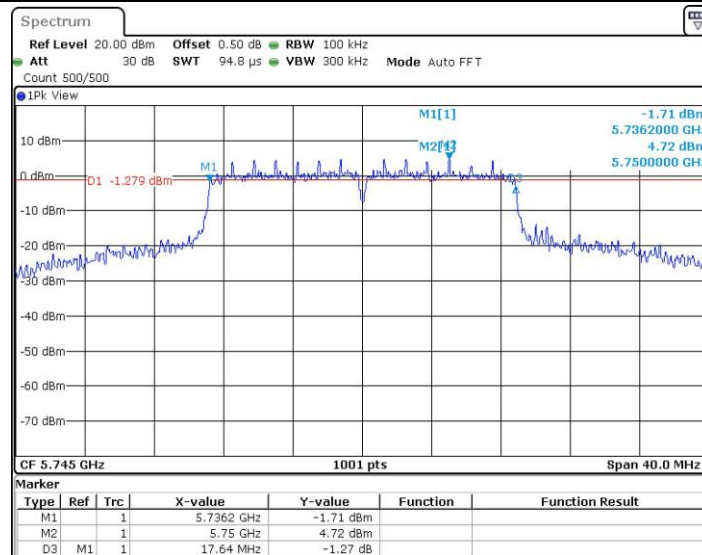
11N40SISO_Ant1_5795



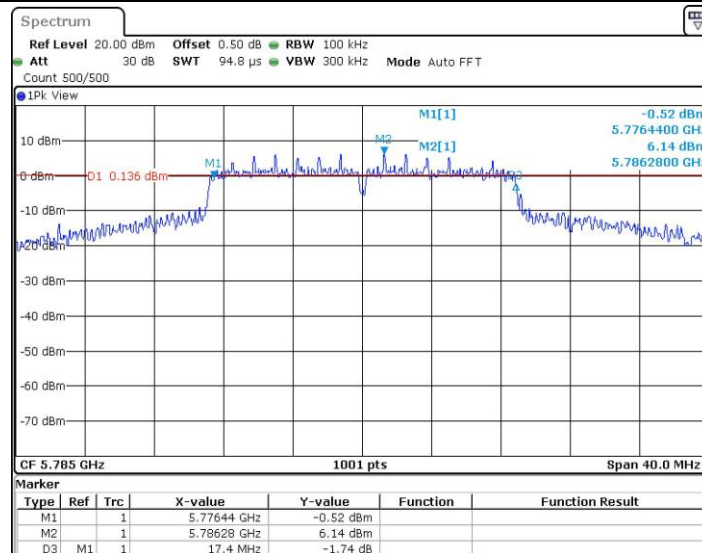
11N40SISO_Ant2_5795



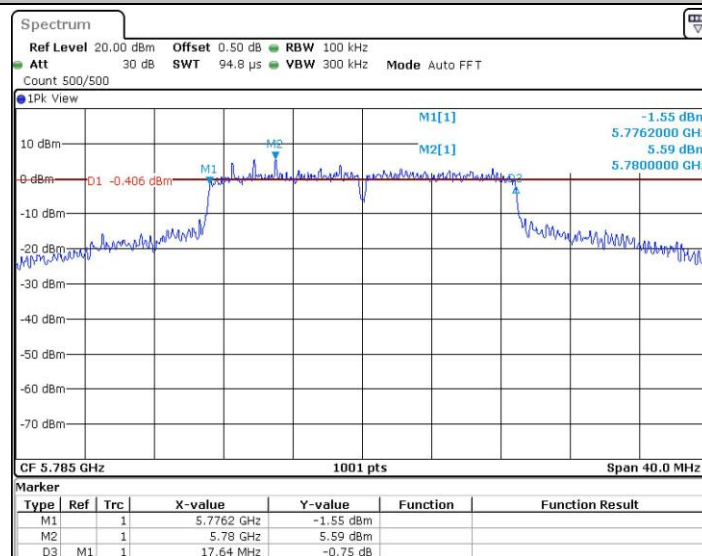
11AC20SISO_Ant1_5745



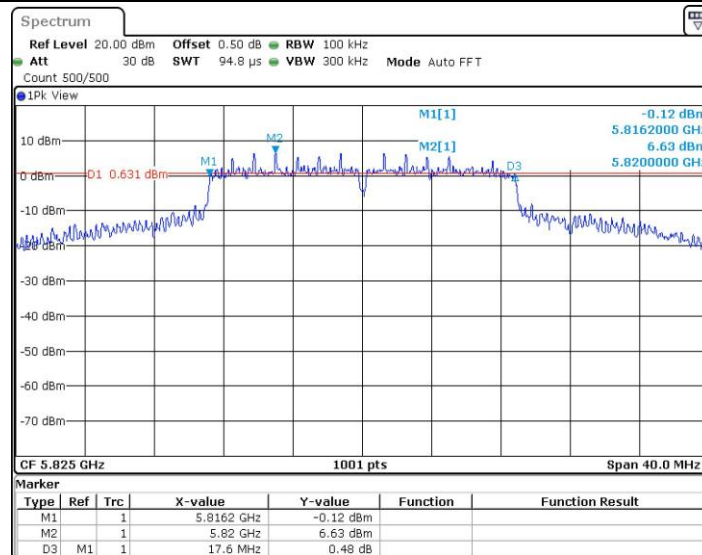
11AC20SISO_Ant2_5745



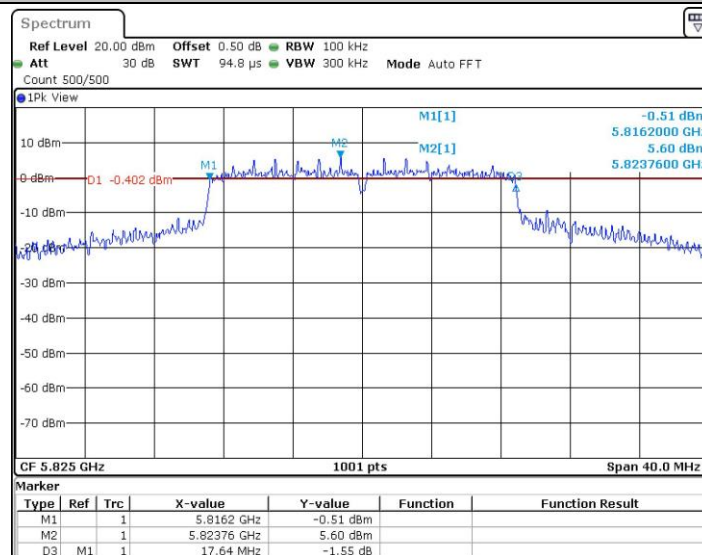
11AC20SISO_Ant1_5785



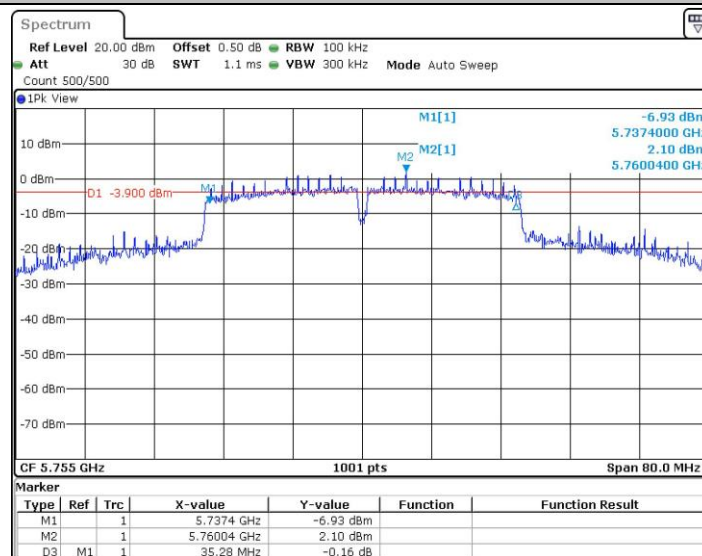
11AC20SISO_Ant2_5785



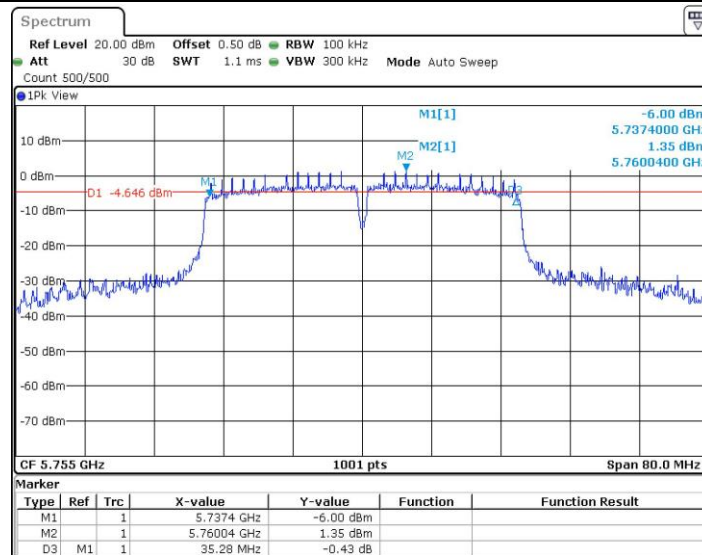
11AC20SISO_Ant1_5825



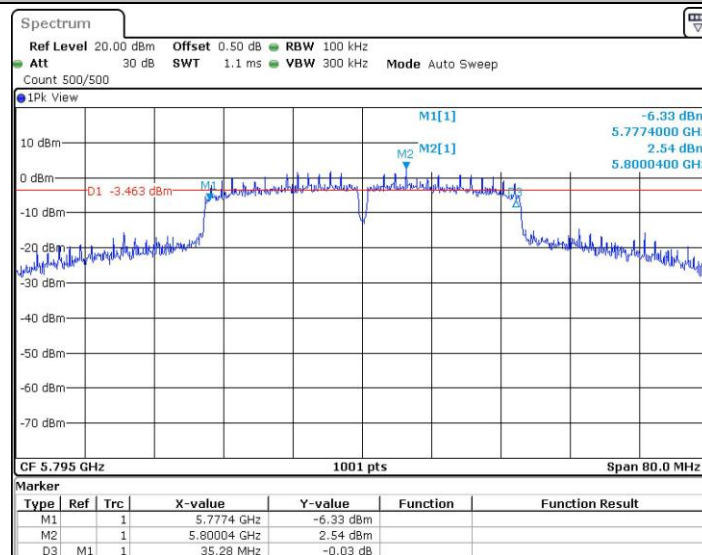
11AC20SISO_Ant2_5825



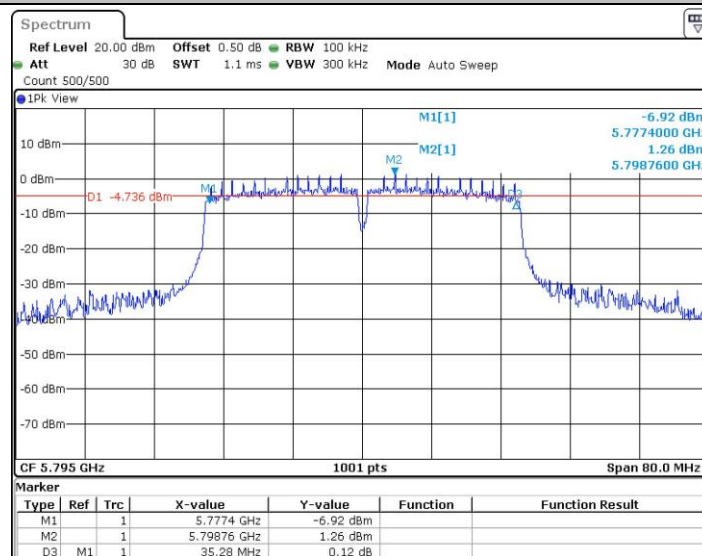
11AC40SISO_Ant1_5755



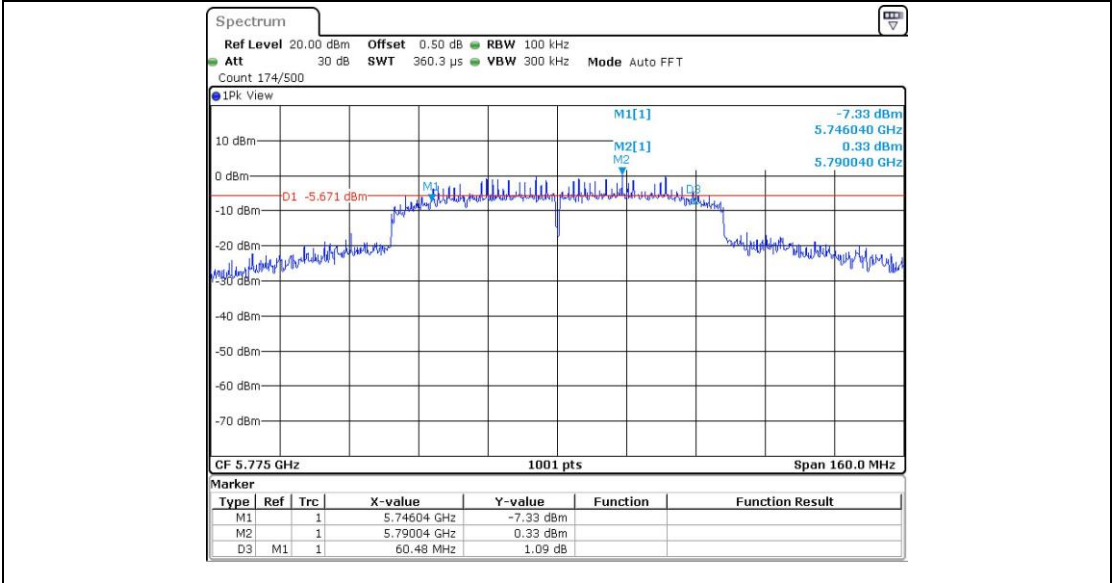
11AC40SISO_Ant2_5755



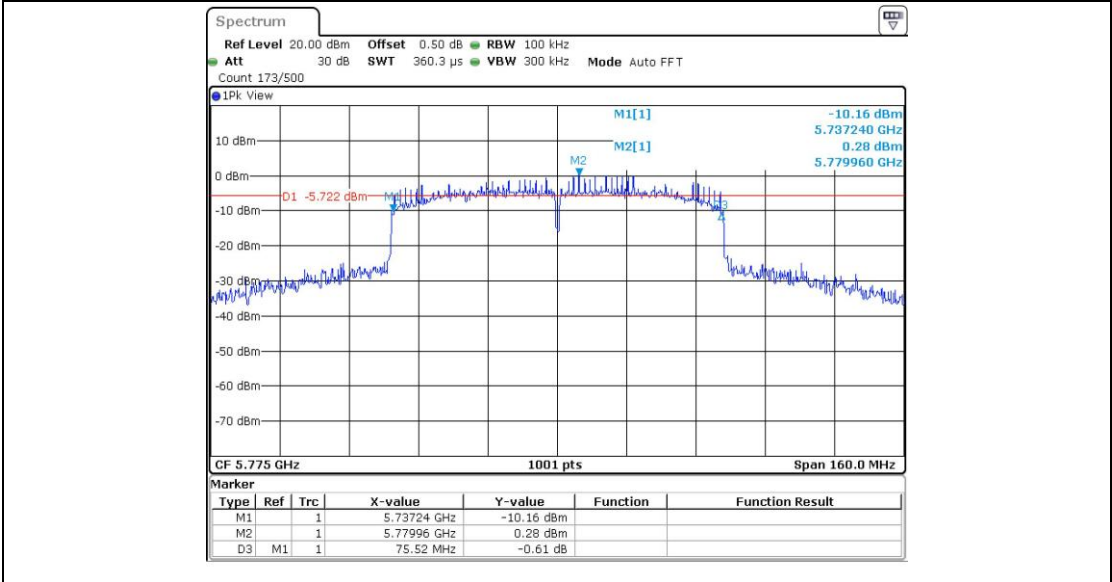
11AC40SISO_Ant1_5795



11AC40SISO_Ant2_5795



11AC80SISO_Ant1_5775



11AC80SISO_Ant2_5775

Appendix B1: Maximum conducted output power

Test Result

TestMode	Antenna	Channel	Result	Limit	Verdict
11A	Ant2	5180	13.93	<=24	PASS
	Ant1	5180	13.42	<=24	PASS
	Ant2	5200	13.94	<=24	PASS
	Ant1	5200	13.52	<=24	PASS
	Ant2	5240	14.17	<=24	PASS
	Ant1	5240	13.56	<=24	PASS
	Ant2	5260	13.53	<=24	PASS
	Ant1	5260	12.74	<=24	PASS
	Ant2	5280	13.53	<=24	PASS
	Ant1	5280	12.80	<=24	PASS
	Ant2	5320	13.44	<=24	PASS
	Ant1	5320	12.70	<=24	PASS
	Ant2	5500	11.84	<=24	PASS
	Ant1	5500	12.09	<=24	PASS
	Ant2	5580	12.61	<=24	PASS
	Ant1	5580	13.55	<=24	PASS
	Ant2	5700	13.81	<=24	PASS
	Ant1	5700	14.37	<=24	PASS
	Ant2	5745	15.64	<=30	PASS
	Ant1	5745	16.92	<=30	PASS
	Ant2	5785	16.34	<=30	PASS
	Ant1	5785	17.11	<=30	PASS
	Ant2	5825	17.49	<=30	PASS
	Ant1	5825	16.96	<=30	PASS
11N20SISO	Ant2	5180	9.11	<=24	PASS
	Ant1	5180	9.52	<=24	PASS
	Ant2	5200	9.47	<=24	PASS
	Ant1	5200	9.97	<=24	PASS
	Ant2	5240	9.78	<=24	PASS
	Ant1	5240	10.21	<=24	PASS
	Ant2	5260	8.88	<=24	PASS
	Ant1	5260	9.53	<=24	PASS
	Ant2	5280	9.17	<=24	PASS
	Ant1	5280	9.38	<=24	PASS
	Ant2	5320	8.89	<=24	PASS
	Ant1	5320	9.21	<=24	PASS
	Ant2	5500	9.84	<=24	PASS
	Ant1	5500	9.69	<=24	PASS

	Ant2	5580	10.47	<=24	PASS
	Ant1	5580	9.90	<=24	PASS
	Ant2	5700	9.30	<=24	PASS
	Ant1	5700	9.59	<=24	PASS
	Ant2	5745	13.99	<=30	PASS
	Ant1	5745	14.81	<=30	PASS
	Ant2	5785	14.77	<=30	PASS
	Ant1	5785	15.19	<=30	PASS
	Ant2	5825	15.68	<=30	PASS
	Ant1	5825	14.94	<=30	PASS
11N40SISO	Ant2	5190	12.65	<=24	PASS
	Ant1	5190	11.68	<=24	PASS
	Ant2	5230	13.06	<=24	PASS
	Ant1	5230	12.01	<=24	PASS
	Ant2	5270	12.19	<=24	PASS
	Ant1	5270	12.49	<=24	PASS
	Ant2	5310	11.82	<=24	PASS
	Ant1	5310	12.01	<=24	PASS
	Ant2	5510	11.57	<=24	PASS
	Ant1	5510	10.81	<=24	PASS
	Ant2	5550	12.51	<=24	PASS
	Ant1	5550	12.10	<=24	PASS
	Ant2	5670	12.69	<=24	PASS
	Ant1	5670	11.72	<=24	PASS
	Ant2	5755	16.34	<=30	PASS
	Ant1	5755	15.52	<=30	PASS
	Ant2	5795	16.80	<=30	PASS
	Ant1	5795	15.89	<=30	PASS
11AC20SISO	Ant2	5180	10.22	<=24	PASS
	Ant1	5180	8.29	<=24	PASS
	Ant2	5200	9.67	<=24	PASS
	Ant1	5200	8.48	<=24	PASS
	Ant2	5240	10.24	<=24	PASS
	Ant1	5240	8.20	<=24	PASS
	Ant2	5260	10.14	<=24	PASS
	Ant1	5260	8.32	<=24	PASS
	Ant2	5280	10.27	<=24	PASS
	Ant1	5280	9.05	<=24	PASS
	Ant2	5320	10.45	<=24	PASS
	Ant1	5320	9.76	<=24	PASS
	Ant2	5500	9.73	<=24	PASS
	Ant1	5500	7.71	<=24	PASS
	Ant2	5580	10.18	<=24	PASS

	Ant1	5580	8.17	<=24	PASS
	Ant2	5700	10.11	<=24	PASS
	Ant1	5700	9.11	<=24	PASS
	Ant2	5745	16.78	<=30	PASS
	Ant1	5745	16.14	<=30	PASS
	Ant2	5785	17.07	<=30	PASS
	Ant1	5785	16.78	<=30	PASS
	Ant2	5825	17.31	<=30	PASS
	Ant1	5825	17.19	<=30	PASS
11AC40SISO	Ant2	5190	11.04	<=24	PASS
	Ant1	5190	11.16	<=24	PASS
	Ant2	5230	11.05	<=24	PASS
	Ant1	5230	11.25	<=24	PASS
	Ant2	5270	11.55	<=24	PASS
	Ant1	5270	11.43	<=24	PASS
	Ant2	5310	12.33	<=24	PASS
	Ant1	5310	11.74	<=24	PASS
	Ant2	5510	12.41	<=24	PASS
	Ant1	5510	11.13	<=24	PASS
	Ant2	5550	12.14	<=24	PASS
	Ant1	5550	11.30	<=24	PASS
	Ant2	5670	11.84	<=24	PASS
	Ant1	5670	11.39	<=24	PASS
	Ant2	5755	14.97	<=30	PASS
	Ant1	5755	14.74	<=30	PASS
	Ant2	5795	15.43	<=30	PASS
	Ant1	5795	14.92	<=30	PASS
11AC80SISO	Ant2	5210	11.86	<=24	PASS
	Ant1	5210	13.91	<=24	PASS
	Ant2	5290	13.48	<=24	PASS
	Ant1	5290	14.29	<=24	PASS
	Ant2	5530	13.74	<=24	PASS
	Ant1	5530	12.34	<=24	PASS
	Ant2	5775	15.93	<=30	PASS
	Ant1	5775	16.85	<=30	PASS
11N20MIMO	Ant2	5180	8.90	<=24	PASS
	Ant1	5180	8.61	<=24	PASS
	total	5180	11.8	<=22.81	PASS
	Ant2	5200	8.63	<=24	PASS
	Ant1	5200	8.98	<=24	PASS
	total	5200	11.8	<=22.81	PASS
	Ant2	5240	8.94	<=24	PASS
	Ant1	5240	8.92	<=24	PASS

	total	5240	11.9	<=22.81	PASS
	Ant2	5260	8.04	<=24	PASS
	Ant1	5260	8.05	<=24	PASS
	total	5260	11.1	<=22.81	PASS
	Ant2	5280	8.09	<=24	PASS
	Ant1	5280	8.10	<=24	PASS
	total	5280	11.1	<=22.81	PASS
	Ant2	5320	7.86	<=24	PASS
	Ant1	5320	8.09	<=24	PASS
	total	5320	11.0	<=22.81	PASS
	Ant2	5500	8.89	<=24	PASS
	Ant1	5500	8.60	<=24	PASS
	total	5500	11.8	<=22.81	PASS
	Ant2	5580	9.03	<=24	PASS
	Ant1	5580	8.63	<=24	PASS
	total	5580	11.8	<=22.81	PASS
	Ant2	5700	8.11	<=24	PASS
	Ant1	5700	8.22	<=24	PASS
	total	5700	11.2	<=22.81	PASS
	Ant2	5745	13.16	<=30	PASS
	Ant1	5745	13.39	<=30	PASS
	total	5745	16.3	<=28.81	PASS
	Ant2	5785	13.59	<=30	PASS
	Ant1	5785	13.65	<=30	PASS
	total	5785	16.6	<=28.81	PASS
	Ant2	5825	14.42	<=30	PASS
	Ant1	5825	13.83	<=30	PASS
	total	5825	17.1	<=28.81	PASS
11N40MIMO	Ant2	5190	12.56	<=24	PASS
	Ant1	5190	11.16	<=24	PASS
	total	5190	14.9	<=22.81	PASS
	Ant2	5230	12.02	<=24	PASS
	Ant1	5230	11.22	<=24	PASS
	total	5230	14.6	<=22.81	PASS
	Ant2	5270	11.51	<=24	PASS
	Ant1	5270	11.63	<=24	PASS
	total	5270	14.6	<=22.81	PASS
	Ant2	5310	11.17	<=24	PASS
	Ant1	5310	10.89	<=24	PASS
	total	5310	14.0	<=22.81	PASS
	Ant2	5510	11.77	<=24	PASS
	Ant1	5510	9.85	<=24	PASS
	total	5510	13.9	<=22.81	PASS

	Ant2	5550	13.19	<=24	PASS
	Ant1	5550	11.18	<=24	PASS
	total	5550	15.3	<=22.81	PASS
	Ant2	5670	11.16	<=24	PASS
	Ant1	5670	10.71	<=24	PASS
	total	5670	14.0	<=22.81	PASS
	Ant2	5755	14.85	<=30	PASS
	Ant1	5755	14.50	<=30	PASS
	total	5755	17.7	<=28.81	PASS
	Ant2	5795	15.59	<=30	PASS
	Ant1	5795	14.90	<=30	PASS
	total	5795	18.3	<=28.81	PASS
11AC20MIMO	Ant2	5180	9.48	<=24	PASS
	Ant1	5180	8.31	<=24	PASS
	total	5180	11.9	<=22.81	PASS
	Ant2	5200	9.26	<=24	PASS
	Ant1	5200	7.65	<=24	PASS
	total	5200	11.5	<=22.81	PASS
	Ant2	5240	10.35	<=24	PASS
	Ant1	5240	7.56	<=24	PASS
	total	5240	12.2	<=22.81	PASS
	Ant2	5260	9.09	<=24	PASS
	Ant1	5260	7.17	<=24	PASS
	total	5260	11.2	<=22.81	PASS
	Ant2	5280	9.28	<=24	PASS
	Ant1	5280	6.99	<=24	PASS
	total	5280	11.3	<=22.81	PASS
	Ant2	5320	9.20	<=24	PASS
	Ant1	5320	6.54	<=24	PASS
	total	5320	11.1	<=22.81	PASS
	Ant2	5500	9.03	<=24	PASS
	Ant1	5500	6.89	<=24	PASS
	total	5500	11.1	<=22.81	PASS
	Ant2	5580	8.97	<=24	PASS
	Ant1	5580	6.85	<=24	PASS
	total	5580	11.0	<=22.81	PASS
	Ant2	5700	9.00	<=24	PASS
	Ant1	5700	8.18	<=24	PASS
	total	5700	11.6	<=22.81	PASS
	Ant2	5745	16.11	<=30	PASS
	Ant1	5745	15.02	<=30	PASS
	total	5745	18.6	<=28.81	PASS
	Ant2	5785	16.40	<=30	PASS

	Ant1	5785	16.02	<=30	PASS
	total	5785	19.2	<=28.81	PASS
	Ant2	5825	16.34	<=30	PASS
	Ant1	5825	16.40	<=30	PASS
	total	5825	19.4	<=28.81	PASS
11AC40MIMO	Ant2	5190	11.77	<=24	PASS
	Ant1	5190	9.85	<=24	PASS
	total	5190	13.9	<=22.81	PASS
	Ant2	5230	11.96	<=24	PASS
	Ant1	5230	9.89	<=24	PASS
	total	5230	14.1	<=22.81	PASS
	Ant2	5270	11.23	<=24	PASS
	Ant1	5270	10.46	<=24	PASS
	total	5270	13.9	<=22.81	PASS
	Ant2	5310	10.97	<=24	PASS
	Ant1	5310	10.02	<=24	PASS
	total	5310	13.5	<=22.81	PASS
	Ant2	5510	10.93	<=24	PASS
	Ant1	5510	9.71	<=24	PASS
	total	5510	13.4	<=22.81	PASS
	Ant2	5550	11.12	<=24	PASS
	Ant1	5550	9.93	<=24	PASS
	total	5550	13.6	<=22.81	PASS
	Ant2	5670	11.49	<=24	PASS
	Ant1	5670	10.07	<=24	PASS
	total	5670	13.8	<=22.81	PASS
	Ant2	5755	14.29	<=30	PASS
	Ant1	5755	13.46	<=30	PASS
	total	5755	16.9	<=28.81	PASS
	Ant2	5795	14.60	<=30	PASS
	Ant1	5795	14.04	<=30	PASS
	total	5795	17.3	<=28.81	PASS
11AC80MIMO	Ant2	5210	12.64	<=24	PASS
	Ant1	5210	13.03	<=24	PASS
	total	5210	15.8	<=22.81	PASS
	Ant2	5290	13.16	<=24	PASS
	Ant1	5290	13.69	<=24	PASS
	total	5290	16.4	<=22.81	PASS
	Ant2	5530	13.74	<=24	PASS
	Ant1	5530	11.67	<=24	PASS
	total	5530	15.8	<=22.81	PASS
	Ant2	5775	15.96	<=30	PASS
	Ant1	5775	16.88	<=30	PASS

Total Quality. Assured.

	total	5775	19.5	<=28.81	PASS
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Note: The Duty Cycle Factor is compensated in the graph.