

Appendix E.4: Maximum conducted output power

Test Result Channel Power

Test Mode	Antenna	Frequency[MHz]	Channel Power [dBm]	Duty Cycle [%]	DC Factor [dBm]	Result [dBm]	Limit [dBm]	Verdict
11A	Ant1	5260	11.68	80.95	0.92	12.60	≤23.98	PASS
		5280	11.65	80.95	0.92	12.57	≤23.98	PASS
		5320	10.49	80.95	0.92	11.41	≤23.98	PASS
11N20SISO	Ant1	5260	11.63	82.61	0.83	12.46	≤23.98	PASS
		5280	11.79	82.61	0.83	12.62	≤23.98	PASS
		5320	10.59	82.61	0.83	11.42	≤23.98	PASS
11N40SISO	Ant1	5270	9.78	53.65	2.70	12.48	≤23.98	PASS
		5310	8.79	53.57	2.71	11.50	≤23.98	PASS
11AC20SISO	Ant1	5260	11.37	80.00	0.97	12.34	≤23.98	PASS
		5280	11.68	80.95	0.92	12.60	≤23.98	PASS
		5320	10.57	80.00	0.97	11.54	≤23.98	PASS
11AC40SISO	Ant1	5270	9.28	69.15	1.60	10.88	≤23.98	PASS
		5310	8.78	69.29	1.59	10.37	≤23.98	PASS
11AC80SISO	Ant1	5290	7.09	48.32	3.16	10.25	≤23.98	PASS
11AX20SISO	Ant1	5260	11.95	78.95	1.03	12.98	≤23.98	PASS
		5280	11.76	84.21	0.75	12.51	≤23.98	PASS
		5320	10.61	84.21	0.75	11.36	≤23.98	PASS
11AX40SISO	Ant1	5270	7.92	56.61	2.47	10.39	≤23.98	PASS
		5310	7.47	56.80	2.46	9.93	≤23.98	PASS
11AX80SISO	Ant1	5290	6.06	50.45	2.97	9.03	≤23.98	PASS

Note: The Duty Cycle Factor is compensated in the graph.

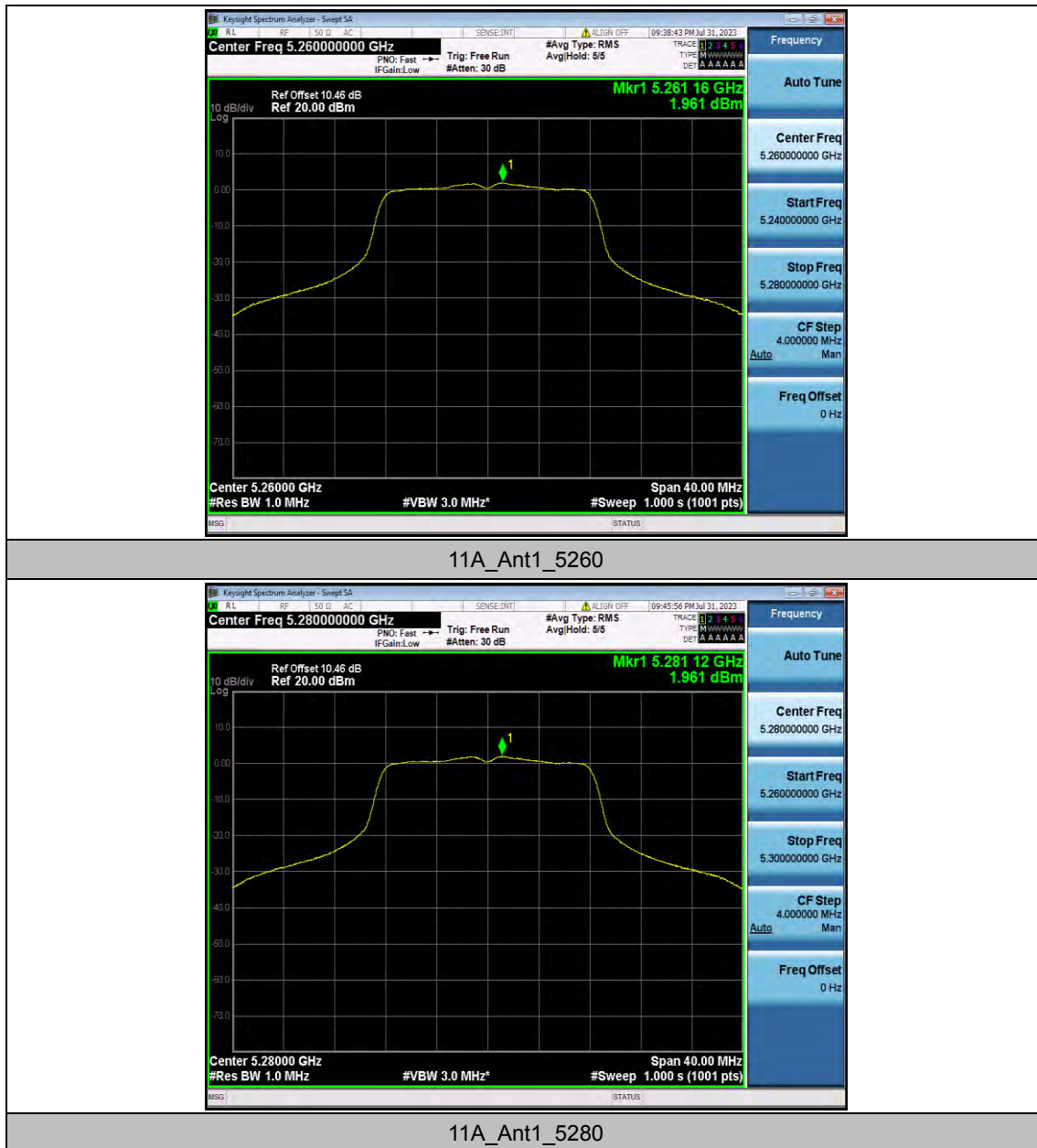
Appendix E.5: Maximum power spectral density

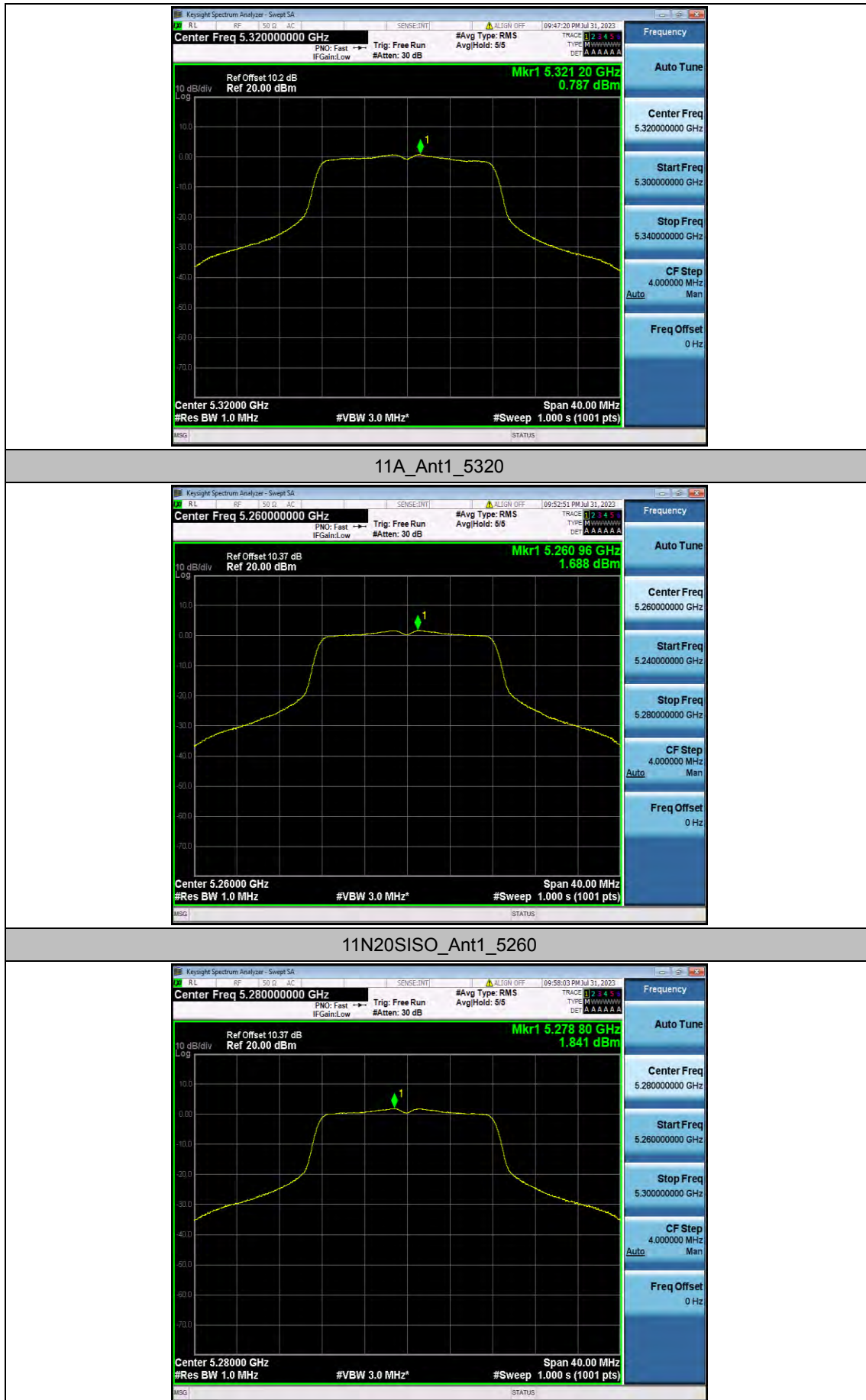
Test Result

Test Mode	Antenna	Frequency[MHz]	Result [dBm/MHz]	Limit[dBm/MHz]	Verdict
11A	Ant1	5260	1.96	≤11.00	PASS
		5280	1.96	≤11.00	PASS
		5320	0.79	≤11.00	PASS
11N20SISO	Ant1	5260	1.69	≤11.00	PASS
		5280	1.84	≤11.00	PASS
		5320	0.83	≤11.00	PASS
11N40SISO	Ant1	5270	-0.99	≤11.00	PASS
		5310	-2.03	≤11.00	PASS
11AC20SISO	Ant1	5260	1.5	≤11.00	PASS
		5280	1.68	≤11.00	PASS
		5320	0.61	≤11.00	PASS
11AC40SISO	Ant1	5270	-1.84	≤11.00	PASS
		5310	-2.16	≤11.00	PASS
11AC80SISO	Ant1	5290	-5.56	≤11.00	PASS
11AX20SISO	Ant1	5260	1.72	≤11.00	PASS
		5280	1.2	≤11.00	PASS
		5320	0.09	≤11.00	PASS
11AX40SISO	Ant1	5270	-3.89	≤11.00	PASS
		5310	-4.18	≤11.00	PASS
11AX80SISO	Ant1	5290	-8.26	≤11.00	PASS

Note: 1.The Result and Limit Unit is dBm/500 kHz in the band 5.725–5.85 GHz.
 2.The Duty Cycle Factor and RBW Factor is compensated in the graph.

Test Graphs





11N20SISO_Ant1_5280



11N20SISO_Ant1_5320



11N40SISO_Ant1_5270



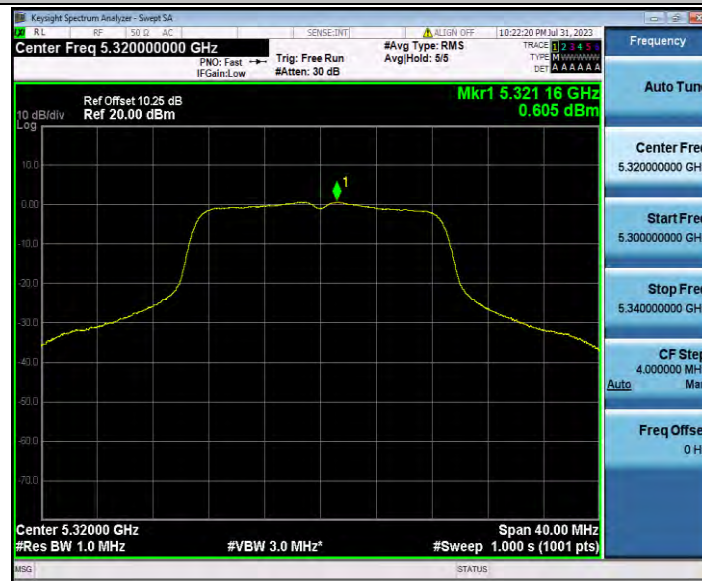
11N40SISO_Ant1_5310



11AC20SISO_Ant1_5260



11AC20SISO_Ant1_5280



11AC20SISO_Ant1_5320



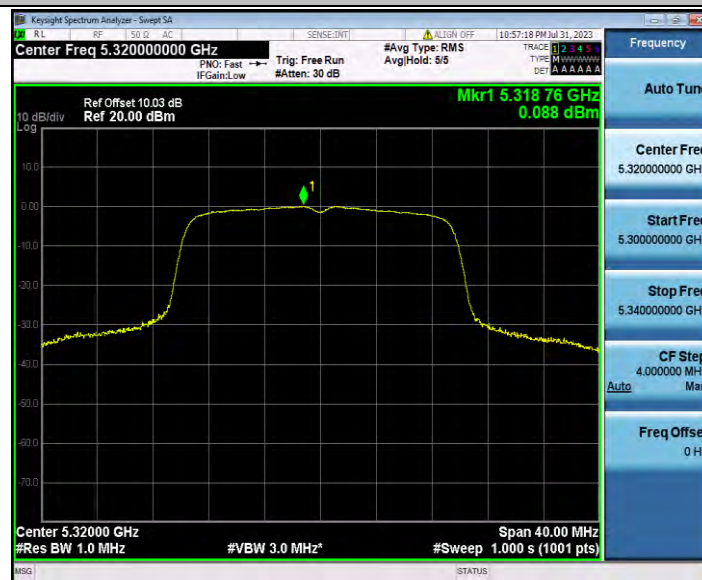
11AC40SISO_Ant1_5270



11AX20SISO_Ant1_5260



11AX20SISO_Ant1_5280



11AX20SISO_Ant1_5320



11AX40SISO_Ant1_5270



11AX40SISO_Ant1_5310



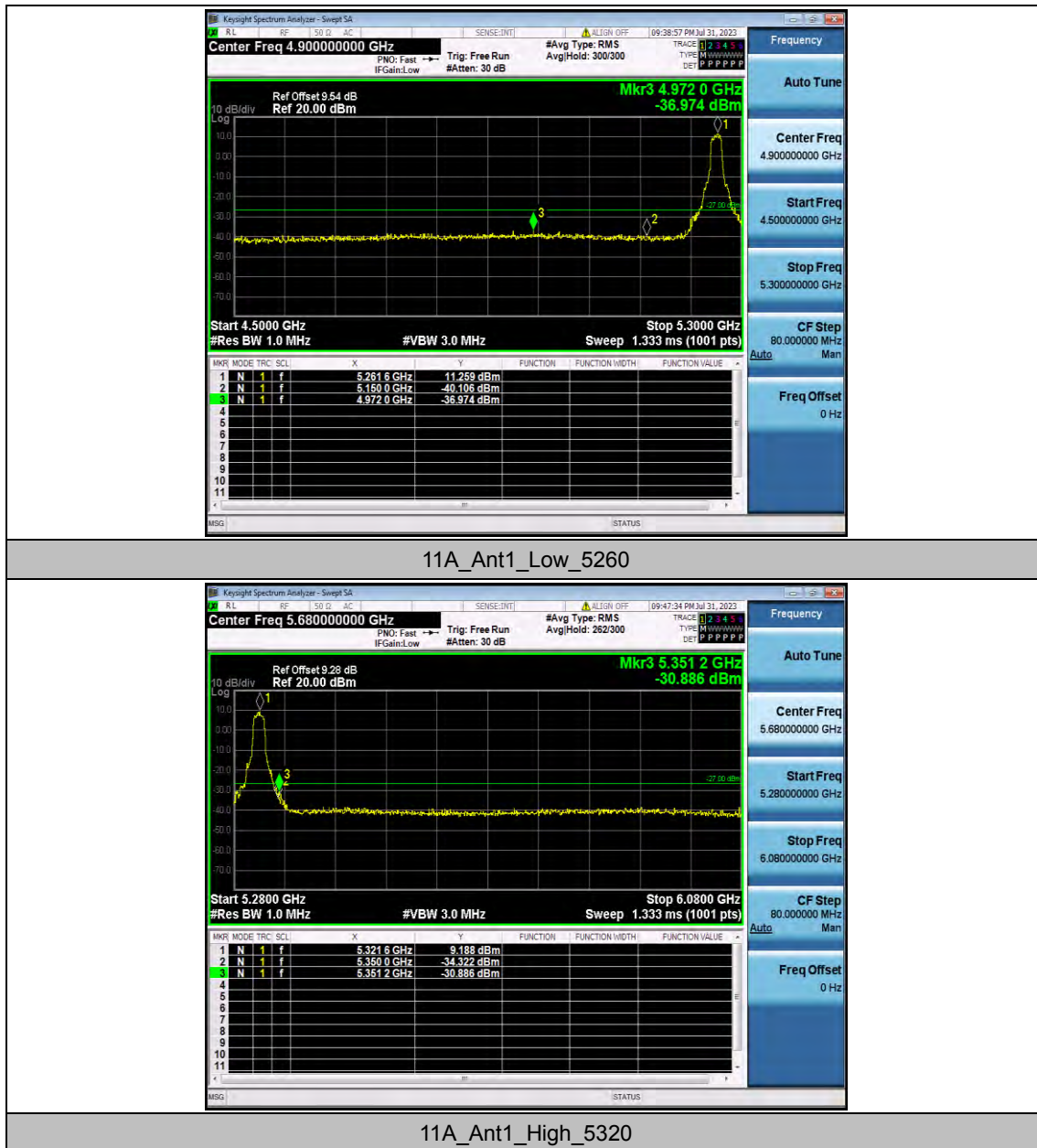
11AX80SISO_Ant1_5290

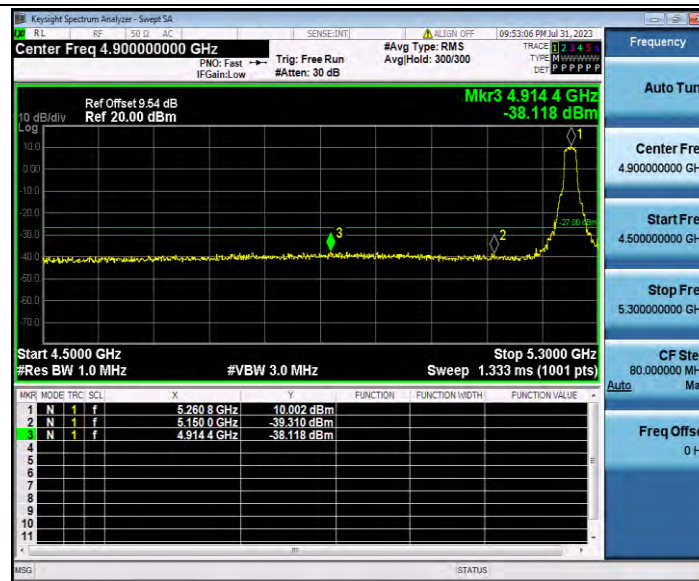
Appendix E.6: Band edge measurements

Test Result B2

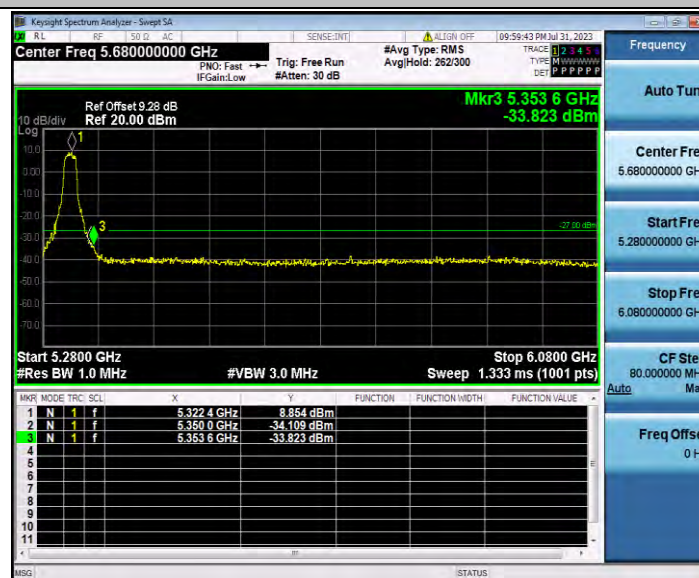
Test Mode	Antenna	ChName	Frequency[MHz]	Result[dBm]	Limit[dBm]	Verdict
11A	Ant1	Low	5260	-36.97	≤ -27	PASS
		High	5320	-30.89	≤ -27	PASS
11N20SISO	Ant1	Low	5260	-38.12	≤ -27	PASS
		High	5320	-33.82	≤ -27	PASS
11N40SISO	Ant1	Low	5270	-37.77	≤ -27	PASS
		High	5310	-30.24	≤ -27	PASS
11AC20SISO	Ant1	Low	5260	-37.4	≤ -27	PASS
		High	5320	-32.56	≤ -27	PASS
11AC40SISO	Ant1	Low	5270	-37.79	≤ -27	PASS
		High	5310	-30.85	≤ -27	PASS
11AC80SISO	Ant1	Low	5290	-37.27	≤ -27	PASS
		High	5290	-34.94	≤ -27	PASS
11AX20SISO	Ant1	Low	5260	-37.44	≤ -27	PASS
		High	5320	-29.48	≤ -27	PASS
11AX40SISO	Ant1	Low	5270	-37.48	≤ -27	PASS
		High	5310	-33.46	≤ -27	PASS
11AX80SISO	Ant1	Low	5290	-37.61	≤ -27	PASS
		High	5290	-36.06	≤ -27	PASS

Test Graphs B2





11N20SISO_Ant1_Low_5260



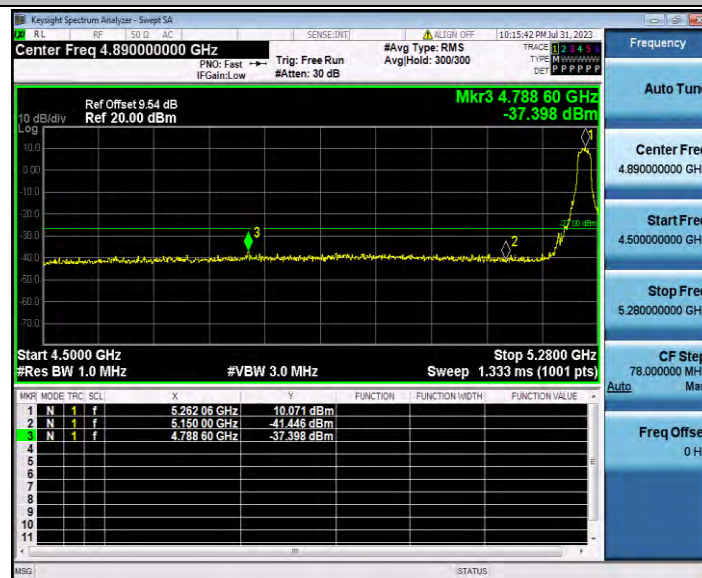
11N20SISO_Ant1_High_5320



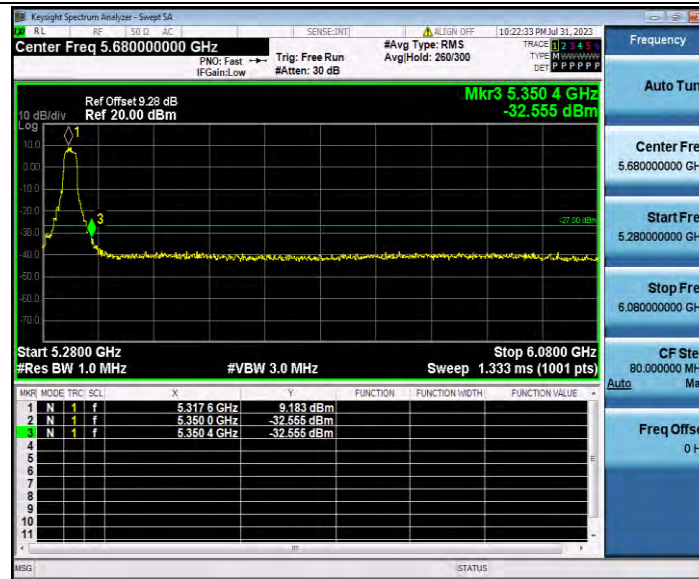
11N40SISO_Ant1_Low_5270



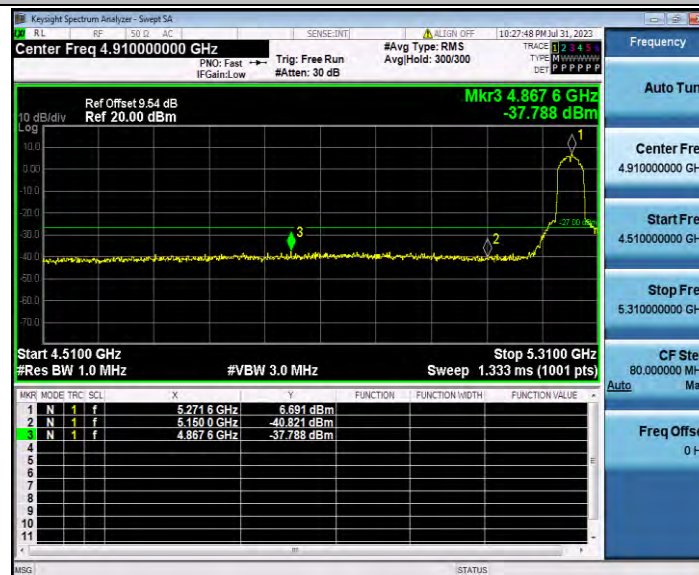
11N40SISO_Ant1_High_5310



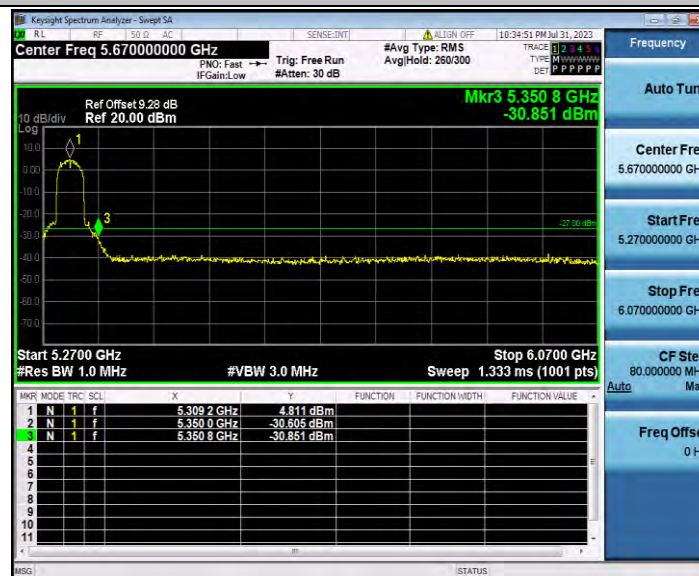
11AC20SISO_Ant1_Low_5260



11AC20SISO_Ant1_High_5320



11AC40SISO_Ant1_Low_5270



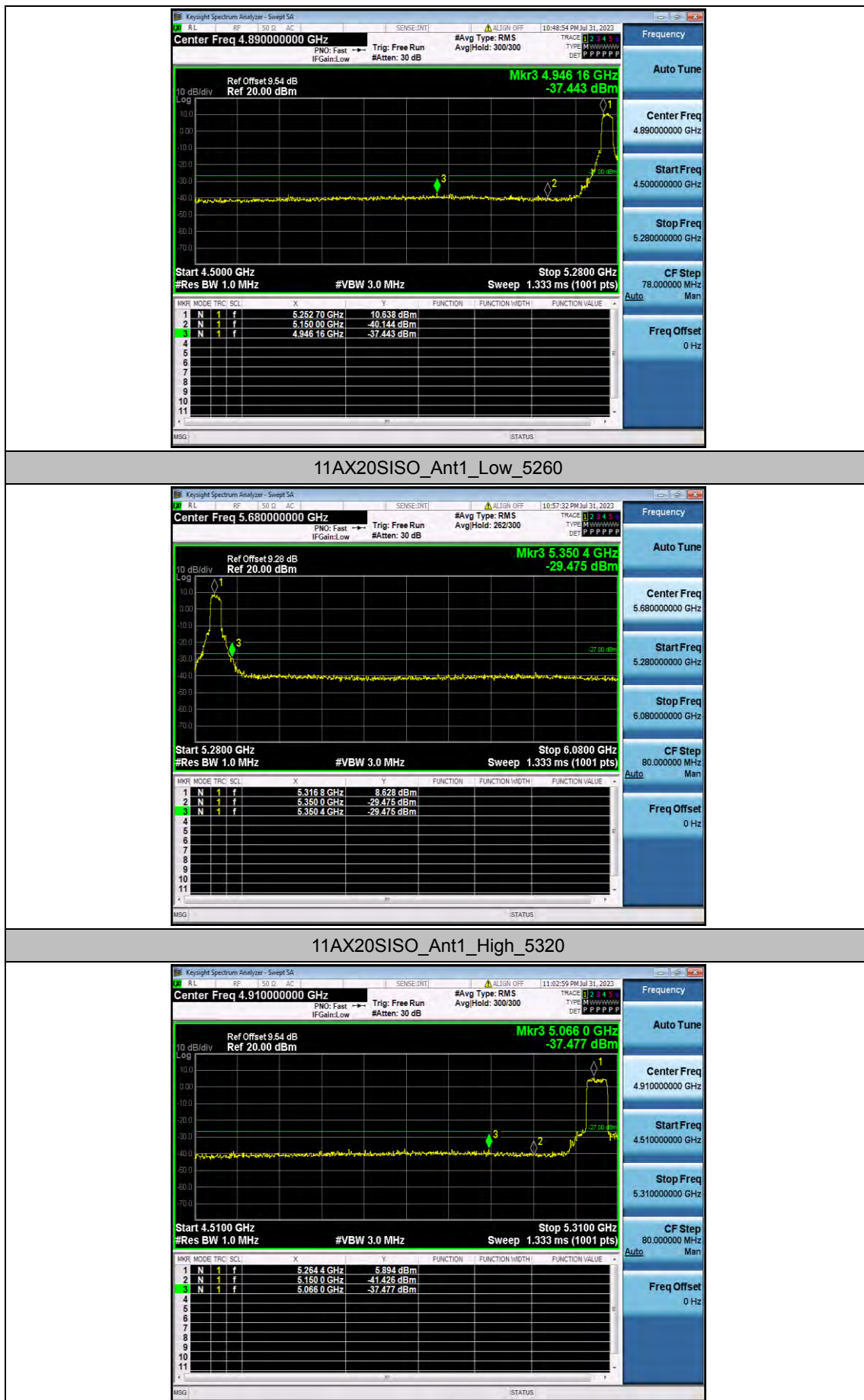
11AC40SISO_Ant1_High_5310



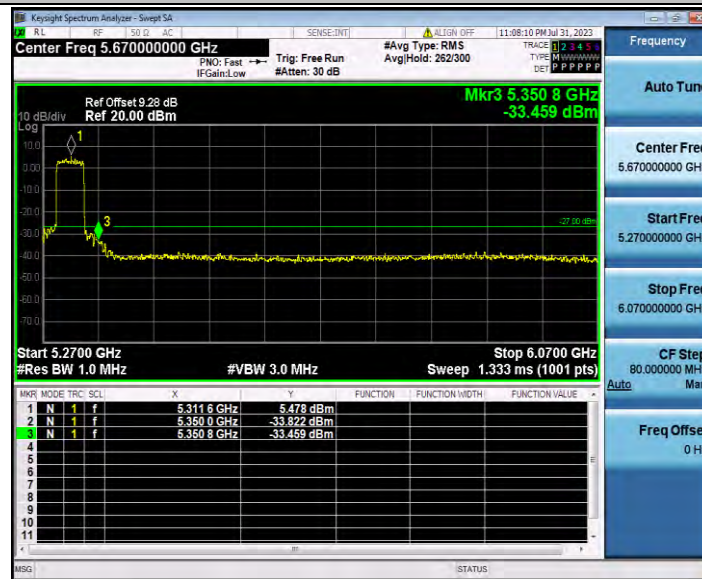
11AC80SISO_Ant1_Low_5290



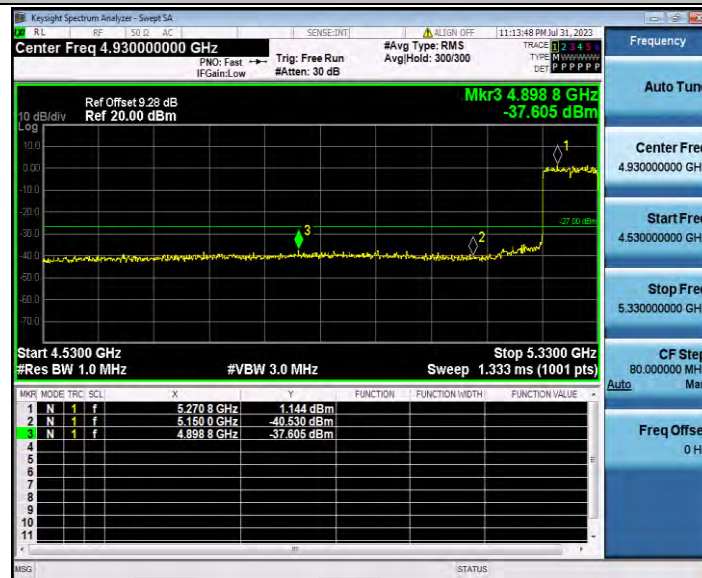
11AC80SISO_Ant1_High_5290



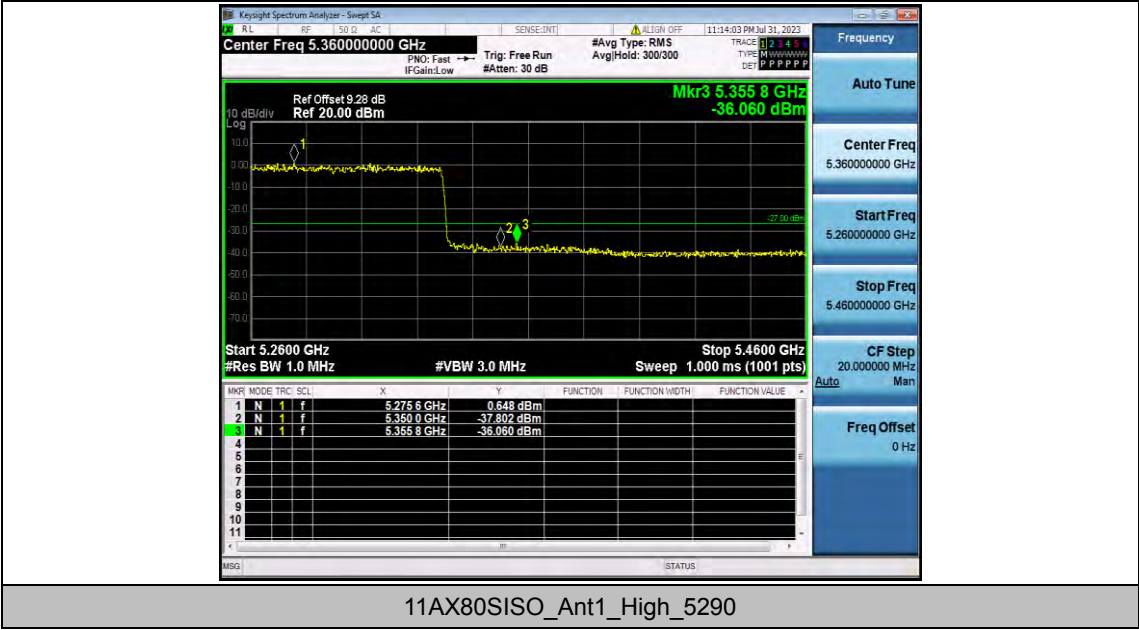
11AX40SISO_Ant1_Low_5270



11AX40SISO_Ant1_High_5310



11AX80SISO_Ant1_Low_5290



11AX80ISO_Ant1_High_5290

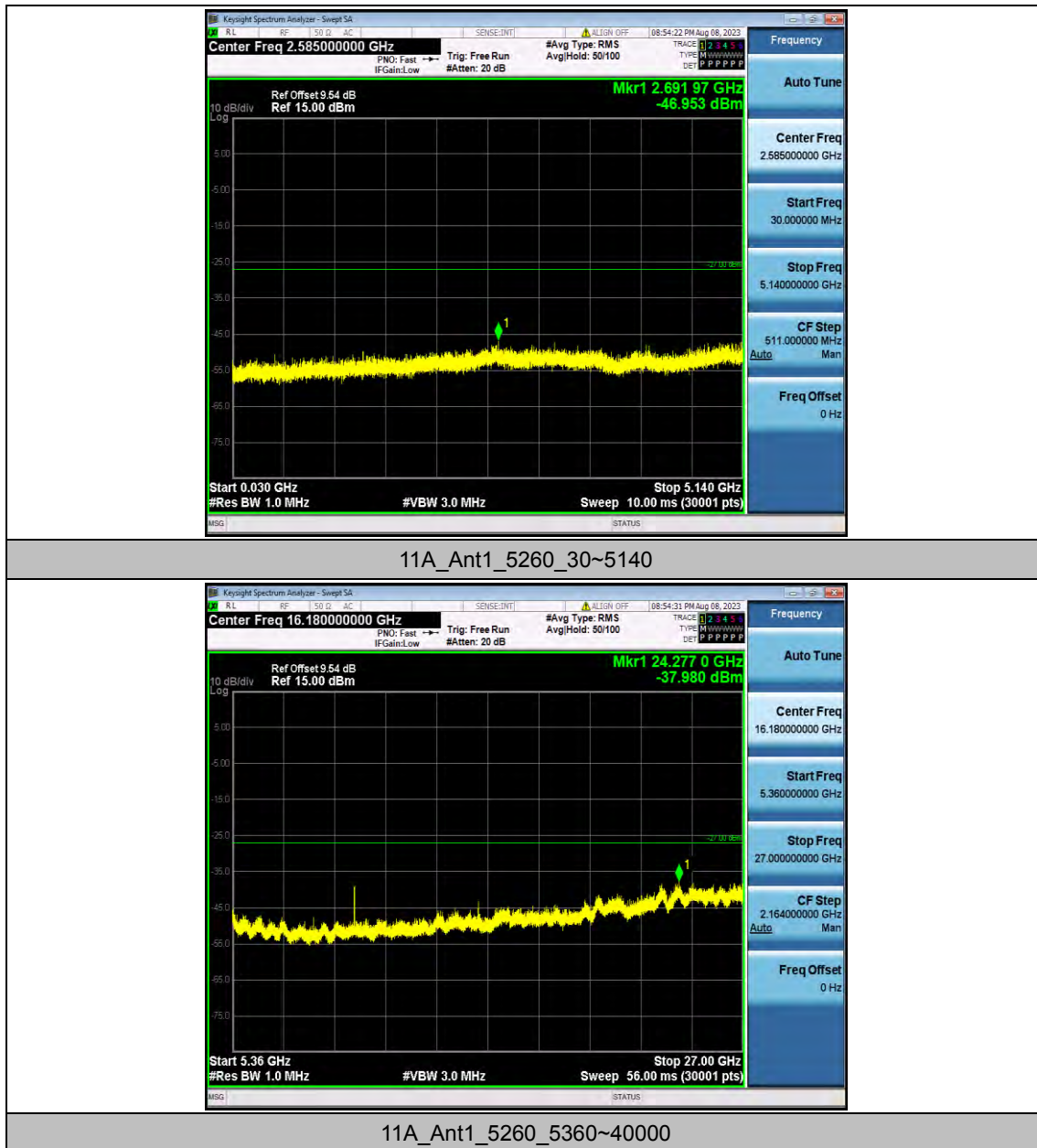
Appendix E.7: Conducted Spurious Emission

Test Result

Test Mode	Antenna	Frequency[MHz]	FreqRange [MHz]	Max. Fre [MHz]	Max. Level [dBm]	Limit [dBm]	Verdict
11A	Ant1	5260	30~5140	2691.97	-46.95	≤-27	PASS
			5360~40000	24276.97	-37.98	≤-27	PASS
		5280	30~5140	2523.34	-47.15	≤-27	PASS
			5360~40000	10560.09	-37.67	≤-27	PASS
		5320	30~5140	4927.42	-47.68	≤-27	PASS
			5360~40000	10641.6	-36.4	≤-27	PASS
11N20SISO	Ant1	5260	30~5140	3007.6	-47.08	≤-27	PASS
			5360~40000	10519.7	-35.99	≤-27	PASS
		5280	30~5140	4885.18	-46.45	≤-27	PASS
			5360~40000	10558.65	-35.27	≤-27	PASS
		5320	30~5140	2464.57	-46.27	≤-27	PASS
			5360~40000	10636.55	-34	≤-27	PASS
11N40SISO	Ant1	5270	30~5140	4927.76	-47.3	≤-27	PASS
			5360~40000	10536.29	-36.86	≤-27	PASS
		5310	30~5140	2555.19	-47.09	≤-27	PASS
			5360~40000	5360.72	-37.53	≤-27	PASS
11AC20SISO	Ant1	5260	30~5140	4944.12	-47.33	≤-27	PASS
			5360~40000	10524.75	-36.78	≤-27	PASS
		5280	30~5140	3069.77	-46.94	≤-27	PASS
			5360~40000	10642.32	-35.88	≤-27	PASS
		5320	30~5140	3603.42	-47.36	≤-27	PASS
			5360~40000	10642.32	-31.98	≤-27	PASS
11AC40SISO	Ant1	5270	30~5140	2652.79	-46.55	≤-27	PASS
			5360~40000	10542.06	-37.88	≤-27	PASS
		5310	30~5140	2331.2	-44.99	≤-27	PASS
			5360~40000	10536.29	-38.32	≤-27	PASS
11AC80SISO	Ant1	5290	30~5140	5017.7	-46.88	≤-27	PASS
			5360~40000	5368.66	-35.4	≤-27	PASS
11AX20SISO	Ant1	5260	30~5140	3027.02	-47.09	≤-27	PASS
			5360~40000	15776.77	-37.09	≤-27	PASS
		5280	30~5140	5080.55	-47.11	≤-27	PASS
			5360~40000	10558.65	-33.65	≤-27	PASS
		5320	30~5140	4906.81	-47.32	≤-27	PASS
			5360~40000	10638.72	-32.58	≤-27	PASS
11AX40SISO	Ant1	5270	30~5140	2606.12	-33.73	≤-27	PASS
			5360~40000	26011.05	-37.45	≤-27	PASS

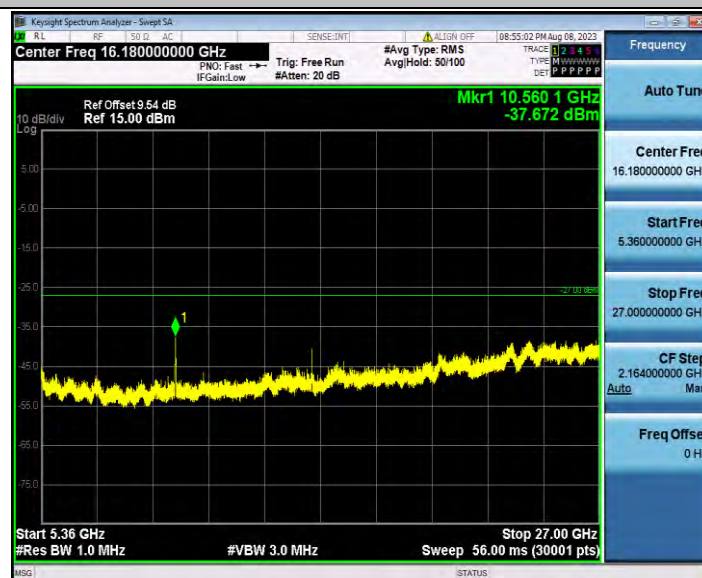
		5310	30~5140	2522.49	-43.06	≤-27	PASS
			5360~40000	24302.93	-38.07	≤-27	PASS
11AX80SISO	Ant1	5290	30~5140	2519.42	-42.98	≤-27	PASS
			5360~40000	24794.16	-37.49	≤-27	PASS

Test Graphs

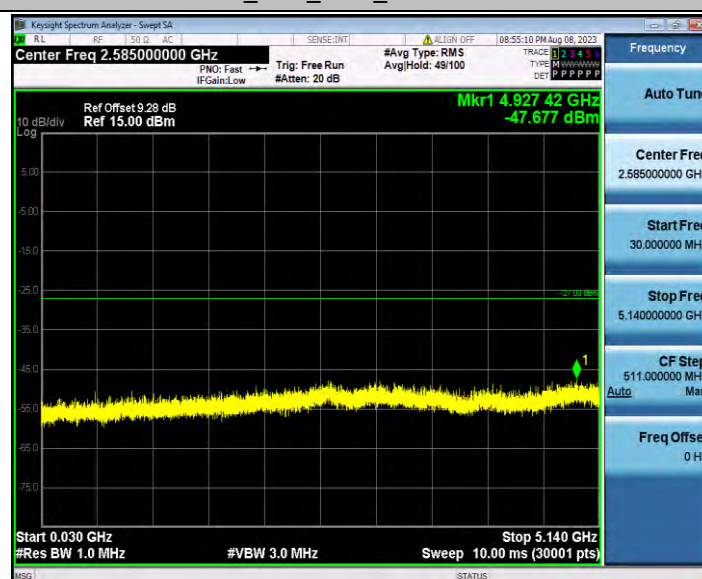




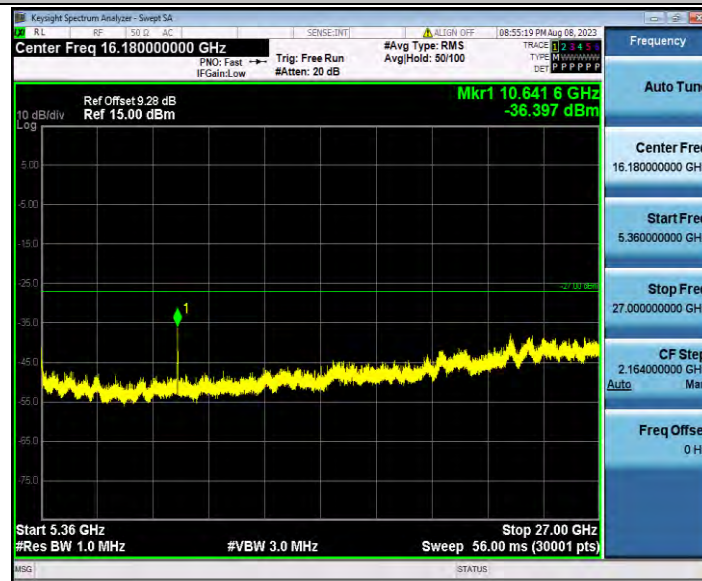
11A_Ant1_5280_30~5140



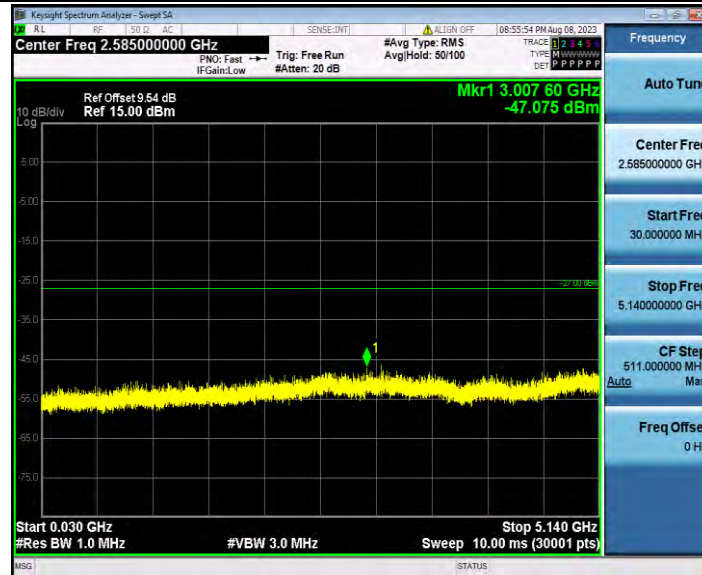
11A_Ant1_5280_5360~40000



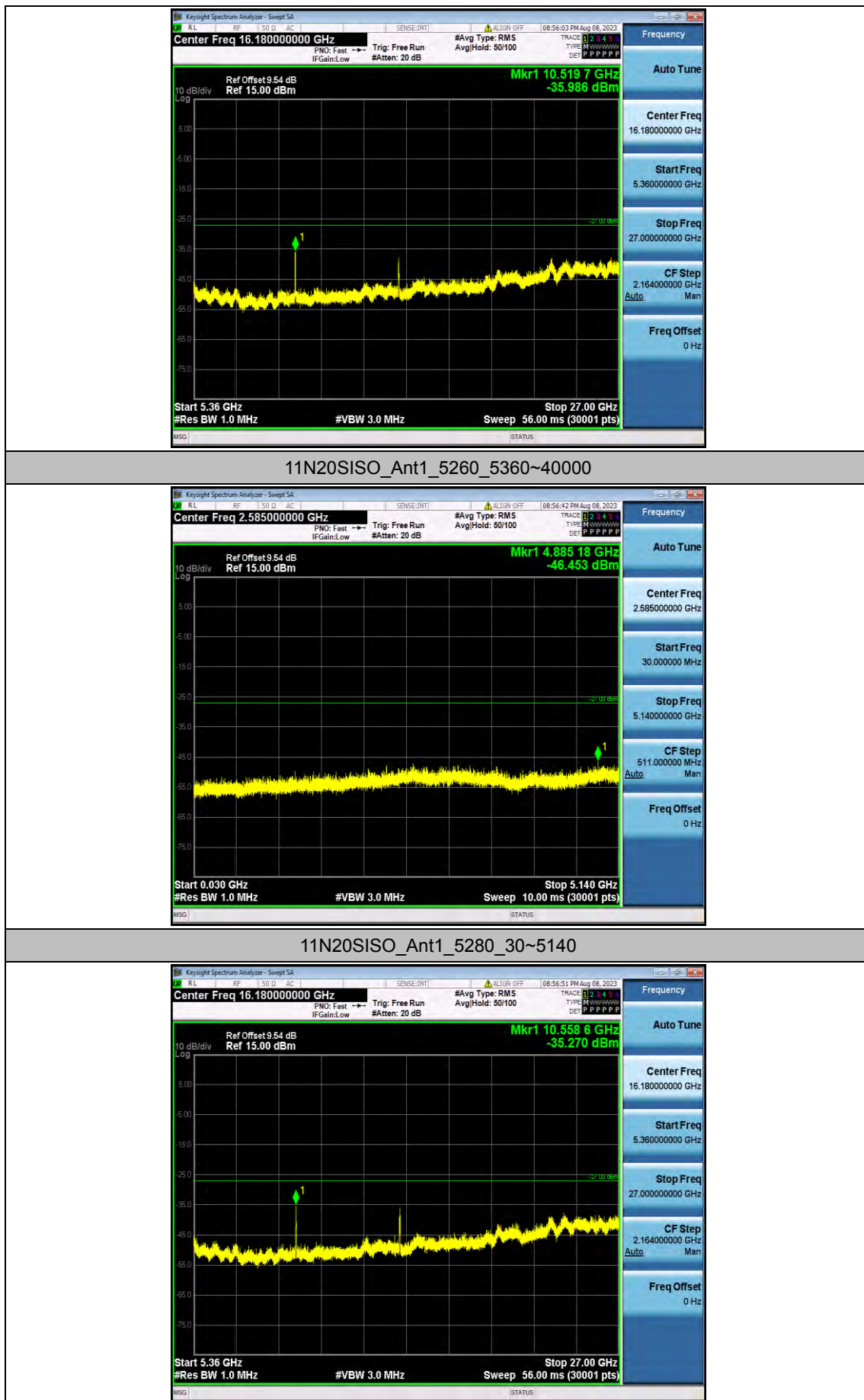
11A_Ant1_5320_30~5140



11A_Ant1_5320_5360~40000



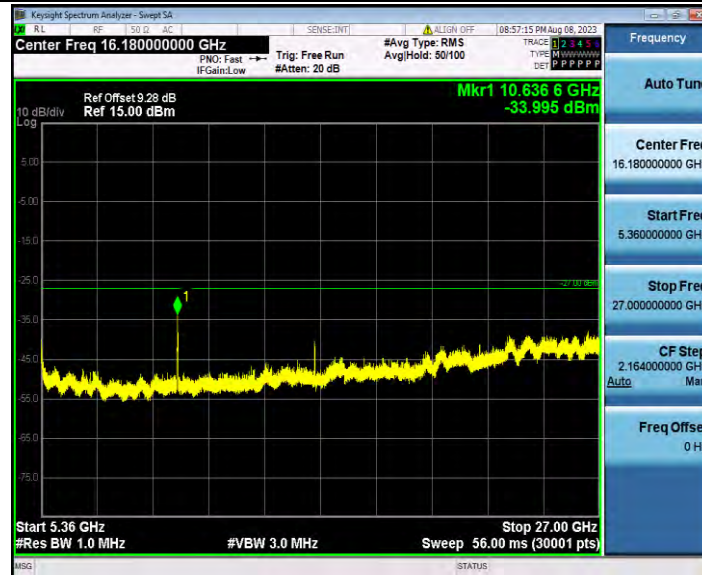
11N20SISO_Ant1_5260_30~5140



11N20SISO_Ant1_5280_5360~40000



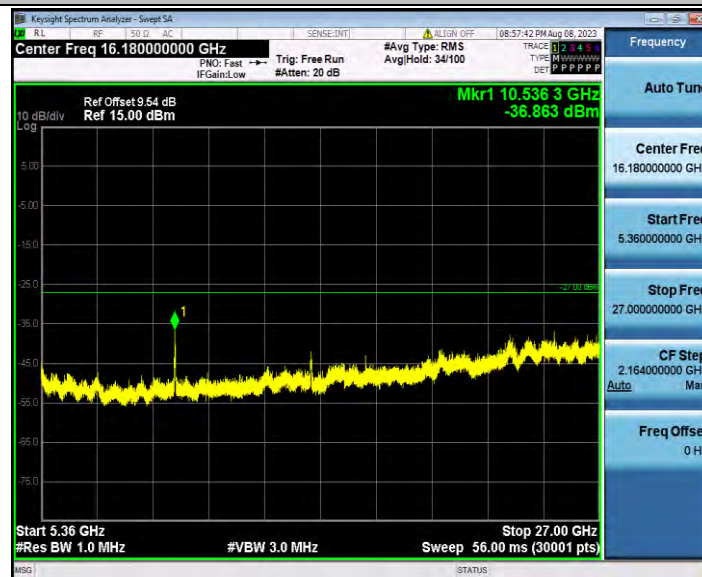
11N20SISO_Ant1_5320_30~5140



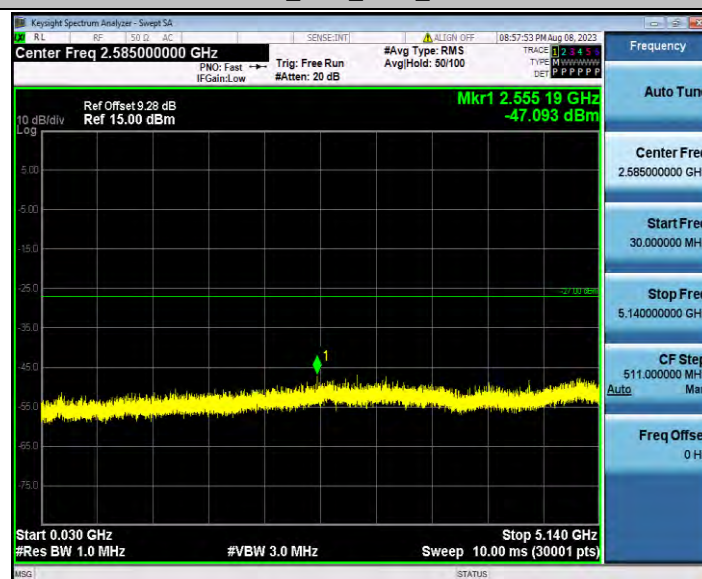
11N20SISO_Ant1_5320_5360~40000



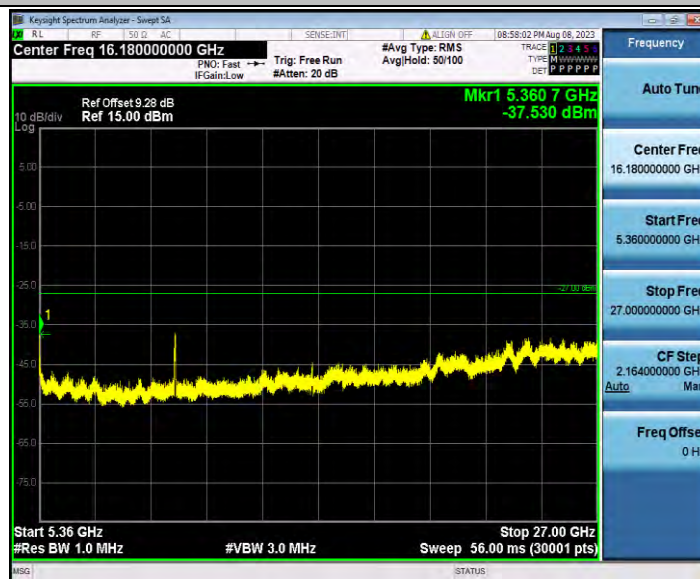
11N40SISO_Ant1_5270_30~5140



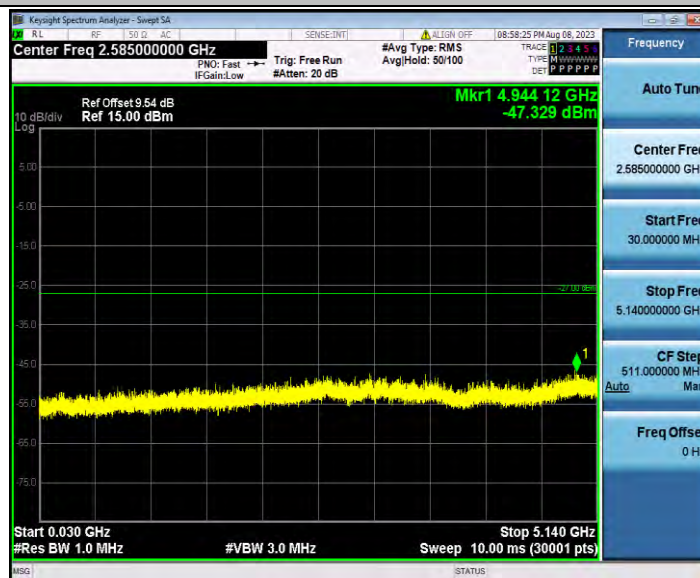
11N40SISO_Ant1_5270_5360~40000



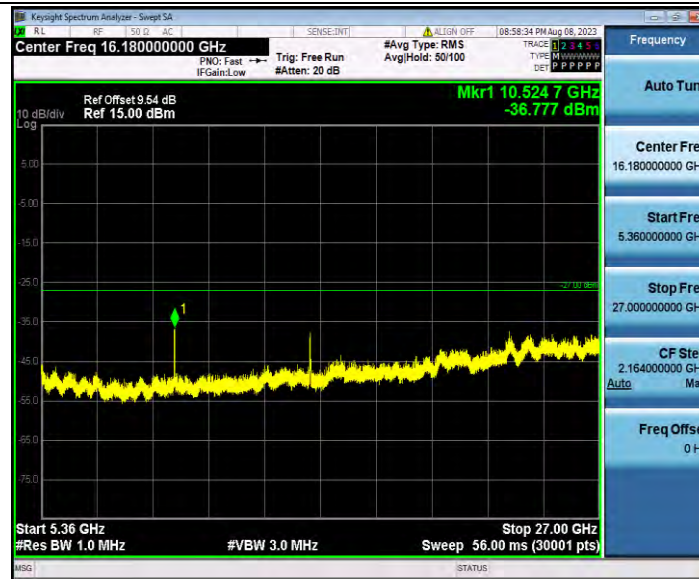
11N40SISO_Ant1_5310_30~5140



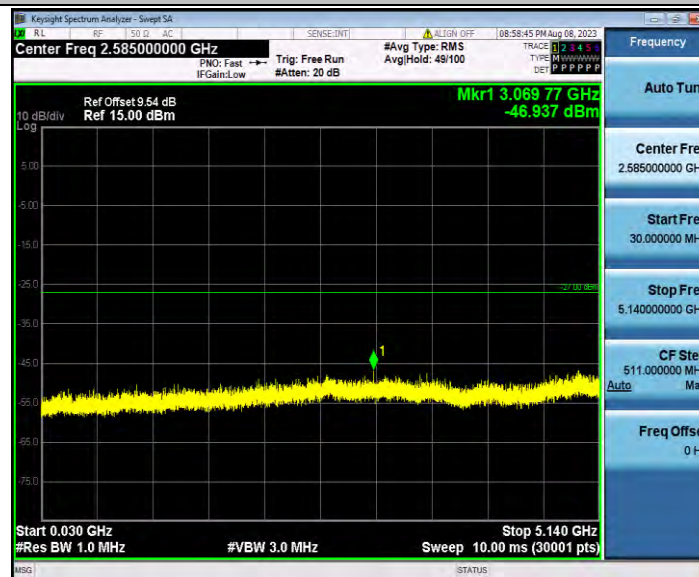
11N40SISO_Ant1_5310_5360~40000



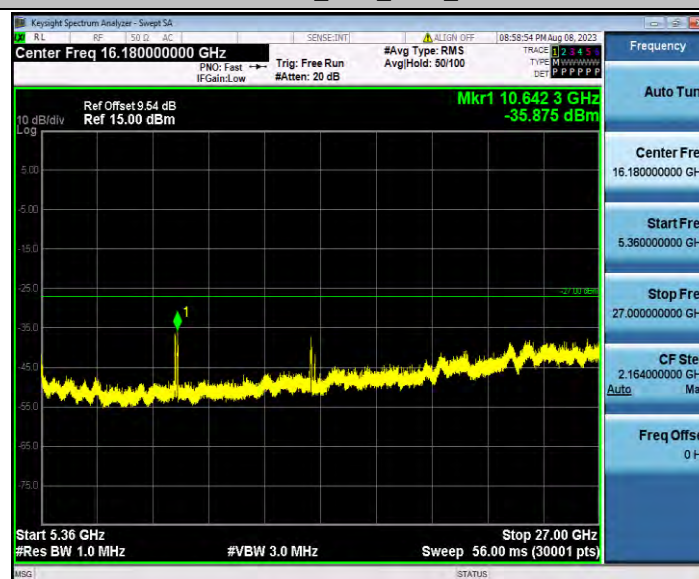
11AC20SISO_Ant1_5260_30~5140



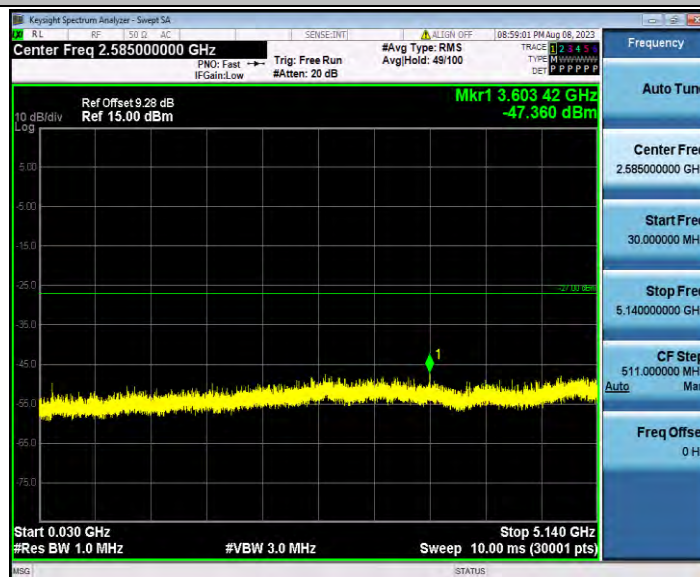
11AC20SISO_Ant1_5260_5360~40000



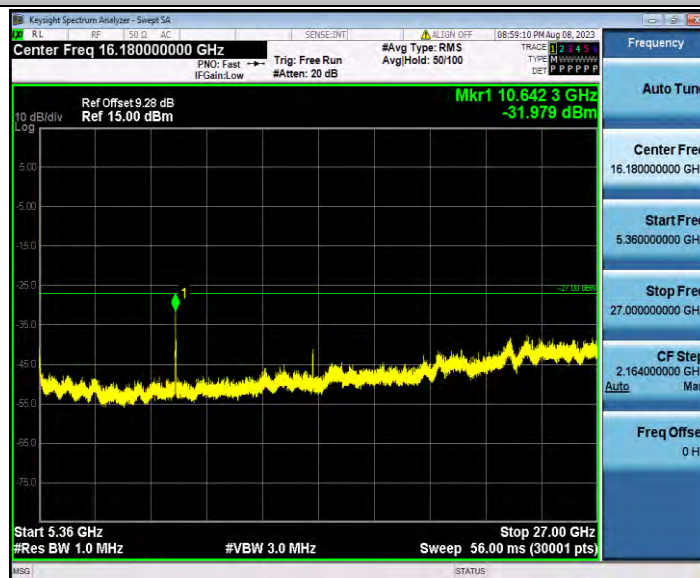
11AC20SISO_Ant1_5280_30~5140



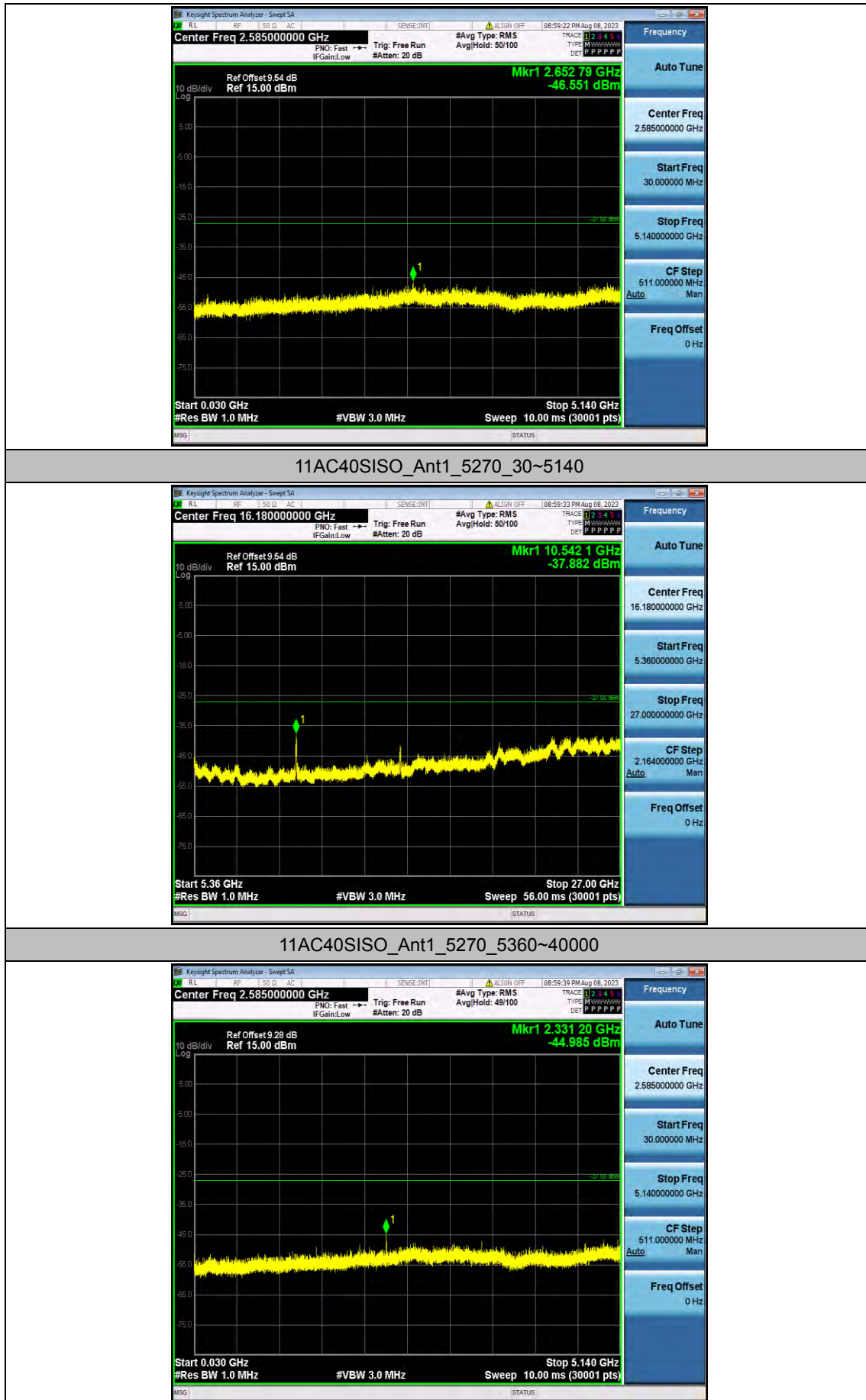
11AC20SISO_Ant1_5280_5360~40000



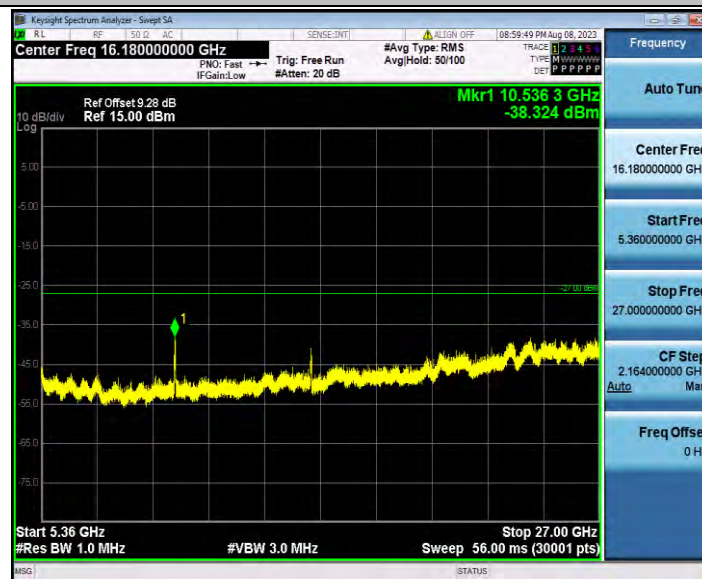
11AC20SISO_Ant1_5320_30~5140



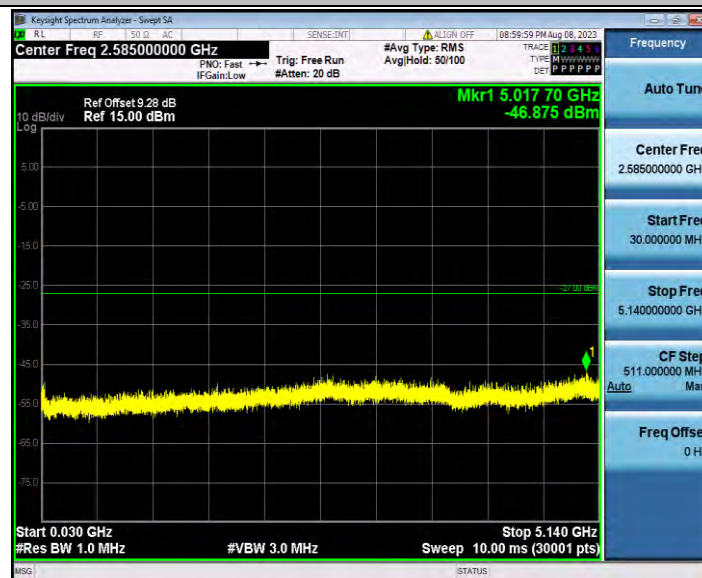
11AC20SISO_Ant1_5320_5360~40000



11AC40SISO_Ant1_5310_30~5140



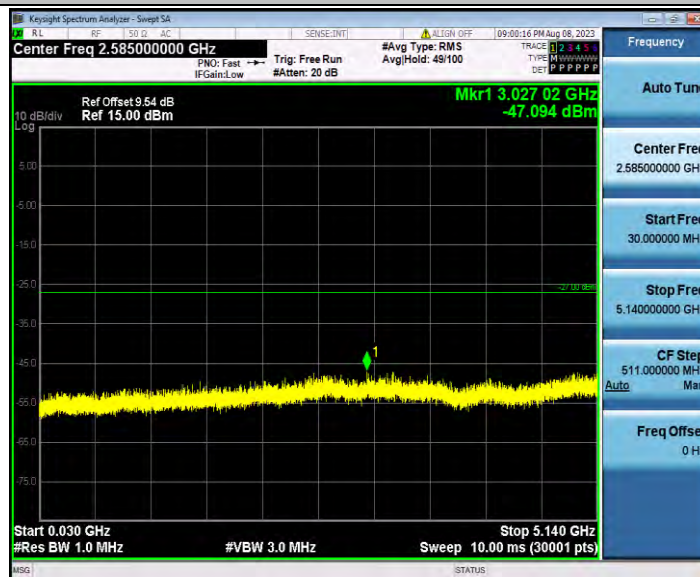
11AC40SISO_Ant1_5310_5360~40000



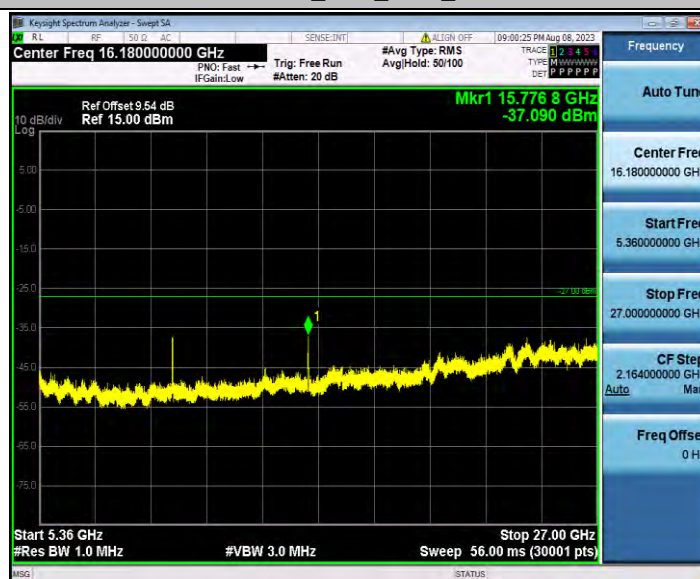
11AC80SISO_Ant1_5290_30~5140



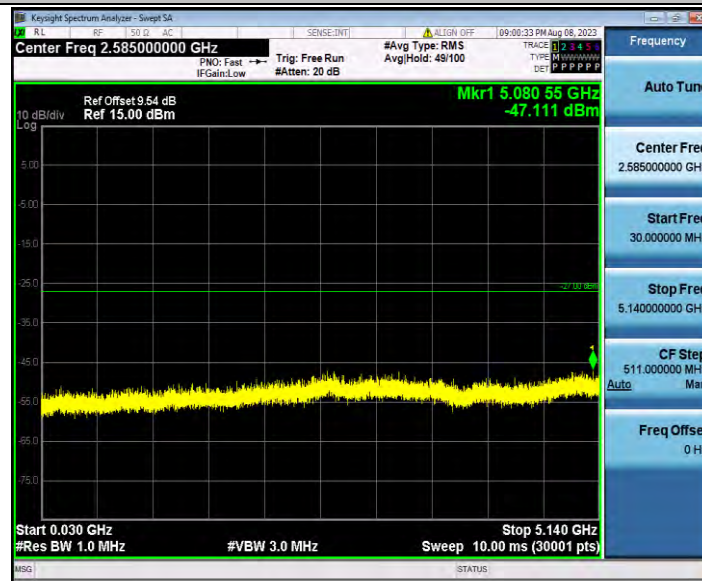
11AC80SISO_Ant1_5290_5360~40000



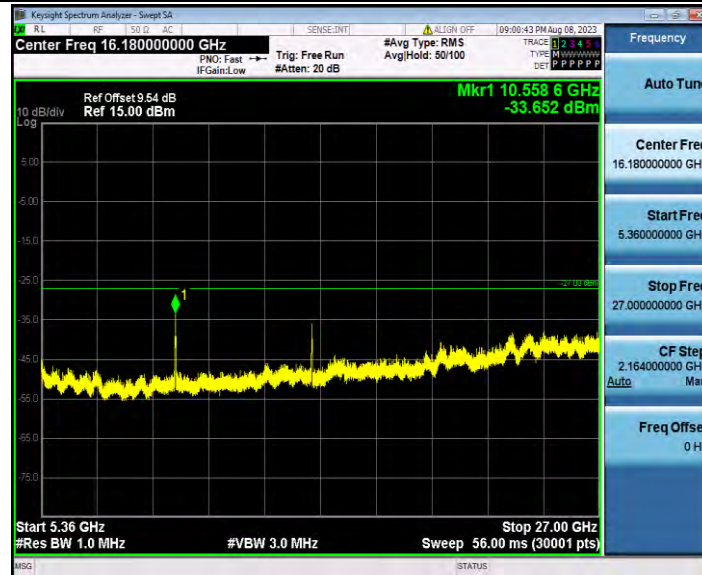
11AX20SISO_Ant1_5260_30~5140



11AX20SISO_Ant1_5260_5360~40000



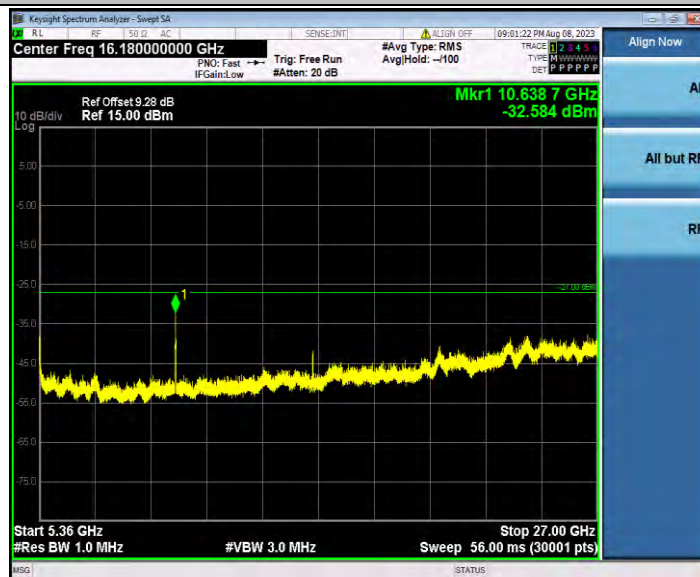
11AX20SISO_Ant1_5280_30~5140



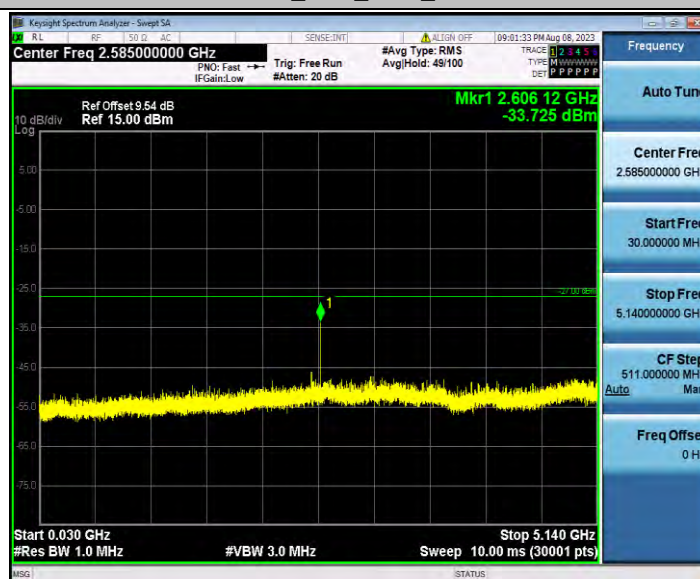
11AX20SISO_Ant1_5280_5360~40000



11AX20SISO_Ant1_5320_30~5140



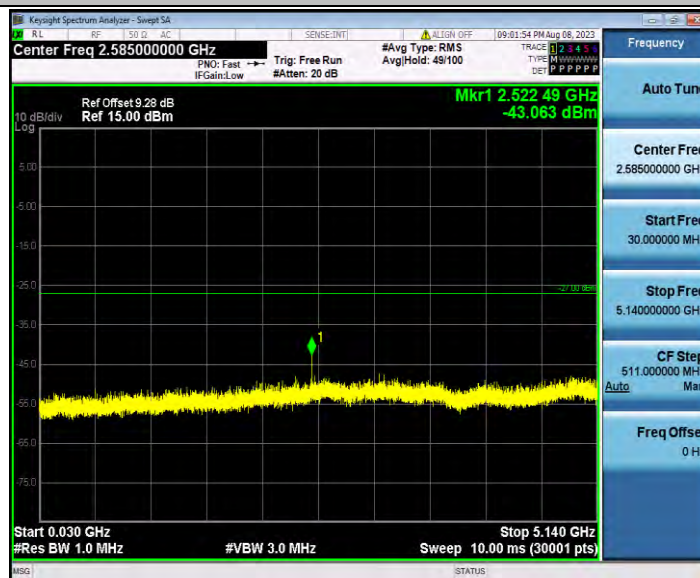
11AX20SISO_Ant1_5320_5360~40000



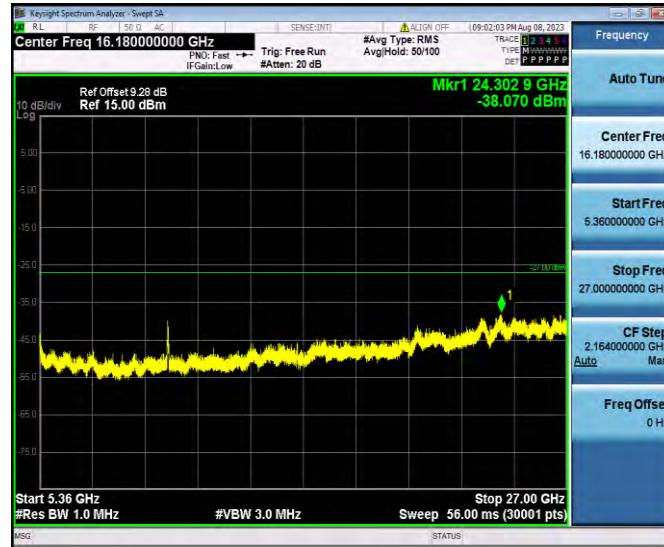
11AX40SISO_Ant1_5270_30~5140



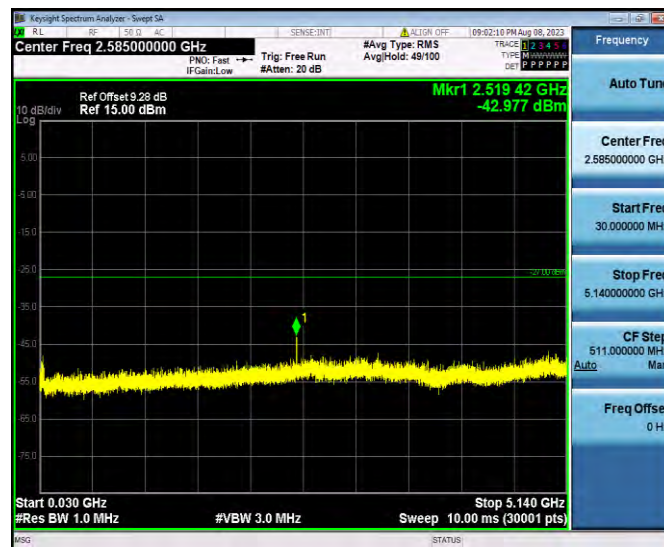
11AX40SISO_Ant1_5270_5360~40000



11AX40SISO_Ant1_5310_30~5140



11AX40SISO_Ant1_5310_5360~40000



11AX80SISO_Ant1_5290_30~5140



11AX80SISO_Ant1_5290_5360~40000