Appendix D: FCC Attestation Letter

Please refer to the following page.

ALINCO INCORPORATED
"Shin Daibiru Building"9F, 1-2-6, Doujimahama, Kita-ku,
Osaka 530-0004, Japan Fax: 06 (4797) 2156 Phone: 06 (4797) 2134

Nov. 1, 2006

Federal Communications Commissions

RE:PH3DR235TMkIII / 800MHz analog cellular telephone band blocking

Dear Sir or Madam.

This is to declare that the device in application PH3DR235TMkIII has been blocked for any and all access of 824.000 to 849.9975MHz and 869.000 to 894.9975MHz.

The device uses double super heterodyne as a receiver circuit and frequency is generated by a PLL synthesizer circuitry. The first local oscillation frequencies are determined by the N value data of the CPU. The 216.000 – 279.995MHz range is determined by N-value data of 185.150 – 249.145MHz, and such values are not able to changed by any means.

The 5 low-pass filters and 5 tuning circuits are used to filter out the unwanted bands. The CPU used in this device, our parts code XA1130, vender's code M38268MCA-075GP is exclusively programmed and burns for this US export model. ALINCO, Inc exports solely this version to the US market, and this CPU can't be modified by any means to receive the declared cellular frequencies. Moreover, the entire circuitry of this device is not designed to cover the cellular frequencies anyway.

To my best of knowledge being informed by the chief-engineer in charge of PH3DR235TMkIII, above declared is true.

Sincerely

Kusuhara

Kazuhiro Kusuhara
General manager, Production Section
Alinco, Inc. Electronics Division