

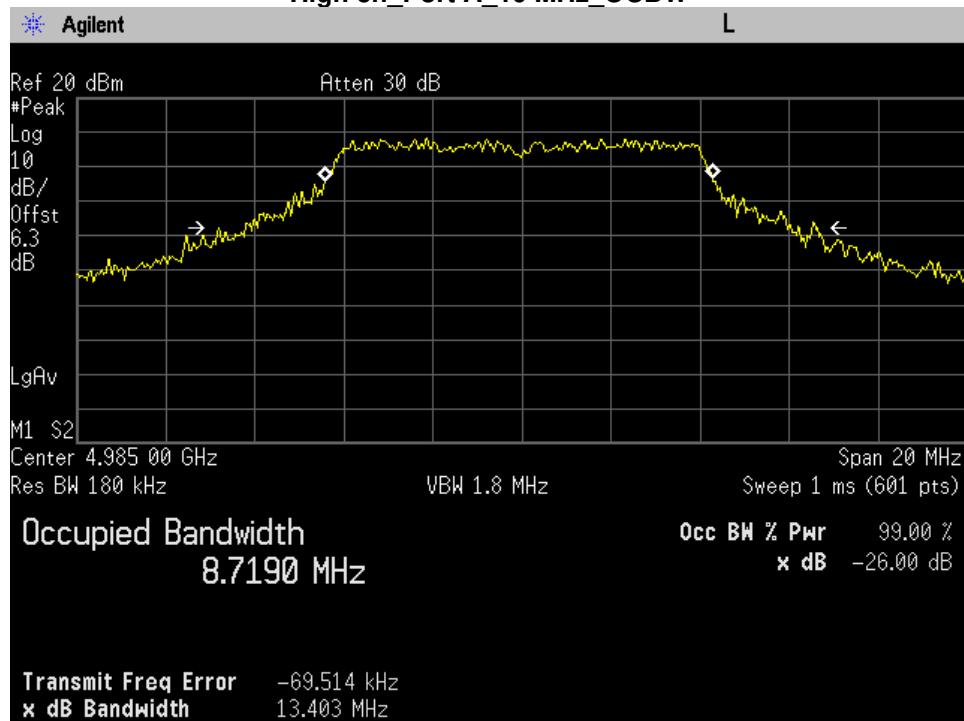


Annex D

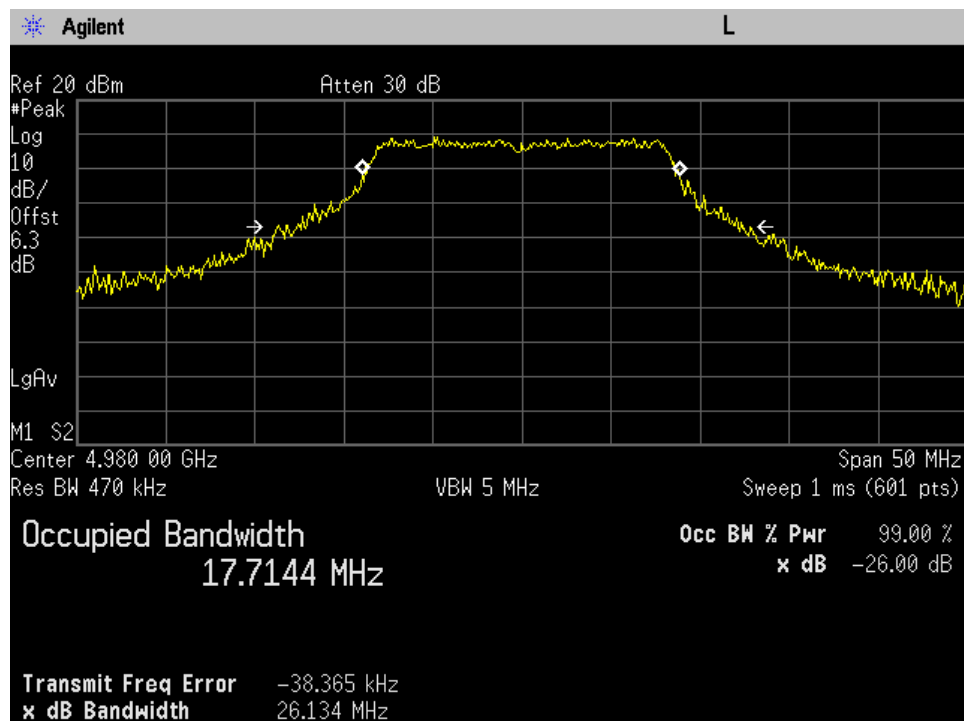
Occupied Bandwidth and Masks



High ch_Port A_10 MHz_OCBW

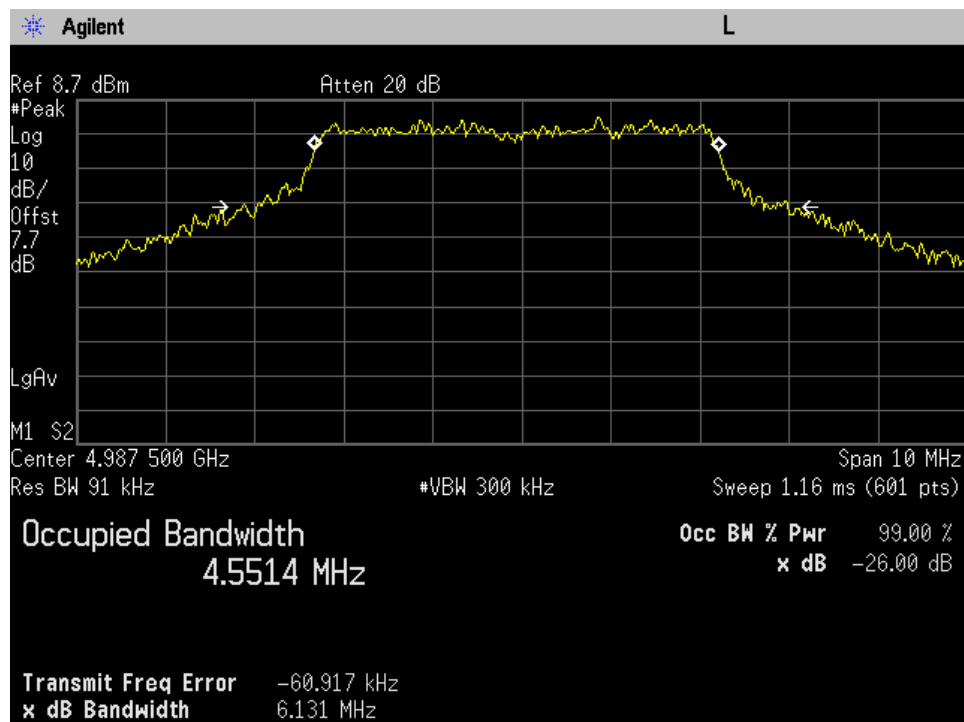


High ch_Port A_20 MHz_OCBW

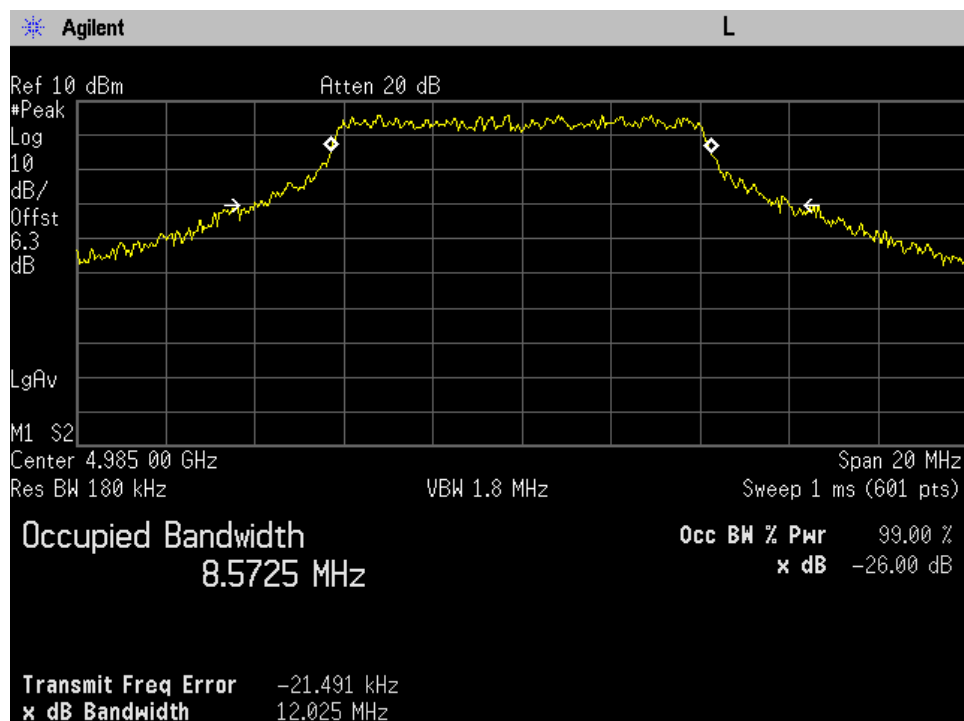




High ch_Port A_5 MHz_OCBW

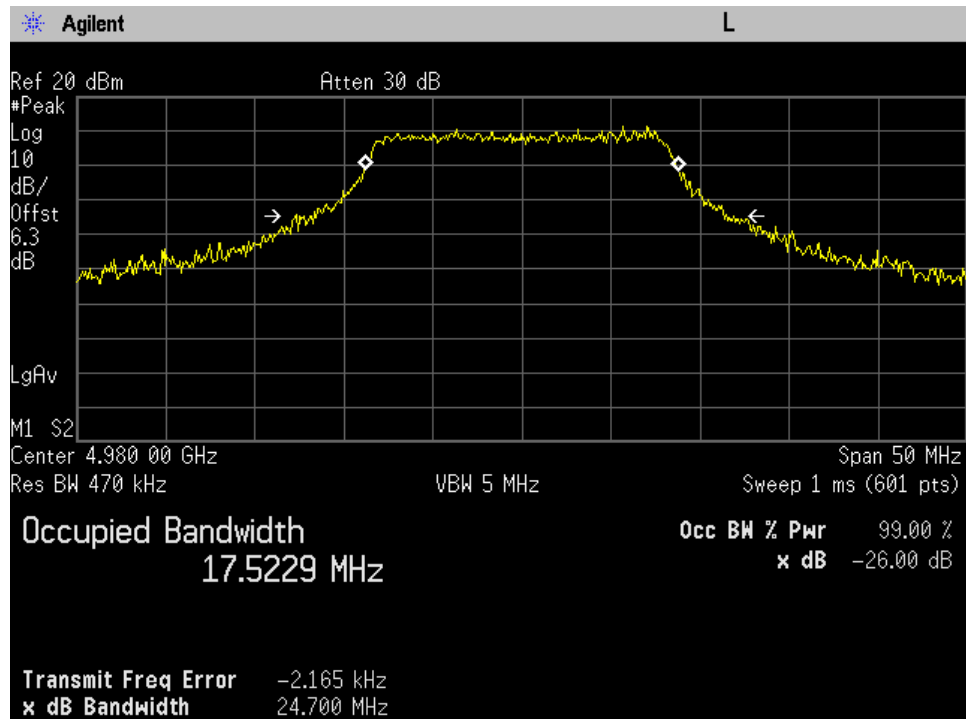


High ch_Port B_10 MHz_OCBW

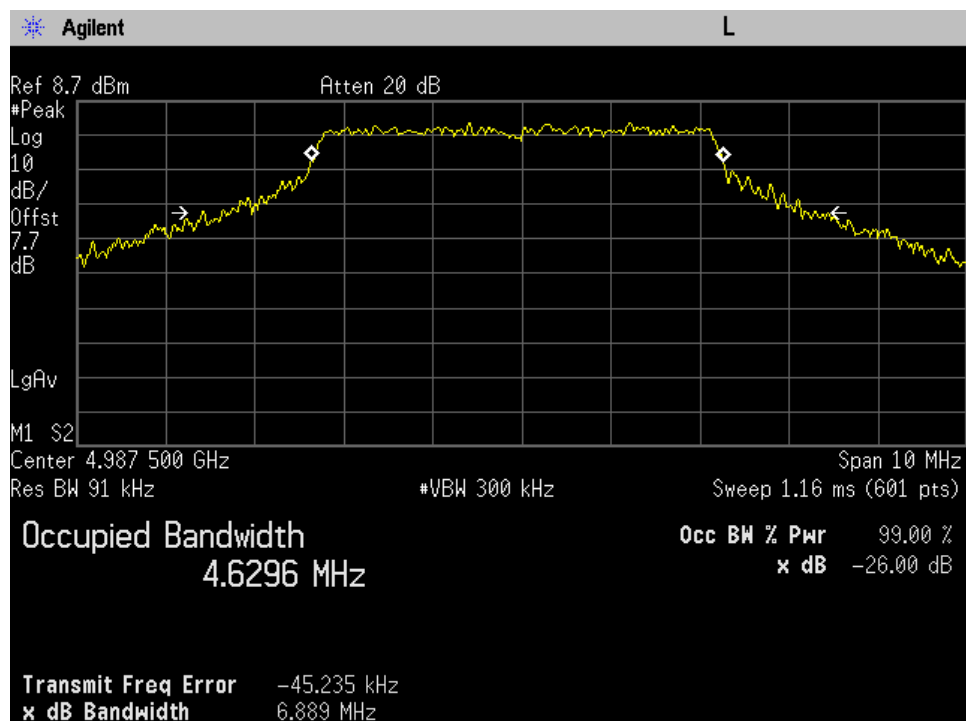




High ch_Port B_20 MHz_OCBW

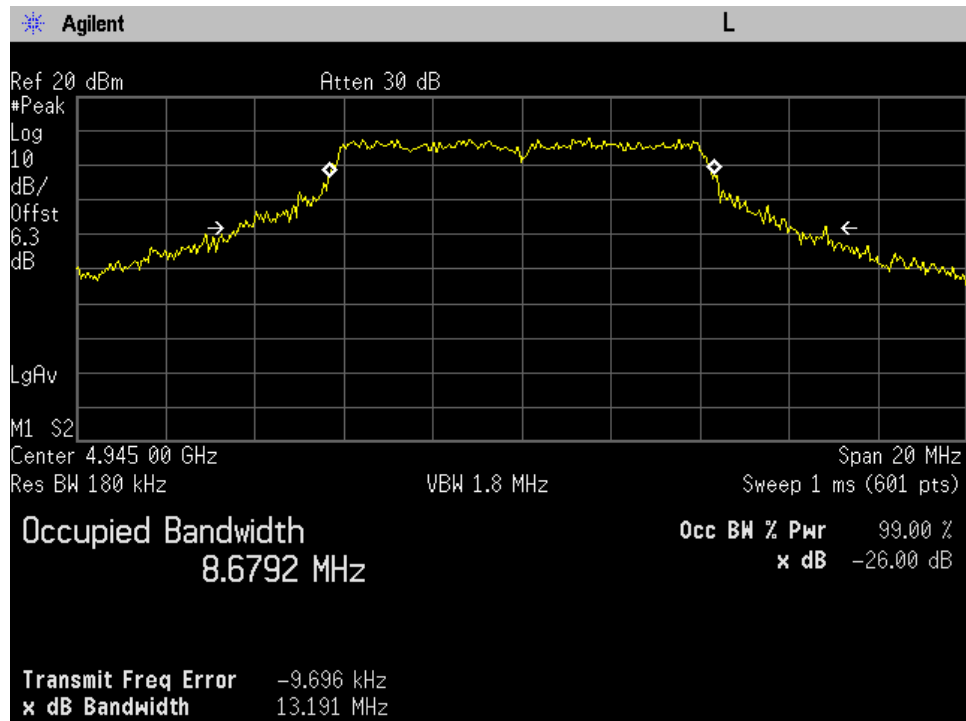


High ch_Port B_5 MHz_OCBW

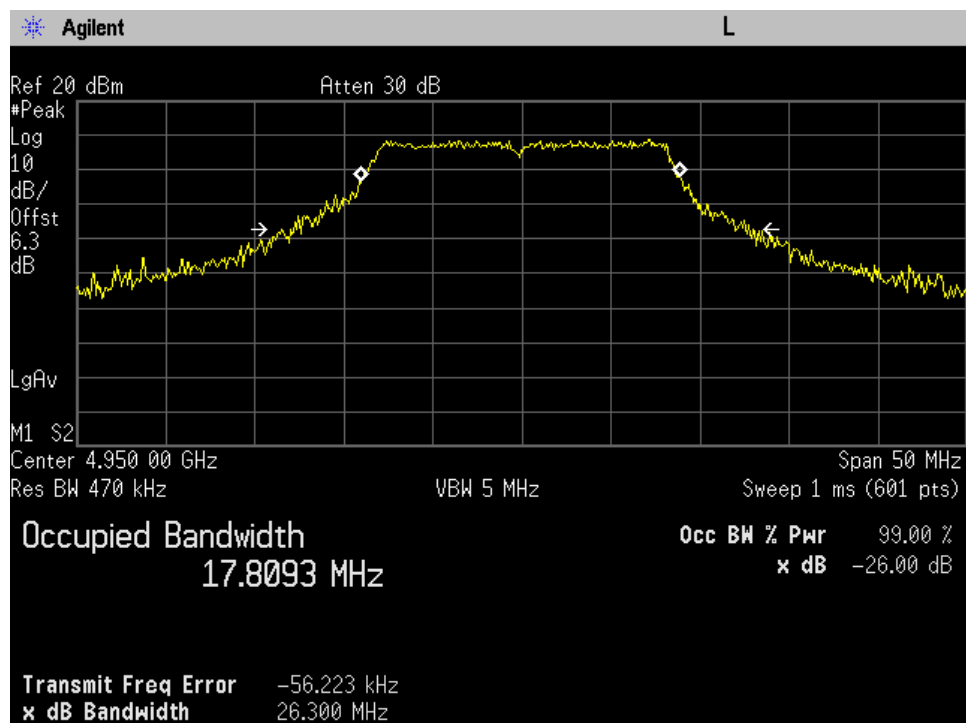




Low ch_Port A_10 MHz_OCBW

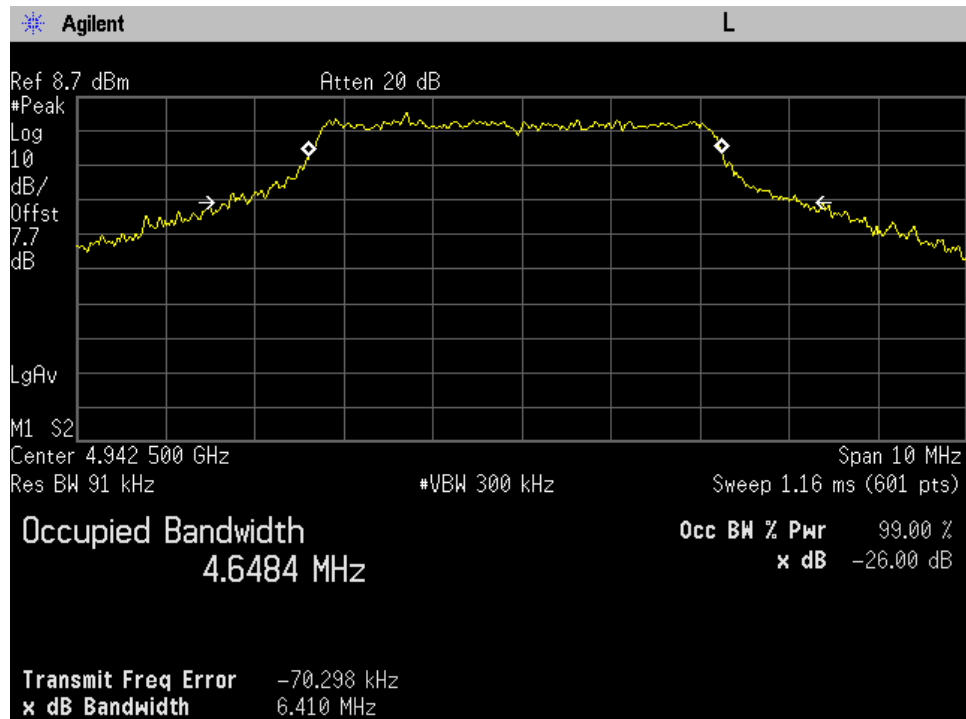


Low ch_Port A_20 MHz_OCBW

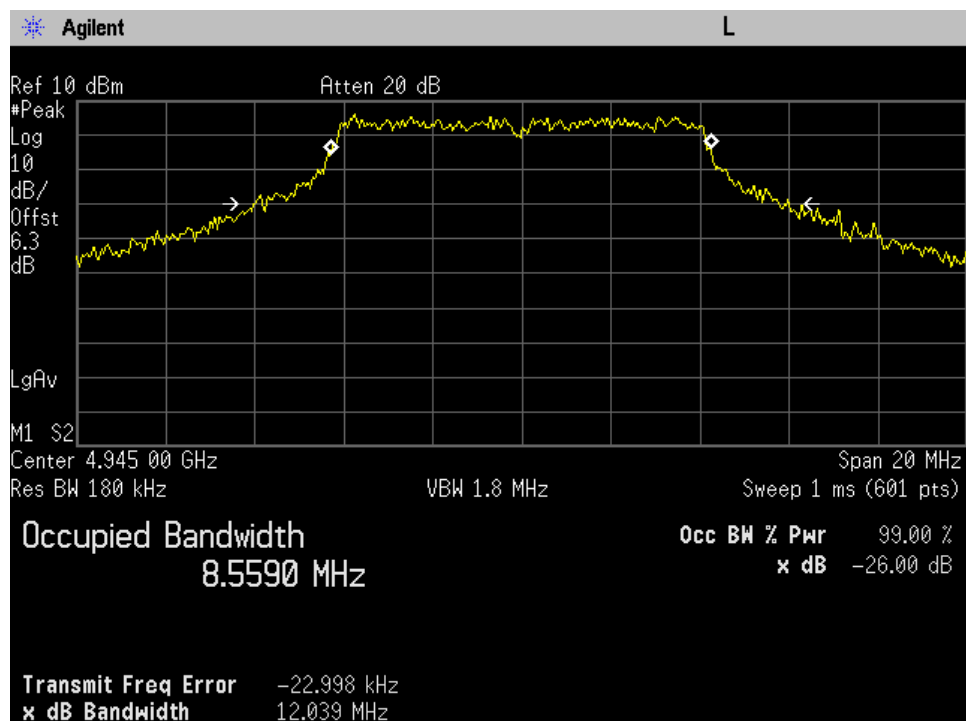




Low ch_Port A_5 MHz_OCBW

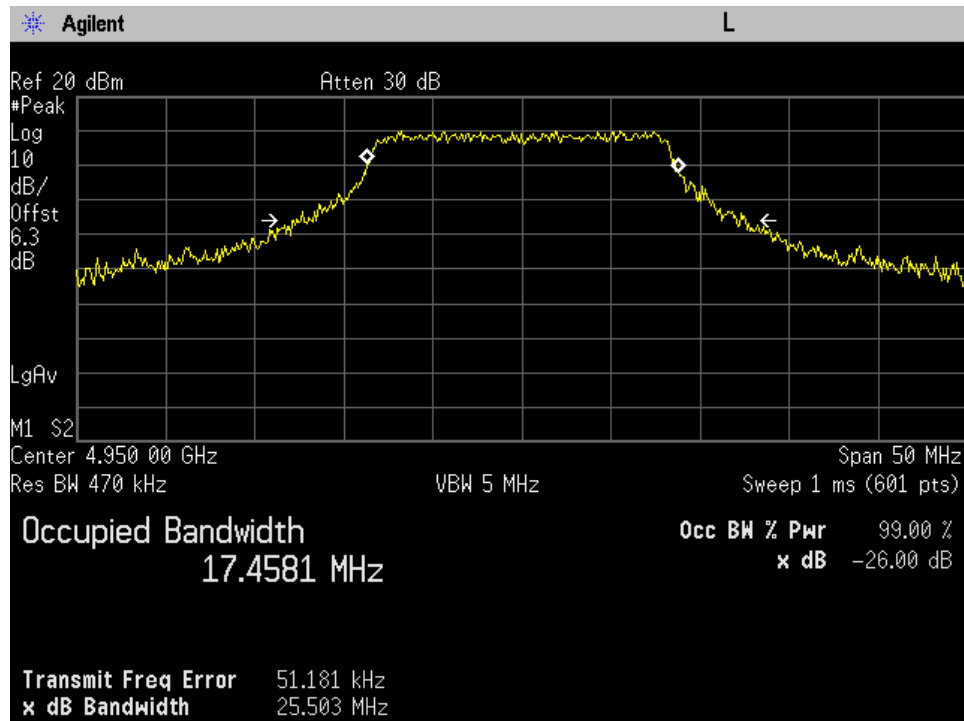


Low ch_Port B_10 MHz_OCBW

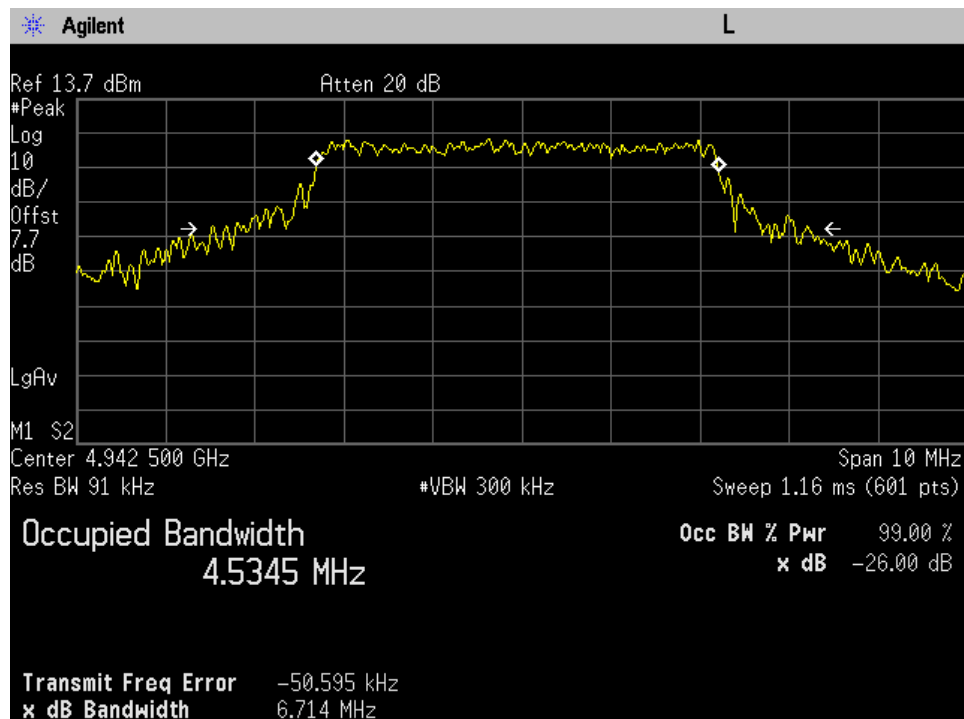




Low ch_Port B_20 MHz_OCBW

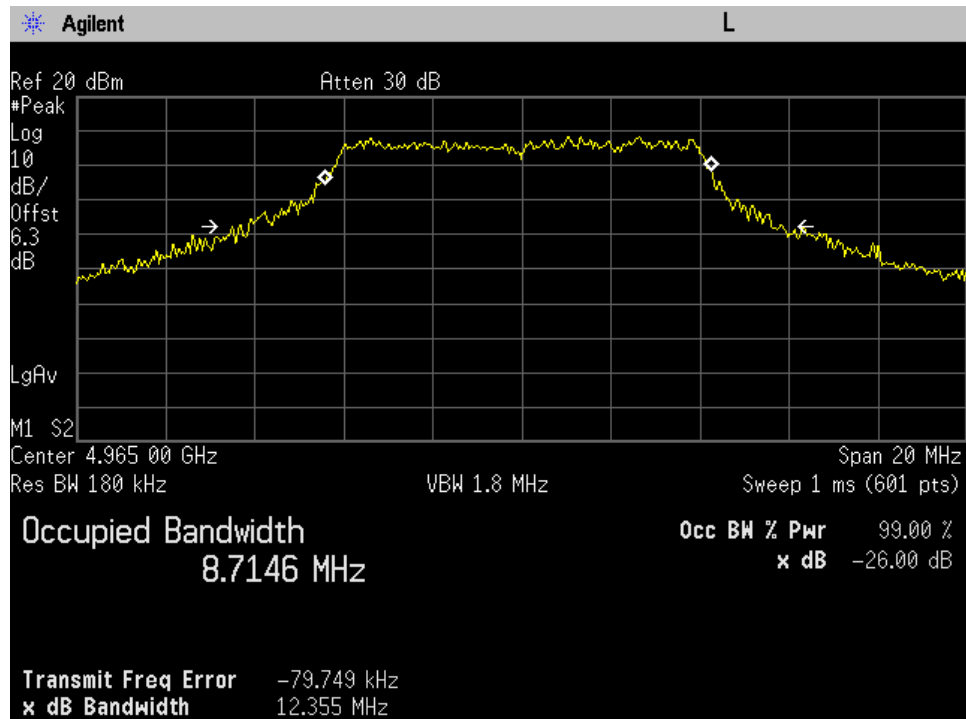


Low ch_Port B_5 MHz_OCBW

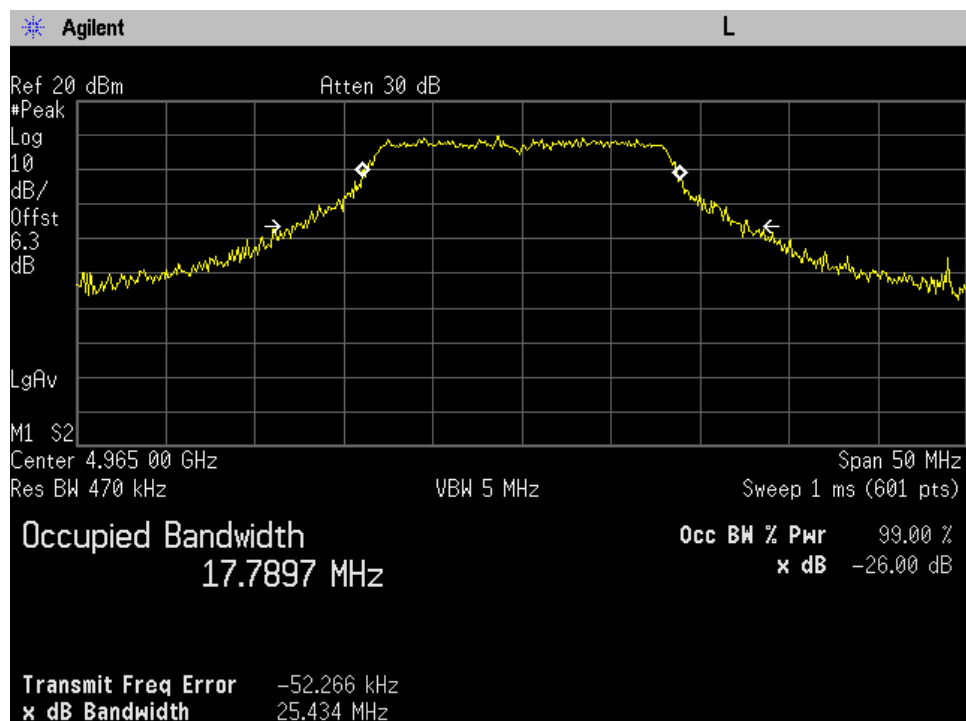




Mid ch_Port A_10 MHz_OCBW

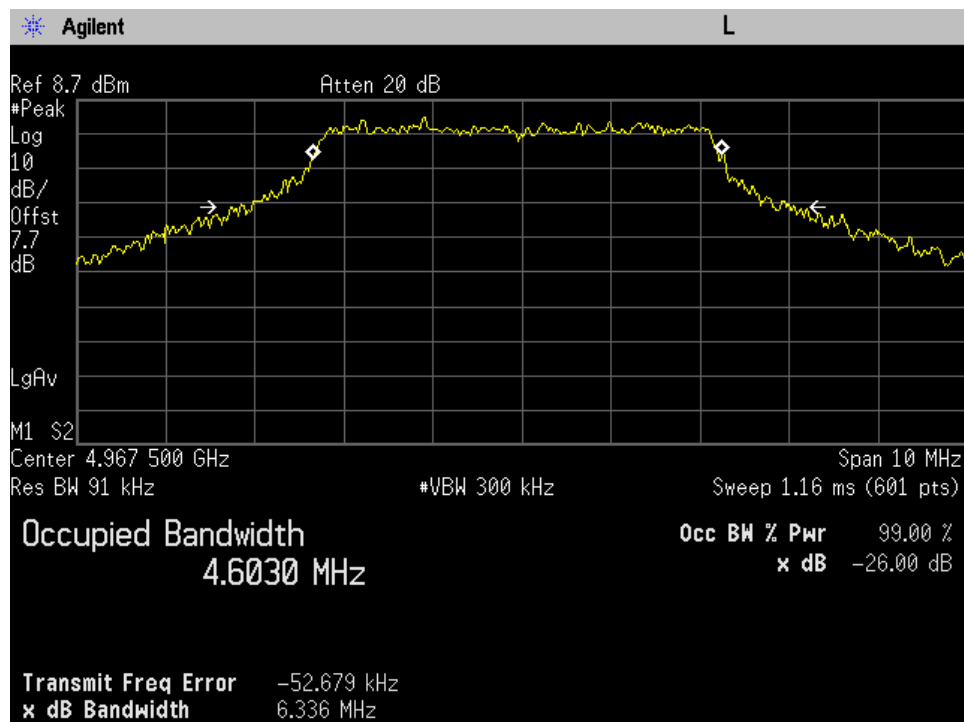


Mid ch_Port A_20 MHz_OCBW

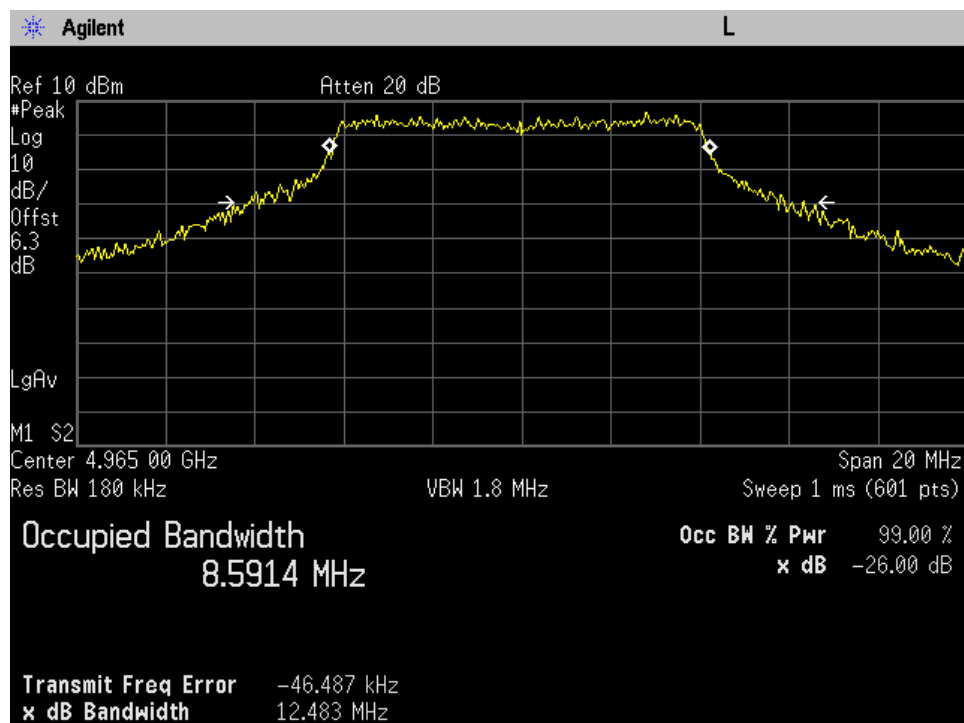




Mid ch_Port A_5 MHz_OCBW

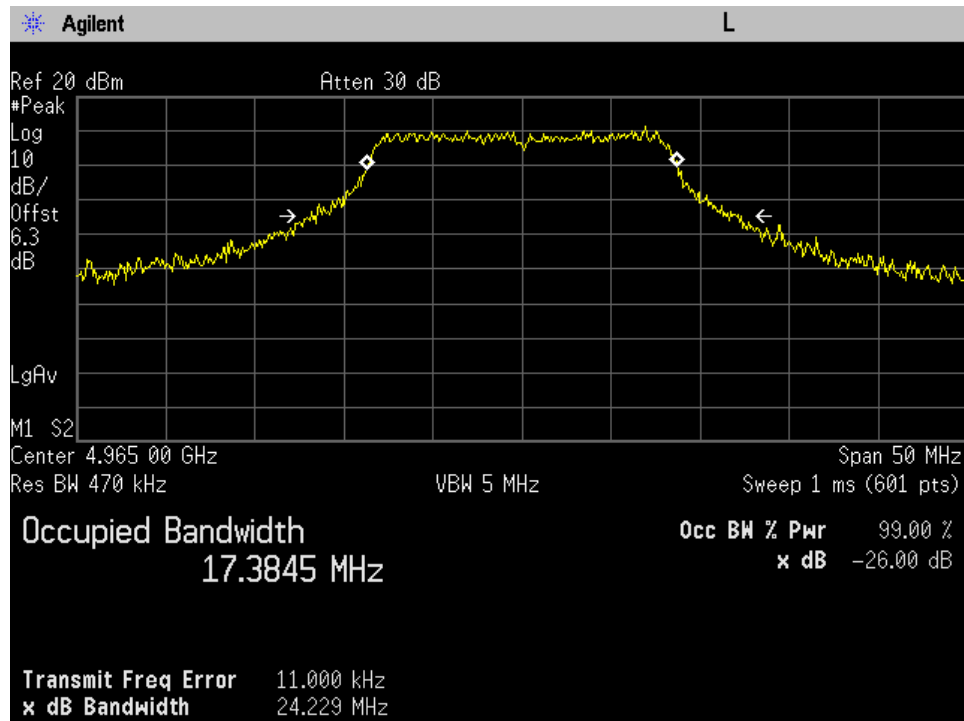


Mid ch_Port B_10 MHz_OCBW

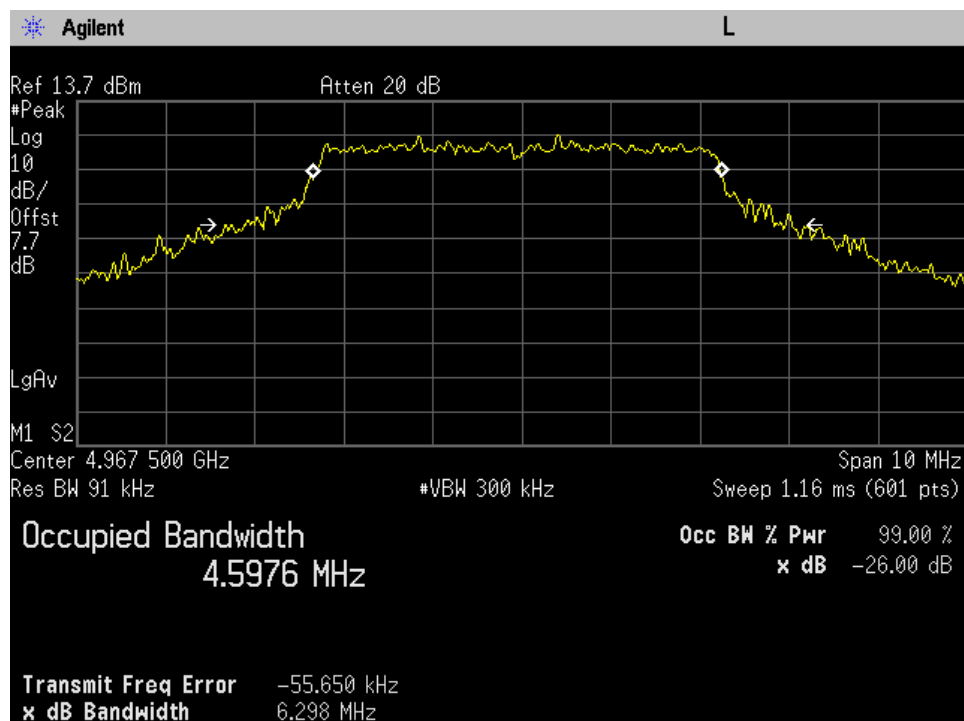




Mid ch_Port B_20 MHz_OCBW

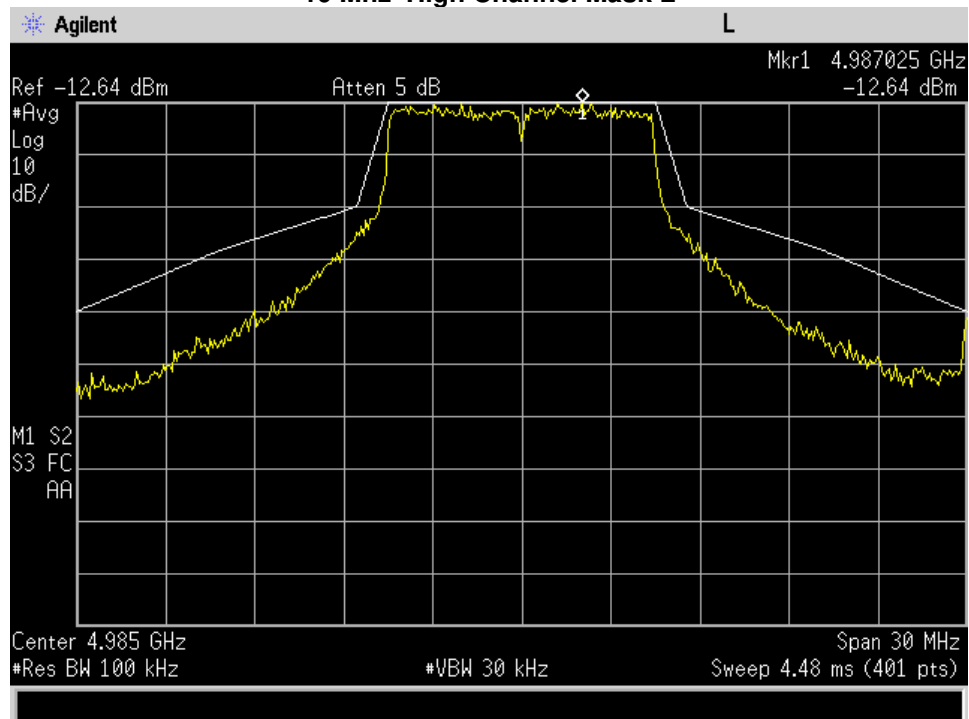


Mid ch_Port B_5 MHz_OCBW

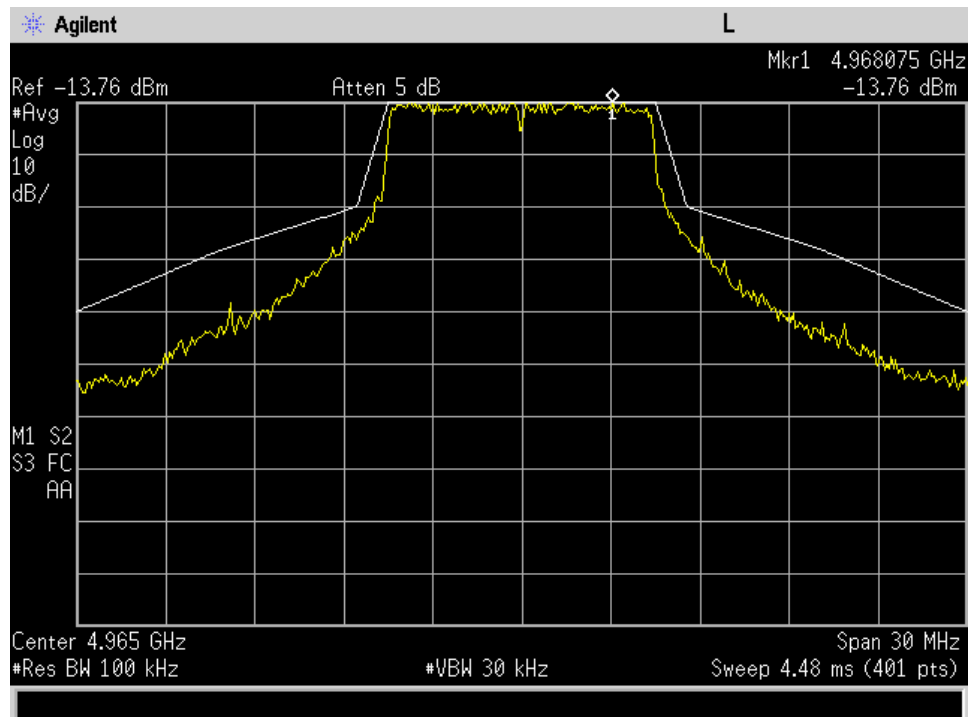




10 Mhz High Channel Mask L

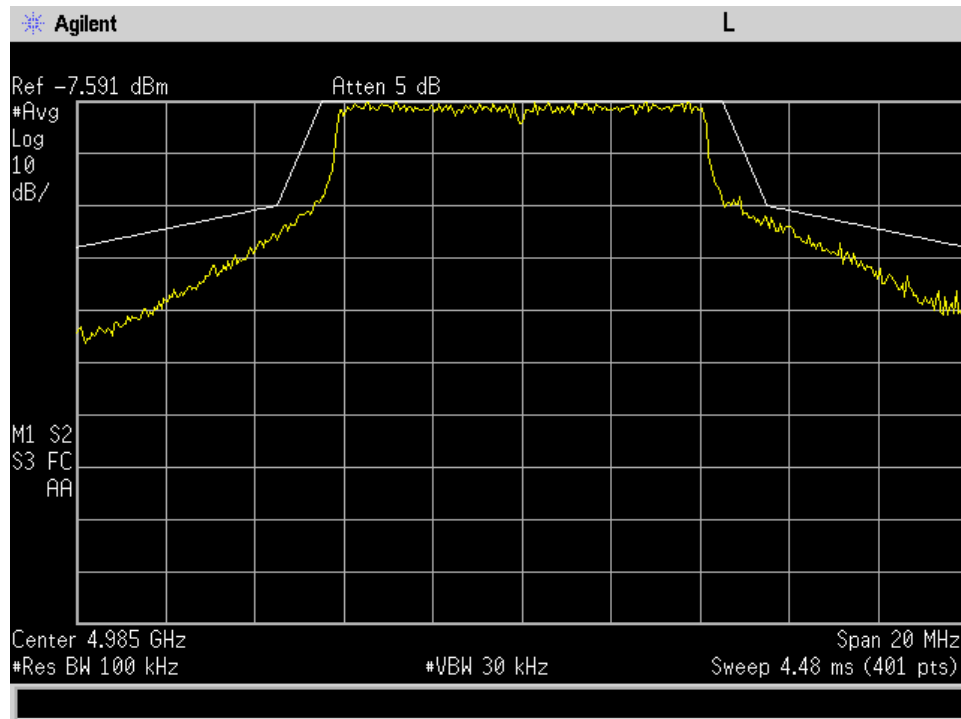


10 Mhz Mid Channel Mask L

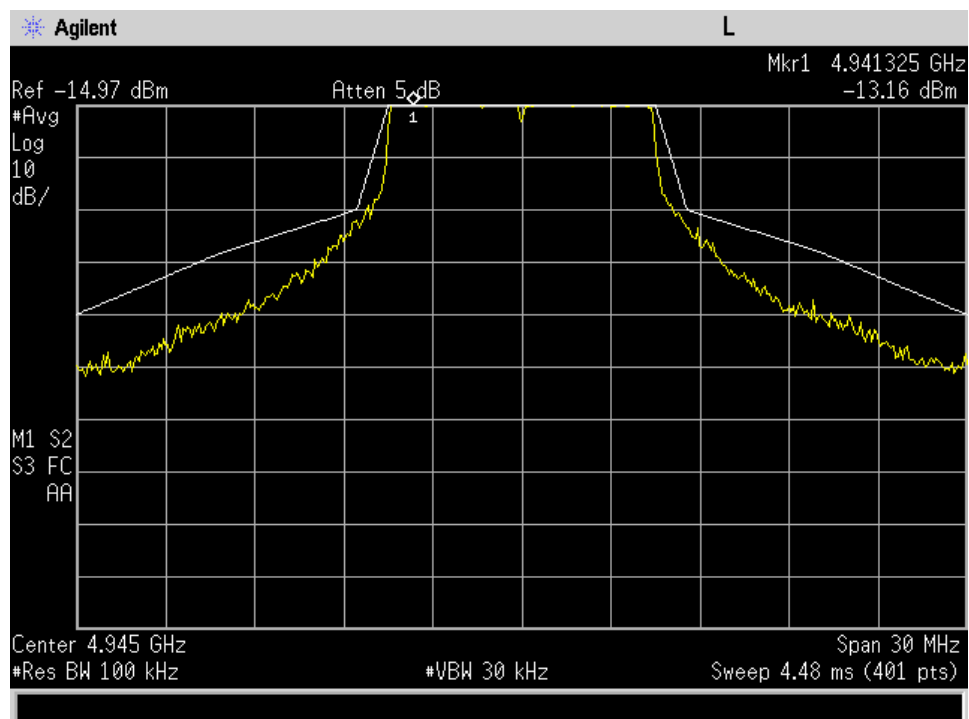




10 Mhz High Channel Mask L_Port B

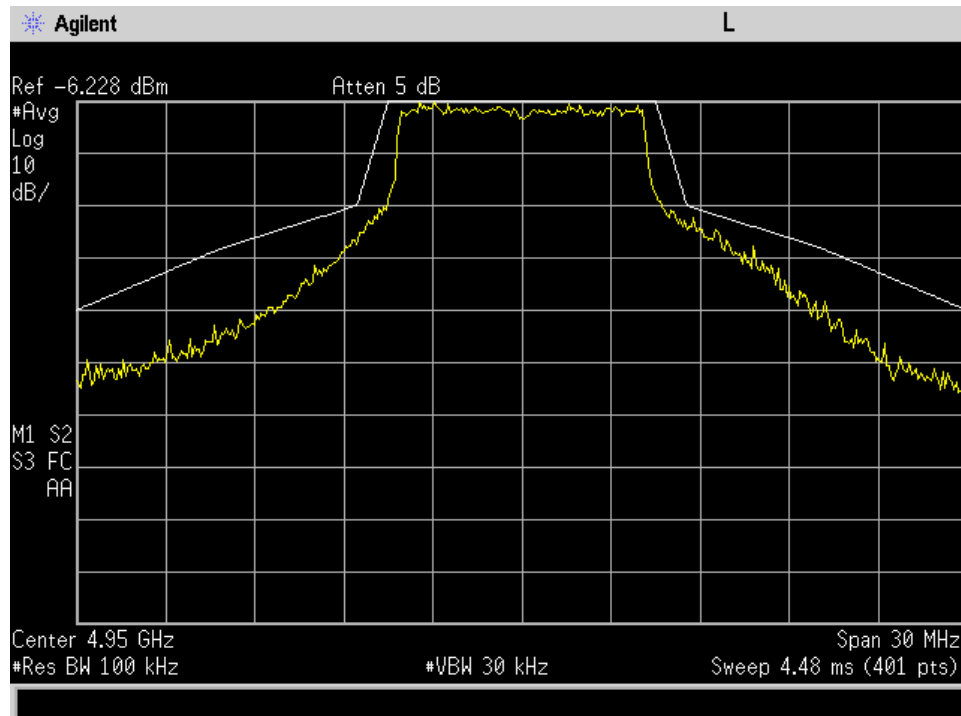


10 Mhz Low Channel Mask L

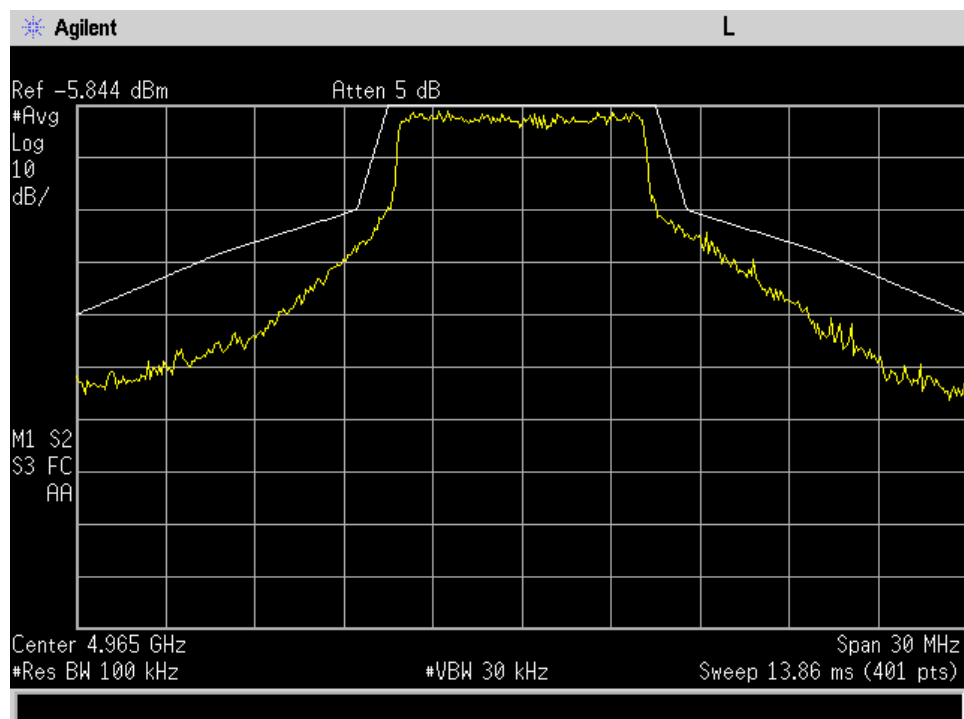




10 Mhz Low Channel Mask L_Port B

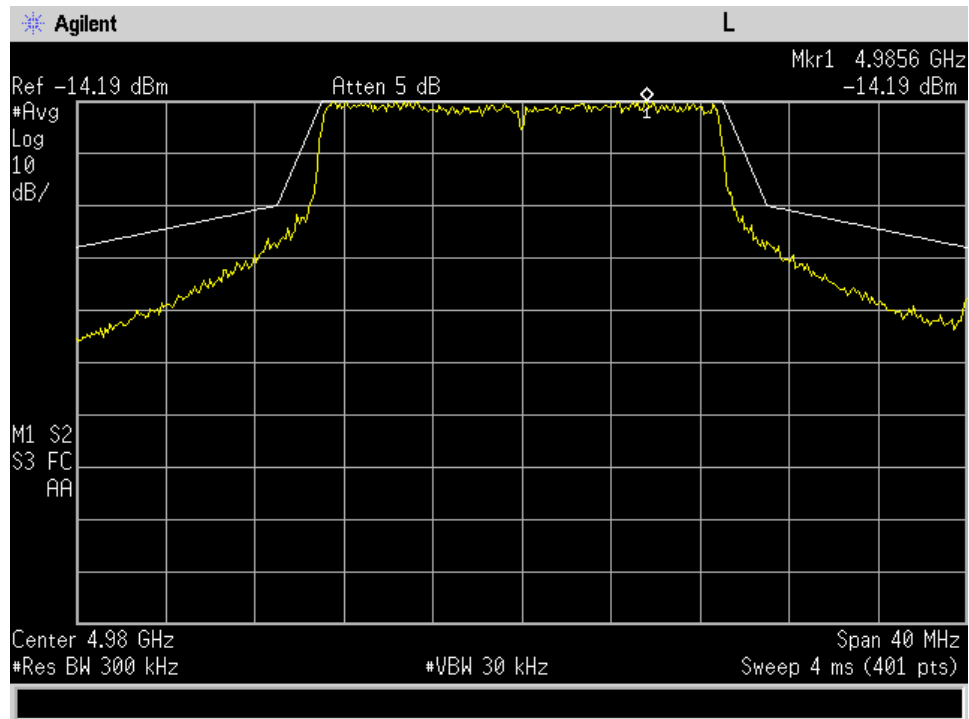


10 Mhz Mid Channel Mask L_Port B

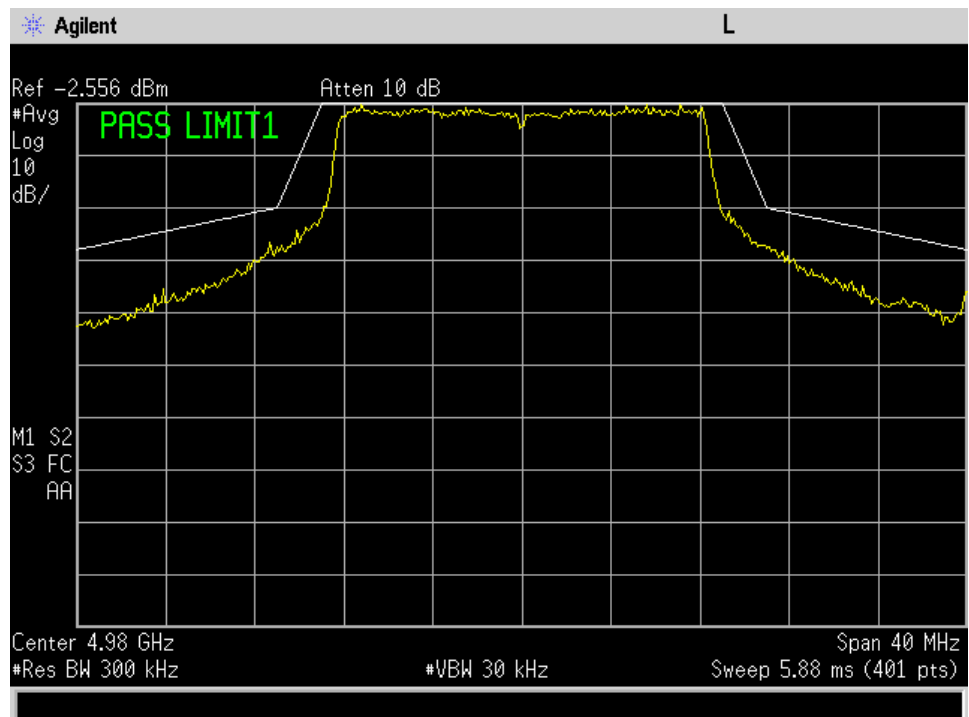




20 Mhz High Channel Mask L

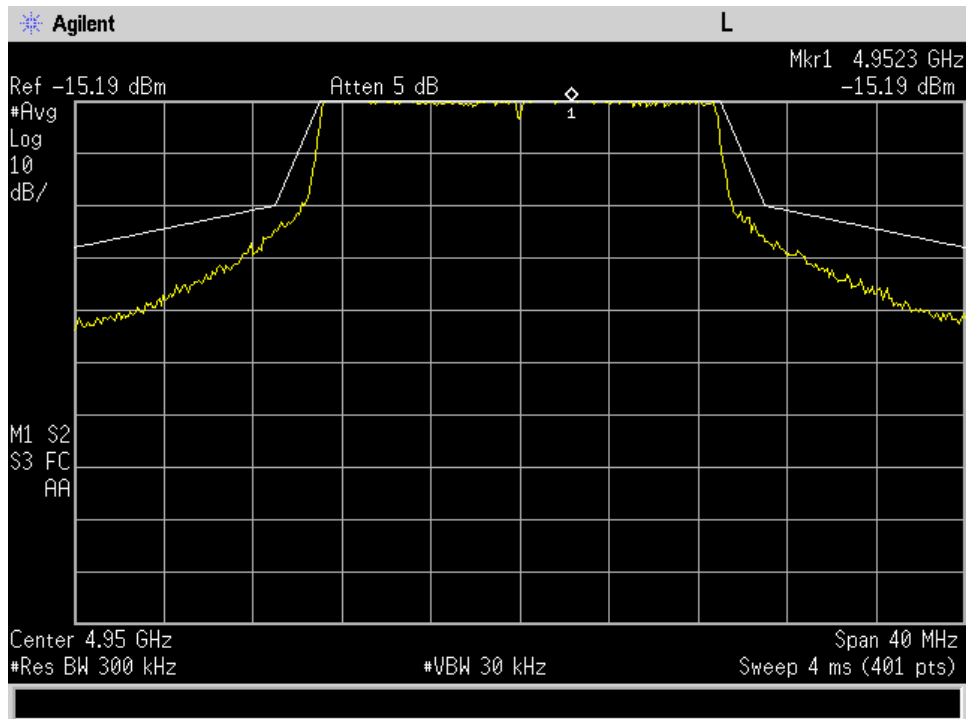


20 Mhz High Channel Mask L_Port B

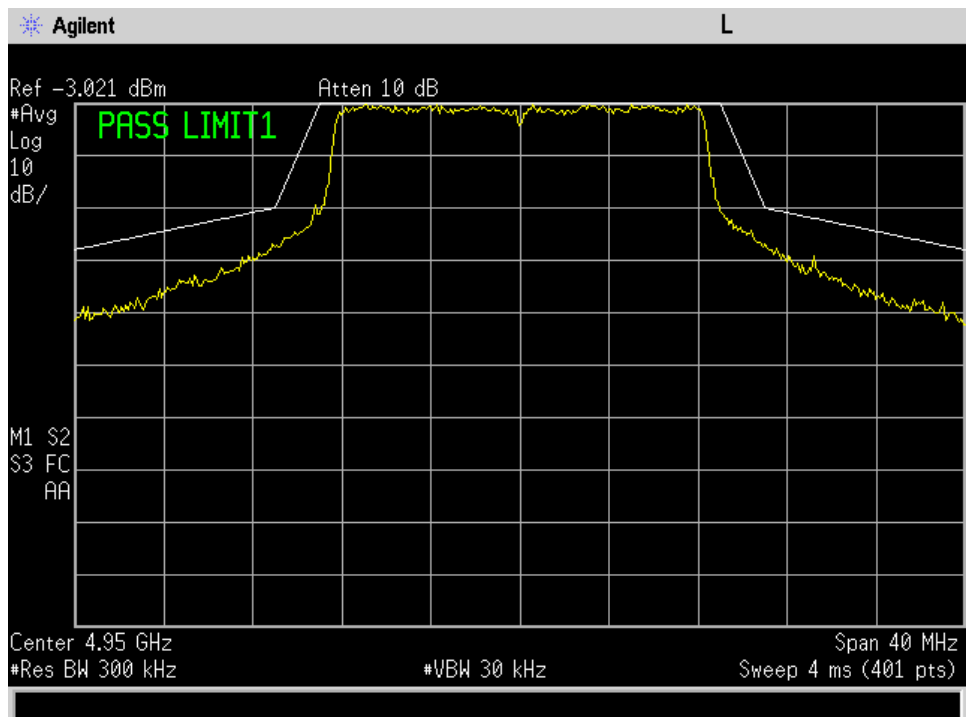




20 Mhz Low Channel Mask L

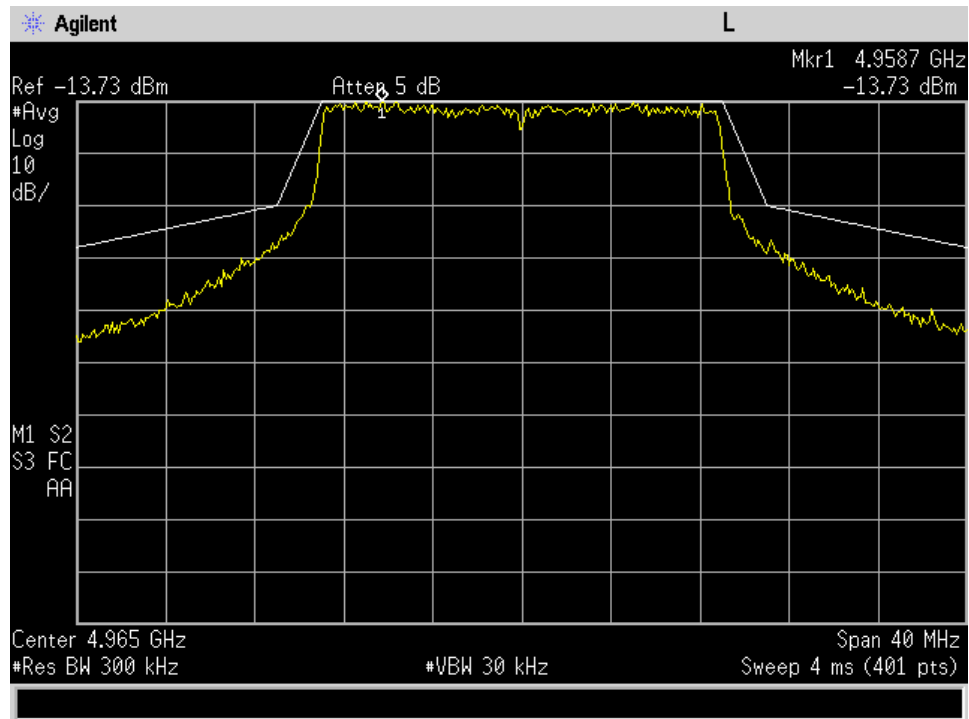


20 Mhz Low Channel Mask L_Port B

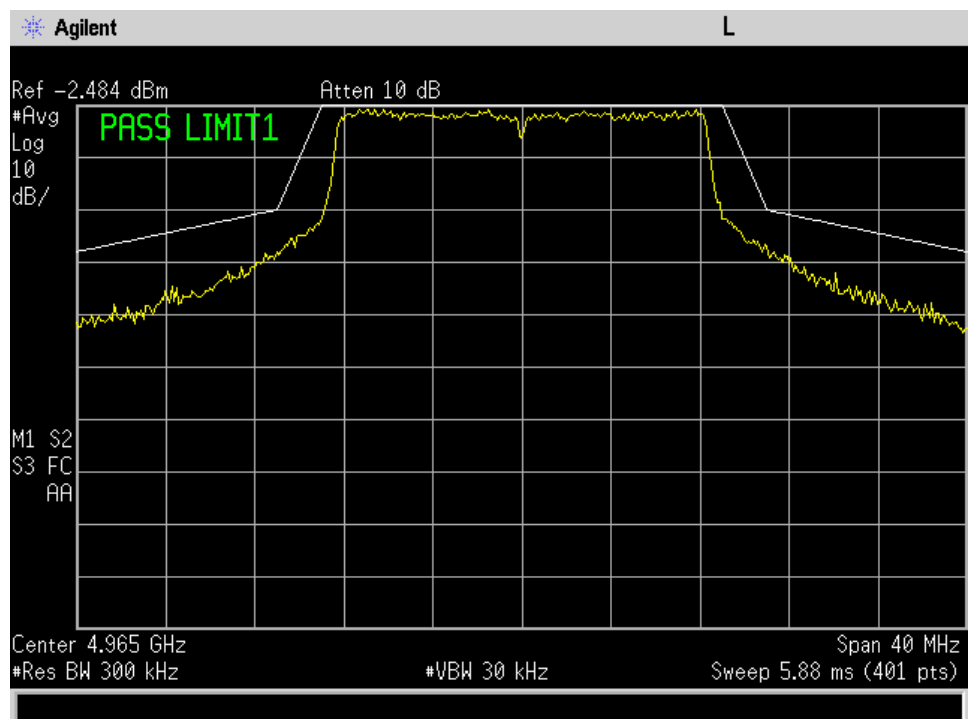




20 Mhz Mid Channel Mask L

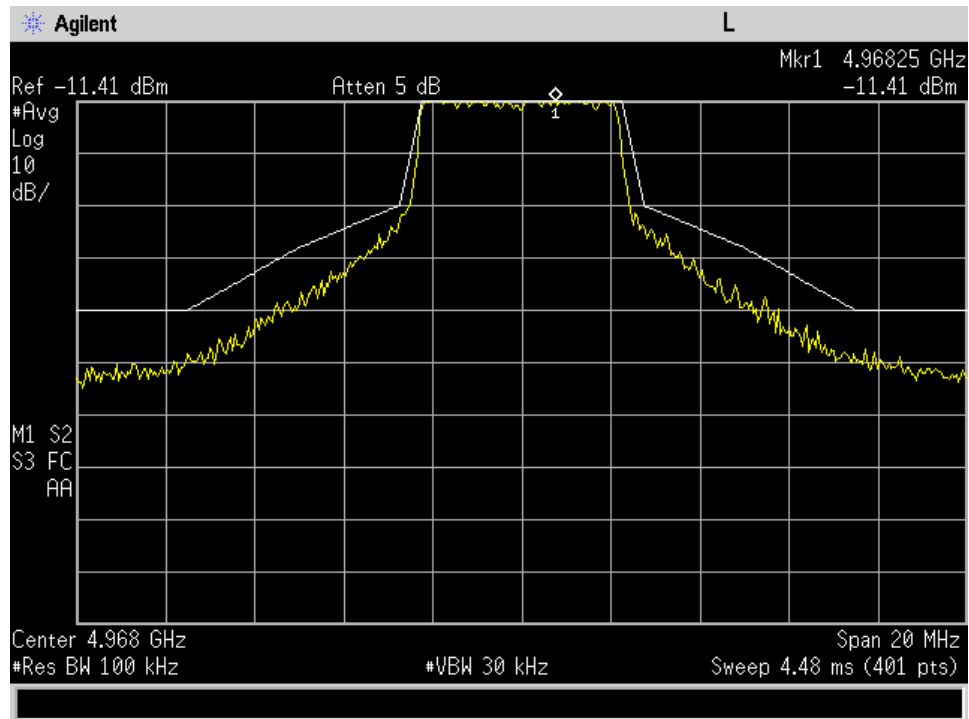


20 Mhz Mid Channel Mask L_Port B

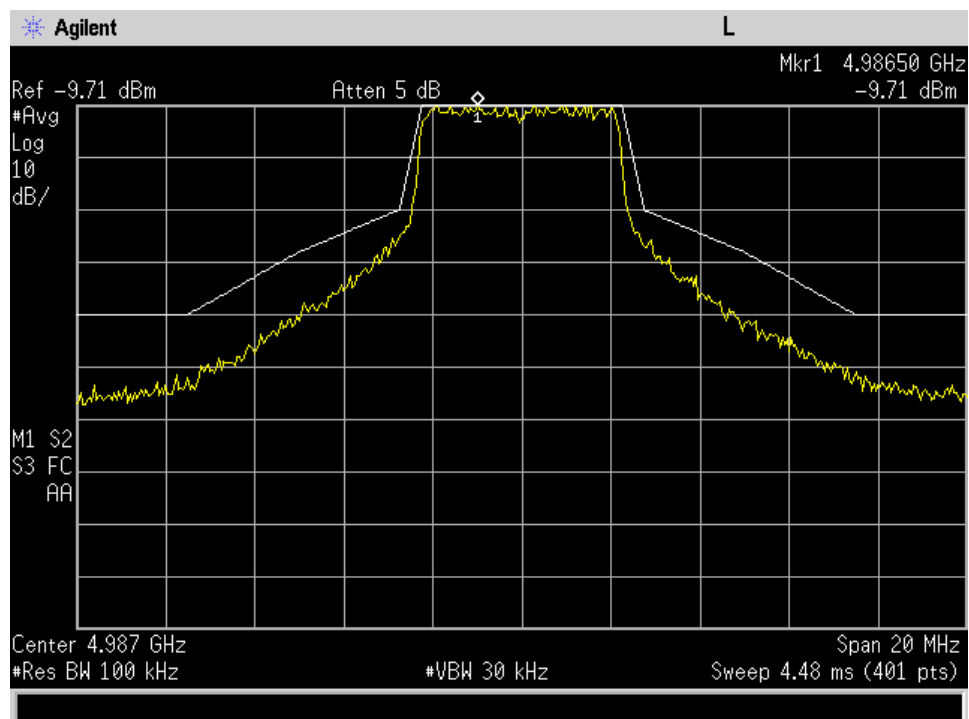




5 Mhz Mid Channel Mask L

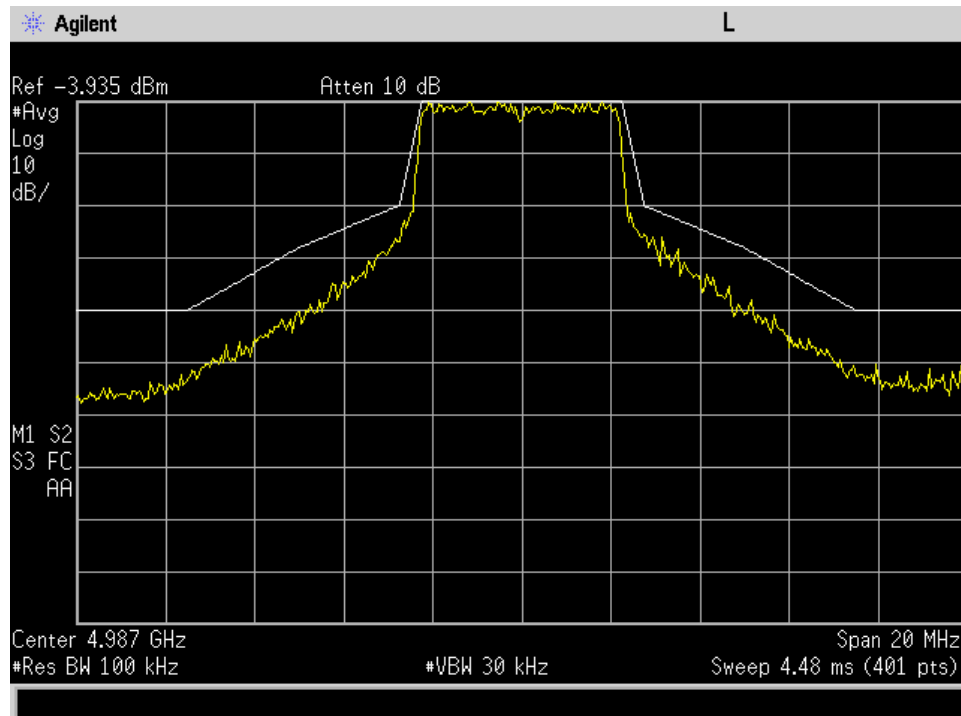


5 Mhz High Channel Mask L

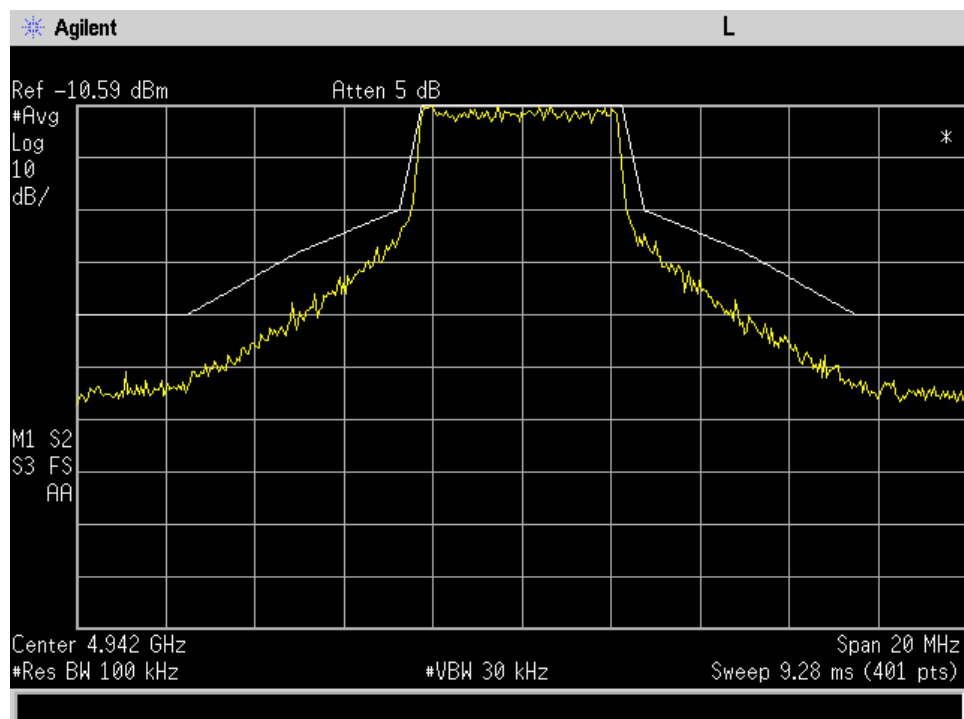




5 Mhz High Channel Mask L_Port B

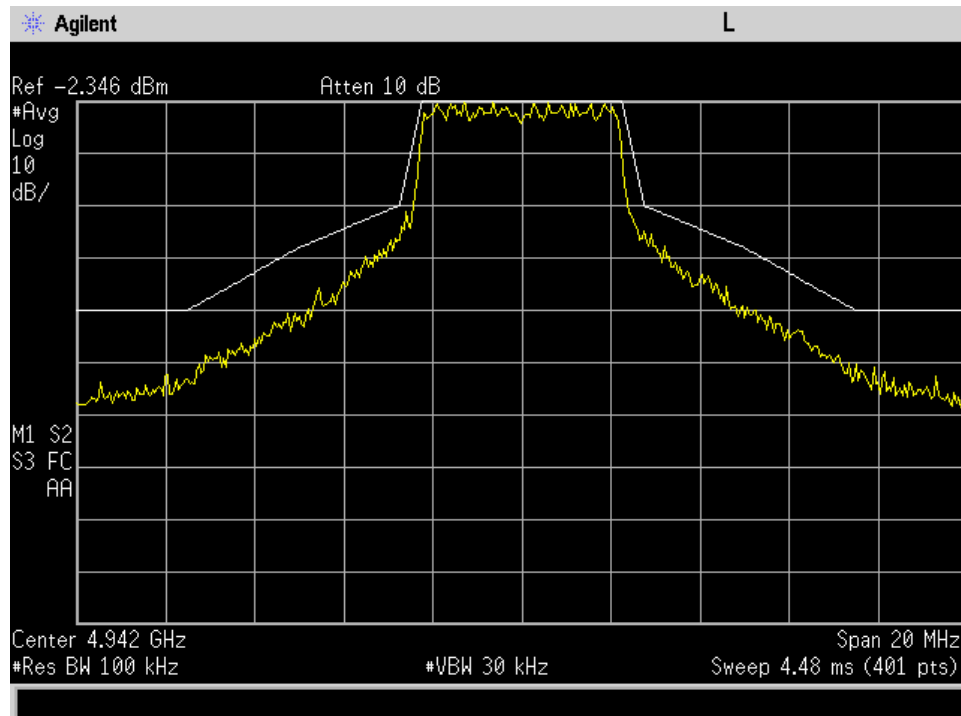


5 Mhz Low Channel Mask L





5 Mhz Low Channel Mask L_Port B



5 Mhz Mid Channel Mask L_Port B

