

N5	15	10	DFT-64QAM	L	Edge_1RB_Right	23.10	PC3	PASS
N5	15	10	DFT-64QAM	L	Outer_Full	23.98	PC3	PASS
N5	15	10	DFT-64QAM	L	Inner_Full	23.08	PC3	PASS
N5	15	10	DFT-256QAM	L	Edge_1RB_Left	22.39	PC3	PASS
N5	15	10	DFT-256QAM	L	Edge_1RB_Right	23.29	PC3	PASS
N5	15	10	DFT-256QAM	L	Outer_Full	23.04	PC3	PASS
N5	15	10	DFT-256QAM	L	Inner_Full	23.11	PC3	PASS
N5	15	10	CP-QPSK	L	Edge_1RB_Left	23.47	PC3	PASS
N5	15	10	CP-QPSK	L	Edge_1RB_Right	23.11	PC3	PASS
N5	15	10	CP-QPSK	L	Outer_Full	23.74	PC3	PASS
N5	15	10	CP-QPSK	L	Inner_Full	23.88	PC3	PASS
N5	15	10	CP-16QAM	L	Edge_1RB_Left	23.64	PC3	PASS
N5	15	10	CP-16QAM	L	Edge_1RB_Right	23.32	PC3	PASS
N5	15	10	CP-16QAM	L	Outer_Full	23.74	PC3	PASS
N5	15	10	CP-16QAM	L	Inner_Full	23.88	PC3	PASS
N5	15	10	CP-64QAM	L	Edge_1RB_Left	23.59	PC3	PASS
N5	15	10	CP-64QAM	L	Edge_1RB_Right	23.53	PC3	PASS
N5	15	10	CP-64QAM	L	Outer_Full	23.81	PC3	PASS
N5	15	10	CP-64QAM	L	Inner_Full	23.86	PC3	PASS
N5	15	10	CP-256QAM	L	Edge_1RB_Left	23.58	PC3	PASS
N5	15	10	CP-256QAM	L	Edge_1RB_Right	22.99	PC3	PASS
N5	15	10	CP-256QAM	L	Outer_Full	23.79	PC3	PASS
N5	15	10	CP-256QAM	L	Inner_Full	23.91	PC3	PASS
N5	15	10	DFT-PI2BPSK	M	Edge_1RB_Left	24.10	PC3	PASS
N5	15	10	DFT-PI2BPSK	M	Edge_1RB_Right	23.15	PC3	PASS
N5	15	10	DFT-PI2BPSK	M	Outer_Full	24.16	PC3	PASS
N5	15	10	DFT-PI2BPSK	M	Inner_Full	23.85	PC3	PASS
N5	15	10	DFT-QPSK	M	Edge_1RB_Left	22.85	PC3	PASS
N5	15	10	DFT-QPSK	M	Edge_1RB_Right	23.18	PC3	PASS
N5	15	10	DFT-QPSK	M	Outer_Full	24.13	PC3	PASS
N5	15	10	DFT-QPSK	M	Inner_Full	23.63	PC3	PASS
N5	15	10	DFT-16QAM	M	Edge_1RB_Left	22.12	PC3	PASS
N5	15	10	DFT-16QAM	M	Edge_1RB_Right	22.35	PC3	PASS
N5	15	10	DFT-16QAM	M	Outer_Full	23.09	PC3	PASS
N5	15	10	DFT-16QAM	M	Inner_Full	24.21	PC3	PASS
N5	15	10	DFT-64QAM	M	Edge_1RB_Left	23.99	PC3	PASS
N5	15	10	DFT-64QAM	M	Edge_1RB_Right	23.17	PC3	PASS
N5	15	10	DFT-64QAM	M	Outer_Full	24.09	PC3	PASS
N5	15	10	DFT-64QAM	M	Inner_Full	24.22	PC3	PASS
N5	15	10	DFT-256QAM	M	Edge_1RB_Left	23.85	PC3	PASS
N5	15	10	DFT-256QAM	M	Edge_1RB_Right	23.26	PC3	PASS
N5	15	10	DFT-256QAM	M	Outer_Full	24.13	PC3	PASS

N5	15	10	DFT-256QAM	M	Inner_Full	24.19	PC3	PASS
N5	15	10	CP-QPSK	M	Edge_1RB_Left	23.84	PC3	PASS
N5	15	10	CP-QPSK	M	Edge_1RB_Right	23.08	PC3	PASS
N5	15	10	CP-QPSK	M	Outer_Full	23.83	PC3	PASS
N5	15	10	CP-QPSK	M	Inner_Full	23.92	PC3	PASS
N5	15	10	CP-16QAM	M	Edge_1RB_Left	24.25	PC3	PASS
N5	15	10	CP-16QAM	M	Edge_1RB_Right	23.38	PC3	PASS
N5	15	10	CP-16QAM	M	Outer_Full	23.88	PC3	PASS
N5	15	10	CP-16QAM	M	Inner_Full	23.94	PC3	PASS
N5	15	10	CP-64QAM	M	Edge_1RB_Left	23.68	PC3	PASS
N5	15	10	CP-64QAM	M	Edge_1RB_Right	22.76	PC3	PASS
N5	15	10	CP-64QAM	M	Outer_Full	23.86	PC3	PASS
N5	15	10	CP-64QAM	M	Inner_Full	23.89	PC3	PASS
N5	15	10	CP-256QAM	M	Edge_1RB_Left	23.90	PC3	PASS
N5	15	10	CP-256QAM	M	Edge_1RB_Right	23.20	PC3	PASS
N5	15	10	CP-256QAM	M	Outer_Full	23.63	PC3	PASS
N5	15	10	CP-256QAM	M	Inner_Full	23.70	PC3	PASS
N5	15	10	DFT-PI2BPSK	H	Edge_1RB_Left	23.98	PC3	PASS
N5	15	10	DFT-PI2BPSK	H	Edge_1RB_Right	22.84	PC3	PASS
N5	15	10	DFT-PI2BPSK	H	Outer_Full	23.11	PC3	PASS
N5	15	10	DFT-PI2BPSK	H	Inner_Full	22.91	PC3	PASS
N5	15	10	DFT-QPSK	H	Edge_1RB_Left	23.15	PC3	PASS
N5	15	10	DFT-QPSK	H	Edge_1RB_Right	22.95	PC3	PASS
N5	15	10	DFT-QPSK	H	Outer_Full	23.09	PC3	PASS
N5	15	10	DFT-QPSK	H	Inner_Full	22.72	PC3	PASS
N5	15	10	DFT-16QAM	H	Edge_1RB_Left	23.64	PC3	PASS
N5	15	10	DFT-16QAM	H	Edge_1RB_Right	22.37	PC3	PASS
N5	15	10	DFT-16QAM	H	Outer_Full	23.05	PC3	PASS
N5	15	10	DFT-16QAM	H	Inner_Full	22.31	PC3	PASS
N5	15	10	DFT-64QAM	H	Edge_1RB_Left	23.81	PC3	PASS
N5	15	10	DFT-64QAM	H	Edge_1RB_Right	22.85	PC3	PASS
N5	15	10	DFT-64QAM	H	Outer_Full	23.12	PC3	PASS
N5	15	10	DFT-64QAM	H	Inner_Full	23.25	PC3	PASS
N5	15	10	DFT-256QAM	H	Edge_1RB_Left	23.50	PC3	PASS
N5	15	10	DFT-256QAM	H	Edge_1RB_Right	23.05	PC3	PASS
N5	15	10	DFT-256QAM	H	Outer_Full	24.04	PC3	PASS
N5	15	10	DFT-256QAM	H	Inner_Full	24.26	PC3	PASS
N5	15	10	CP-QPSK	H	Edge_1RB_Left	23.19	PC3	PASS
N5	15	10	CP-QPSK	H	Edge_1RB_Right	22.79	PC3	PASS
N5	15	10	CP-QPSK	H	Outer_Full	22.83	PC3	PASS
N5	15	10	CP-QPSK	H	Inner_Full	23.02	PC3	PASS
N5	15	10	CP-16QAM	H	Edge_1RB_Left	22.50	PC3	PASS

N5	15	10	CP-16QAM	H	Edge_1RB_Right	22.69	PC3	PASS
N5	15	10	CP-16QAM	H	Outer_Full	23.87	PC3	PASS
N5	15	10	CP-16QAM	H	Inner_Full	24.00	PC3	PASS
N5	15	10	CP-64QAM	H	Edge_1RB_Left	23.69	PC3	PASS
N5	15	10	CP-64QAM	H	Edge_1RB_Right	23.11	PC3	PASS
N5	15	10	CP-64QAM	H	Outer_Full	23.86	PC3	PASS
N5	15	10	CP-64QAM	H	Inner_Full	23.98	PC3	PASS
N5	15	10	CP-256QAM	H	Edge_1RB_Left	23.84	PC3	PASS
N5	15	10	CP-256QAM	H	Edge_1RB_Right	23.06	PC3	PASS
N5	15	10	CP-256QAM	H	Outer_Full	23.89	PC3	PASS
N5	15	10	CP-256QAM	H	Inner_Full	22.80	PC3	PASS
N5	15	15	DFT-PI2BPSK	L	Edge_1RB_Left	24.45	PC3	PASS
N5	15	15	DFT-PI2BPSK	L	Edge_1RB_Right	23.50	PC3	PASS
N5	15	15	DFT-PI2BPSK	L	Outer_Full	24.37	PC3	PASS
N5	15	15	DFT-PI2BPSK	L	Inner_Full	23.82	PC3	PASS
N5	15	15	DFT-QPSK	L	Edge_1RB_Left	22.56	PC3	PASS
N5	15	15	DFT-QPSK	L	Edge_1RB_Right	23.77	PC3	PASS
N5	15	15	DFT-QPSK	L	Outer_Full	24.31	PC3	PASS
N5	15	15	DFT-QPSK	L	Inner_Full	23.56	PC3	PASS
N5	15	15	DFT-16QAM	L	Edge_1RB_Left	23.59	PC3	PASS
N5	15	15	DFT-16QAM	L	Edge_1RB_Right	22.47	PC3	PASS
N5	15	15	DFT-16QAM	L	Outer_Full	24.28	PC3	PASS
N5	15	15	DFT-16QAM	L	Inner_Full	24.37	PC3	PASS
N5	15	15	DFT-64QAM	L	Edge_1RB_Left	23.11	PC3	PASS
N5	15	15	DFT-64QAM	L	Edge_1RB_Right	23.61	PC3	PASS
N5	15	15	DFT-64QAM	L	Outer_Full	24.26	PC3	PASS
N5	15	15	DFT-64QAM	L	Inner_Full	24.33	PC3	PASS
N5	15	15	DFT-256QAM	L	Edge_1RB_Left	24.47	PC3	PASS
N5	15	15	DFT-256QAM	L	Edge_1RB_Right	23.96	PC3	PASS
N5	15	15	DFT-256QAM	L	Outer_Full	24.29	PC3	PASS
N5	15	15	DFT-256QAM	L	Inner_Full	23.36	PC3	PASS
N5	15	15	CP-QPSK	L	Edge_1RB_Left	24.17	PC3	PASS
N5	15	15	CP-QPSK	L	Edge_1RB_Right	23.73	PC3	PASS
N5	15	15	CP-QPSK	L	Outer_Full	24.00	PC3	PASS
N5	15	15	CP-QPSK	L	Inner_Full	24.08	PC3	PASS
N5	15	15	CP-16QAM	L	Edge_1RB_Left	23.53	PC3	PASS
N5	15	15	CP-16QAM	L	Edge_1RB_Right	23.31	PC3	PASS
N5	15	15	CP-16QAM	L	Outer_Full	24.11	PC3	PASS
N5	15	15	CP-16QAM	L	Inner_Full	24.07	PC3	PASS
N5	15	15	CP-64QAM	L	Edge_1RB_Left	24.05	PC3	PASS
N5	15	15	CP-64QAM	L	Edge_1RB_Right	23.67	PC3	PASS
N5	15	15	CP-64QAM	L	Outer_Full	24.06	PC3	PASS

N5	15	15	CP-64QAM	L	Inner_Full	24.10	PC3	PASS
N5	15	15	CP-256QAM	L	Edge_1RB_Left	23.70	PC3	PASS
N5	15	15	CP-256QAM	L	Edge_1RB_Right	22.23	PC3	PASS
N5	15	15	CP-256QAM	L	Outer_Full	23.60	PC3	PASS
N5	15	15	CP-256QAM	L	Inner_Full	23.50	PC3	PASS
N5	15	15	DFT-PI2BPSK	M	Edge_1RB_Left	23.38	PC3	PASS
N5	15	15	DFT-PI2BPSK	M	Edge_1RB_Right	23.53	PC3	PASS
N5	15	15	DFT-PI2BPSK	M	Outer_Full	23.46	PC3	PASS
N5	15	15	DFT-PI2BPSK	M	Inner_Full	23.86	PC3	PASS
N5	15	15	DFT-QPSK	M	Edge_1RB_Left	22.56	PC3	PASS
N5	15	15	DFT-QPSK	M	Edge_1RB_Right	23.80	PC3	PASS
N5	15	15	DFT-QPSK	M	Outer_Full	24.43	PC3	PASS
N5	15	15	DFT-QPSK	M	Inner_Full	24.39	PC3	PASS
N5	15	15	DFT-16QAM	M	Edge_1RB_Left	24.54	PC3	PASS
N5	15	15	DFT-16QAM	M	Edge_1RB_Right	23.46	PC3	PASS
N5	15	15	DFT-16QAM	M	Outer_Full	24.42	PC3	PASS
N5	15	15	DFT-16QAM	M	Inner_Full	24.31	PC3	PASS
N5	15	15	DFT-64QAM	M	Edge_1RB_Left	24.08	PC3	PASS
N5	15	15	DFT-64QAM	M	Edge_1RB_Right	24.11	PC3	PASS
N5	15	15	DFT-64QAM	M	Outer_Full	24.40	PC3	PASS
N5	15	15	DFT-64QAM	M	Inner_Full	24.31	PC3	PASS
N5	15	15	DFT-256QAM	M	Edge_1RB_Left	23.49	PC3	PASS
N5	15	15	DFT-256QAM	M	Edge_1RB_Right	23.03	PC3	PASS
N5	15	15	DFT-256QAM	M	Outer_Full	23.65	PC3	PASS
N5	15	15	DFT-256QAM	M	Inner_Full	24.33	PC3	PASS
N5	15	15	CP-QPSK	M	Edge_1RB_Left	24.12	PC3	PASS
N5	15	15	CP-QPSK	M	Edge_1RB_Right	23.77	PC3	PASS
N5	15	15	CP-QPSK	M	Outer_Full	24.13	PC3	PASS
N5	15	15	CP-QPSK	M	Inner_Full	24.05	PC3	PASS
N5	15	15	CP-16QAM	M	Edge_1RB_Left	23.33	PC3	PASS
N5	15	15	CP-16QAM	M	Edge_1RB_Right	23.93	PC3	PASS
N5	15	15	CP-16QAM	M	Outer_Full	23.22	PC3	PASS
N5	15	15	CP-16QAM	M	Inner_Full	24.09	PC3	PASS
N5	15	15	CP-64QAM	M	Edge_1RB_Left	24.02	PC3	PASS
N5	15	15	CP-64QAM	M	Edge_1RB_Right	23.81	PC3	PASS
N5	15	15	CP-64QAM	M	Outer_Full	24.19	PC3	PASS
N5	15	15	CP-64QAM	M	Inner_Full	23.13	PC3	PASS
N5	15	15	CP-256QAM	M	Edge_1RB_Left	23.62	PC3	PASS
N5	15	15	CP-256QAM	M	Edge_1RB_Right	23.49	PC3	PASS
N5	15	15	CP-256QAM	M	Outer_Full	23.62	PC3	PASS
N5	15	15	CP-256QAM	M	Inner_Full	23.39	PC3	PASS
N5	15	15	DFT-PI2BPSK	H	Edge_1RB_Left	23.25	PC3	PASS

N5	15	15	DFT-PI2BPSK	H	Edge_1RB_Right	24.32	PC3	PASS
N5	15	15	DFT-PI2BPSK	H	Outer_Full	23.49	PC3	PASS
N5	15	15	DFT-PI2BPSK	H	Inner_Full	23.11	PC3	PASS
N5	15	15	DFT-QPSK	H	Edge_1RB_Left	23.86	PC3	PASS
N5	15	15	DFT-QPSK	H	Edge_1RB_Right	23.35	PC3	PASS
N5	15	15	DFT-QPSK	H	Outer_Full	24.46	PC3	PASS
N5	15	15	DFT-QPSK	H	Inner_Full	24.17	PC3	PASS
N5	15	15	DFT-16QAM	H	Edge_1RB_Left	23.07	PC3	PASS
N5	15	15	DFT-16QAM	H	Edge_1RB_Right	23.58	PC3	PASS
N5	15	15	DFT-16QAM	H	Outer_Full	24.45	PC3	PASS
N5	15	15	DFT-16QAM	H	Inner_Full	24.68	PC3	PASS
N5	15	15	DFT-64QAM	H	Edge_1RB_Left	23.95	PC3	PASS
N5	15	15	DFT-64QAM	H	Edge_1RB_Right	23.69	PC3	PASS
N5	15	15	DFT-64QAM	H	Outer_Full	24.43	PC3	PASS
N5	15	15	DFT-64QAM	H	Inner_Full	24.65	PC3	PASS
N5	15	15	DFT-256QAM	H	Edge_1RB_Left	24.38	PC3	PASS
N5	15	15	DFT-256QAM	H	Edge_1RB_Right	23.71	PC3	PASS
N5	15	15	DFT-256QAM	H	Outer_Full	23.43	PC3	PASS
N5	15	15	DFT-256QAM	H	Inner_Full	24.68	PC3	PASS
N5	15	15	CP-QPSK	H	Edge_1RB_Left	23.80	PC3	PASS
N5	15	15	CP-QPSK	H	Edge_1RB_Right	23.40	PC3	PASS
N5	15	15	CP-QPSK	H	Outer_Full	24.14	PC3	PASS
N5	15	15	CP-QPSK	H	Inner_Full	24.44	PC3	PASS
N5	15	15	CP-16QAM	H	Edge_1RB_Left	23.26	PC3	PASS
N5	15	15	CP-16QAM	H	Edge_1RB_Right	23.03	PC3	PASS
N5	15	15	CP-16QAM	H	Outer_Full	24.25	PC3	PASS
N5	15	15	CP-16QAM	H	Inner_Full	23.89	PC3	PASS
N5	15	15	CP-64QAM	H	Edge_1RB_Left	23.94	PC3	PASS
N5	15	15	CP-64QAM	H	Edge_1RB_Right	23.46	PC3	PASS
N5	15	15	CP-64QAM	H	Outer_Full	24.30	PC3	PASS
N5	15	15	CP-64QAM	H	Inner_Full	24.52	PC3	PASS
N5	15	15	CP-256QAM	H	Edge_1RB_Left	23.84	PC3	PASS
N5	15	15	CP-256QAM	H	Edge_1RB_Right	23.32	PC3	PASS
N5	15	15	CP-256QAM	H	Outer_Full	23.78	PC3	PASS
N5	15	15	CP-256QAM	H	Inner_Full	23.80	PC3	PASS
N5	15	20	DFT-PI2BPSK	L	Edge_1RB_Left	24.26	PC3	PASS
N5	15	20	DFT-PI2BPSK	L	Edge_1RB_Right	23.38	PC3	PASS
N5	15	20	DFT-PI2BPSK	L	Outer_Full	24.49	PC3	PASS
N5	15	20	DFT-PI2BPSK	L	Inner_Full	23.85	PC3	PASS
N5	15	20	DFT-QPSK	L	Edge_1RB_Left	23.59	PC3	PASS
N5	15	20	DFT-QPSK	L	Edge_1RB_Right	23.47	PC3	PASS
N5	15	20	DFT-QPSK	L	Outer_Full	24.43	PC3	PASS

N5	15	20	DFT-QPSK	L	Inner_Full	23.52	PC3	PASS
N5	15	20	DFT-16QAM	L	Edge_1RB_Left	22.76	PC3	PASS
N5	15	20	DFT-16QAM	L	Edge_1RB_Right	23.58	PC3	PASS
N5	15	20	DFT-16QAM	L	Outer_Full	24.44	PC3	PASS
N5	15	20	DFT-16QAM	L	Inner_Full	24.49	PC3	PASS
N5	15	20	DFT-64QAM	L	Edge_1RB_Left	23.38	PC3	PASS
N5	15	20	DFT-64QAM	L	Edge_1RB_Right	23.77	PC3	PASS
N5	15	20	DFT-64QAM	L	Outer_Full	24.42	PC3	PASS
N5	15	20	DFT-64QAM	L	Inner_Full	23.54	PC3	PASS
N5	15	20	DFT-256QAM	L	Edge_1RB_Left	24.34	PC3	PASS
N5	15	20	DFT-256QAM	L	Edge_1RB_Right	23.64	PC3	PASS
N5	15	20	DFT-256QAM	L	Outer_Full	24.38	PC3	PASS
N5	15	20	DFT-256QAM	L	Inner_Full	23.43	PC3	PASS
N5	15	20	CP-QPSK	L	Edge_1RB_Left	23.95	PC3	PASS
N5	15	20	CP-QPSK	L	Edge_1RB_Right	22.85	PC3	PASS
N5	15	20	CP-QPSK	L	Outer_Full	23.11	PC3	PASS
N5	15	20	CP-QPSK	L	Inner_Full	23.18	PC3	PASS
N5	15	20	CP-16QAM	L	Edge_1RB_Left	23.39	PC3	PASS
N5	15	20	CP-16QAM	L	Edge_1RB_Right	23.12	PC3	PASS
N5	15	20	CP-16QAM	L	Outer_Full	24.13	PC3	PASS
N5	15	20	CP-16QAM	L	Inner_Full	24.24	PC3	PASS
N5	15	20	CP-64QAM	L	Edge_1RB_Left	23.86	PC3	PASS
N5	15	20	CP-64QAM	L	Edge_1RB_Right	23.31	PC3	PASS
N5	15	20	CP-64QAM	L	Outer_Full	23.17	PC3	PASS
N5	15	20	CP-64QAM	L	Inner_Full	24.21	PC3	PASS
N5	15	20	CP-256QAM	L	Edge_1RB_Left	23.77	PC3	PASS
N5	15	20	CP-256QAM	L	Edge_1RB_Right	23.28	PC3	PASS
N5	15	20	CP-256QAM	L	Outer_Full	23.51	PC3	PASS
N5	15	20	CP-256QAM	L	Inner_Full	23.76	PC3	PASS
N5	15	20	DFT-PI2BPSK	M	Edge_1RB_Left	23.97	PC3	PASS
N5	15	20	DFT-PI2BPSK	M	Edge_1RB_Right	23.13	PC3	PASS
N5	15	20	DFT-PI2BPSK	M	Outer_Full	24.49	PC3	PASS
N5	15	20	DFT-PI2BPSK	M	Inner_Full	23.93	PC3	PASS
N5	15	20	DFT-QPSK	M	Edge_1RB_Left	22.99	PC3	PASS
N5	15	20	DFT-QPSK	M	Edge_1RB_Right	23.11	PC3	PASS
N5	15	20	DFT-QPSK	M	Outer_Full	24.45	PC3	PASS
N5	15	20	DFT-QPSK	M	Inner_Full	24.56	PC3	PASS
N5	15	20	DFT-16QAM	M	Edge_1RB_Left	22.13	PC3	PASS
N5	15	20	DFT-16QAM	M	Edge_1RB_Right	22.51	PC3	PASS
N5	15	20	DFT-16QAM	M	Outer_Full	23.45	PC3	PASS
N5	15	20	DFT-16QAM	M	Inner_Full	23.53	PC3	PASS
N5	15	20	DFT-64QAM	M	Edge_1RB_Left	23.71	PC3	PASS

N5	15	20	DFT-64QAM	M	Edge_1RB_Right	23.40	PC3	PASS
N5	15	20	DFT-64QAM	M	Outer_Full	23.44	PC3	PASS
N5	15	20	DFT-64QAM	M	Inner_Full	24.48	PC3	PASS
N5	15	20	DFT-256QAM	M	Edge_1RB_Left	23.60	PC3	PASS
N5	15	20	DFT-256QAM	M	Edge_1RB_Right	24.38	PC3	PASS
N5	15	20	DFT-256QAM	M	Outer_Full	23.47	PC3	PASS
N5	15	20	DFT-256QAM	M	Inner_Full	23.52	PC3	PASS
N5	15	20	CP-QPSK	M	Edge_1RB_Left	23.73	PC3	PASS
N5	15	20	CP-QPSK	M	Edge_1RB_Right	23.05	PC3	PASS
N5	15	20	CP-QPSK	M	Outer_Full	23.14	PC3	PASS
N5	15	20	CP-QPSK	M	Inner_Full	23.30	PC3	PASS
N5	15	20	CP-16QAM	M	Edge_1RB_Left	23.09	PC3	PASS
N5	15	20	CP-16QAM	M	Edge_1RB_Right	22.83	PC3	PASS
N5	15	20	CP-16QAM	M	Outer_Full	23.20	PC3	PASS
N5	15	20	CP-16QAM	M	Inner_Full	23.35	PC3	PASS
N5	15	20	CP-64QAM	M	Edge_1RB_Left	22.71	PC3	PASS
N5	15	20	CP-64QAM	M	Edge_1RB_Right	22.36	PC3	PASS
N5	15	20	CP-64QAM	M	Outer_Full	23.23	PC3	PASS
N5	15	20	CP-64QAM	M	Inner_Full	22.31	PC3	PASS
N5	15	20	CP-256QAM	M	Edge_1RB_Left	22.58	PC3	PASS
N5	15	20	CP-256QAM	M	Edge_1RB_Right	22.98	PC3	PASS
N5	15	20	CP-256QAM	M	Outer_Full	23.70	PC3	PASS
N5	15	20	CP-256QAM	M	Inner_Full	23.75	PC3	PASS
N5	15	20	DFT-PI2BPSK	H	Edge_1RB_Left	24.28	PC3	PASS
N5	15	20	DFT-PI2BPSK	H	Edge_1RB_Right	22.85	PC3	PASS
N5	15	20	DFT-PI2BPSK	H	Outer_Full	24.54	PC3	PASS
N5	15	20	DFT-PI2BPSK	H	Inner_Full	24.02	PC3	PASS
N5	15	20	DFT-QPSK	H	Edge_1RB_Left	22.74	PC3	PASS
N5	15	20	DFT-QPSK	H	Edge_1RB_Right	22.97	PC3	PASS
N5	15	20	DFT-QPSK	H	Outer_Full	24.49	PC3	PASS
N5	15	20	DFT-QPSK	H	Inner_Full	24.68	PC3	PASS
N5	15	20	DFT-16QAM	H	Edge_1RB_Left	22.94	PC3	PASS
N5	15	20	DFT-16QAM	H	Edge_1RB_Right	23.61	PC3	PASS
N5	15	20	DFT-16QAM	H	Outer_Full	24.50	PC3	PASS
N5	15	20	DFT-16QAM	H	Inner_Full	24.66	PC3	PASS
N5	15	20	DFT-64QAM	H	Edge_1RB_Left	23.97	PC3	PASS
N5	15	20	DFT-64QAM	H	Edge_1RB_Right	23.32	PC3	PASS
N5	15	20	DFT-64QAM	H	Outer_Full	24.47	PC3	PASS
N5	15	20	DFT-64QAM	H	Inner_Full	23.60	PC3	PASS
N5	15	20	DFT-256QAM	H	Edge_1RB_Left	23.29	PC3	PASS
N5	15	20	DFT-256QAM	H	Edge_1RB_Right	23.23	PC3	PASS
N5	15	20	DFT-256QAM	H	Outer_Full	24.43	PC3	PASS

N5	15	20	DFT-256QAM	H	Inner_Full	24.58	PC3	PASS
N5	15	20	CP-QPSK	H	Edge_1RB_Left	23.97	PC3	PASS
N5	15	20	CP-QPSK	H	Edge_1RB_Right	22.90	PC3	PASS
N5	15	20	CP-QPSK	H	Outer_Full	24.15	PC3	PASS
N5	15	20	CP-QPSK	H	Inner_Full	24.37	PC3	PASS
N5	15	20	CP-16QAM	H	Edge_1RB_Left	23.40	PC3	PASS
N5	15	20	CP-16QAM	H	Edge_1RB_Right	22.77	PC3	PASS
N5	15	20	CP-16QAM	H	Outer_Full	24.22	PC3	PASS
N5	15	20	CP-16QAM	H	Inner_Full	24.38	PC3	PASS
N5	15	20	CP-64QAM	H	Edge_1RB_Left	24.77	PC3	PASS
N5	15	20	CP-64QAM	H	Edge_1RB_Right	23.25	PC3	PASS
N5	15	20	CP-64QAM	H	Outer_Full	24.22	PC3	PASS
N5	15	20	CP-64QAM	H	Inner_Full	24.40	PC3	PASS
N5	15	20	CP-256QAM	H	Edge_1RB_Left	23.59	PC3	PASS
N5	15	20	CP-256QAM	H	Edge_1RB_Right	22.87	PC3	PASS
N5	15	20	CP-256QAM	H	Outer_Full	23.69	PC3	PASS
N5	15	20	CP-256QAM	H	Inner_Full	23.75	PC3	PASS

5G NR(P1):

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Power (dBm)	Power Class	Verdict
N41	30	10	DFT-PI2BPSK	L	Edge_1RB_Left	22.05	PC3	PASS
N41	30	10	DFT-PI2BPSK	L	Edge_1RB_Right	22.11	PC3	PASS
N41	30	10	DFT-PI2BPSK	L	Outer_Full	22.01	PC3	PASS
N41	30	10	DFT-PI2BPSK	L	Inner_Full	22.49	PC3	PASS
N41	30	10	DFT-QPSK	L	Edge_1RB_Left	21.36	PC3	PASS
N41	30	10	DFT-QPSK	L	Edge_1RB_Right	21.49	PC3	PASS
N41	30	10	DFT-QPSK	L	Outer_Full	21.52	PC3	PASS
N41	30	10	DFT-QPSK	L	Inner_Full	22.45	PC3	PASS
N41	30	10	DFT-16QAM	L	Edge_1RB_Left	21.40	PC3	PASS
N41	30	10	DFT-16QAM	L	Edge_1RB_Right	21.80	PC3	PASS
N41	30	10	DFT-16QAM	L	Outer_Full	21.63	PC3	PASS
N41	30	10	DFT-16QAM	L	Inner_Full	21.40	PC3	PASS
N41	30	10	DFT-64QAM	L	Edge_1RB_Left	21.00	PC3	PASS
N41	30	10	DFT-64QAM	L	Edge_1RB_Right	21.98	PC3	PASS
N41	30	10	DFT-64QAM	L	Outer_Full	21.45	PC3	PASS
N41	30	10	DFT-64QAM	L	Inner_Full	21.36	PC3	PASS
N41	30	10	DFT-256QAM	L	Edge_1RB_Left	21.38	PC3	PASS
N41	30	10	DFT-256QAM	L	Edge_1RB_Right	21.48	PC3	PASS
N41	30	10	DFT-256QAM	L	Outer_Full	21.53	PC3	PASS

N41	30	10	DFT-256QAM	L	Inner_Full	21.70	PC3	PASS
N41	30	10	CP-QPSK	L	Edge_1RB_Left	21.54	PC3	PASS
N41	30	10	CP-QPSK	L	Edge_1RB_Right	21.59	PC3	PASS
N41	30	10	CP-QPSK	L	Outer_Full	21.62	PC3	PASS
N41	30	10	CP-QPSK	L	Inner_Full	21.58	PC3	PASS
N41	30	10	CP-16QAM	L	Edge_1RB_Left	21.43	PC3	PASS
N41	30	10	CP-16QAM	L	Edge_1RB_Right	21.86	PC3	PASS
N41	30	10	CP-16QAM	L	Outer_Full	21.57	PC3	PASS
N41	30	10	CP-16QAM	L	Inner_Full	21.52	PC3	PASS
N41	30	10	CP-64QAM	L	Edge_1RB_Left	21.75	PC3	PASS
N41	30	10	CP-64QAM	L	Edge_1RB_Right	21.48	PC3	PASS
N41	30	10	CP-64QAM	L	Outer_Full	21.98	PC3	PASS
N41	30	10	CP-64QAM	L	Inner_Full	21.78	PC3	PASS
N41	30	10	CP-256QAM	L	Edge_1RB_Left	21.92	PC3	PASS
N41	30	10	CP-256QAM	L	Edge_1RB_Right	21.99	PC3	PASS
N41	30	10	CP-256QAM	L	Outer_Full	21.50	PC3	PASS
N41	30	10	CP-256QAM	L	Inner_Full	21.75	PC3	PASS
N41	30	10	DFT-PI2BPSK	M	Edge_1RB_Left	21.94	PC3	PASS
N41	30	10	DFT-PI2BPSK	M	Edge_1RB_Right	22.18	PC3	PASS
N41	30	10	DFT-PI2BPSK	M	Outer_Full	22.20	PC3	PASS
N41	30	10	DFT-PI2BPSK	M	Inner_Full	22.69	PC3	PASS
N41	30	10	DFT-QPSK	M	Edge_1RB_Left	21.51	PC3	PASS
N41	30	10	DFT-QPSK	M	Edge_1RB_Right	21.61	PC3	PASS
N41	30	10	DFT-QPSK	M	Outer_Full	21.73	PC3	PASS
N41	30	10	DFT-QPSK	M	Inner_Full	22.69	PC3	PASS
N41	30	10	DFT-16QAM	M	Edge_1RB_Left	21.92	PC3	PASS
N41	30	10	DFT-16QAM	M	Edge_1RB_Right	21.81	PC3	PASS
N41	30	10	DFT-16QAM	M	Outer_Full	21.00	PC3	PASS
N41	30	10	DFT-16QAM	M	Inner_Full	22.65	PC3	PASS
N41	30	10	DFT-64QAM	M	Edge_1RB_Left	21.51	PC3	PASS
N41	30	10	DFT-64QAM	M	Edge_1RB_Right	21.48	PC3	PASS
N41	30	10	DFT-64QAM	M	Outer_Full	21.21	PC3	PASS
N41	30	10	DFT-64QAM	M	Inner_Full	21.31	PC3	PASS
N41	30	10	DFT-256QAM	M	Edge_1RB_Left	21.57	PC3	PASS
N41	30	10	DFT-256QAM	M	Edge_1RB_Right	21.55	PC3	PASS
N41	30	10	DFT-256QAM	M	Outer_Full	21.67	PC3	PASS
N41	30	10	DFT-256QAM	M	Inner_Full	21.51	PC3	PASS
N41	30	10	CP-QPSK	M	Edge_1RB_Left	21.72	PC3	PASS
N41	30	10	CP-QPSK	M	Edge_1RB_Right	21.54	PC3	PASS
N41	30	10	CP-QPSK	M	Outer_Full	22.74	PC3	PASS
N41	30	10	CP-QPSK	M	Inner_Full	22.14	PC3	PASS
N41	30	10	CP-16QAM	M	Edge_1RB_Left	21.84	PC3	PASS

N41	30	10	CP-16QAM	M	Edge_1RB_Right	21.90	PC3	PASS
N41	30	10	CP-16QAM	M	Outer_Full	21.60	PC3	PASS
N41	30	10	CP-16QAM	M	Inner_Full	21.74	PC3	PASS
N41	30	10	CP-64QAM	M	Edge_1RB_Left	21.99	PC3	PASS
N41	30	10	CP-64QAM	M	Edge_1RB_Right	21.51	PC3	PASS
N41	30	10	CP-64QAM	M	Outer_Full	21.74	PC3	PASS
N41	30	10	CP-64QAM	M	Inner_Full	21.52	PC3	PASS
N41	30	10	CP-256QAM	M	Edge_1RB_Left	21.67	PC3	PASS
N41	30	10	CP-256QAM	M	Edge_1RB_Right	21.70	PC3	PASS
N41	30	10	CP-256QAM	M	Outer_Full	21.61	PC3	PASS
N41	30	10	CP-256QAM	M	Inner_Full	21.50	PC3	PASS
N41	30	10	DFT-PI2BPSK	H	Edge_1RB_Left	21.74	PC3	PASS
N41	30	10	DFT-PI2BPSK	H	Edge_1RB_Right	21.33	PC3	PASS
N41	30	10	DFT-PI2BPSK	H	Outer_Full	21.59	PC3	PASS
N41	30	10	DFT-PI2BPSK	H	Inner_Full	22.01	PC3	PASS
N41	30	10	DFT-QPSK	H	Edge_1RB_Left	21.17	PC3	PASS
N41	30	10	DFT-QPSK	H	Edge_1RB_Right	21.87	PC3	PASS
N41	30	10	DFT-QPSK	H	Outer_Full	21.06	PC3	PASS
N41	30	10	DFT-QPSK	H	Inner_Full	22.00	PC3	PASS
N41	30	10	DFT-16QAM	H	Edge_1RB_Left	21.12	PC3	PASS
N41	30	10	DFT-16QAM	H	Edge_1RB_Right	21.71	PC3	PASS
N41	30	10	DFT-16QAM	H	Outer_Full	21.07	PC3	PASS
N41	30	10	DFT-16QAM	H	Inner_Full	21.96	PC3	PASS
N41	30	10	DFT-64QAM	H	Edge_1RB_Left	21.80	PC3	PASS
N41	30	10	DFT-64QAM	H	Outer_Full	21.55	PC3	PASS
N41	30	10	DFT-64QAM	H	Inner_Full	21.63	PC3	PASS
N41	30	10	DFT-256QAM	H	Edge_1RB_Left	21.60	PC3	PASS
N41	30	10	DFT-256QAM	H	Edge_1RB_Right	21.65	PC3	PASS
N41	30	10	DFT-256QAM	H	Outer_Full	21.61	PC3	PASS
N41	30	10	DFT-256QAM	H	Inner_Full	21.41	PC3	PASS
N41	30	10	CP-QPSK	H	Edge_1RB_Left	21.56	PC3	PASS
N41	30	10	CP-QPSK	H	Edge_1RB_Right	21.90	PC3	PASS
N41	30	10	CP-QPSK	H	Outer_Full	21.99	PC3	PASS
N41	30	10	CP-QPSK	H	Inner_Full	21.46	PC3	PASS
N41	30	10	CP-16QAM	H	Edge_1RB_Left	21.69	PC3	PASS
N41	30	10	CP-16QAM	H	Edge_1RB_Right	21.98	PC3	PASS
N41	30	10	CP-16QAM	H	Outer_Full	21.96	PC3	PASS
N41	30	10	CP-16QAM	H	Inner_Full	21.99	PC3	PASS
N41	30	10	CP-64QAM	H	Edge_1RB_Left	21.73	PC3	PASS
N41	30	10	CP-64QAM	H	Edge_1RB_Right	21.65	PC3	PASS
N41	30	10	CP-64QAM	H	Outer_Full	21.45	PC3	PASS
N41	30	10	CP-64QAM	H	Inner_Full	21.62	PC3	PASS

N41	30	10	CP-256QAM	H	Edge_1RB_Left	21.81	PC3	PASS
N41	30	10	CP-256QAM	H	Edge_1RB_Right	21.58	PC3	PASS
N41	30	10	CP-256QAM	H	Outer_Full	21.46	PC3	PASS
N41	30	10	CP-256QAM	H	Inner_Full	21.56	PC3	PASS
N41	30	15	DFT-PI2BPSK	L	Edge_1RB_Left	21.92	PC3	PASS
N41	30	15	DFT-PI2BPSK	L	Edge_1RB_Right	21.96	PC3	PASS
N41	30	15	DFT-PI2BPSK	L	Outer_Full	22.07	PC3	PASS
N41	30	15	DFT-PI2BPSK	L	Inner_Full	22.55	PC3	PASS
N41	30	15	DFT-QPSK	L	Edge_1RB_Left	21.34	PC3	PASS
N41	30	15	DFT-QPSK	L	Edge_1RB_Right	21.40	PC3	PASS
N41	30	15	DFT-QPSK	L	Outer_Full	21.55	PC3	PASS
N41	30	15	DFT-QPSK	L	Inner_Full	22.55	PC3	PASS
N41	30	15	DFT-16QAM	L	Edge_1RB_Left	21.72	PC3	PASS
N41	30	15	DFT-16QAM	L	Edge_1RB_Right	21.43	PC3	PASS
N41	30	15	DFT-16QAM	L	Outer_Full	21.63	PC3	PASS
N41	30	15	DFT-16QAM	L	Inner_Full	21.54	PC3	PASS
N41	30	15	DFT-64QAM	L	Edge_1RB_Left	21.54	PC3	PASS
N41	30	15	DFT-64QAM	L	Edge_1RB_Right	21.81	PC3	PASS
N41	30	15	DFT-64QAM	L	Outer_Full	21.53	PC3	PASS
N41	30	15	DFT-64QAM	L	Inner_Full	21.60	PC3	PASS
N41	30	15	DFT-256QAM	L	Edge_1RB_Left	21.57	PC3	PASS
N41	30	15	DFT-256QAM	L	Edge_1RB_Right	21.51	PC3	PASS
N41	30	15	DFT-256QAM	L	Outer_Full	21.71	PC3	PASS
N41	30	15	DFT-256QAM	L	Inner_Full	21.64	PC3	PASS
N41	30	15	CP-QPSK	L	Edge_1RB_Left	21.88	PC3	PASS
N41	30	15	CP-QPSK	L	Edge_1RB_Right	21.64	PC3	PASS
N41	30	15	CP-QPSK	L	Outer_Full	21.48	PC3	PASS
N41	30	15	CP-QPSK	L	Inner_Full	21.99	PC3	PASS
N41	30	15	CP-16QAM	L	Edge_1RB_Left	21.73	PC3	PASS
N41	30	15	CP-16QAM	L	Edge_1RB_Right	21.56	PC3	PASS
N41	30	15	CP-16QAM	L	Outer_Full	21.55	PC3	PASS
N41	30	15	CP-16QAM	L	Inner_Full	21.58	PC3	PASS
N41	30	15	CP-64QAM	L	Edge_1RB_Left	21.71	PC3	PASS
N41	30	15	CP-64QAM	L	Edge_1RB_Right	21.86	PC3	PASS
N41	30	15	CP-64QAM	L	Outer_Full	21.95	PC3	PASS
N41	30	15	CP-64QAM	L	Inner_Full	21.88	PC3	PASS
N41	30	15	CP-256QAM	L	Edge_1RB_Left	21.69	PC3	PASS
N41	30	15	CP-256QAM	L	Edge_1RB_Right	21.56	PC3	PASS
N41	30	15	CP-256QAM	L	Outer_Full	21.95	PC3	PASS
N41	30	15	CP-256QAM	L	Inner_Full	21.75	PC3	PASS
N41	30	15	DFT-PI2BPSK	M	Edge_1RB_Left	21.36	PC3	PASS
N41	30	15	DFT-PI2BPSK	M	Edge_1RB_Right	22.23	PC3	PASS

N41	30	15	DFT-PI2BPSK	M	Outer_Full	23.18	PC3	PASS
N41	30	15	DFT-PI2BPSK	M	Inner_Full	23.69	PC3	PASS
N41	30	15	DFT-QPSK	M	Edge_1RB_Left	21.44	PC3	PASS
N41	30	15	DFT-QPSK	M	Edge_1RB_Right	21.64	PC3	PASS
N41	30	15	DFT-QPSK	M	Outer_Full	22.70	PC3	PASS
N41	30	15	DFT-QPSK	M	Inner_Full	23.61	PC3	PASS
N41	30	15	DFT-16QAM	M	Edge_1RB_Left	21.44	PC3	PASS
N41	30	15	DFT-16QAM	M	Edge_1RB_Right	22.11	PC3	PASS
N41	30	15	DFT-16QAM	M	Outer_Full	21.61	PC3	PASS
N41	30	15	DFT-16QAM	M	Inner_Full	22.70	PC3	PASS
N41	30	15	DFT-64QAM	M	Edge_1RB_Left	22.07	PC3	PASS
N41	30	15	DFT-64QAM	M	Edge_1RB_Right	21.40	PC3	PASS
N41	30	15	DFT-64QAM	M	Outer_Full	21.21	PC3	PASS
N41	30	15	DFT-64QAM	M	Inner_Full	21.15	PC3	PASS
N41	30	15	DFT-256QAM	M	Edge_1RB_Left	21.49	PC3	PASS
N41	30	15	DFT-256QAM	M	Edge_1RB_Right	21.58	PC3	PASS
N41	30	15	DFT-256QAM	M	Outer_Full	21.78	PC3	PASS
N41	30	15	DFT-256QAM	M	Inner_Full	21.62	PC3	PASS
N41	30	15	CP-QPSK	M	Edge_1RB_Left	21.62	PC3	PASS
N41	30	15	CP-QPSK	M	Edge_1RB_Right	21.70	PC3	PASS
N41	30	15	CP-QPSK	M	Outer_Full	21.71	PC3	PASS
N41	30	15	CP-QPSK	M	Inner_Full	21.54	PC3	PASS
N41	30	15	CP-16QAM	M	Edge_1RB_Left	21.97	PC3	PASS
N41	30	15	CP-16QAM	M	Edge_1RB_Right	21.21	PC3	PASS
N41	30	15	CP-16QAM	M	Outer_Full	21.68	PC3	PASS
N41	30	15	CP-16QAM	M	Inner_Full	21.56	PC3	PASS
N41	30	15	CP-64QAM	M	Edge_1RB_Left	21.79	PC3	PASS
N41	30	15	CP-64QAM	M	Edge_1RB_Right	21.42	PC3	PASS
N41	30	15	CP-64QAM	M	Outer_Full	21.56	PC3	PASS
N41	30	15	CP-64QAM	M	Inner_Full	21.75	PC3	PASS
N41	30	15	CP-256QAM	M	Edge_1RB_Left	21.97	PC3	PASS
N41	30	15	CP-256QAM	M	Edge_1RB_Right	21.58	PC3	PASS
N41	30	15	CP-256QAM	M	Outer_Full	21.71	PC3	PASS
N41	30	15	CP-256QAM	M	Inner_Full	21.66	PC3	PASS
N41	30	15	DFT-PI2BPSK	H	Edge_1RB_Right	22.28	PC3	PASS
N41	30	15	DFT-PI2BPSK	H	Outer_Full	22.69	PC3	PASS
N41	30	15	DFT-PI2BPSK	H	Inner_Full	22.10	PC3	PASS
N41	30	15	DFT-QPSK	H	Edge_1RB_Left	22.50	PC3	PASS
N41	30	15	DFT-QPSK	H	Edge_1RB_Right	21.83	PC3	PASS
N41	30	15	DFT-QPSK	H	Outer_Full	22.14	PC3	PASS
N41	30	15	DFT-QPSK	H	Inner_Full	22.12	PC3	PASS
N41	30	15	DFT-16QAM	H	Edge_1RB_Left	21.48	PC3	PASS

N41	30	15	DFT-16QAM	H	Edge_1RB_Right	21.67	PC3	PASS
N41	30	15	DFT-16QAM	H	Outer_Full	21.12	PC3	PASS
N41	30	15	DFT-16QAM	H	Inner_Full	21.22	PC3	PASS
N41	30	15	DFT-64QAM	H	Edge_1RB_Left	21.30	PC3	PASS
N41	30	15	DFT-64QAM	H	Edge_1RB_Right	21.65	PC3	PASS
N41	30	15	DFT-64QAM	H	Outer_Full	21.60	PC3	PASS
N41	30	15	DFT-64QAM	H	Inner_Full	21.67	PC3	PASS
N41	30	15	DFT-256QAM	H	Edge_1RB_Left	21.46	PC3	PASS
N41	30	15	DFT-256QAM	H	Edge_1RB_Right	21.48	PC3	PASS
N41	30	15	DFT-256QAM	H	Outer_Full	21.71	PC3	PASS
N41	30	15	DFT-256QAM	H	Inner_Full	21.78	PC3	PASS
N41	30	15	CP-QPSK	H	Edge_1RB_Left	21.60	PC3	PASS
N41	30	15	CP-QPSK	H	Edge_1RB_Right	21.89	PC3	PASS
N41	30	15	CP-QPSK	H	Outer_Full	21.76	PC3	PASS
N41	30	15	CP-QPSK	H	Inner_Full	21.62	PC3	PASS
N41	30	15	CP-16QAM	H	Edge_1RB_Left	21.92	PC3	PASS
N41	30	15	CP-16QAM	H	Edge_1RB_Right	21.58	PC3	PASS
N41	30	15	CP-16QAM	H	Outer_Full	21.56	PC3	PASS
N41	30	15	CP-16QAM	H	Inner_Full	21.07	PC3	PASS
N41	30	15	CP-64QAM	H	Edge_1RB_Left	21.87	PC3	PASS
N41	30	15	CP-64QAM	H	Edge_1RB_Right	21.57	PC3	PASS
N41	30	15	CP-64QAM	H	Outer_Full	21.64	PC3	PASS
N41	30	15	CP-64QAM	H	Inner_Full	21.69	PC3	PASS
N41	30	15	CP-256QAM	H	Edge_1RB_Left	21.85	PC3	PASS
N41	30	15	CP-256QAM	H	Edge_1RB_Right	21.63	PC3	PASS
N41	30	15	CP-256QAM	H	Outer_Full	21.62	PC3	PASS
N41	30	15	CP-256QAM	H	Inner_Full	21.53	PC3	PASS
N41	30	20	DFT-PI2BPSK	L	Edge_1RB_Left	21.59	PC3	PASS
N41	30	20	DFT-PI2BPSK	L	Edge_1RB_Right	21.83	PC3	PASS
N41	30	20	DFT-PI2BPSK	L	Outer_Full	22.10	PC3	PASS
N41	30	20	DFT-PI2BPSK	L	Inner_Full	22.56	PC3	PASS
N41	30	20	DFT-QPSK	L	Edge_1RB_Left	21.32	PC3	PASS
N41	30	20	DFT-QPSK	L	Edge_1RB_Right	21.30	PC3	PASS
N41	30	20	DFT-QPSK	L	Outer_Full	21.59	PC3	PASS
N41	30	20	DFT-QPSK	L	Inner_Full	22.57	PC3	PASS
N41	30	20	DFT-16QAM	L	Edge_1RB_Left	21.62	PC3	PASS
N41	30	20	DFT-16QAM	L	Edge_1RB_Right	21.70	PC3	PASS
N41	30	20	DFT-16QAM	L	Outer_Full	21.50	PC3	PASS
N41	30	20	DFT-16QAM	L	Inner_Full	21.66	PC3	PASS
N41	30	20	DFT-64QAM	L	Edge_1RB_Left	21.72	PC3	PASS
N41	30	20	DFT-64QAM	L	Edge_1RB_Right	21.51	PC3	PASS
N41	30	20	DFT-64QAM	L	Outer_Full	21.95	PC3	PASS

N41	30	20	DFT-64QAM	L	Inner_Full	21.50	PC3	PASS
N41	30	20	DFT-256QAM	L	Edge_1RB_Left	21.42	PC3	PASS
N41	30	20	DFT-256QAM	L	Edge_1RB_Right	21.31	PC3	PASS
N41	30	20	DFT-256QAM	L	Outer_Full	21.71	PC3	PASS
N41	30	20	DFT-256QAM	L	Inner_Full	21.57	PC3	PASS
N41	30	20	CP-QPSK	L	Edge_1RB_Left	21.51	PC3	PASS
N41	30	20	CP-QPSK	L	Edge_1RB_Right	21.45	PC3	PASS
N41	30	20	CP-QPSK	L	Outer_Full	21.51	PC3	PASS
N41	30	20	CP-QPSK	L	Inner_Full	21.03	PC3	PASS
N41	30	20	CP-16QAM	L	Edge_1RB_Left	21.76	PC3	PASS
N41	30	20	CP-16QAM	L	Edge_1RB_Right	21.90	PC3	PASS
N41	30	20	CP-16QAM	L	Outer_Full	21.33	PC3	PASS
N41	30	20	CP-16QAM	L	Inner_Full	21.49	PC3	PASS
N41	30	20	CP-64QAM	L	Edge_1RB_Left	21.63	PC3	PASS
N41	30	20	CP-64QAM	L	Edge_1RB_Right	21.75	PC3	PASS
N41	30	20	CP-64QAM	L	Outer_Full	21.53	PC3	PASS
N41	30	20	CP-64QAM	L	Inner_Full	21.47	PC3	PASS
N41	30	20	CP-256QAM	L	Edge_1RB_Left	21.31	PC3	PASS
N41	30	20	CP-256QAM	L	Edge_1RB_Right	21.82	PC3	PASS
N41	30	20	CP-256QAM	L	Outer_Full	21.61	PC3	PASS
N41	30	20	CP-256QAM	L	Inner_Full	21.01	PC3	PASS
N41	30	20	DFT-PI2BPSK	M	Edge_1RB_Left	22.02	PC3	PASS
N41	30	20	DFT-PI2BPSK	M	Edge_1RB_Right	22.34	PC3	PASS
N41	30	20	DFT-PI2BPSK	M	Outer_Full	21.21	PC3	PASS
N41	30	20	DFT-PI2BPSK	M	Inner_Full	22.66	PC3	PASS
N41	30	20	DFT-QPSK	M	Edge_1RB_Left	22.35	PC3	PASS
N41	30	20	DFT-QPSK	M	Edge_1RB_Right	22.77	PC3	PASS
N41	30	20	DFT-QPSK	M	Outer_Full	22.66	PC3	PASS
N41	30	20	DFT-QPSK	M	Inner_Full	22.66	PC3	PASS
N41	30	20	DFT-16QAM	M	Edge_1RB_Left	21.69	PC3	PASS
N41	30	20	DFT-16QAM	M	Edge_1RB_Right	21.71	PC3	PASS
N41	30	20	DFT-16QAM	M	Outer_Full	21.67	PC3	PASS
N41	30	20	DFT-16QAM	M	Inner_Full	22.61	PC3	PASS
N41	30	20	DFT-64QAM	M	Edge_1RB_Left	21.41	PC3	PASS
N41	30	20	DFT-64QAM	M	Edge_1RB_Right	21.12	PC3	PASS
N41	30	20	DFT-64QAM	M	Outer_Full	21.19	PC3	PASS
N41	30	20	DFT-64QAM	M	Inner_Full	21.16	PC3	PASS
N41	30	20	DFT-256QAM	M	Edge_1RB_Left	21.38	PC3	PASS
N41	30	20	DFT-256QAM	M	Edge_1RB_Right	21.45	PC3	PASS
N41	30	20	DFT-256QAM	M	Outer_Full	21.06	PC3	PASS
N41	30	20	DFT-256QAM	M	Inner_Full	21.83	PC3	PASS
N41	30	20	CP-QPSK	M	Edge_1RB_Left	21.43	PC3	PASS

N41	30	20	CP-QPSK	M	Edge_1RB_Right	21.62	PC3	PASS
N41	30	20	CP-QPSK	M	Outer_Full	21.59	PC3	PASS
N41	30	20	CP-QPSK	M	Inner_Full	22.15	PC3	PASS
N41	30	20	CP-16QAM	M	Edge_1RB_Left	21.85	PC3	PASS
N41	30	20	CP-16QAM	M	Edge_1RB_Right	21.15	PC3	PASS
N41	30	20	CP-16QAM	M	Outer_Full	21.61	PC3	PASS
N41	30	20	CP-16QAM	M	Inner_Full	21.62	PC3	PASS
N41	30	20	CP-64QAM	M	Edge_1RB_Left	21.81	PC3	PASS
N41	30	20	CP-64QAM	M	Edge_1RB_Right	21.53	PC3	PASS
N41	30	20	CP-64QAM	M	Outer_Full	21.66	PC3	PASS
N41	30	20	CP-64QAM	M	Inner_Full	21.76	PC3	PASS
N41	30	20	CP-256QAM	M	Edge_1RB_Left	21.96	PC3	PASS
N41	30	20	CP-256QAM	M	Edge_1RB_Right	21.63	PC3	PASS
N41	30	20	CP-256QAM	M	Outer_Full	21.58	PC3	PASS
N41	30	20	CP-256QAM	M	Inner_Full	21.97	PC3	PASS
N41	30	20	DFT-PI2BPSK	H	Edge_1RB_Left	22.07	PC3	PASS
N41	30	20	DFT-PI2BPSK	H	Edge_1RB_Right	22.24	PC3	PASS
N41	30	20	DFT-PI2BPSK	H	Outer_Full	22.76	PC3	PASS
N41	30	20	DFT-PI2BPSK	H	Inner_Full	21.98	PC3	PASS
N41	30	20	DFT-QPSK	H	Edge_1RB_Left	22.31	PC3	PASS
N41	30	20	DFT-QPSK	H	Edge_1RB_Right	21.56	PC3	PASS
N41	30	20	DFT-QPSK	H	Outer_Full	21.98	PC3	PASS
N41	30	20	DFT-QPSK	H	Inner_Full	22.03	PC3	PASS
N41	30	20	DFT-16QAM	H	Edge_1RB_Left	21.40	PC3	PASS
N41	30	20	DFT-16QAM	H	Edge_1RB_Right	21.54	PC3	PASS
N41	30	20	DFT-16QAM	H	Outer_Full	21.01	PC3	PASS
N41	30	20	DFT-16QAM	H	Inner_Full	22.04	PC3	PASS
N41	30	20	DFT-64QAM	H	Edge_1RB_Left	21.95	PC3	PASS
N41	30	20	DFT-64QAM	H	Edge_1RB_Right	21.50	PC3	PASS
N41	30	20	DFT-64QAM	H	Outer_Full	21.45	PC3	PASS
N41	30	20	DFT-64QAM	H	Inner_Full	21.48	PC3	PASS
N41	30	20	DFT-256QAM	H	Edge_1RB_Left	21.79	PC3	PASS
N41	30	20	DFT-256QAM	H	Edge_1RB_Right	21.81	PC3	PASS
N41	30	20	DFT-256QAM	H	Outer_Full	21.42	PC3	PASS
N41	30	20	DFT-256QAM	H	Inner_Full	21.51	PC3	PASS
N41	30	20	CP-QPSK	H	Edge_1RB_Left	21.74	PC3	PASS
N41	30	20	CP-QPSK	H	Edge_1RB_Right	21.85	PC3	PASS
N41	30	20	CP-QPSK	H	Outer_Full	21.92	PC3	PASS
N41	30	20	CP-QPSK	H	Inner_Full	21.44	PC3	PASS
N41	30	20	CP-16QAM	H	Edge_1RB_Left	21.95	PC3	PASS
N41	30	20	CP-16QAM	H	Edge_1RB_Right	21.92	PC3	PASS
N41	30	20	CP-16QAM	H	Outer_Full	21.98	PC3	PASS

N41	30	20	CP-16QAM	H	Inner_Full	21.04	PC3	PASS
N41	30	20	CP-64QAM	H	Edge_1RB_Left	21.81	PC3	PASS
N41	30	20	CP-64QAM	H	Edge_1RB_Right	21.59	PC3	PASS
N41	30	20	CP-64QAM	H	Outer_Full	21.42	PC3	PASS
N41	30	20	CP-64QAM	H	Inner_Full	21.59	PC3	PASS
N41	30	20	CP-256QAM	H	Edge_1RB_Left	21.78	PC3	PASS
N41	30	20	CP-256QAM	H	Edge_1RB_Right	21.83	PC3	PASS
N41	30	20	CP-256QAM	H	Outer_Full	21.45	PC3	PASS
N41	30	20	CP-256QAM	H	Inner_Full	21.92	PC3	PASS
N41	30	30	DFT-PI2BPSK	L	Edge_1RB_Left	21.64	PC3	PASS
N41	30	30	DFT-PI2BPSK	L	Edge_1RB_Right	21.36	PC3	PASS
N41	30	30	DFT-PI2BPSK	L	Outer_Full	21.89	PC3	PASS
N41	30	30	DFT-PI2BPSK	L	Inner_Full	22.44	PC3	PASS
N41	30	30	DFT-QPSK	L	Edge_1RB_Left	21.16	PC3	PASS
N41	30	30	DFT-QPSK	L	Edge_1RB_Right	21.82	PC3	PASS
N41	30	30	DFT-QPSK	L	Outer_Full	21.33	PC3	PASS
N41	30	30	DFT-QPSK	L	Inner_Full	22.47	PC3	PASS
N41	30	30	DFT-16QAM	L	Edge_1RB_Left	21.58	PC3	PASS
N41	30	30	DFT-16QAM	L	Edge_1RB_Right	21.79	PC3	PASS
N41	30	30	DFT-16QAM	L	Outer_Full	21.77	PC3	PASS
N41	30	30	DFT-16QAM	L	Inner_Full	21.49	PC3	PASS
N41	30	30	DFT-64QAM	L	Edge_1RB_Left	21.66	PC3	PASS
N41	30	30	DFT-64QAM	L	Edge_1RB_Right	21.44	PC3	PASS
N41	30	30	DFT-64QAM	L	Outer_Full	21.86	PC3	PASS
N41	30	30	DFT-64QAM	L	Inner_Full	21.54	PC3	PASS
N41	30	30	DFT-256QAM	L	Edge_1RB_Left	21.85	PC3	PASS
N41	30	30	DFT-256QAM	L	Edge_1RB_Right	21.48	PC3	PASS
N41	30	30	DFT-256QAM	L	Outer_Full	21.43	PC3	PASS
N41	30	30	DFT-256QAM	L	Inner_Full	21.46	PC3	PASS
N41	30	30	CP-QPSK	L	Edge_1RB_Left	21.24	PC3	PASS
N41	30	30	CP-QPSK	L	Edge_1RB_Right	21.83	PC3	PASS
N41	30	30	CP-QPSK	L	Outer_Full	21.52	PC3	PASS
N41	30	30	CP-QPSK	L	Inner_Full	21.07	PC3	PASS
N41	30	30	CP-16QAM	L	Edge_1RB_Left	21.35	PC3	PASS
N41	30	30	CP-16QAM	L	Edge_1RB_Right	21.85	PC3	PASS
N41	30	30	CP-16QAM	L	Outer_Full	21.60	PC3	PASS
N41	30	30	CP-16QAM	L	Inner_Full	21.65	PC3	PASS
N41	30	30	CP-64QAM	L	Edge_1RB_Left	21.65	PC3	PASS
N41	30	30	CP-64QAM	L	Edge_1RB_Right	21.85	PC3	PASS
N41	30	30	CP-64QAM	L	Outer_Full	21.97	PC3	PASS
N41	30	30	CP-64QAM	L	Inner_Full	21.83	PC3	PASS
N41	30	30	CP-256QAM	L	Edge_1RB_Left	21.64	PC3	PASS

N41	30	30	CP-256QAM	L	Edge_1RB_Right	21.82	PC3	PASS
N41	30	30	CP-256QAM	L	Outer_Full	21.98	PC3	PASS
N41	30	30	CP-256QAM	L	Inner_Full	21.61	PC3	PASS
N41	30	30	DFT-PI2BPSK	M	Edge_1RB_Left	22.75	PC3	PASS
N41	30	30	DFT-PI2BPSK	M	Edge_1RB_Right	22.34	PC3	PASS
N41	30	30	DFT-PI2BPSK	M	Outer_Full	22.20	PC3	PASS
N41	30	30	DFT-PI2BPSK	M	Inner_Full	22.64	PC3	PASS
N41	30	30	DFT-QPSK	M	Edge_1RB_Left	22.74	PC3	PASS
N41	30	30	DFT-QPSK	M	Edge_1RB_Right	22.80	PC3	PASS
N41	30	30	DFT-QPSK	M	Outer_Full	22.69	PC3	PASS
N41	30	30	DFT-QPSK	M	Inner_Full	22.69	PC3	PASS
N41	30	30	DFT-16QAM	M	Edge_1RB_Left	21.41	PC3	PASS
N41	30	30	DFT-16QAM	M	Edge_1RB_Right	22.00	PC3	PASS
N41	30	30	DFT-16QAM	M	Outer_Full	21.64	PC3	PASS
N41	30	30	DFT-16QAM	M	Inner_Full	22.67	PC3	PASS
N41	30	30	DFT-64QAM	M	Edge_1RB_Left	21.88	PC3	PASS
N41	30	30	DFT-64QAM	M	Edge_1RB_Right	21.56	PC3	PASS
N41	30	30	DFT-64QAM	M	Outer_Full	21.21	PC3	PASS
N41	30	30	DFT-64QAM	M	Inner_Full	21.22	PC3	PASS
N41	30	30	DFT-256QAM	M	Edge_1RB_Left	21.68	PC3	PASS
N41	30	30	DFT-256QAM	M	Edge_1RB_Right	21.55	PC3	PASS
N41	30	30	DFT-256QAM	M	Outer_Full	21.83	PC3	PASS
N41	30	30	DFT-256QAM	M	Inner_Full	21.75	PC3	PASS
N41	30	30	CP-QPSK	M	Edge_1RB_Left	21.69	PC3	PASS
N41	30	30	CP-QPSK	M	Edge_1RB_Right	21.78	PC3	PASS
N41	30	30	CP-QPSK	M	Outer_Full	21.63	PC3	PASS
N41	30	30	CP-QPSK	M	Inner_Full	22.13	PC3	PASS
N41	30	30	CP-16QAM	M	Edge_1RB_Left	21.27	PC3	PASS
N41	30	30	CP-16QAM	M	Edge_1RB_Right	21.77	PC3	PASS
N41	30	30	CP-16QAM	M	Outer_Full	21.66	PC3	PASS
N41	30	30	CP-16QAM	M	Inner_Full	21.67	PC3	PASS
N41	30	30	CP-64QAM	M	Edge_1RB_Left	21.52	PC3	PASS
N41	30	30	CP-64QAM	M	Edge_1RB_Right	21.56	PC3	PASS
N41	30	30	CP-64QAM	M	Outer_Full	21.59	PC3	PASS
N41	30	30	CP-64QAM	M	Inner_Full	21.96	PC3	PASS
N41	30	30	CP-256QAM	M	Edge_1RB_Left	21.98	PC3	PASS
N41	30	30	CP-256QAM	M	Edge_1RB_Right	21.57	PC3	PASS
N41	30	30	CP-256QAM	M	Outer_Full	21.92	PC3	PASS
N41	30	30	CP-256QAM	M	Inner_Full	21.72	PC3	PASS
N41	30	30	DFT-PI2BPSK	H	Edge_1RB_Left	22.83	PC3	PASS
N41	30	30	DFT-PI2BPSK	H	Edge_1RB_Right	22.09	PC3	PASS
N41	30	30	DFT-PI2BPSK	H	Outer_Full	22.93	PC3	PASS

N41	30	30	DFT-PI2BPSK	H	Inner_Full	22.62	PC3	PASS
N41	30	30	DFT-QPSK	H	Edge_1RB_Left	22.37	PC3	PASS
N41	30	30	DFT-QPSK	H	Edge_1RB_Right	21.64	PC3	PASS
N41	30	30	DFT-QPSK	H	Outer_Full	22.44	PC3	PASS
N41	30	30	DFT-QPSK	H	Inner_Full	22.60	PC3	PASS
N41	30	30	DFT-16QAM	H	Edge_1RB_Left	21.48	PC3	PASS
N41	30	30	DFT-16QAM	H	Edge_1RB_Right	21.81	PC3	PASS
N41	30	30	DFT-16QAM	H	Outer_Full	21.44	PC3	PASS
N41	30	30	DFT-16QAM	H	Inner_Full	22.61	PC3	PASS
N41	30	30	DFT-64QAM	H	Edge_1RB_Left	21.09	PC3	PASS
N41	30	30	DFT-64QAM	H	Edge_1RB_Right	21.92	PC3	PASS
N41	30	30	DFT-64QAM	H	Outer_Full	21.67	PC3	PASS
N41	30	30	DFT-64QAM	H	Inner_Full	21.15	PC3	PASS
N41	30	30	DFT-256QAM	H	Edge_1RB_Left	21.83	PC3	PASS
N41	30	30	DFT-256QAM	H	Edge_1RB_Right	21.76	PC3	PASS
N41	30	30	DFT-256QAM	H	Outer_Full	21.90	PC3	PASS
N41	30	30	DFT-256QAM	H	Inner_Full	21.54	PC3	PASS
N41	30	30	CP-QPSK	H	Edge_1RB_Left	21.98	PC3	PASS
N41	30	30	CP-QPSK	H	Edge_1RB_Right	21.69	PC3	PASS
N41	30	30	CP-QPSK	H	Outer_Full	21.48	PC3	PASS
N41	30	30	CP-QPSK	H	Inner_Full	22.01	PC3	PASS
N41	30	30	CP-16QAM	H	Edge_1RB_Left	21.40	PC3	PASS
N41	30	30	CP-16QAM	H	Edge_1RB_Right	21.98	PC3	PASS
N41	30	30	CP-64QAM	H	Edge_1RB_Left	21.50	PC3	PASS
N41	30	30	CP-64QAM	H	Edge_1RB_Right	21.95	PC3	PASS
N41	30	30	CP-64QAM	H	Outer_Full	21.89	PC3	PASS
N41	30	30	CP-64QAM	H	Inner_Full	21.58	PC3	PASS
N41	30	30	CP-256QAM	H	Edge_1RB_Left	21.67	PC3	PASS
N41	30	30	CP-256QAM	H	Edge_1RB_Right	21.79	PC3	PASS
N41	30	30	CP-256QAM	H	Outer_Full	21.97	PC3	PASS
N41	30	30	CP-256QAM	H	Inner_Full	21.52	PC3	PASS
N41	30	40	DFT-PI2BPSK	L	Edge_1RB_Left	21.70	PC3	PASS
N41	30	40	DFT-PI2BPSK	L	Edge_1RB_Right	21.29	PC3	PASS
N41	30	40	DFT-PI2BPSK	L	Outer_Full	21.85	PC3	PASS
N41	30	40	DFT-PI2BPSK	L	Inner_Full	22.66	PC3	PASS
N41	30	40	DFT-QPSK	L	Edge_1RB_Left	21.23	PC3	PASS
N41	30	40	DFT-QPSK	L	Edge_1RB_Right	21.67	PC3	PASS
N41	30	40	DFT-QPSK	L	Outer_Full	21.41	PC3	PASS
N41	30	40	DFT-QPSK	L	Inner_Full	22.60	PC3	PASS
N41	30	40	DFT-16QAM	L	Edge_1RB_Left	21.29	PC3	PASS
N41	30	40	DFT-16QAM	L	Edge_1RB_Right	21.95	PC3	PASS
N41	30	40	DFT-16QAM	L	Outer_Full	21.54	PC3	PASS

N41	30	40	DFT-16QAM	L	Inner_Full	21.56	PC3	PASS
N41	30	40	DFT-64QAM	L	Edge_1RB_Left	21.80	PC3	PASS
N41	30	40	DFT-64QAM	L	Edge_1RB_Right	21.43	PC3	PASS
N41	30	40	DFT-64QAM	L	Outer_Full	21.81	PC3	PASS
N41	30	40	DFT-64QAM	L	Inner_Full	21.68	PC3	PASS
N41	30	40	DFT-256QAM	L	Edge_1RB_Left	21.72	PC3	PASS
N41	30	40	DFT-256QAM	L	Edge_1RB_Right	21.53	PC3	PASS
N41	30	40	DFT-256QAM	L	Outer_Full	21.99	PC3	PASS
N41	30	40	DFT-256QAM	L	Inner_Full	21.74	PC3	PASS
N41	30	40	CP-QPSK	L	Edge_1RB_Left	21.97	PC3	PASS
N41	30	40	CP-QPSK	L	Edge_1RB_Right	21.56	PC3	PASS
N41	30	40	CP-QPSK	L	Outer_Full	21.52	PC3	PASS
N41	30	40	CP-QPSK	L	Inner_Full	21.00	PC3	PASS
N41	30	40	CP-16QAM	L	Edge_1RB_Left	21.69	PC3	PASS
N41	30	40	CP-16QAM	L	Edge_1RB_Right	21.70	PC3	PASS
N41	30	40	CP-16QAM	L	Outer_Full	21.57	PC3	PASS
N41	30	40	CP-16QAM	L	Inner_Full	21.45	PC3	PASS
N41	30	40	CP-64QAM	L	Edge_1RB_Left	21.81	PC3	PASS
N41	30	40	CP-64QAM	L	Edge_1RB_Right	21.55	PC3	PASS
N41	30	40	CP-64QAM	L	Outer_Full	21.82	PC3	PASS
N41	30	40	CP-64QAM	L	Inner_Full	21.52	PC3	PASS
N41	30	40	CP-256QAM	L	Edge_1RB_Left	21.78	PC3	PASS
N41	30	40	CP-256QAM	L	Edge_1RB_Right	21.45	PC3	PASS
N41	30	40	CP-256QAM	L	Outer_Full	21.00	PC3	PASS
N41	30	40	DFT-QPSK	M	Edge_1RB_Right	22.73	PC3	PASS
N41	30	40	DFT-QPSK	M	Outer_Full	22.67	PC3	PASS
N41	30	40	DFT-QPSK	M	Inner_Full	22.74	PC3	PASS
N41	30	40	DFT-16QAM	M	Edge_1RB_Left	21.97	PC3	PASS
N41	30	40	DFT-16QAM	M	Edge_1RB_Right	21.98	PC3	PASS
N41	30	40	DFT-16QAM	M	Outer_Full	21.57	PC3	PASS
N41	30	40	DFT-16QAM	M	Inner_Full	22.73	PC3	PASS
N41	30	40	DFT-64QAM	M	Edge_1RB_Left	21.44	PC3	PASS
N41	30	40	DFT-64QAM	M	Edge_1RB_Right	21.50	PC3	PASS
N41	30	40	DFT-64QAM	M	Outer_Full	21.00	PC3	PASS
N41	30	40	DFT-256QAM	M	Inner_Full	21.30	PC3	PASS
N41	30	40	CP-QPSK	M	Edge_1RB_Left	21.66	PC3	PASS
N41	30	40	CP-QPSK	M	Edge_1RB_Right	21.78	PC3	PASS
N41	30	40	CP-QPSK	M	Outer_Full	21.71	PC3	PASS
N41	30	40	CP-QPSK	M	Inner_Full	22.21	PC3	PASS
N41	30	40	CP-16QAM	M	Edge_1RB_Left	21.87	PC3	PASS
N41	30	40	CP-16QAM	M	Edge_1RB_Right	21.90	PC3	PASS
N41	30	40	CP-16QAM	M	Outer_Full	21.66	PC3	PASS