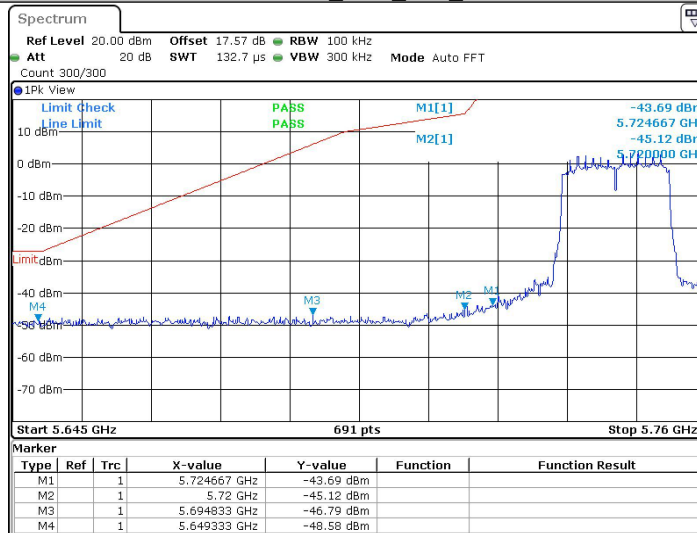
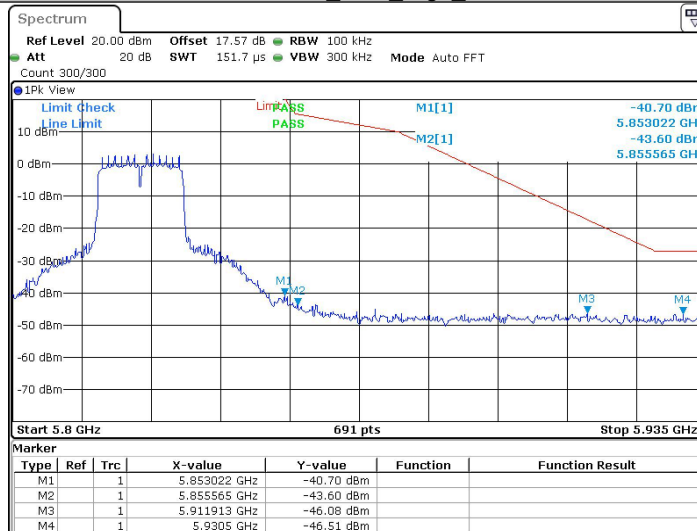


11N20SISO_Ant1_Low_5745



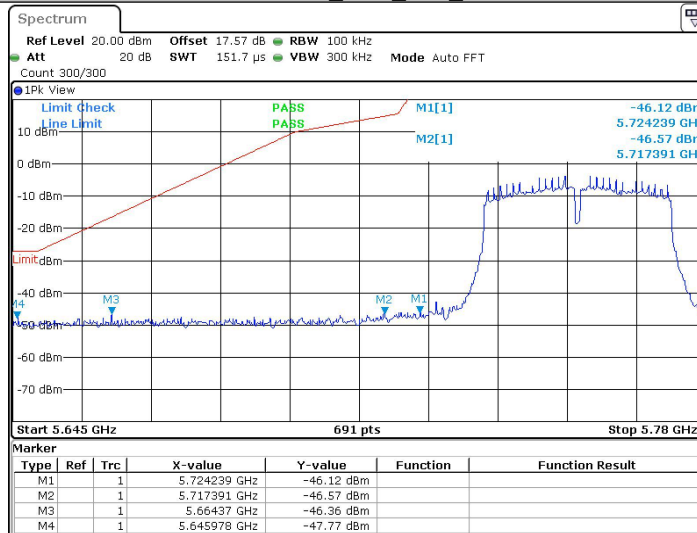
Date: 21 MAR 2024 19:55:31

11N20SISO_Ant1_High_5825



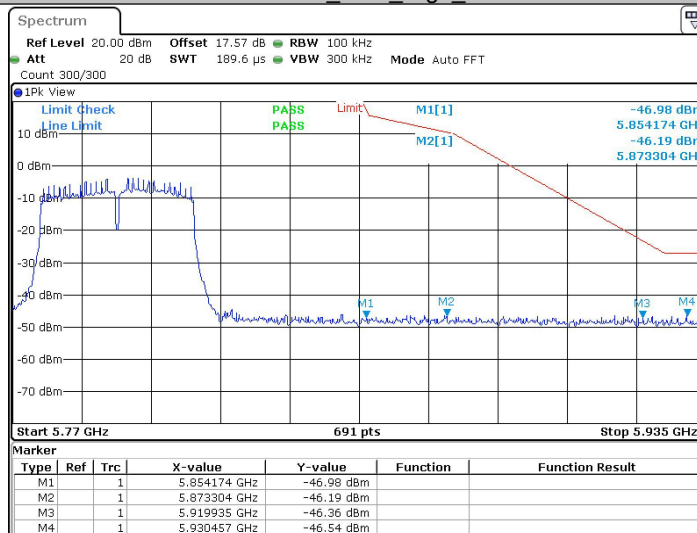
Date: 22 MAR 2024 14:49:22

11N40SISO_Ant1_Low_5755



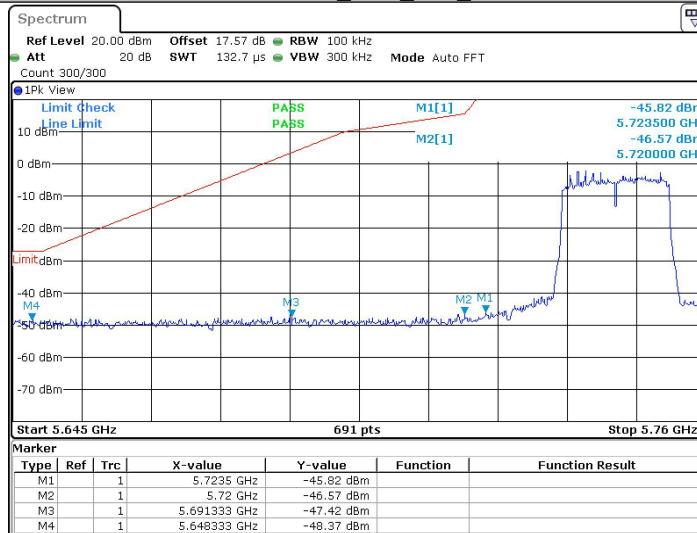
Date: 21 MAR 2024 20:07:27

11N40SISO_Ant1_High_5795



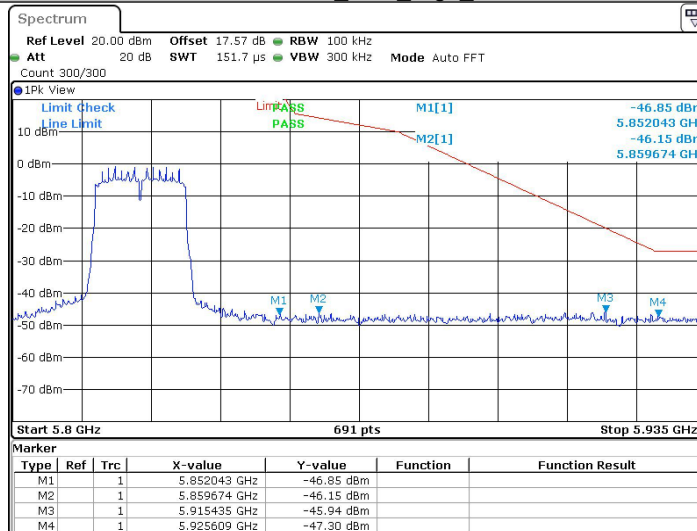
Date: 21 MAR 2024 20:09:59

11AC20SISO_Ant1_Low_5745



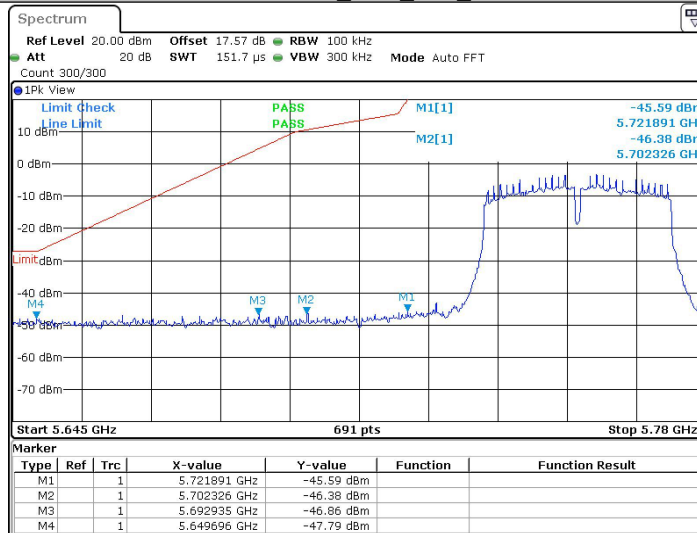
Date: 21 MAR 2024 20:19:06

11AC20SISO_Ant1_High_5825



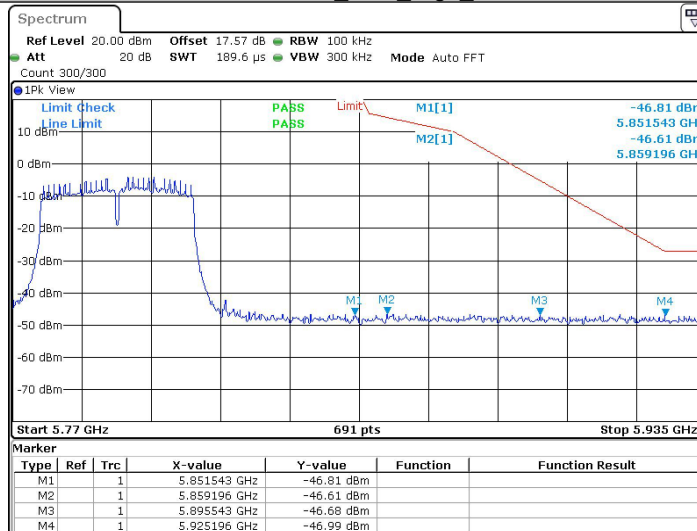
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11AC40SISO_Ant1_Low_5755



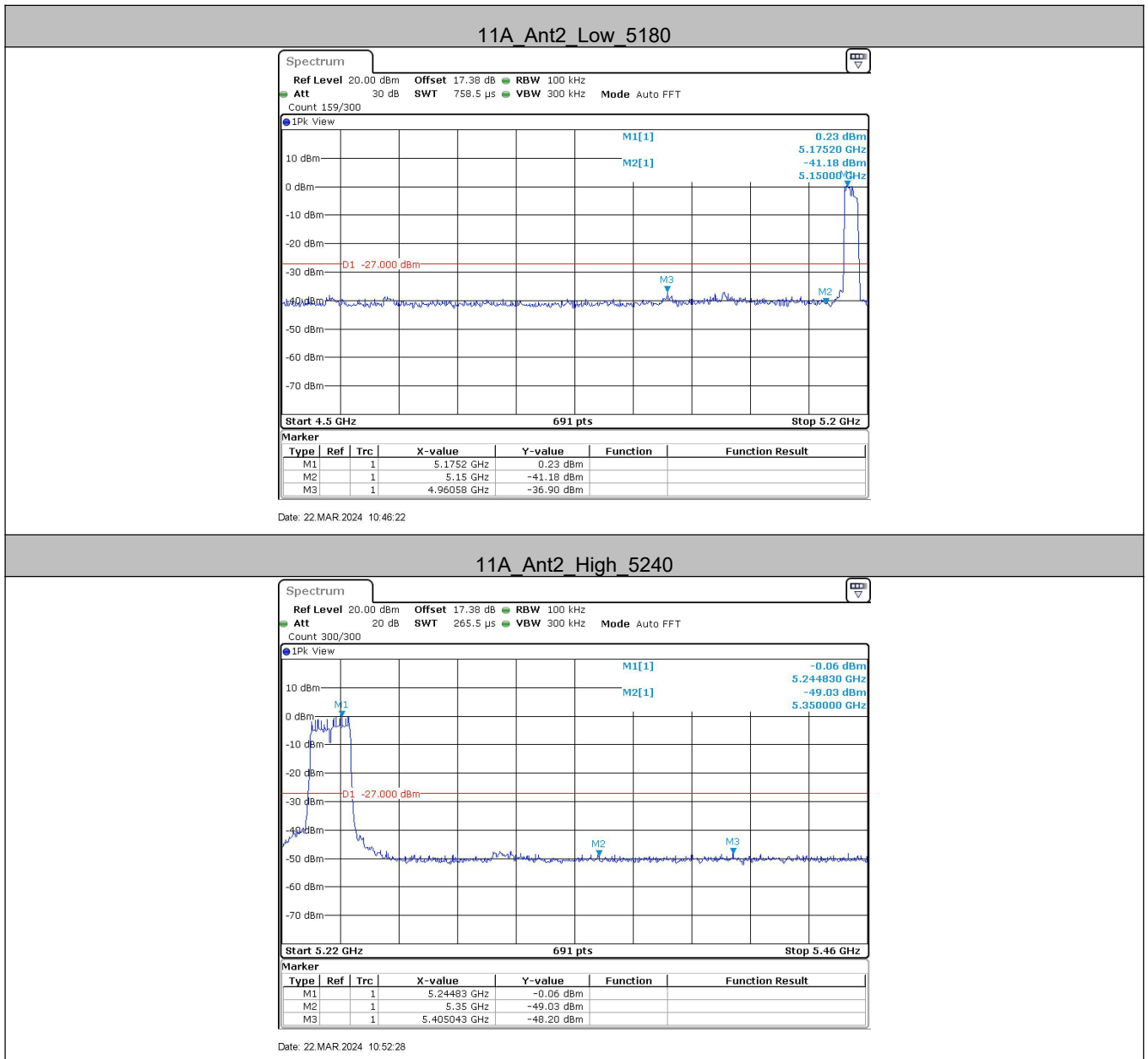
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11AC40SISO_Ant1_High_5795

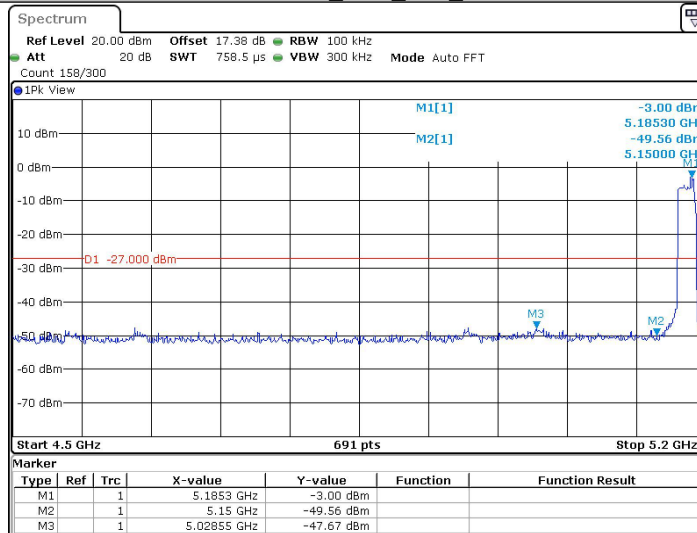


Date: 21.MAR.2024 20:36:08

7.1.3 Test Graphs B1

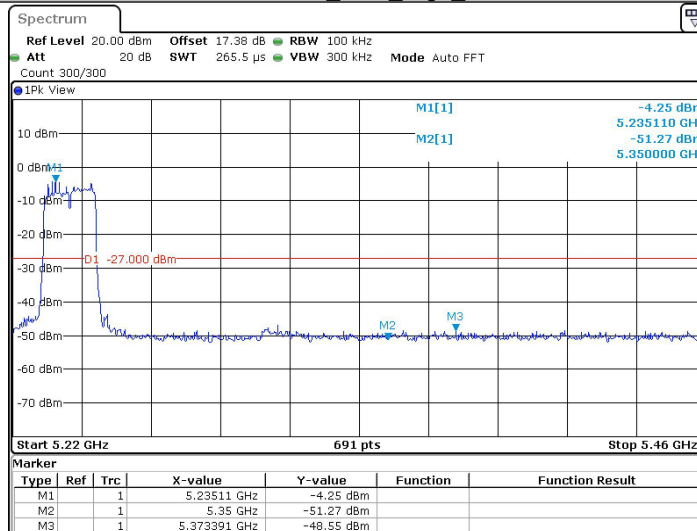


11N20SISO_Ant2_Low_5180



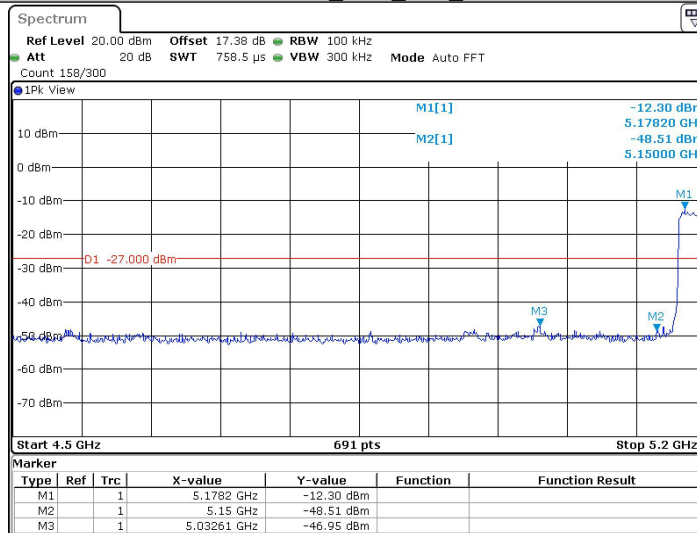
Date: 22.MAR.2024 12:41:01

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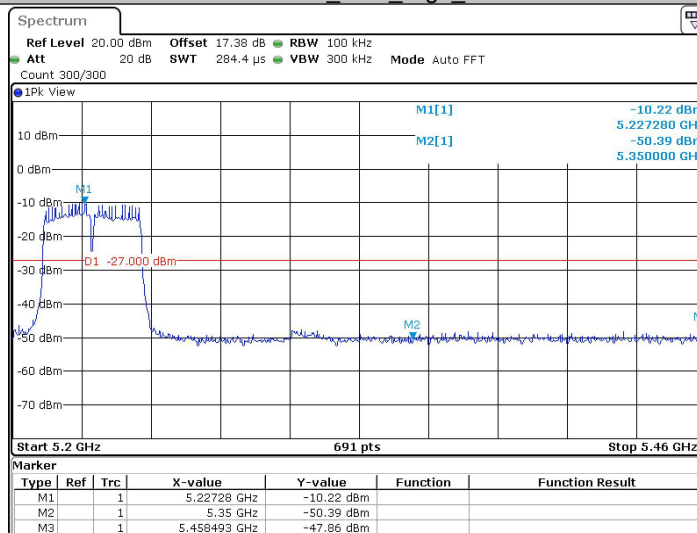
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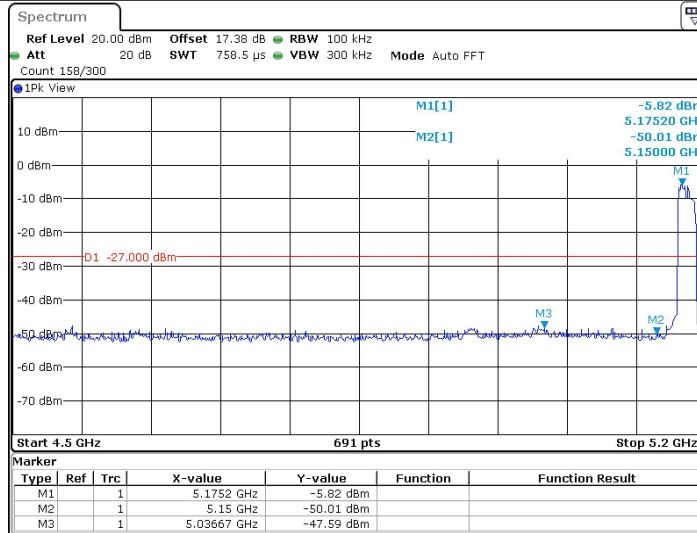
Date: 22.MAR.2024 12:59:37

11N40SISO_Ant2_High_5230



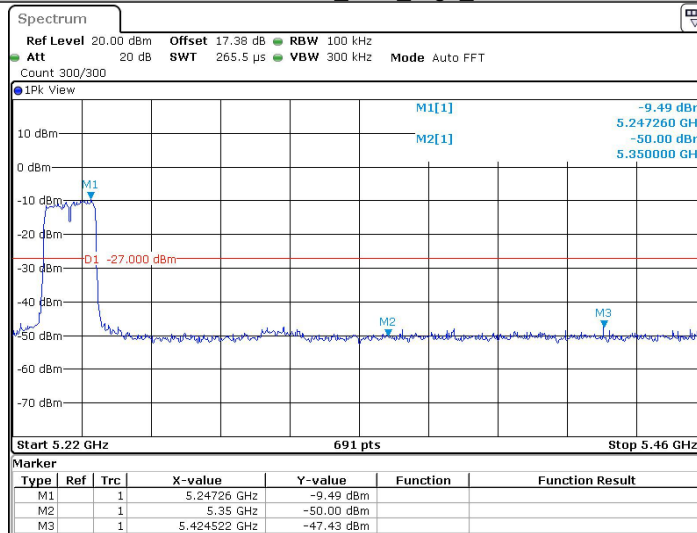
Date: 22.MAR.2024 13:02:38

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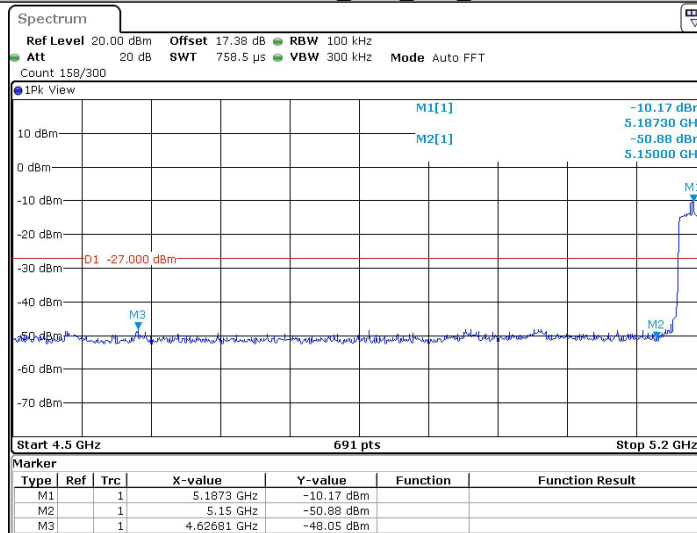
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11AC20SISO_Ant2_High_5240



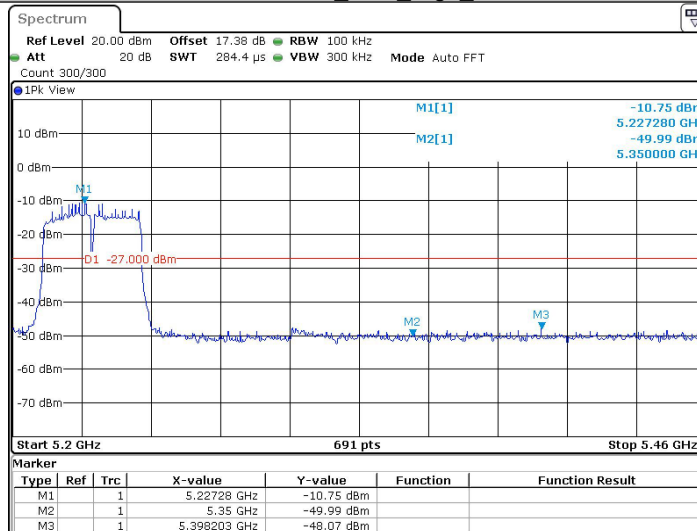
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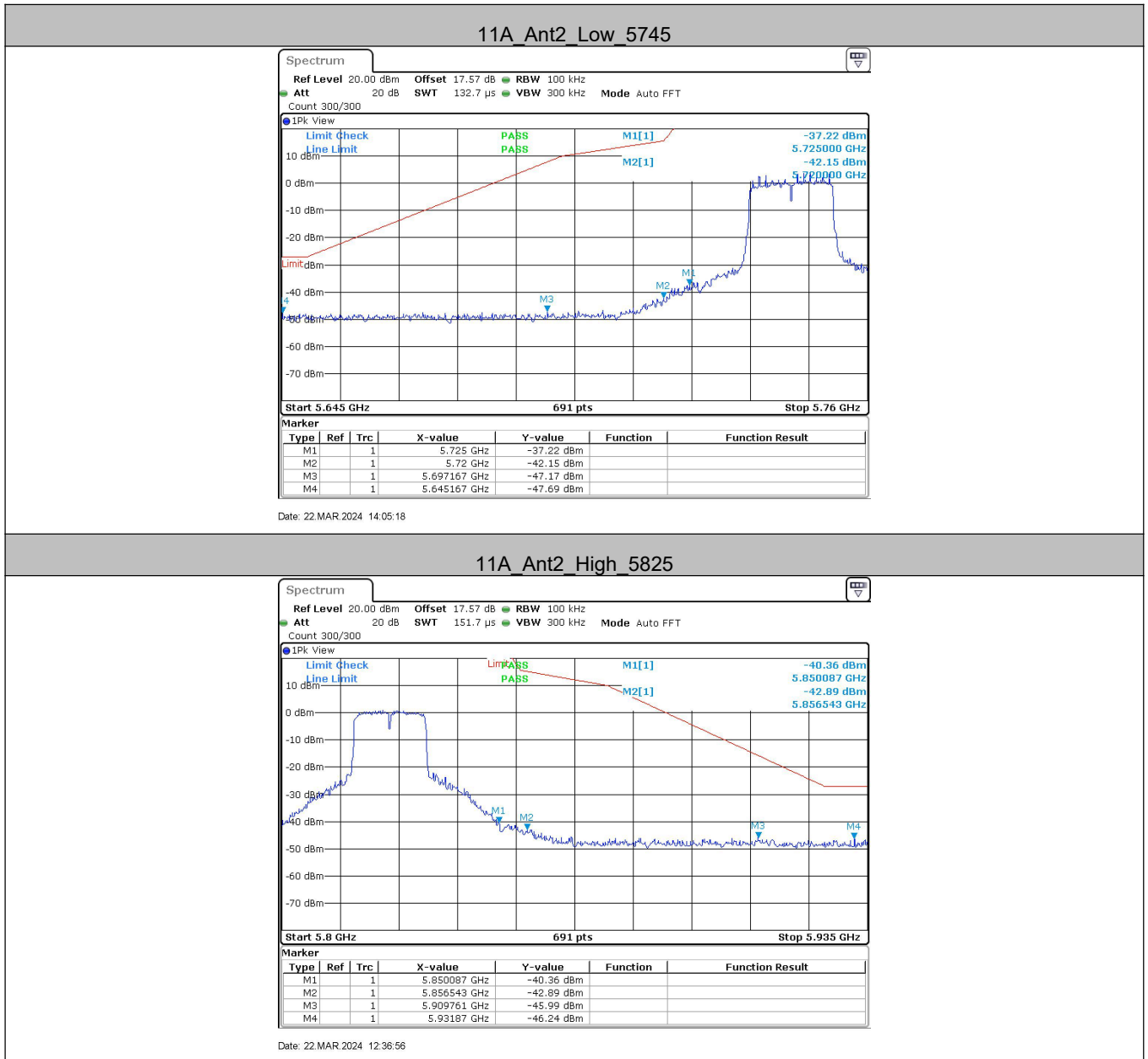
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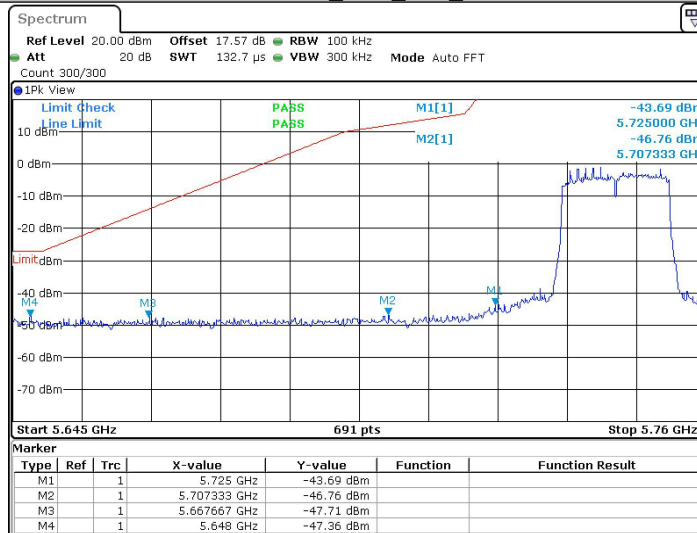


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7.1.4 Test Graphs B4

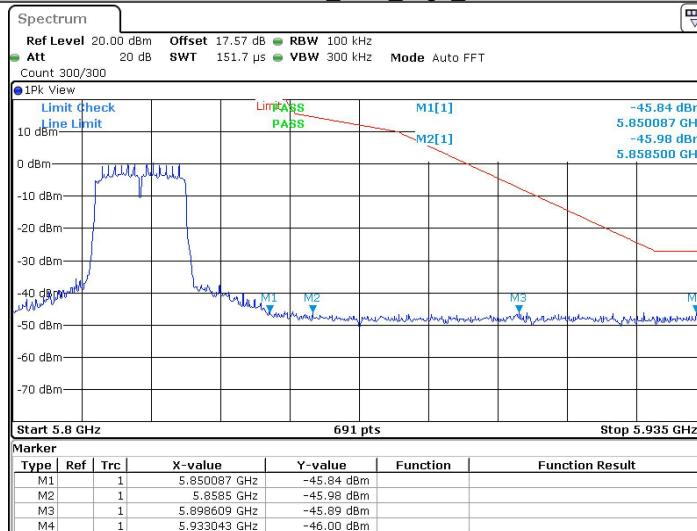


11N20SISO_Ant2_Low_5745



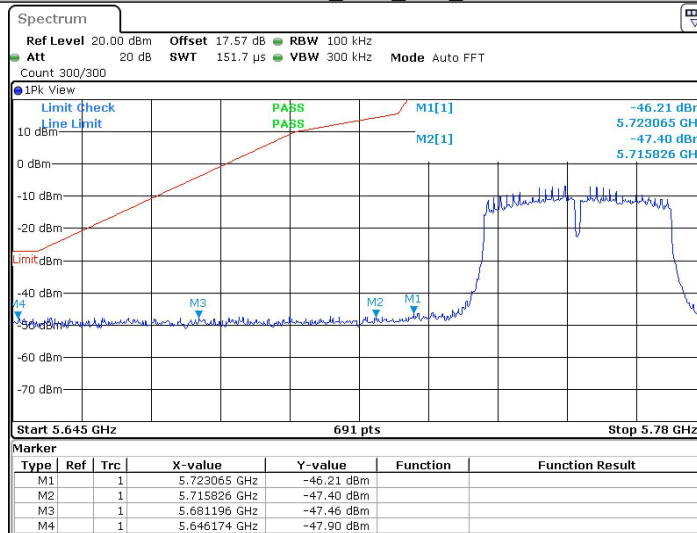
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11N20SISO_Ant2_High_5825



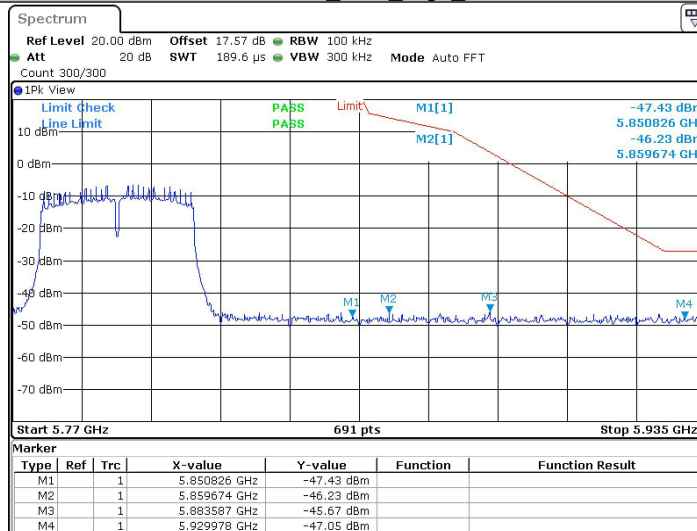
Date: 22.MAR.2024 12:56:34

11N40SISO_Ant2_Low_5755



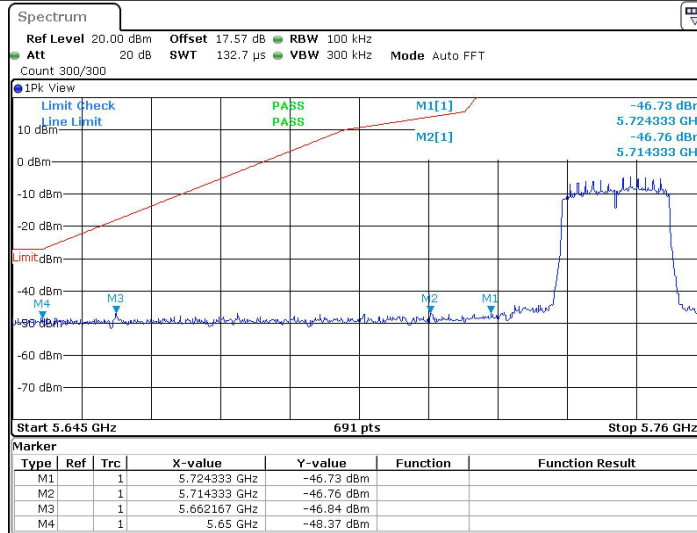
Date: 22.MAR.2024 13:05:05

11N40SISO_Ant2_High_5795



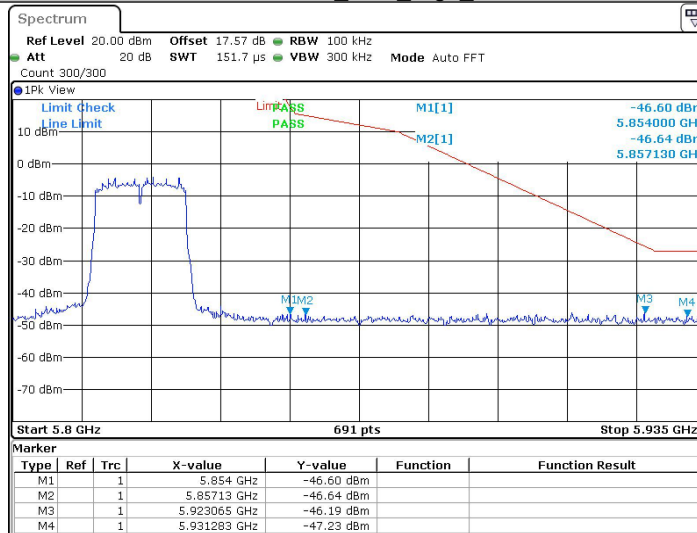
Date: 22.MAR.2024 13:07:33

11AC20SISO_Ant2_Low_5745



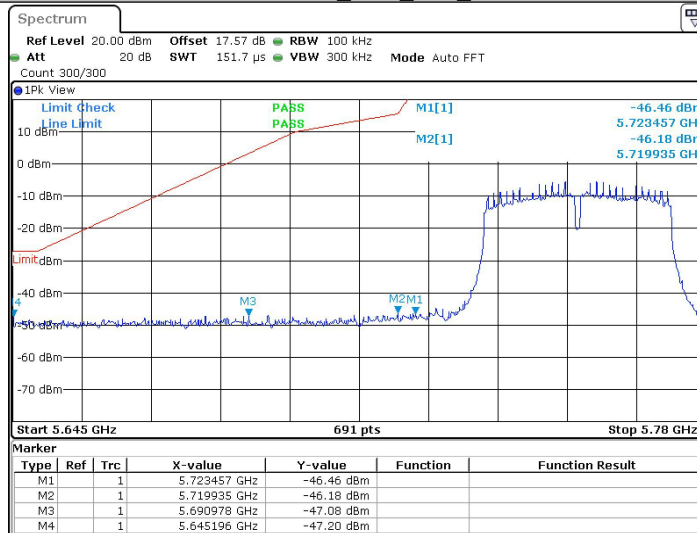
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11AC20SISO_Ant2_High_5825



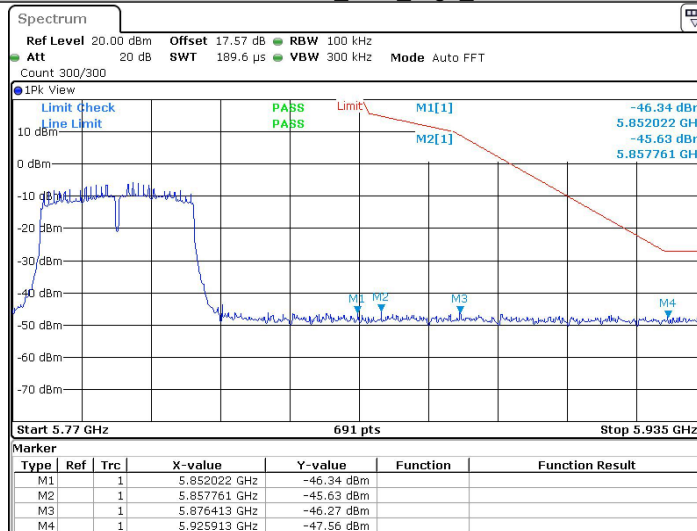
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11AC40SISO_Ant2_Low_5755



Date: 22.MAR.2024 13:55:32

11AC40SISO_Ant2_High_5795



Date: 22.MAR.2024 13:58:34

Appendix E): Frequency Stability

Test Requirement 47 CFR Part 15, Subpart C 15.407 (g)

Test Method: ANSI C63.10 (2013) Section 6.8

Limit: The frequency tolerance shall be maintained within the band of operation frequency over a temperature variation of 14.4 degrees to 17.6 degrees C at normal supply voltage, and for a variation in the primary supply voltage from 85% to 115% of the rated supply voltage at a temperature of 20 degrees C.

Test Setup Diagram

