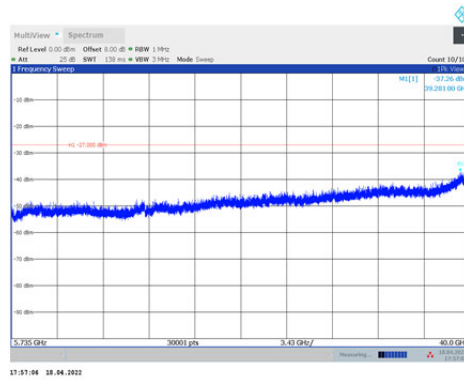
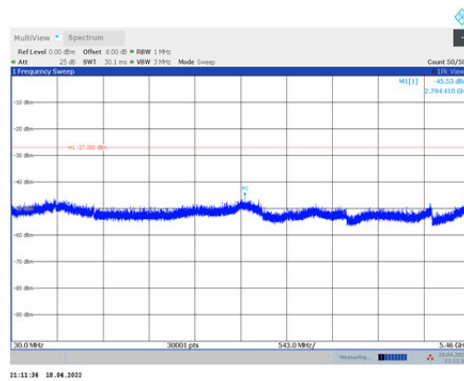


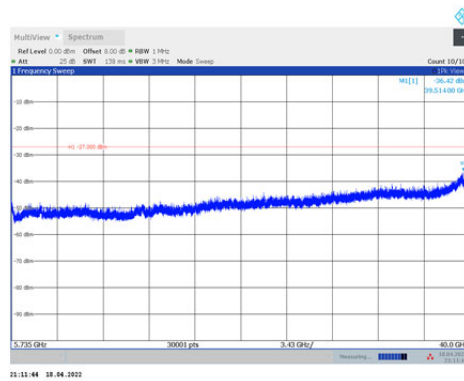
## 11AC80SISO\_Ant2\_5530\_5735~40000



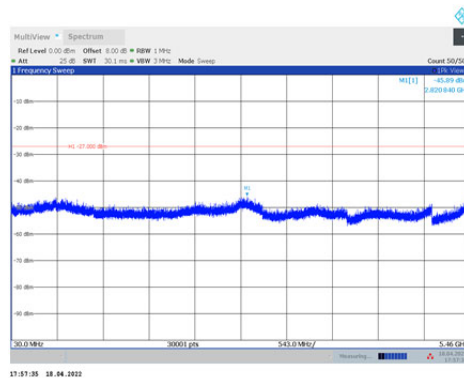
## 11AC80SISO\_Ant1\_5610\_30~5460



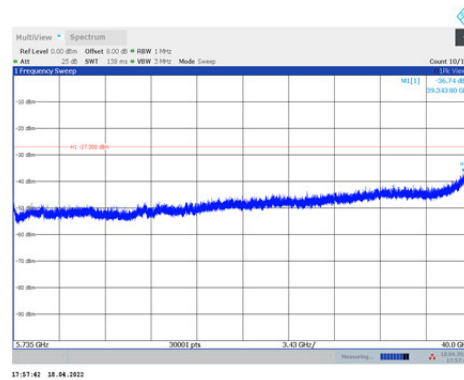
## 11AC80SISO\_Ant1\_5610\_5735~40000



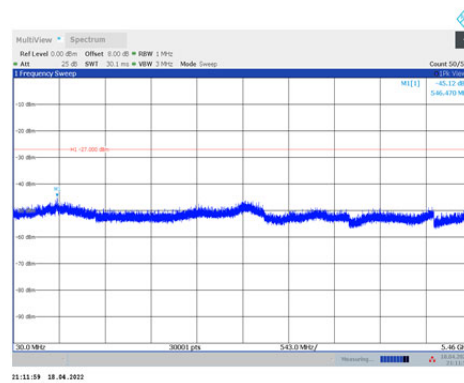
## 11AC80SISO\_Ant2\_5610\_30~5460



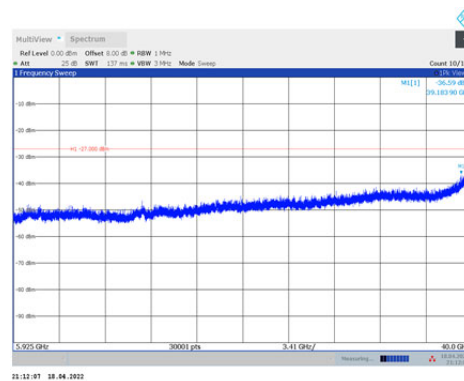
## 11AC80SISO\_Ant2\_5610\_5735~40000



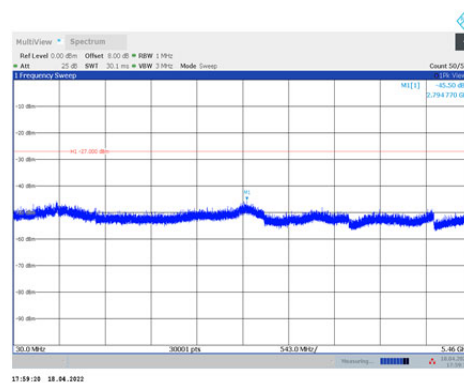
## 11AC80SISO\_Ant1\_5690\_30~5460



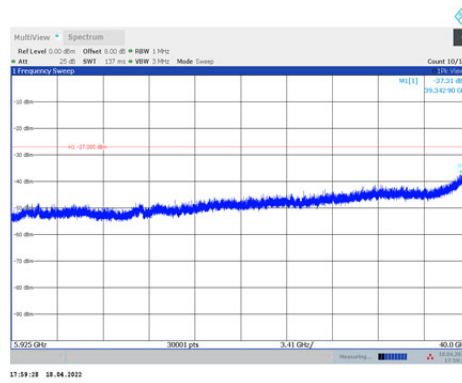
## 11AC80SISO\_Ant1\_5690\_5925~40000



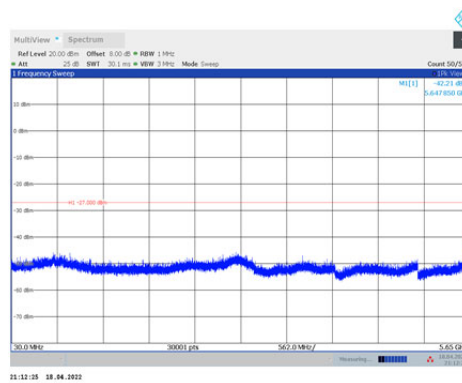
## 11AC80SISO\_Ant2\_5690\_30~5460



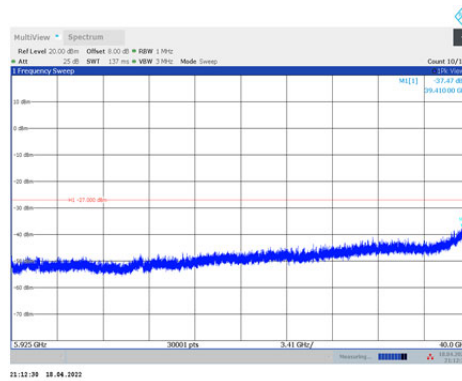
## 11AC80SISO\_Ant2\_5690\_5925~40000



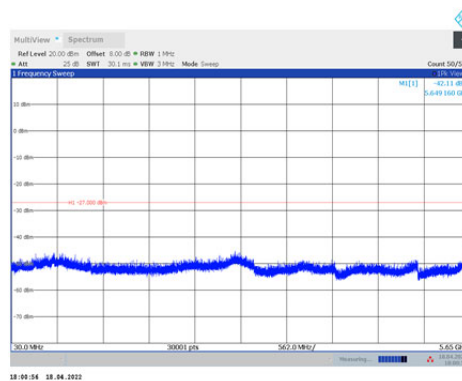
## 11AC80SISO\_Ant1\_5775\_30~5650



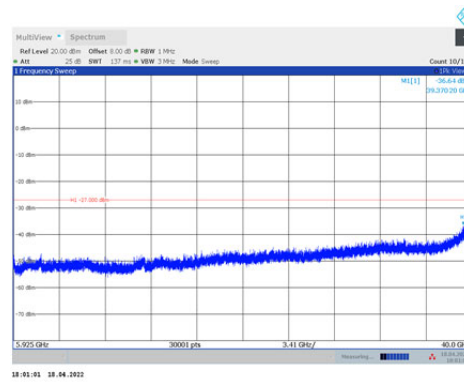
## 11AC80SISO\_Ant1\_5775\_5925~40000



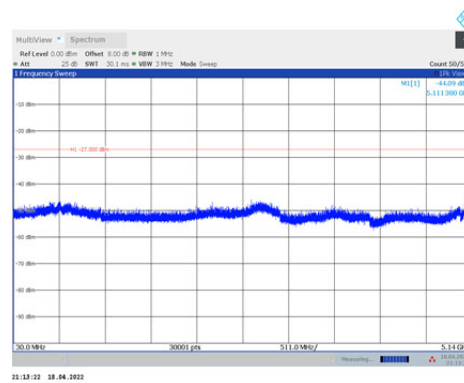
## 11AC80SISO\_Ant2\_5775\_30~5650



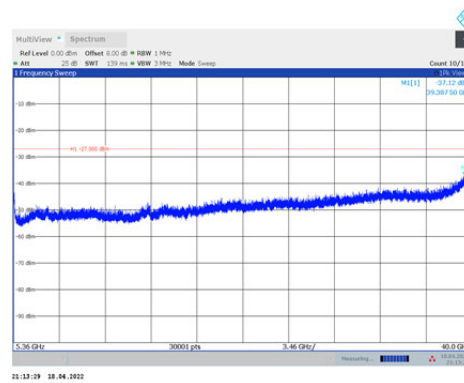
## 11AC80SISO\_Ant2\_5775\_5925~40000



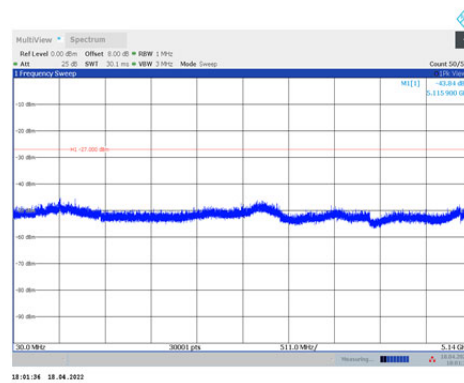
## 11AC160SISO\_Ant1\_5250\_30~5140



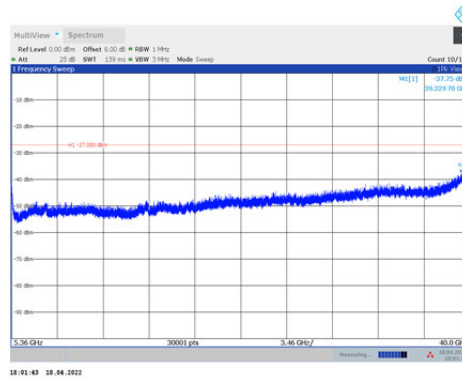
## 11AC160SISO\_Ant1\_5250\_5360~40000



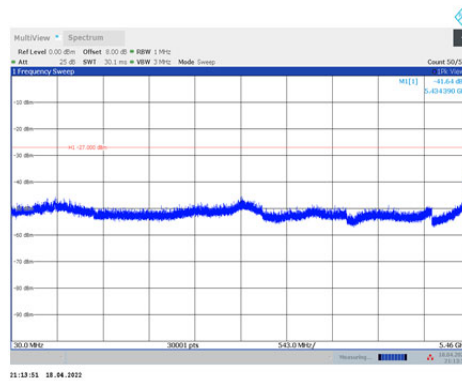
## 11AC160SISO\_Ant2\_5250\_30~5140



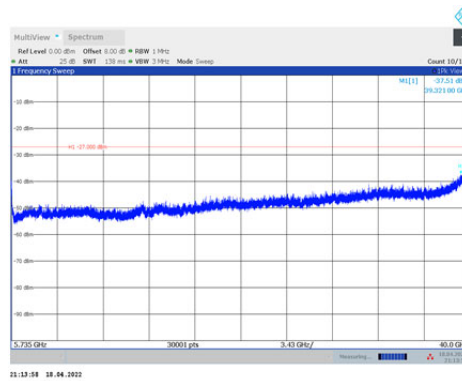
## 11AC160SISO\_Ant2\_5250\_5360~40000



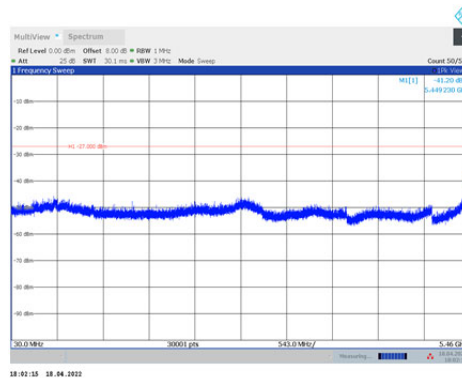
## 11AC160SISO\_Ant1\_5570\_30~5460



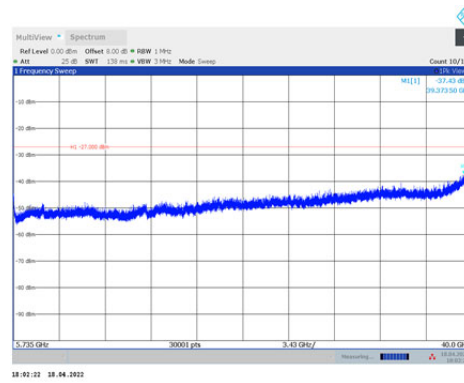
## 11AC160SISO\_Ant1\_5570\_5735~40000



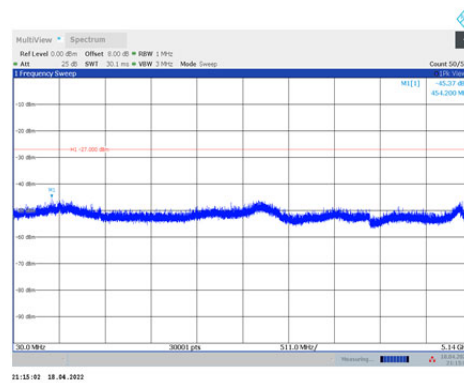
## 11AC160SISO\_Ant2\_5570\_30~5460



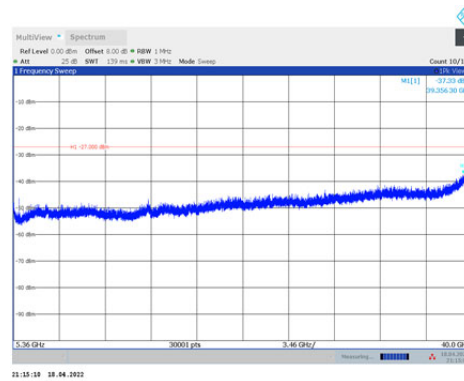
## 11AC160SISO\_Ant2\_5570\_5735~40000



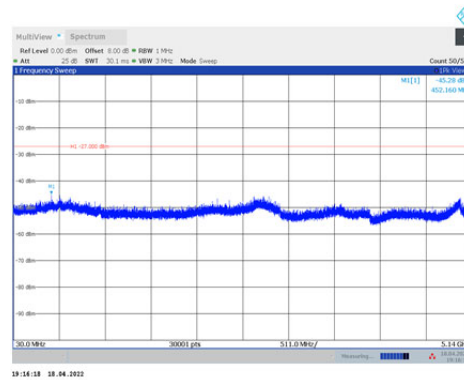
## 11AX20SISO\_Ant1\_5180\_30~5140



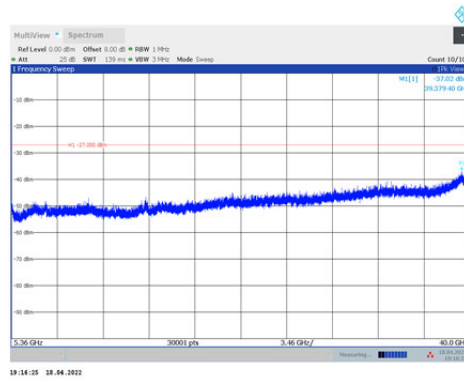
## 11AX20SISO\_Ant1\_5180\_5360~40000



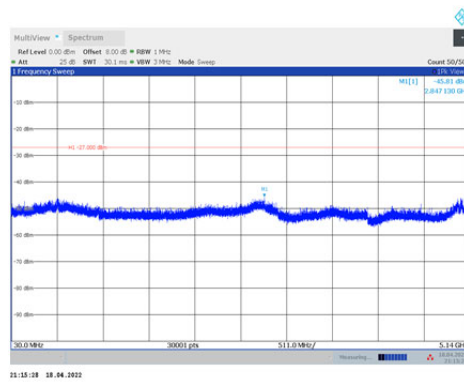
## 11AX20SISO\_Ant2\_5180\_30~5140



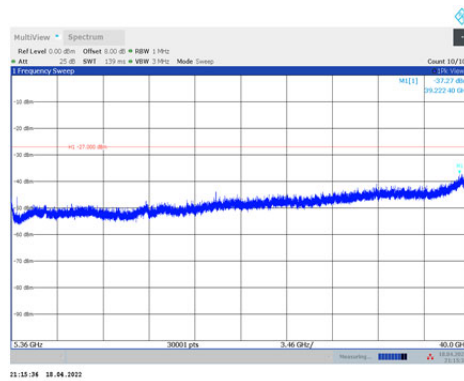
## 11AX20SISO\_Ant2\_5180\_5360~40000



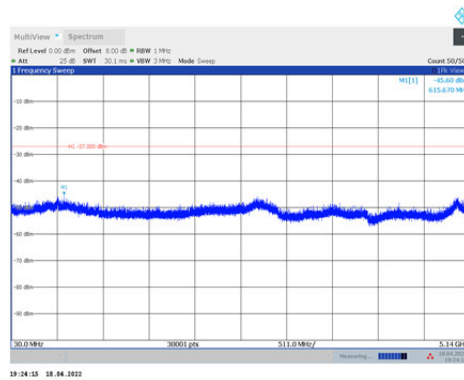
## 11AX20SISO\_Ant1\_5200\_30~5140



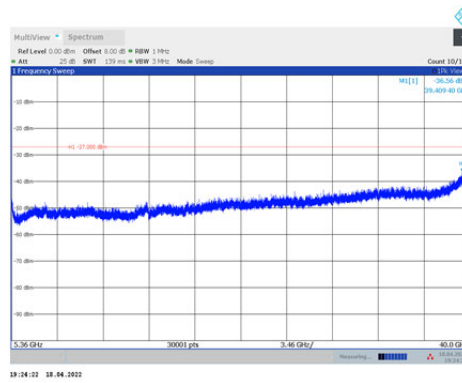
## 11AX20SISO\_Ant1\_5200\_5360~40000



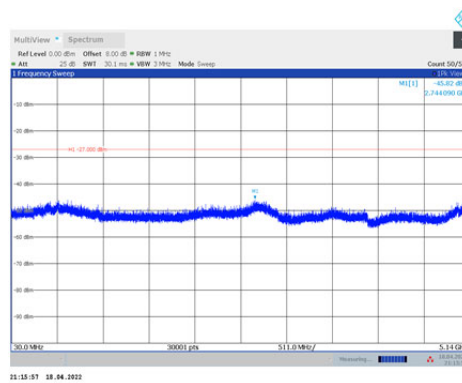
## 11AX20SISO\_Ant2\_5200\_30~5140



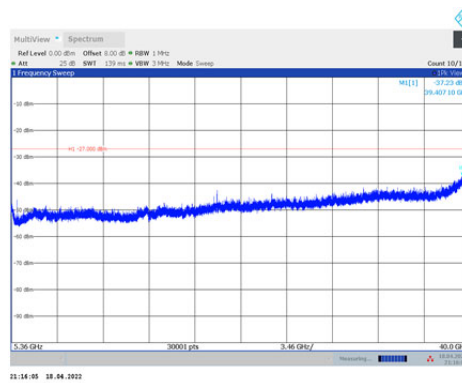
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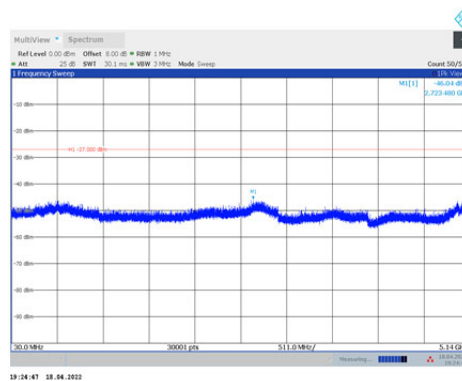
## 11AX20SISO\_Ant1\_5220\_30~5140



## 11AX20SISO\_Ant1\_5220\_5360~40000

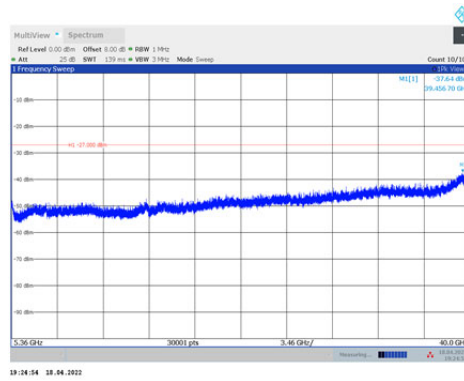


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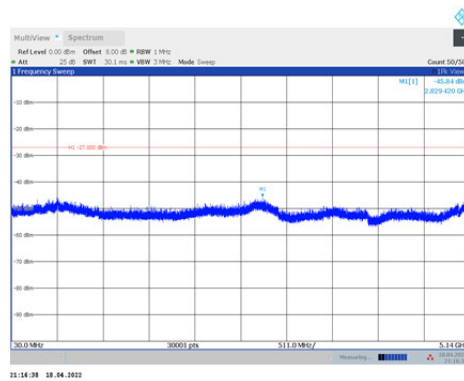




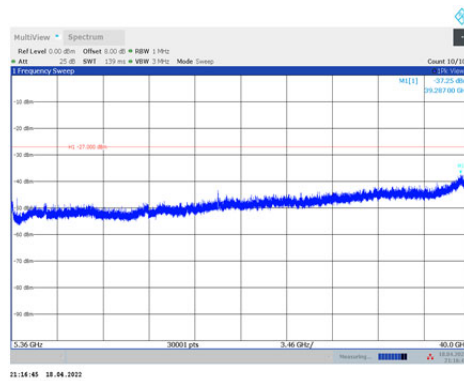
## 11AX20SISO\_Ant2\_5220\_5360~40000



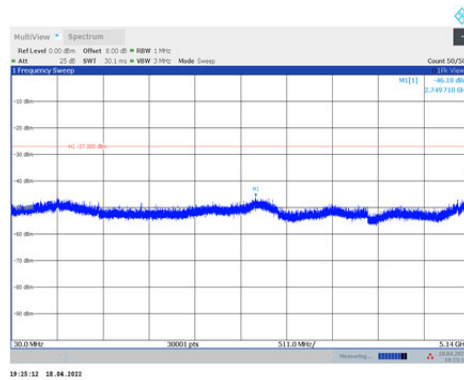
## 11AX20SISO\_Ant1\_5240\_30~5140



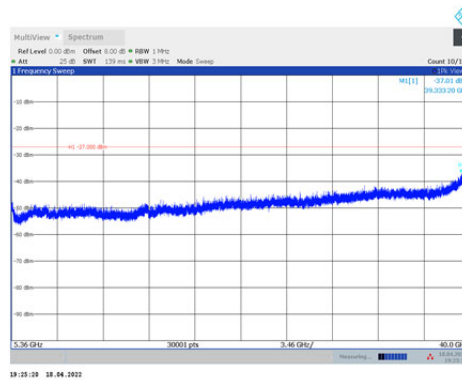
## 11AX20SISO\_Ant1\_5240\_5360~40000



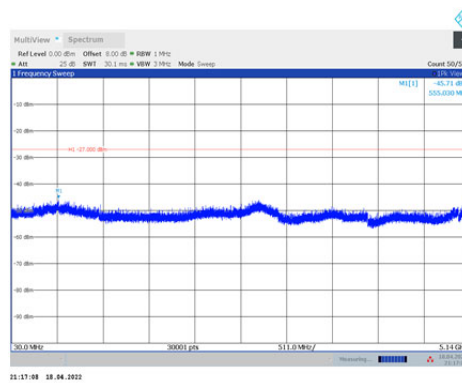
## 11AX20SISO\_Ant2\_5240\_30~5140



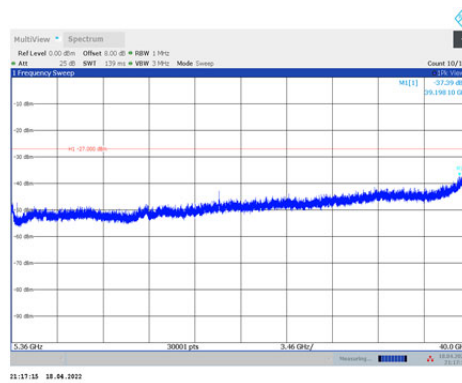
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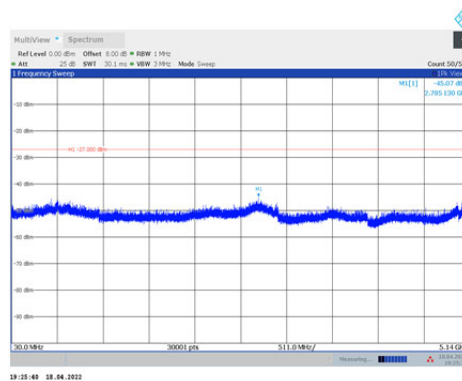
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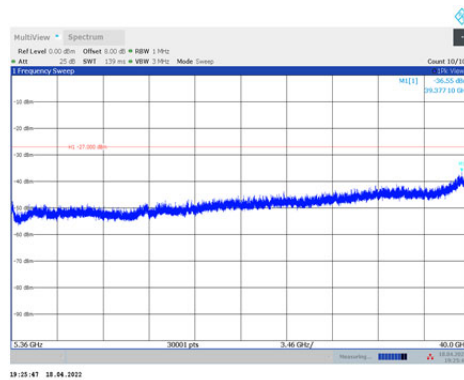
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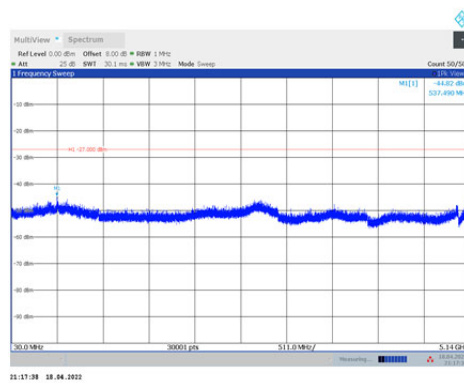
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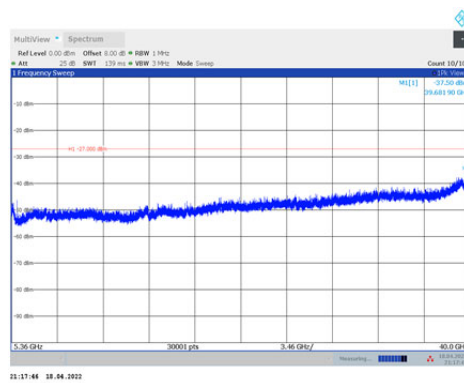
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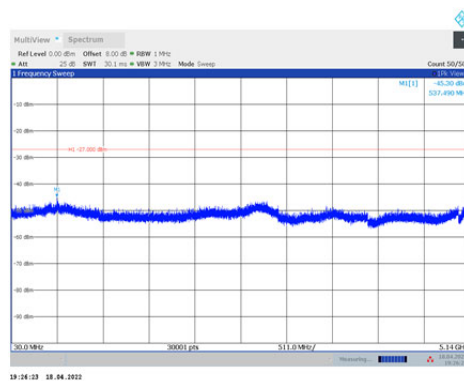
## 11AX20SISO\_Ant1\_5300\_30~5140



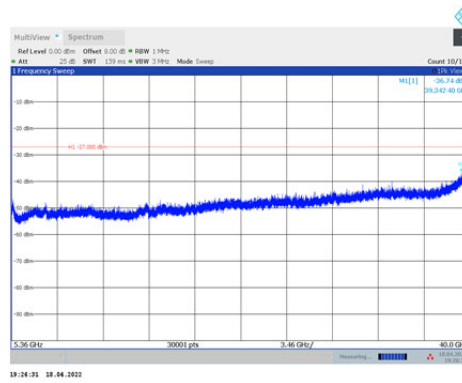
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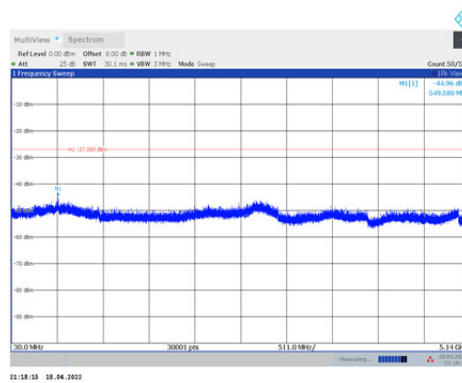
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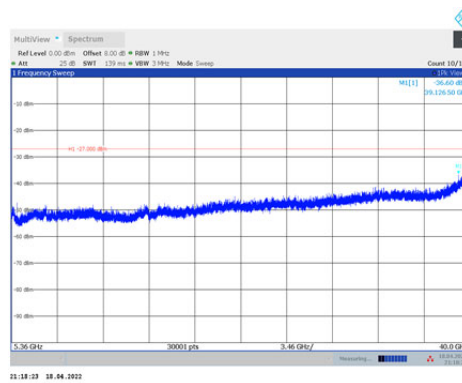
## 11AX20SISO\_Ant2\_5300\_5360~40000



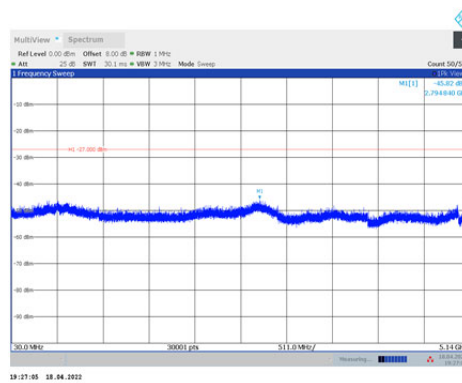
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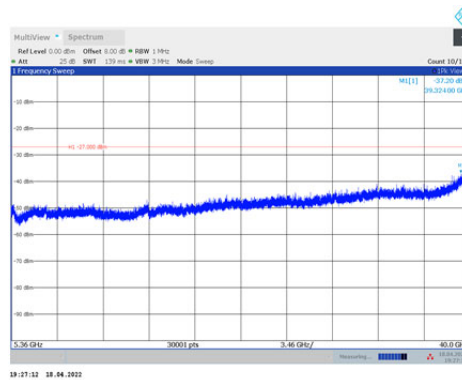
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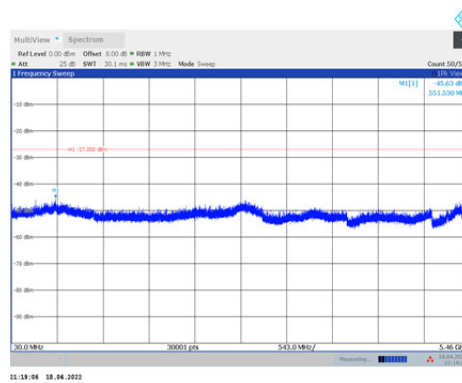
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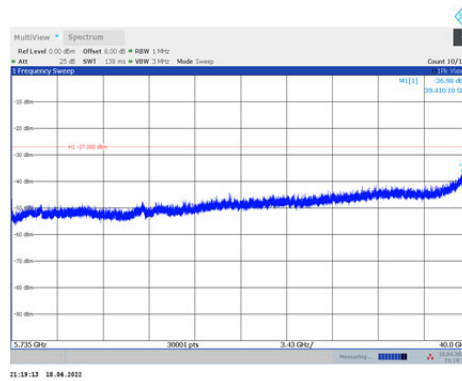
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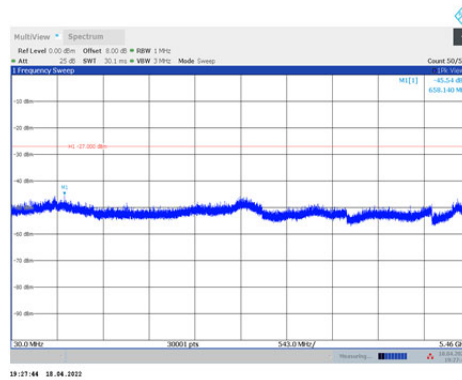
## 11AX20SISO\_Ant1\_5500\_30~5460



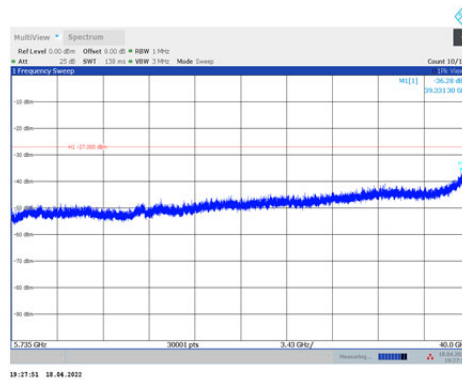
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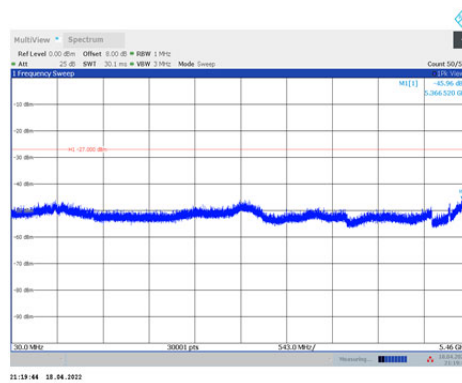
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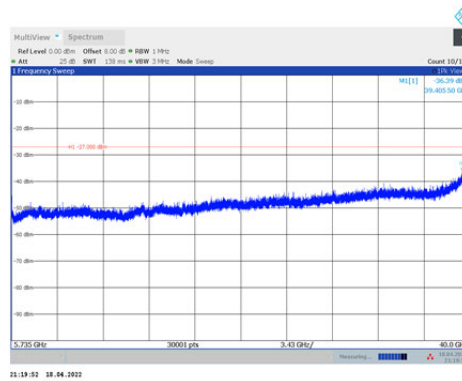
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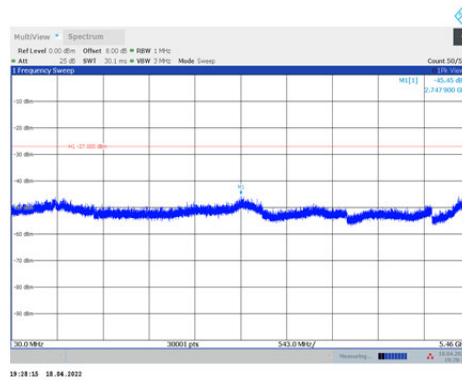
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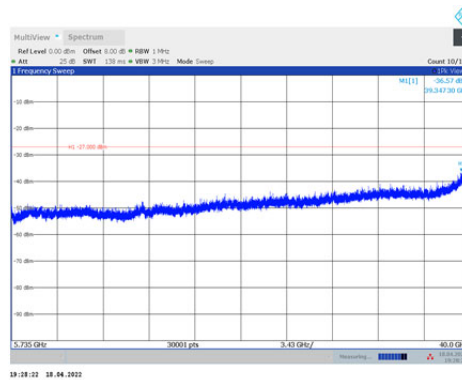
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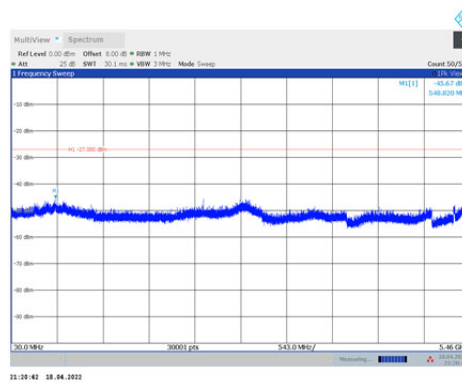
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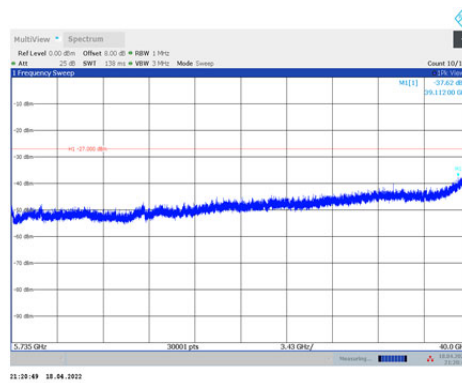
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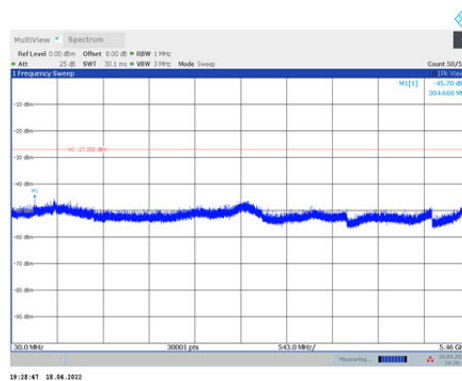
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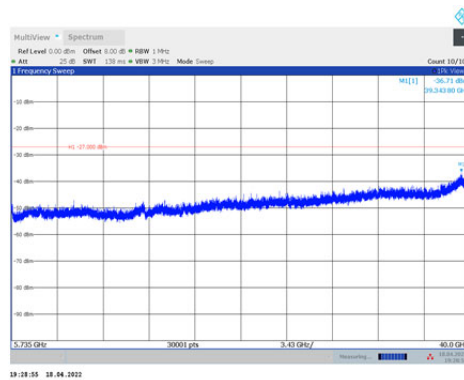
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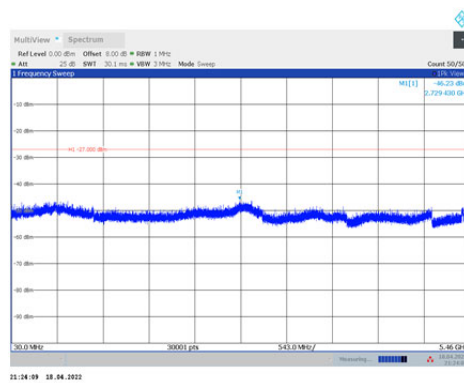
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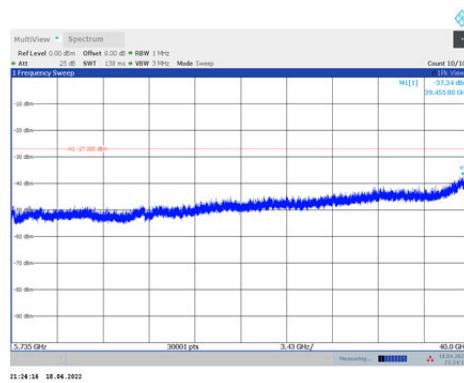
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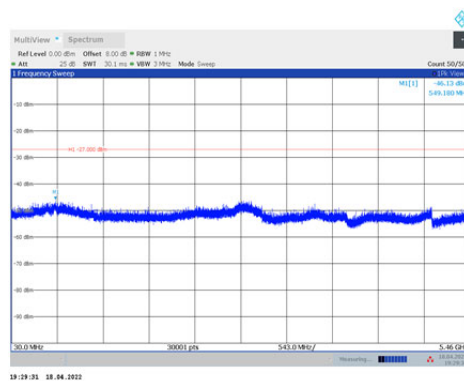
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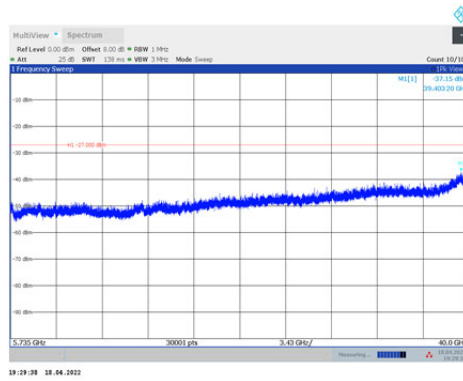


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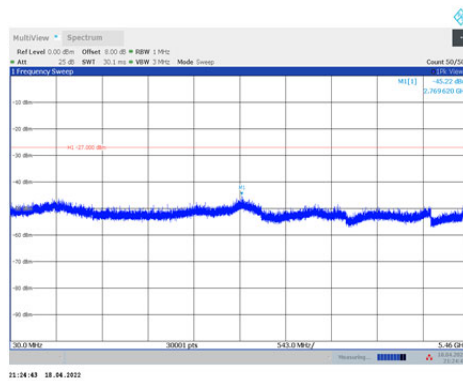




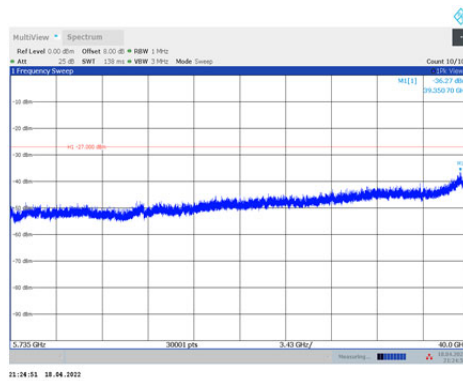
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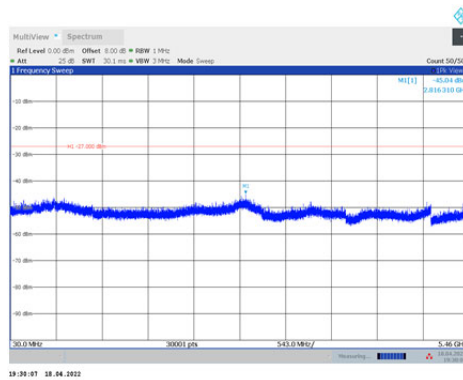
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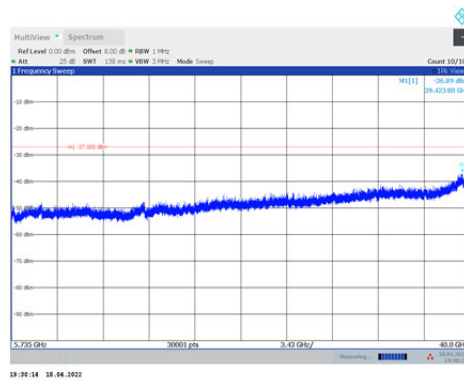
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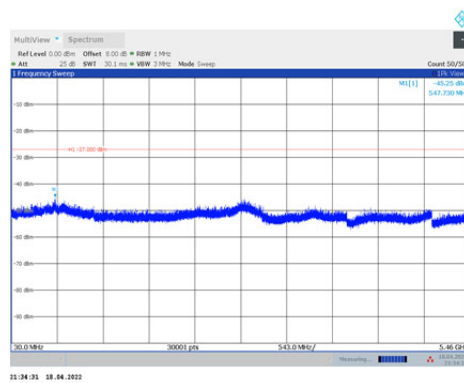
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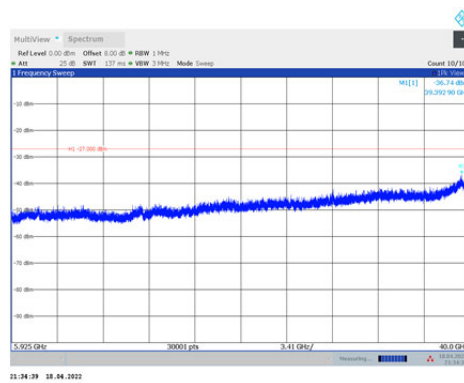
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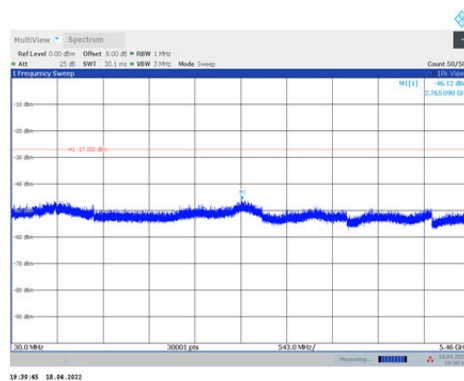
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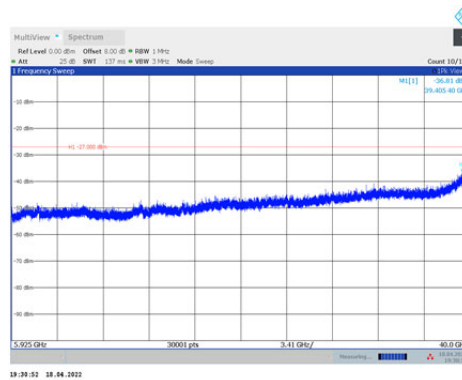
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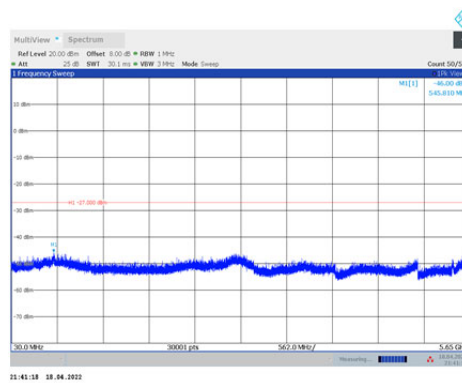
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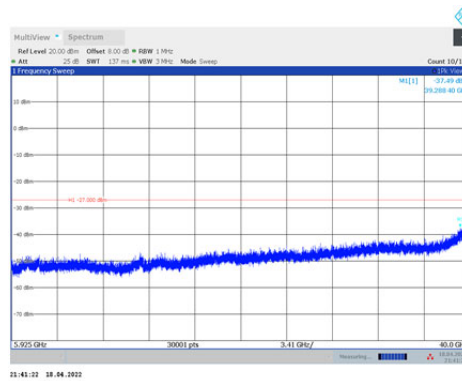
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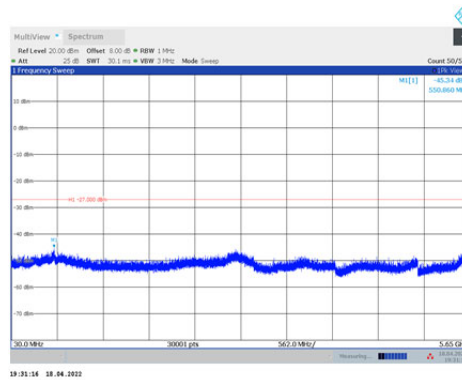
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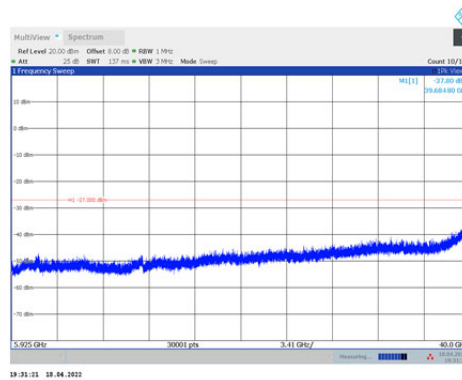
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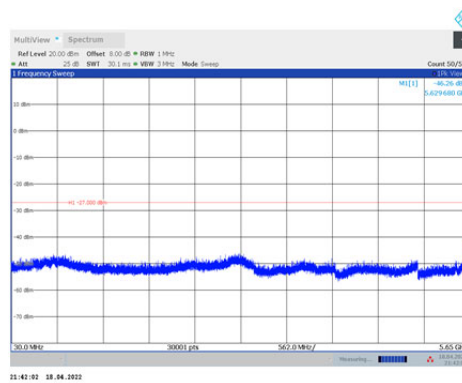
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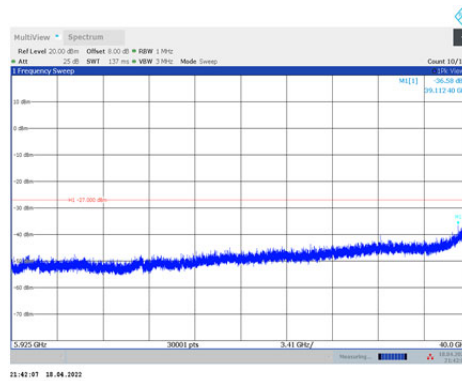
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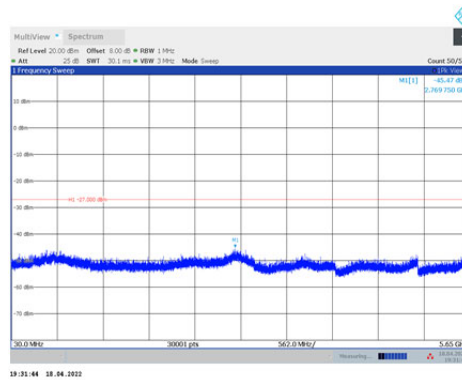
## 11AX20SISO\_Ant1\_5785\_30~5650



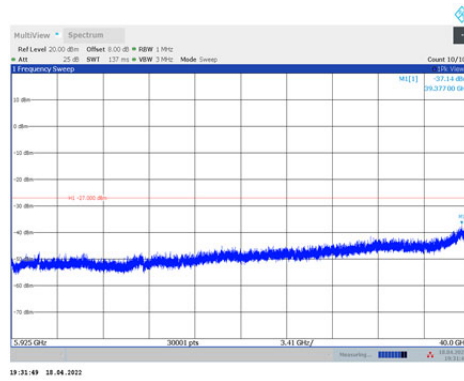
## 11AX20SISO\_Ant1\_5785\_5925~40000



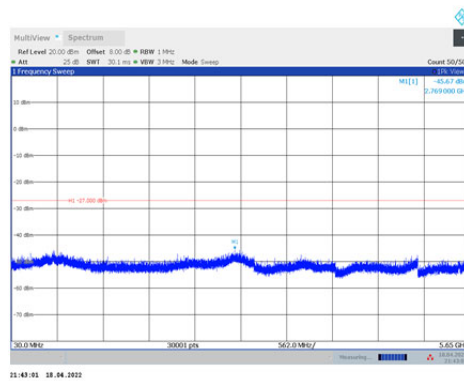
## 11AX20SISO\_Ant2\_5785\_30~5650



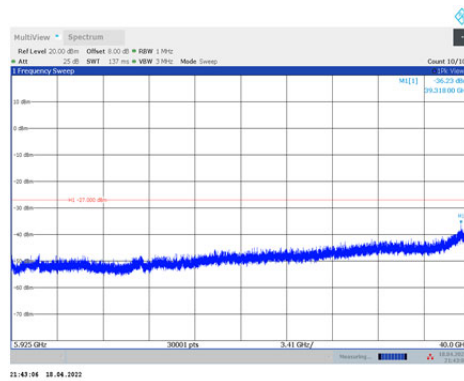
## 11AX20SISO\_Ant2\_5785\_5925~40000



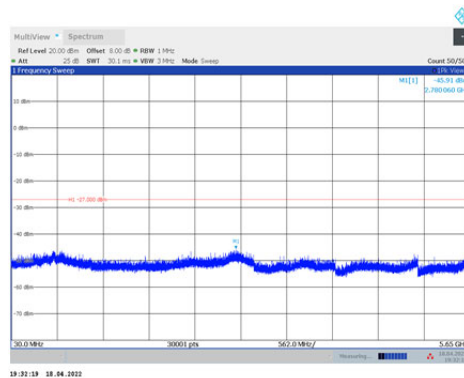
## 11AX20SISO\_Ant1\_5825\_30~5650



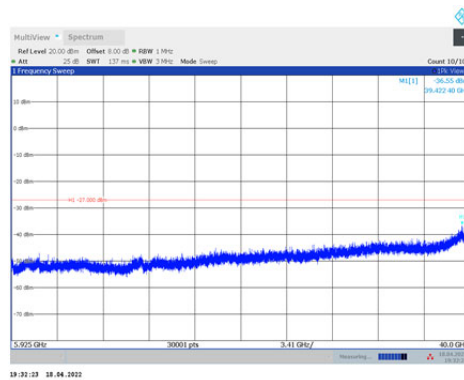
## 11AX20SISO\_Ant1\_5825\_5925~40000



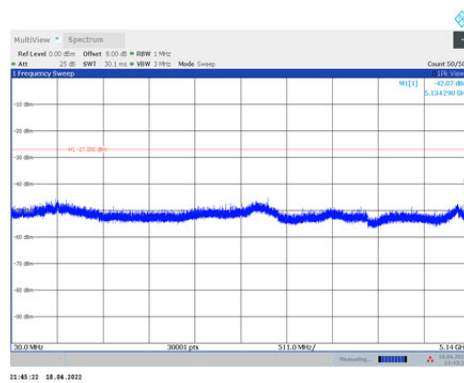
## 11AX20SISO\_Ant2\_5825\_30~5650



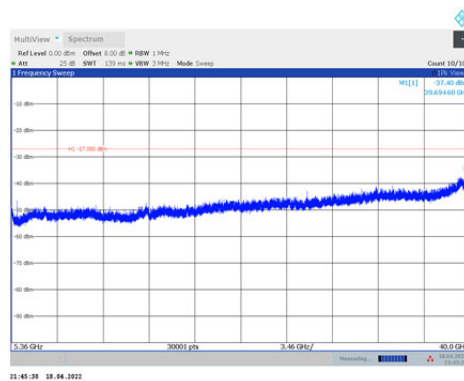
## 11AX20SISO\_Ant2\_5825\_5925~40000



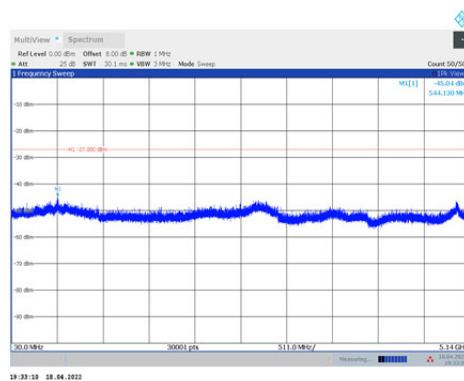
## 11AX40SISO\_Ant1\_5190\_30~5140



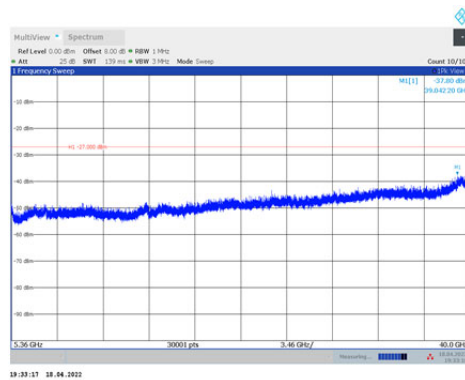
## 11AX40SISO\_Ant1\_5190\_5360~40000



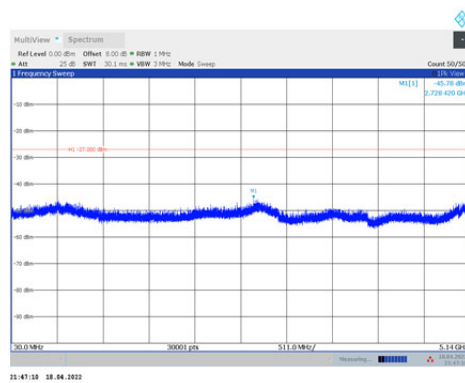
## 11AX40SISO\_Ant2\_5190\_30~5140



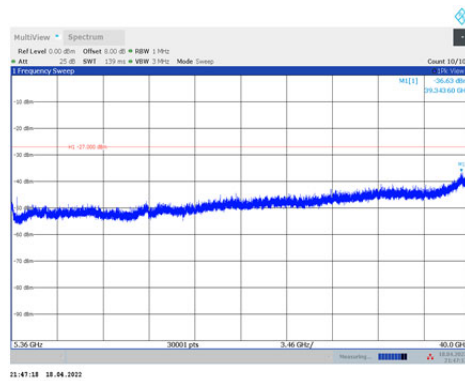
## 11AX40SISO\_Ant2\_5190\_5360~40000



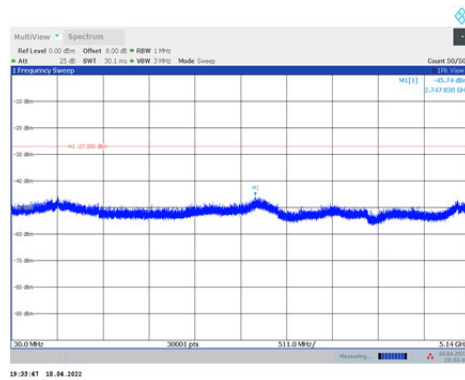
## 11AX40SISO\_Ant1\_5230\_30~5140



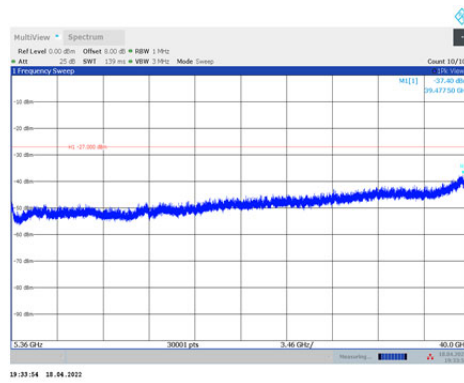
## 11AX40SISO\_Ant1\_5230\_5360~40000



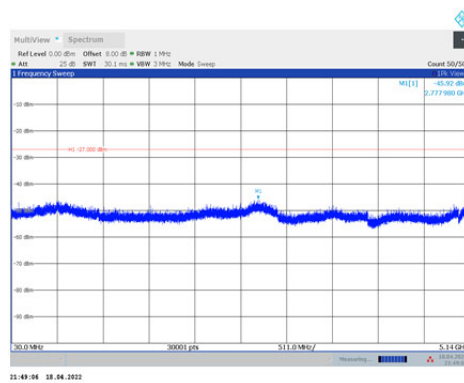
## 11AX40SISO\_Ant2\_5230\_30~5140



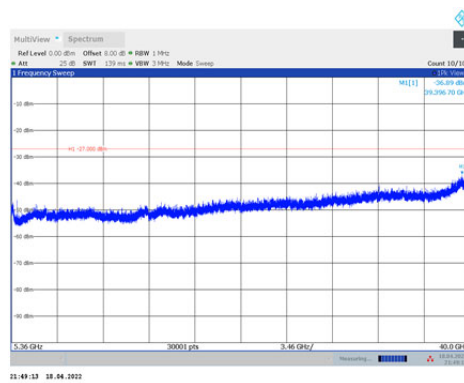
## 11AX40SISO\_Ant2\_5230\_5360~40000



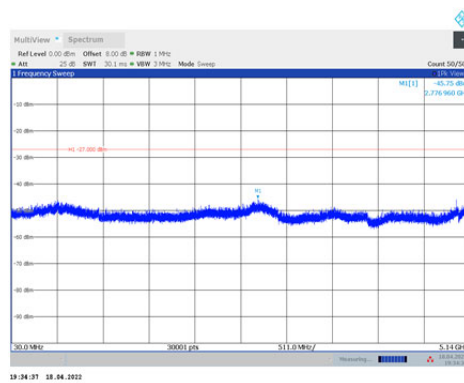
## 11AX40SISO\_Ant1\_5270\_30~5140



## 11AX40SISO\_Ant1\_5270\_5360~40000

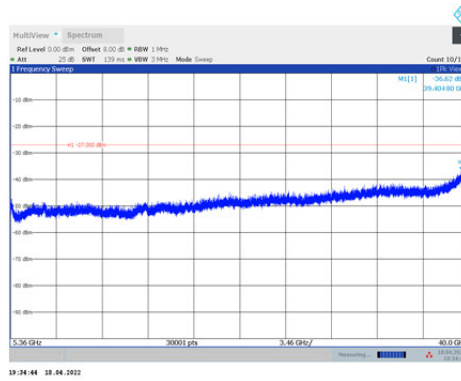


## 11AX40SISO\_Ant2\_5270\_30~5140

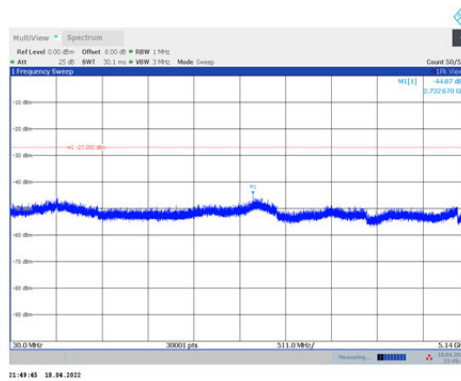




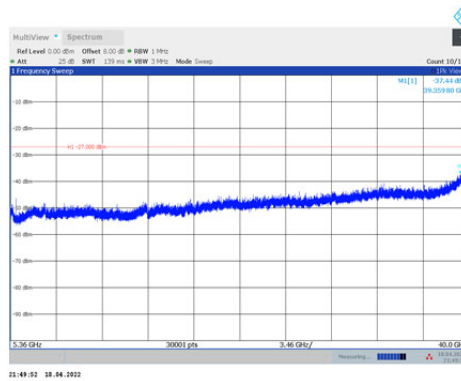
## 11AX40SISO\_Ant2\_5270\_5360~40000



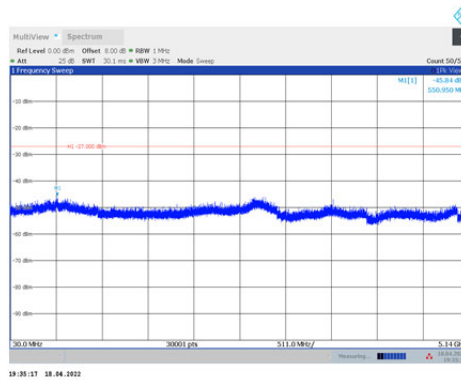
## 11AX40SISO\_Ant1\_5310\_30~5140



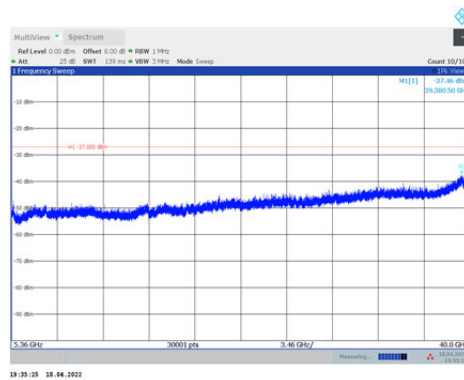
## 11AX40SISO\_Ant1\_5310\_5360~40000



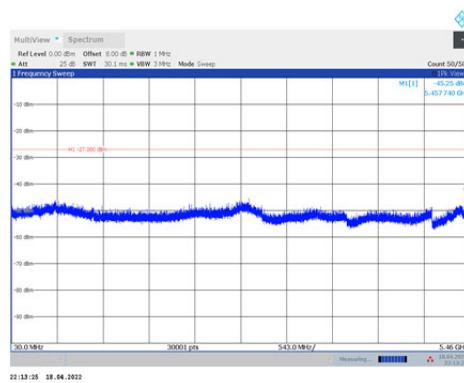
## 11AX40SISO\_Ant2\_5310\_30~5140



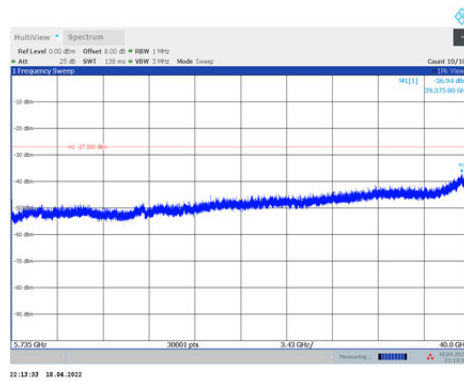
## 11AX40SISO\_Ant2\_5310\_5360~40000



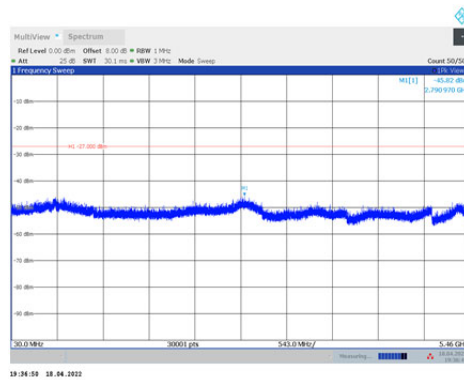
## 11AX40SISO\_Ant1\_5510\_30~5460



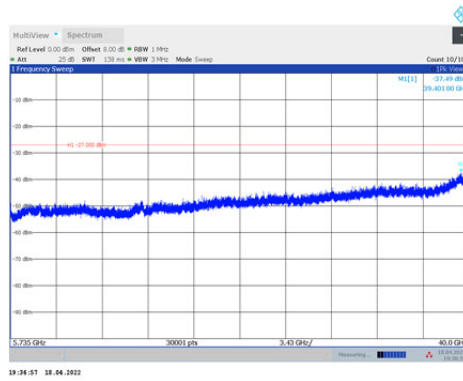
## 11AX40SISO\_Ant1\_5510\_5735~40000



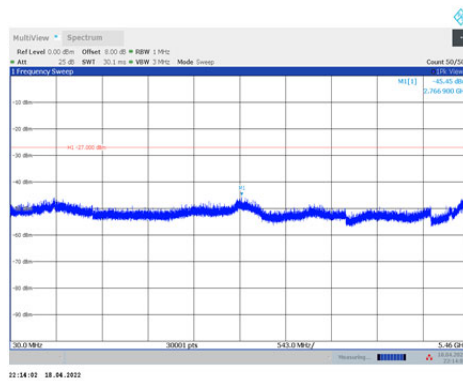
## 11AX40SISO\_Ant2\_5510\_30~5460



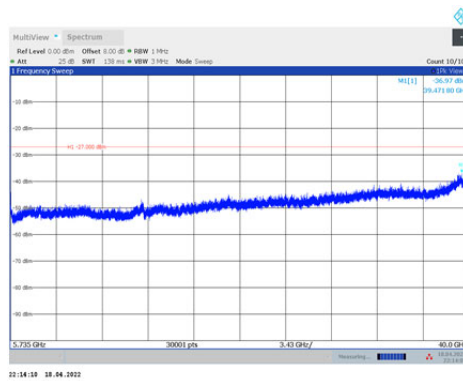
## 11AX40SISO\_Ant2\_5510\_5735~40000



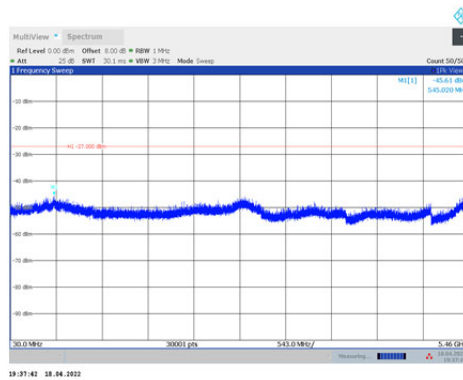
## 11AX40SISO\_Ant1\_5550\_30~5460



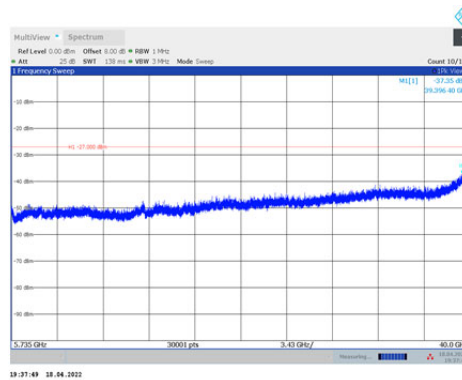
## 11AX40SISO\_Ant1\_5550\_5735~40000



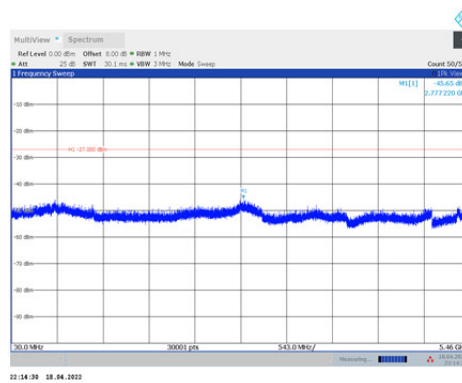
## 11AX40SISO\_Ant2\_5550\_30~5460



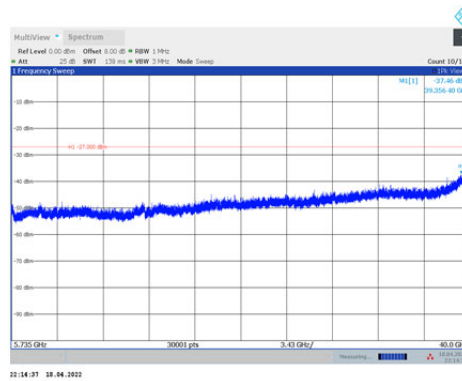
## 11AX40SISO\_Ant2\_5550\_5735~40000



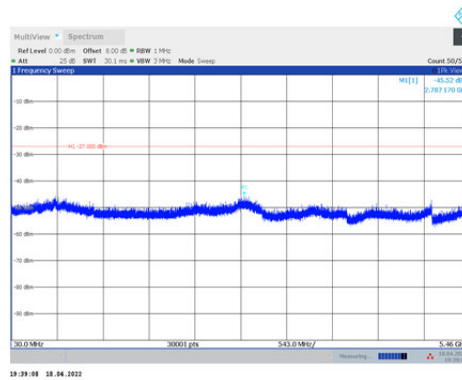
## 11AX40SISO\_Ant1\_5630\_30~5460



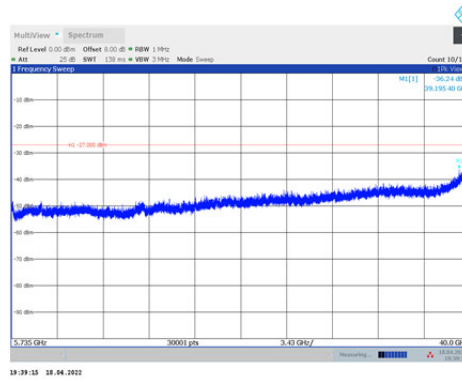
## 11AX40SISO\_Ant1\_5630\_5735~40000



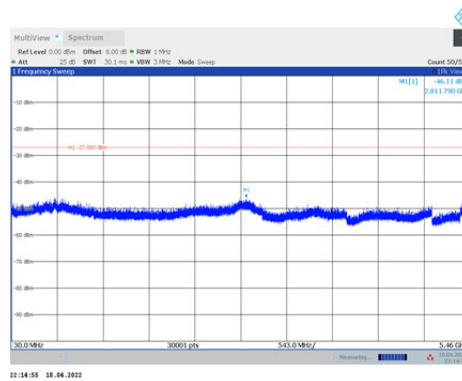
## 11AX40SISO\_Ant2\_5630\_30~5460



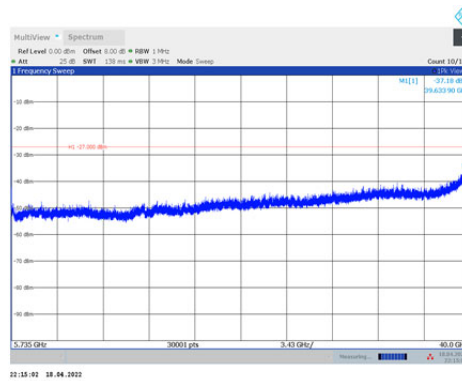
## 11AX40SISO\_Ant2\_5630\_5735~40000



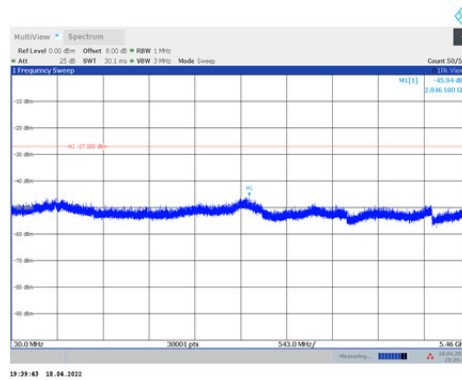
## 11AX40SISO\_Ant1\_5670\_30~5460



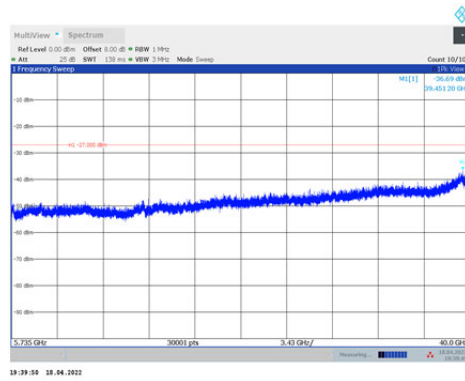
## 11AX40SISO\_Ant1\_5670\_5735~40000



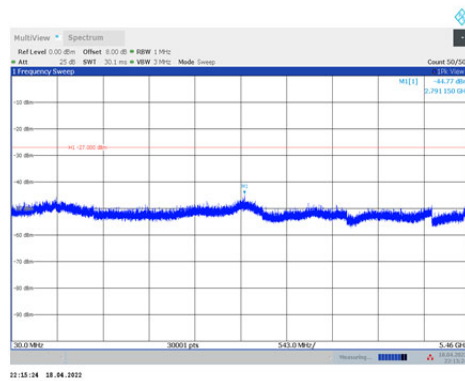
## 11AX40SISO\_Ant2\_5670\_30~5460



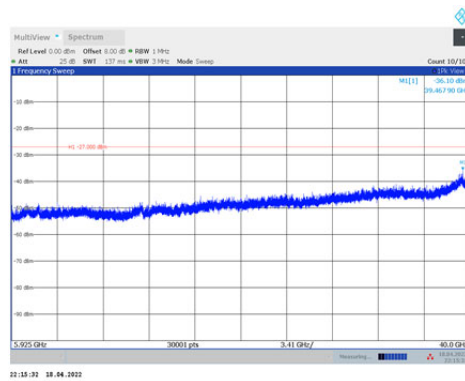
## 11AX40SISO\_Ant2\_5670\_5735~40000



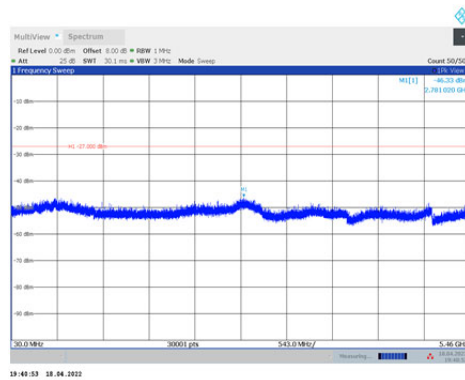
## 11AX40SISO\_Ant1\_5710\_30~5460



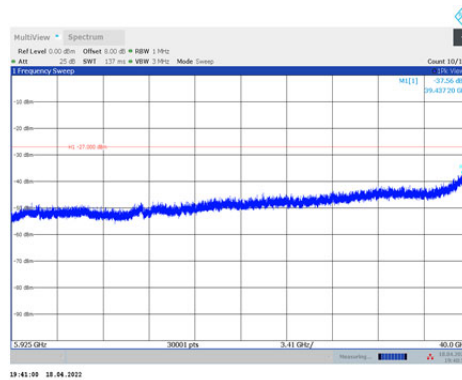
## 11AX40SISO\_Ant1\_5710\_5925~40000



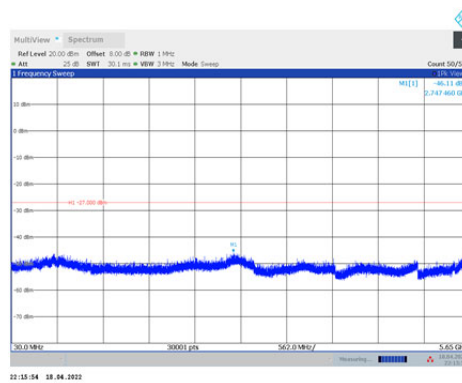
## 11AX40SISO\_Ant2\_5710\_30~5460



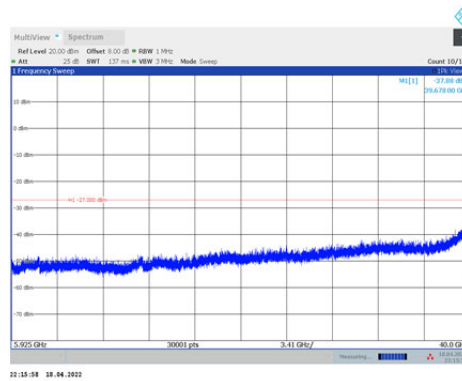
## 11AX40SISO\_Ant2\_5710\_5925~40000



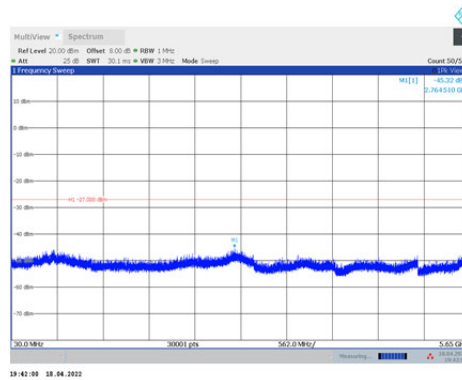
## 11AX40SISO\_Ant1\_5755\_30~5650



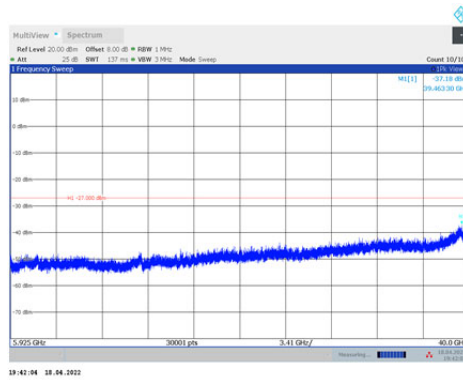
## 11AX40SISO\_Ant1\_5755\_5925~40000



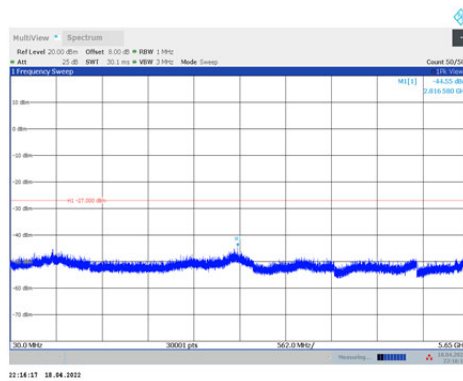
## 11AX40SISO\_Ant2\_5755\_30~5650



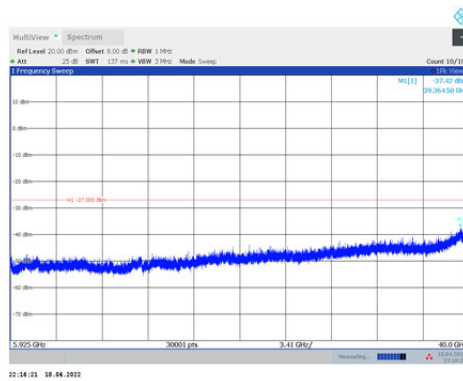
## 11AX40SISO\_Ant2\_5755\_5925~40000



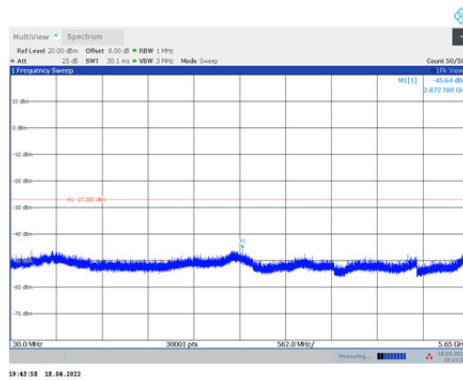
## 11AX40SISO\_Ant1\_5795\_30~5650



## 11AX40SISO\_Ant1\_5795\_5925~40000

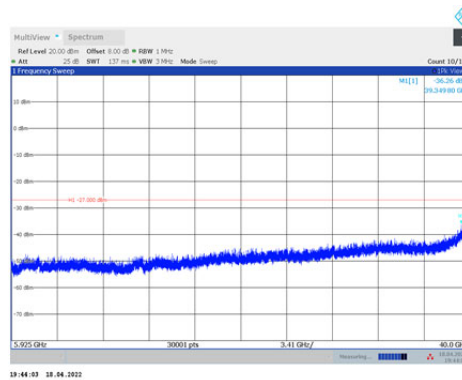


## 11AX40SISO\_Ant2\_5795\_30~5650

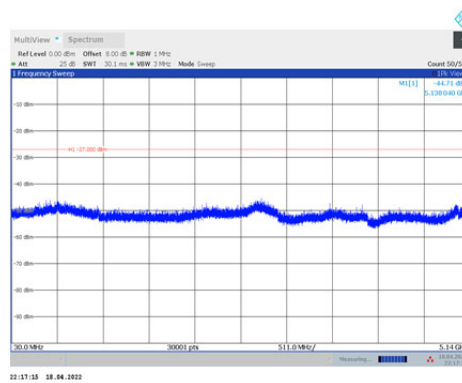




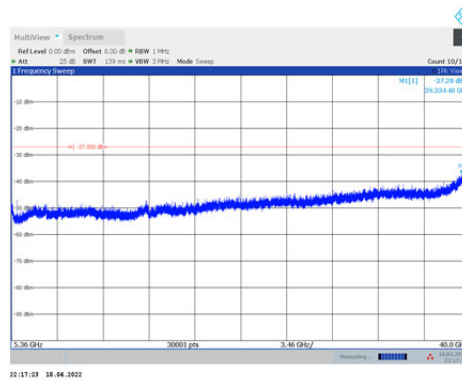
## 11AX40SISO\_Ant2\_5795\_5925~40000



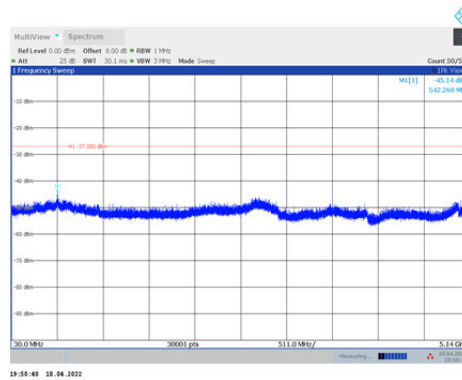
## 11AX80SISO\_Ant1\_5210\_30~5140



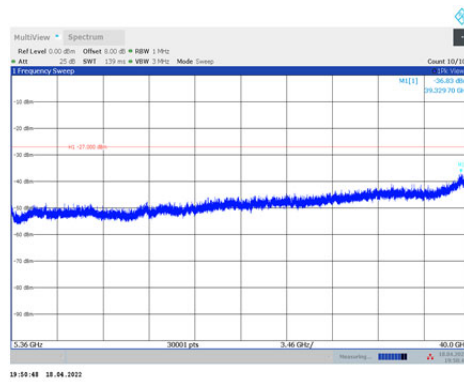
## 11AX80SISO\_Ant1\_5210\_5360~40000



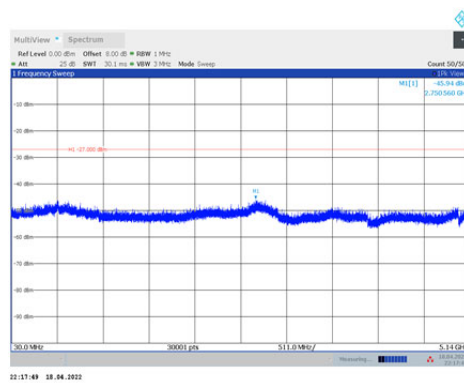
## 11AX80SISO\_Ant2\_5210\_30~5140



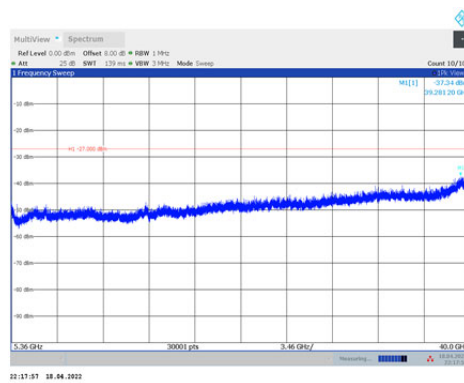
## 11AX80SISO\_Ant2\_5210\_5360~40000



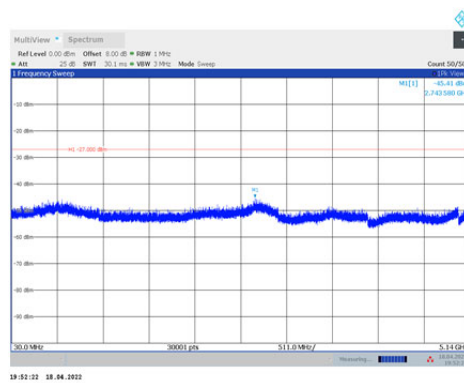
## 11AX80SISO\_Ant1\_5290\_30~5140



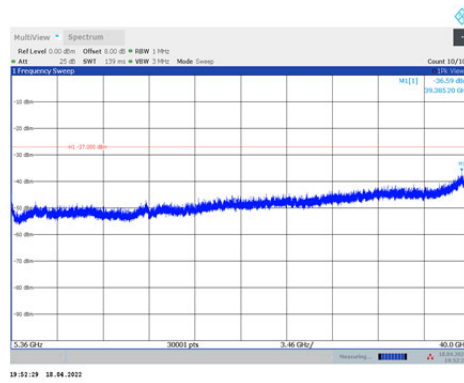
## 11AX80SISO\_Ant1\_5290\_5360~40000



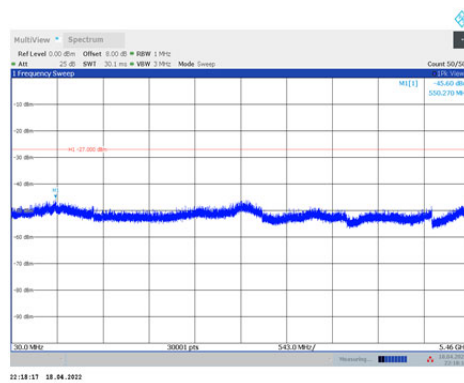
## 11AX80SISO\_Ant2\_5290\_30~5140



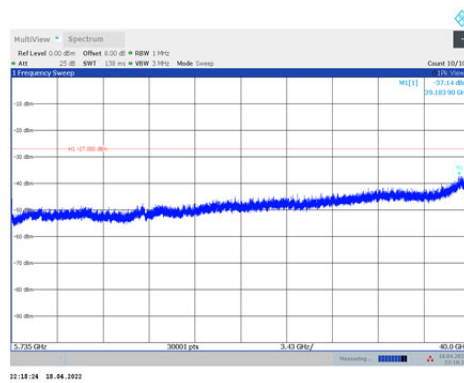
## 11AX80SISO\_Ant2\_5290\_5360~40000



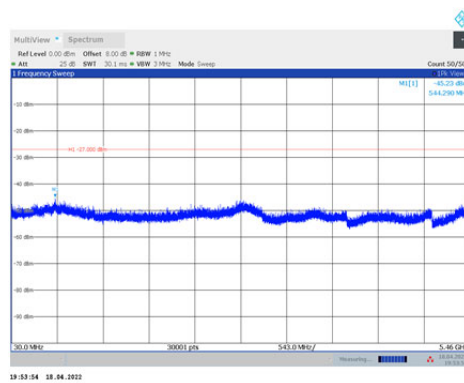
## 11AX80SISO\_Ant1\_5530\_30~5460



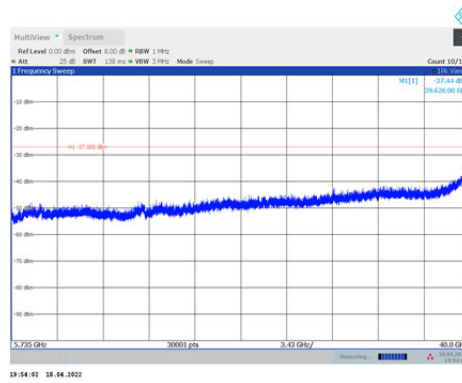
## 11AX80SISO\_Ant1\_5530\_5735~40000



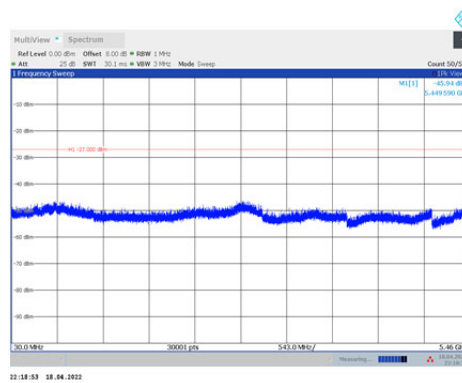
## 11AX80SISO\_Ant2\_5530\_30~5460



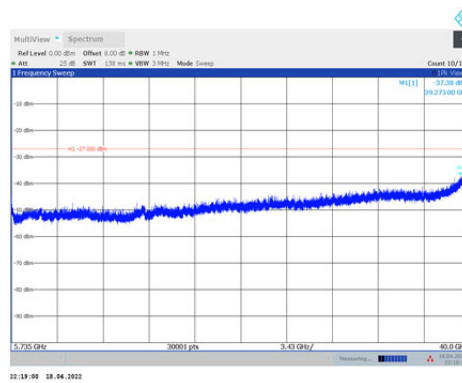
## 11AX80SISO\_Ant2\_5530\_5735~40000



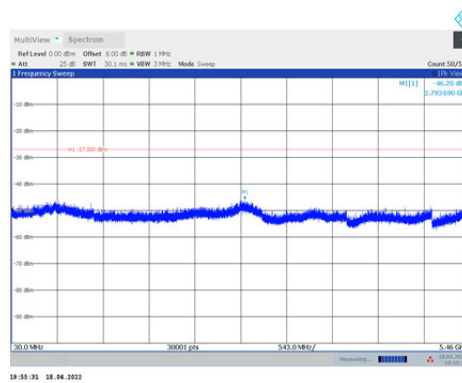
## 11AX80SISO\_Ant1\_5610\_30~5460



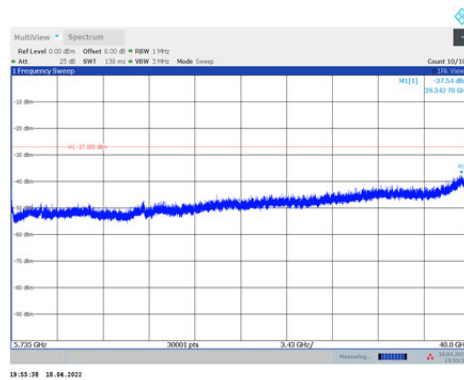
## 11AX80SISO\_Ant1\_5610\_5735~40000



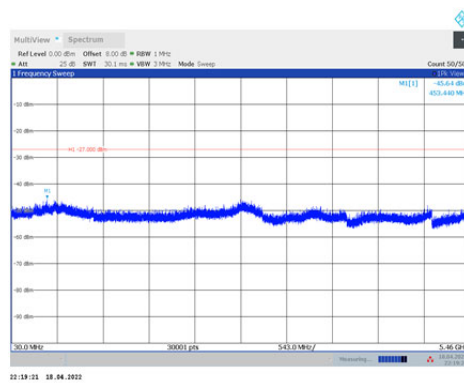
## 11AX80SISO\_Ant2\_5610\_30~5460



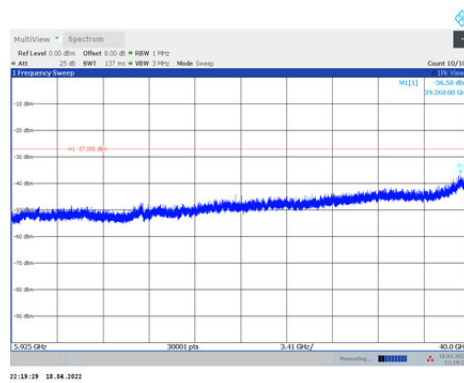
## 11AX80SISO\_Ant2\_5610\_5735~40000



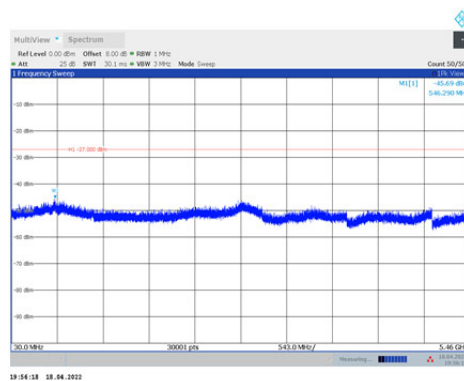
## 11AX80SISO\_Ant1\_5690\_30~5460



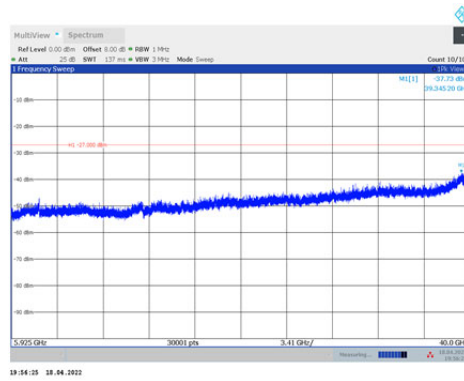
## 11AX80SISO\_Ant1\_5690\_5925~40000



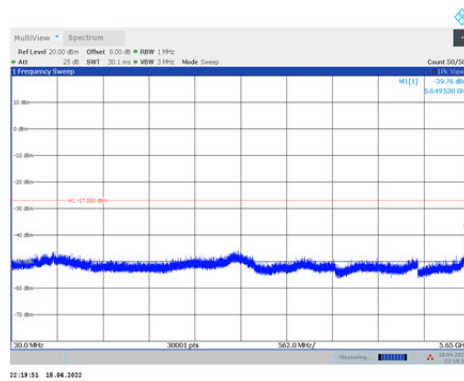
## 11AX80SISO\_Ant2\_5690\_30~5460



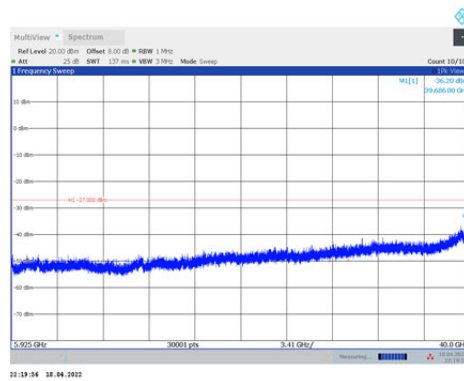
## 11AX80SISO\_Ant2\_5690\_5925~40000



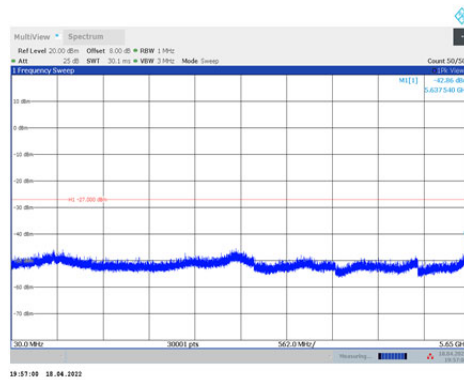
## 11AX80SISO\_Ant1\_5775\_30~5650



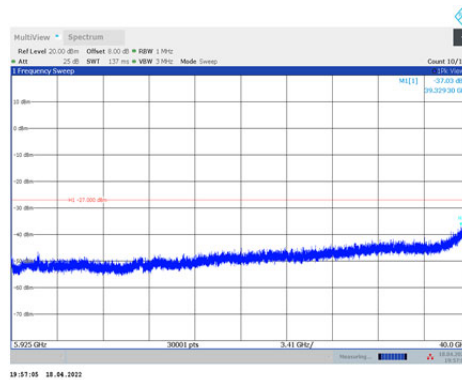
## 11AX80SISO\_Ant1\_5775\_5925~40000



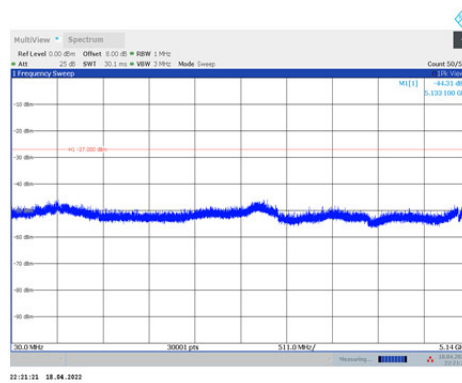
## 11AX80SISO\_Ant2\_5775\_30~5650



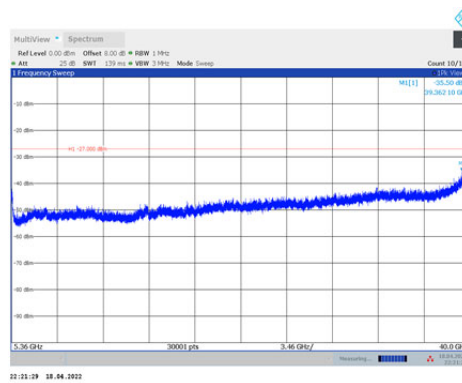
## 11AX80SISO\_Ant2\_5775\_5925~40000



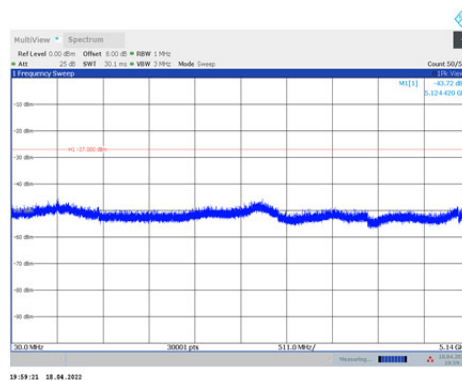
## 11AX160SISO\_Ant1\_5250\_30~5140



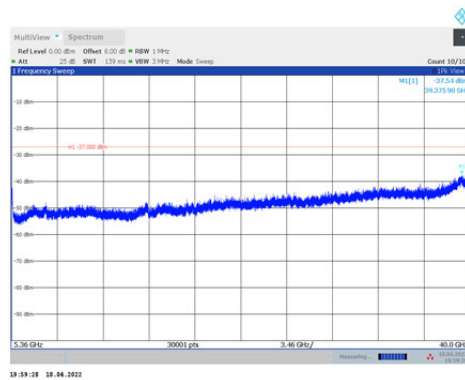
## 11AX160SISO\_Ant1\_5250\_5360~40000



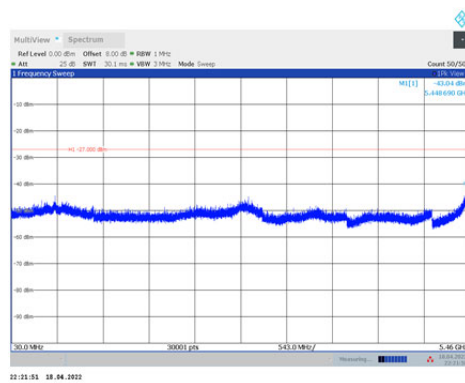
## 11AX160SISO\_Ant2\_5250\_30~5140



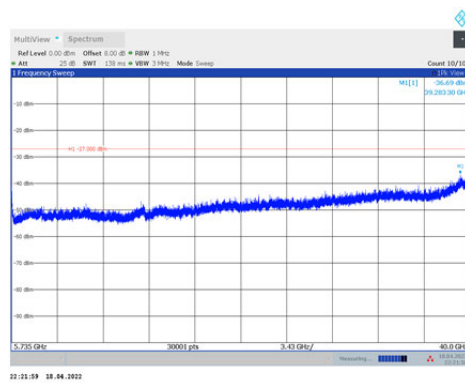
## 11AX160SISO\_Ant2\_5250\_5360~40000



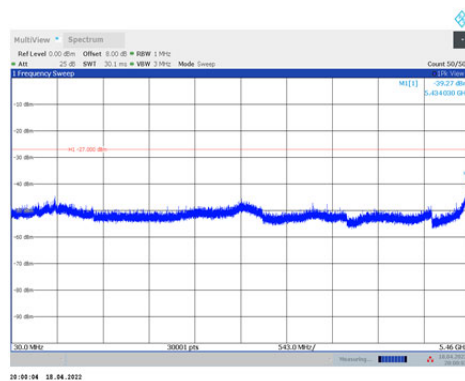
## 11AX160SISO\_Ant1\_5570\_30~5460



## 11AX160SISO\_Ant1\_5570\_5735~40000

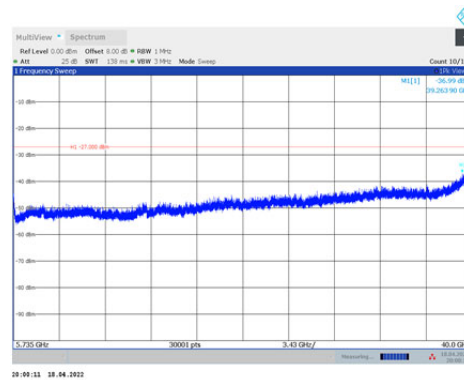


## 11AX160SISO\_Ant2\_5570\_30~5460

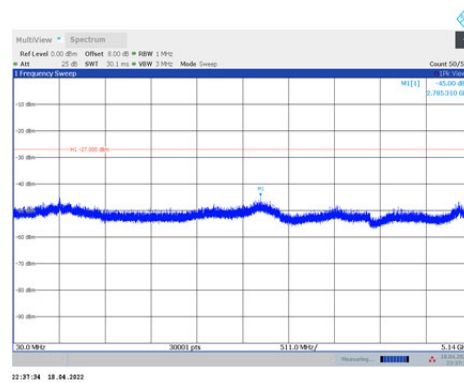




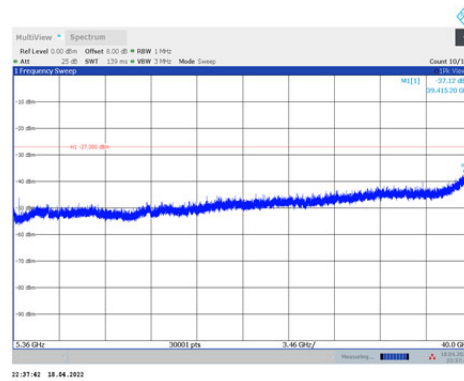
## 11AX160SISO\_Ant2\_5570\_5735~40000



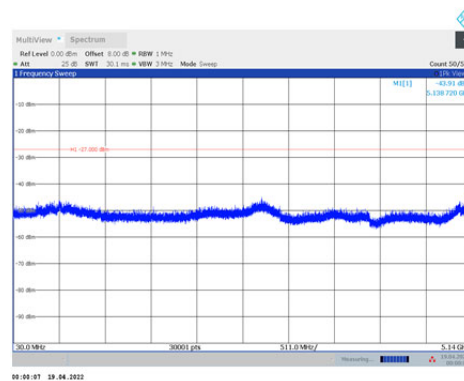
## 11A-CDD\_Ant1\_5180\_30~5140



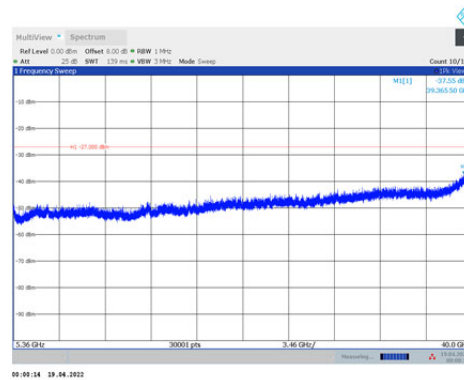
## 11A-CDD\_Ant1\_5180\_5360~40000



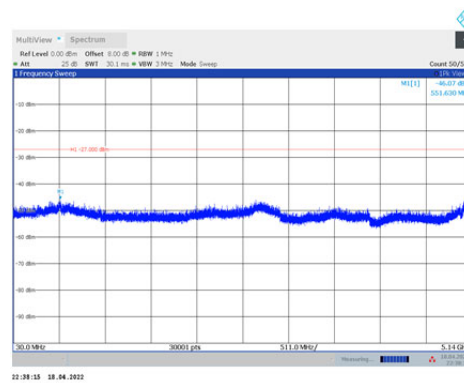
## 11A-CDD\_Ant2\_5180\_30~5140



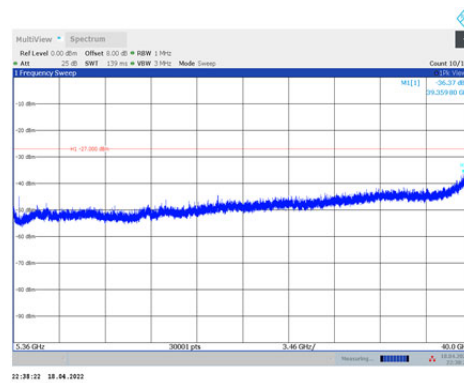
## 11A-CDD\_Ant2\_5180\_5360~40000



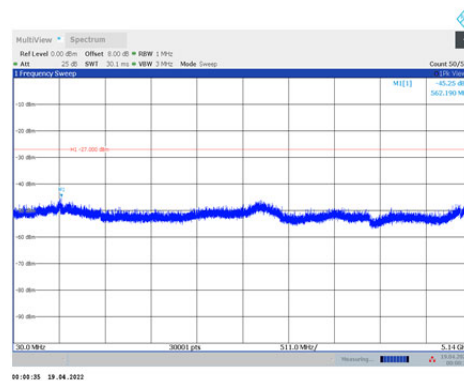
## 11A-CDD\_Ant1\_5200\_30~5140



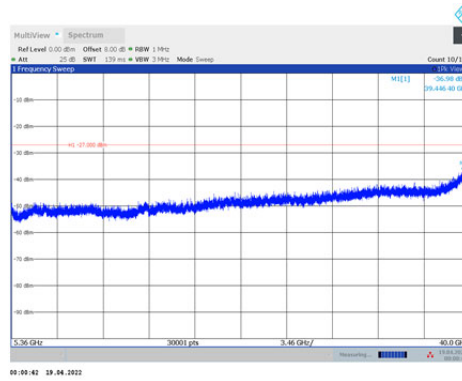
## 11A-CDD\_Ant1\_5200\_5360~40000



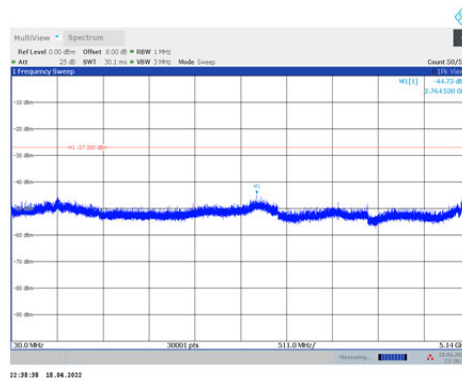
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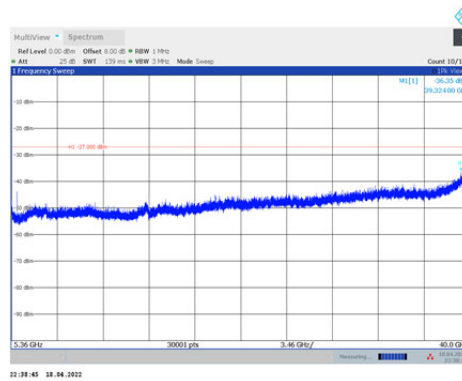
## 11A-CDD\_Ant2\_5200\_5360~40000



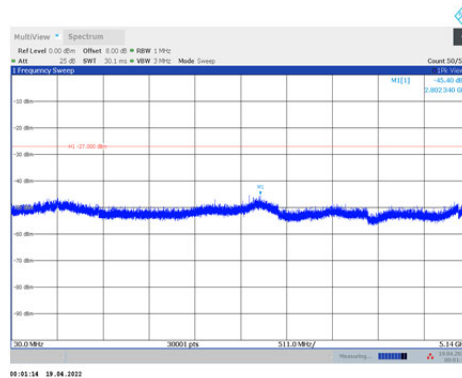
## 11A-CDD\_Ant1\_5220\_30~5140



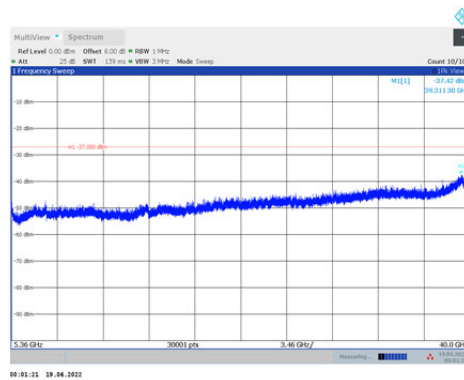
## 11A-CDD\_Ant1\_5220\_5360~40000



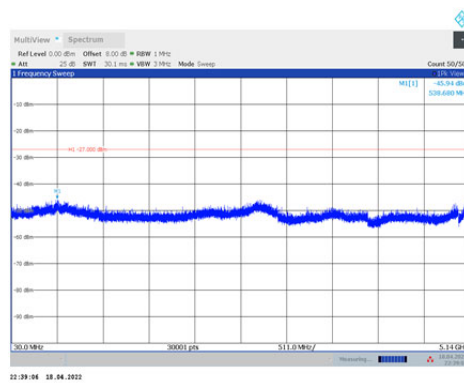
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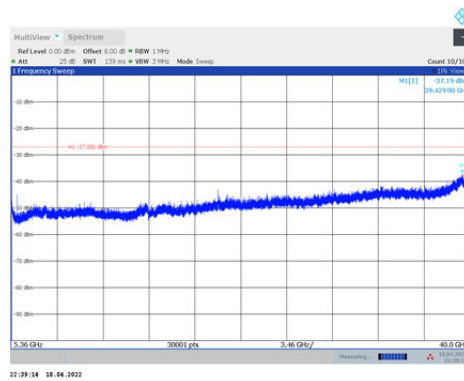
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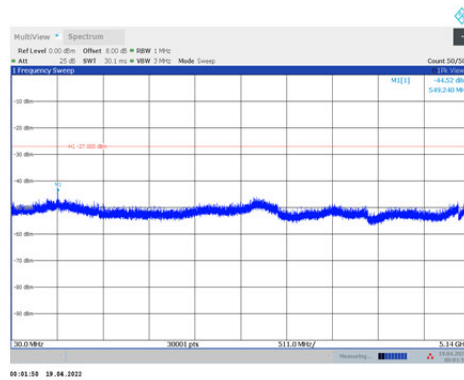
## 11A-CDD\_Ant1\_5240\_30~5140



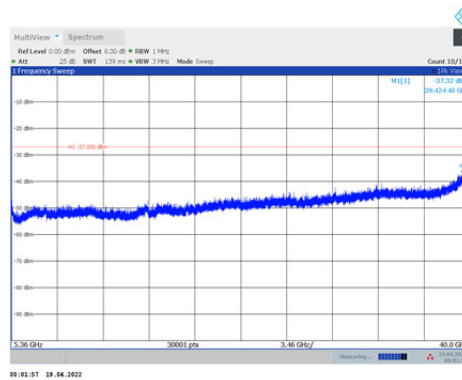
## 11A-CDD\_Ant1\_5240\_5360~40000



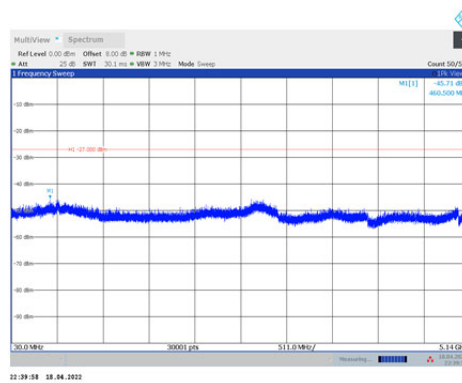
## 11A-CDD\_Ant2\_5240\_30~5140



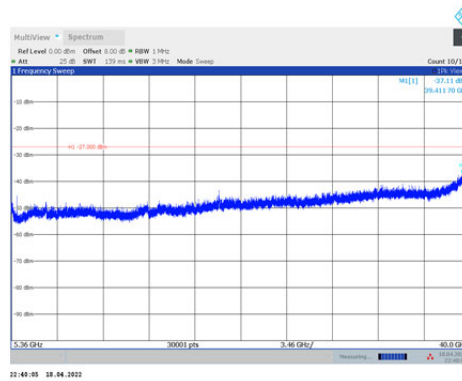
## 11A-CDD\_Ant2\_5240\_5360~40000



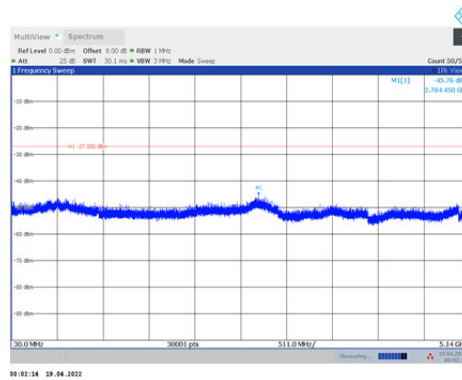
## 11A-CDD\_Ant1\_5260\_30~5140



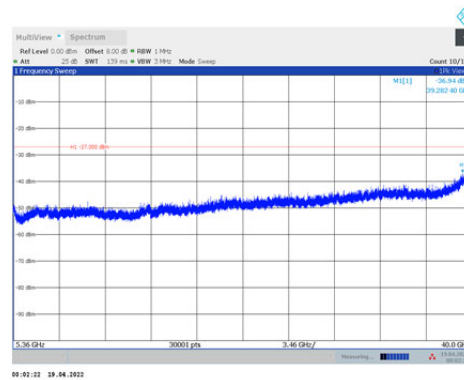
## 11A-CDD\_Ant1\_5260\_5360~40000



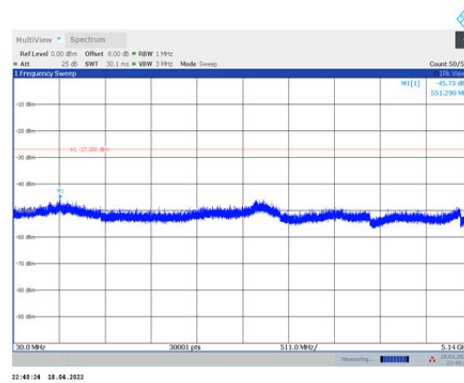
## 11A-CDD\_Ant2\_5260\_30~5140



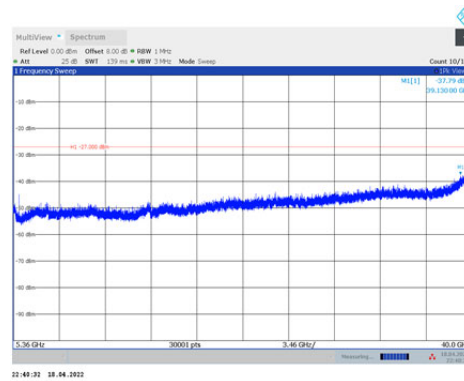
## 11A-CDD\_Ant2\_5260\_5360~40000



## 11A-CDD\_Ant1\_5300\_30~5140



## 11A-CDD\_Ant1\_5300\_5360~40000



## 11A-CDD\_Ant2\_5300\_30~5140

