

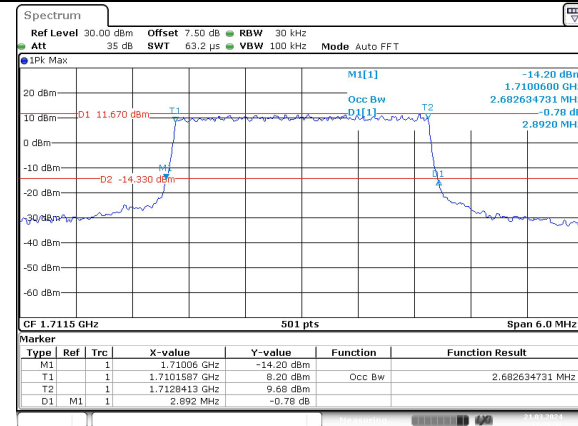
Occupied Bandwidth

Channel

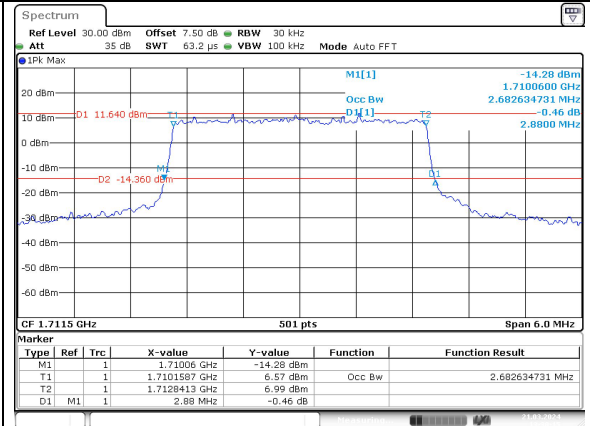
3MHz Bandwidth QPSK

3MHz Bandwidth 16QAM

Lowest

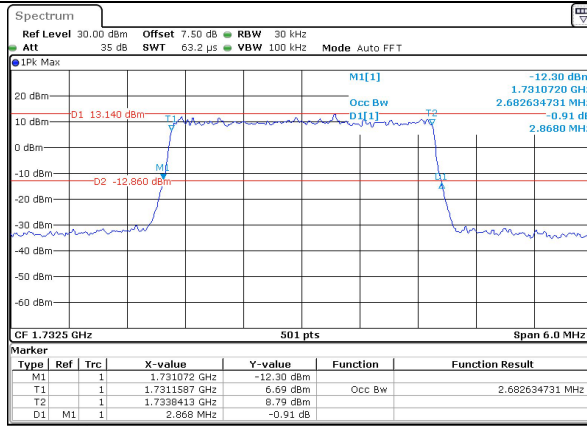


ProjectNo.:DG1240227-09527E Tester:Loge Long
Date: 21.MAR.2024 13:29:53

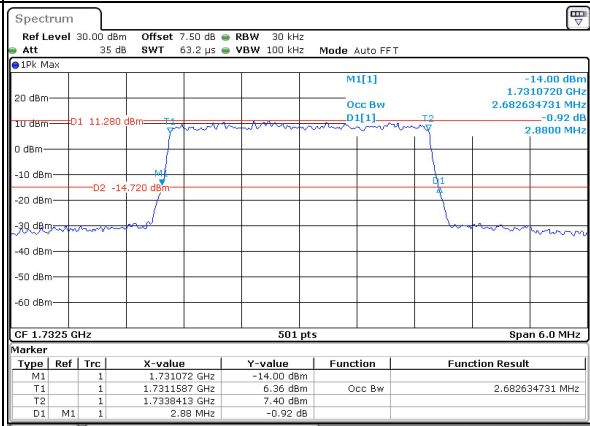


ProjectNo.:DG1240227-09527E Tester:Loge Long
Date: 21.MAR.2024 13:30:15

Middle

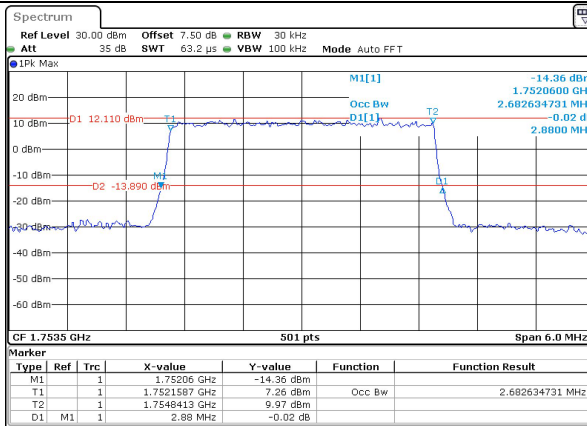


ProjectNo.:DG1240227-09527E Tester:Loge Long
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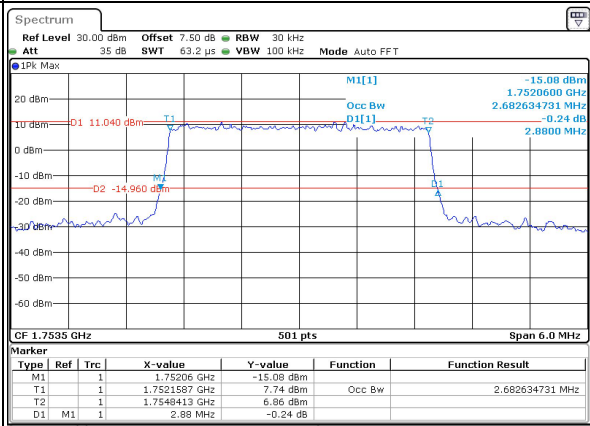


ProjectNo.:DG1240227-09527E Tester:Loge Long
Date: 21.MAR.2024 13:30:46

Highest



ProjectNo.:DG1240227-09527E Tester:Loge Long
Date: 21.MAR.2024 13:31:00



ProjectNo.:DG1240227-09527E Tester:Loge Long
Date: 21.MAR.2024 13:31:16

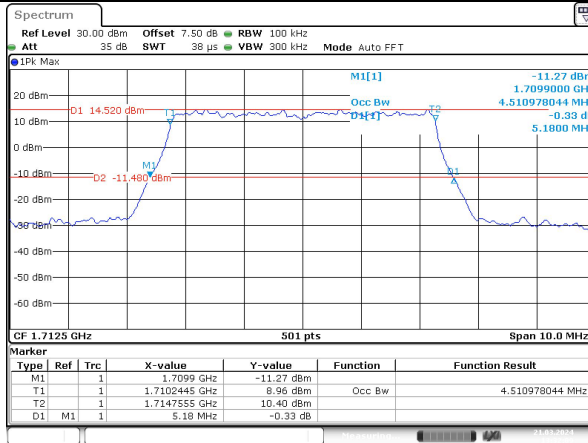
Occupied Bandwidth

Channel

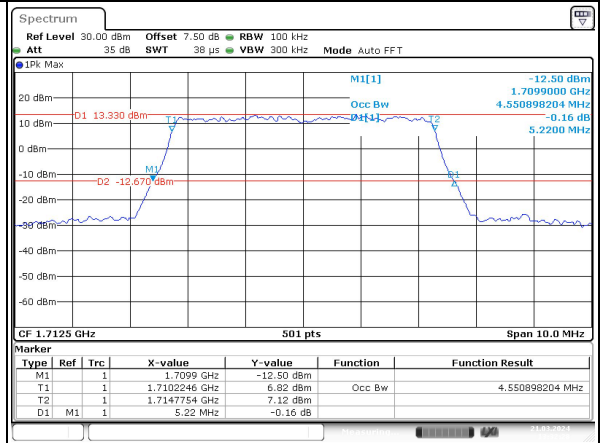
5MHz Bandwidth QPSK

5MHz Bandwidth 16QAM

Lowest

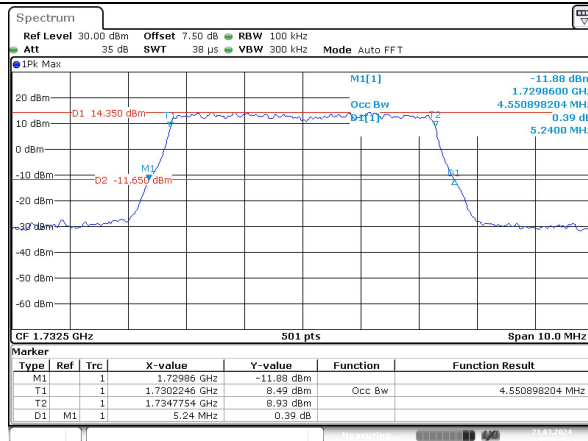


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Date: 21.MAR.2024 13:32:09

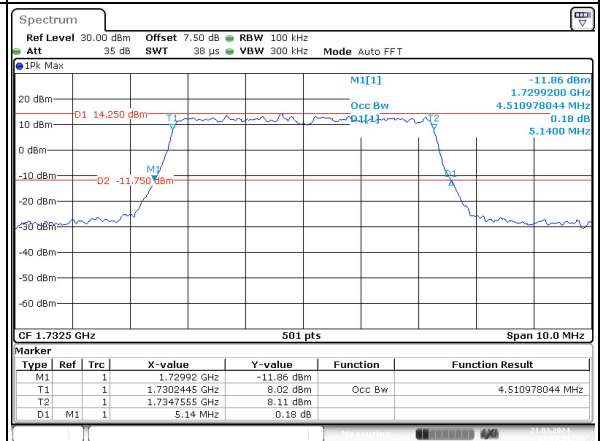


ProjectNo.:DG1240227-09527E Tester:Loge Long
Date: 21.MAR.2024 13:32:29

Middle

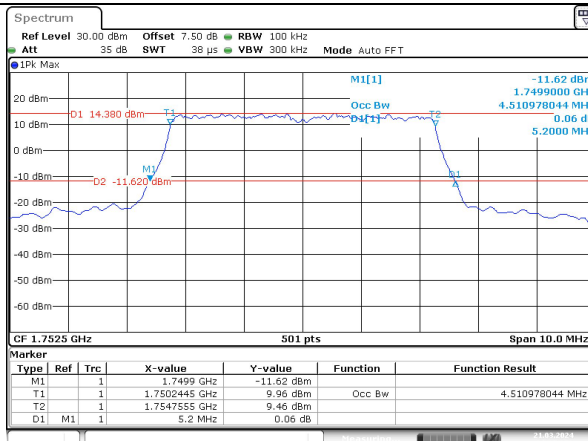


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Date: 21.MAR.2024 13:32:53

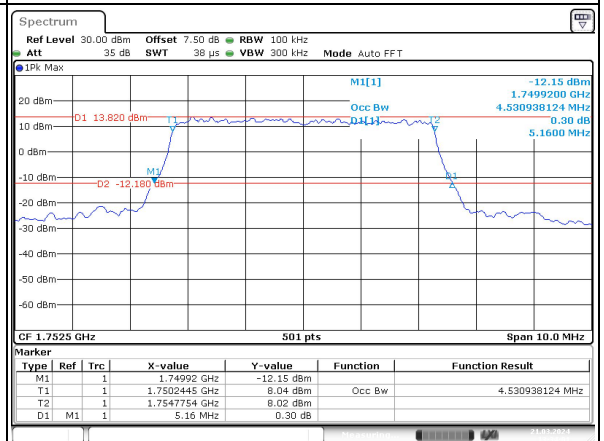


ProjectNo.:DG1240227-09527E Tester:Loge Long
Date: 21.MAR.2024 13:33:15

Highest



ProjectNo.:DG1240227-09527E Tester:Loge Long
Date: 21.MAR.2024 13:33:39



ProjectNo.:DG1240227-09527E Tester:Loge Long
Date: 21.MAR.2024 13:34:02

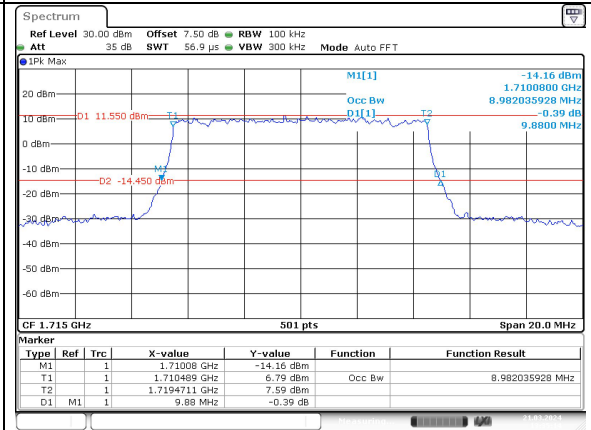
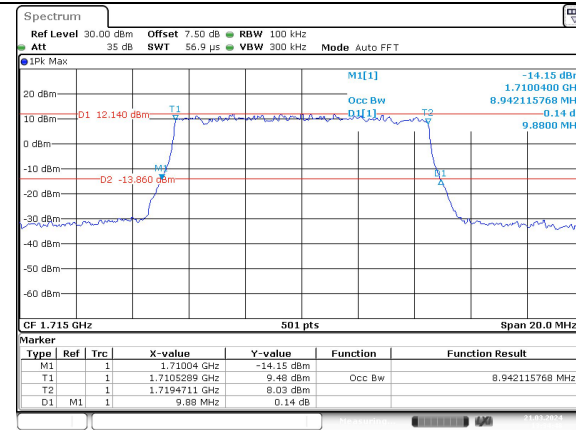
Occupied Bandwidth

Channel

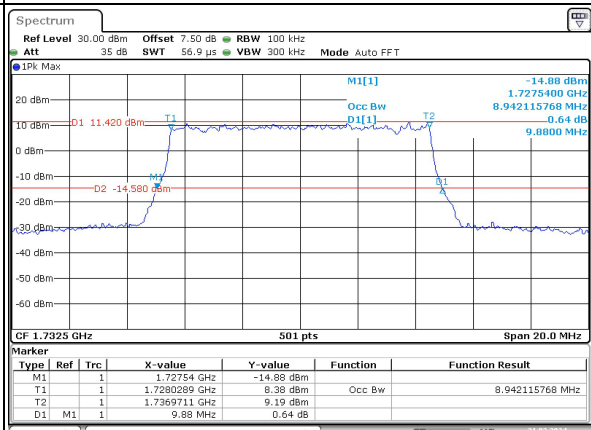
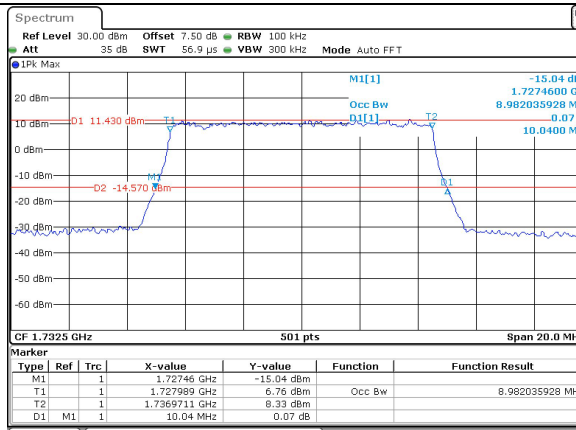
10MHz Bandwidth QPSK

10MHz Bandwidth 16QAM

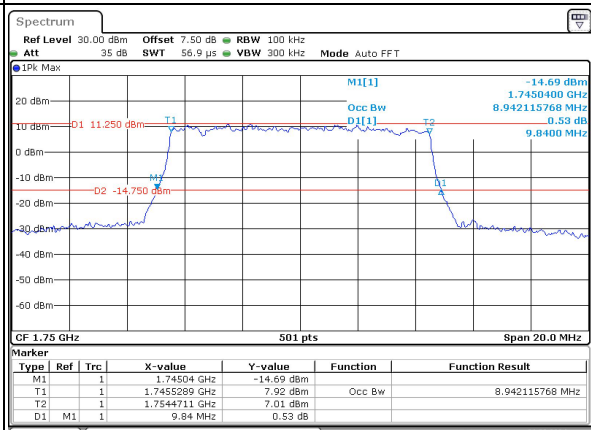
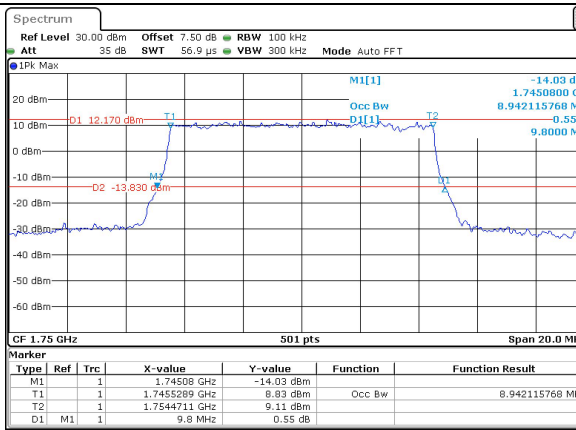
Lowest



Middle



Highest



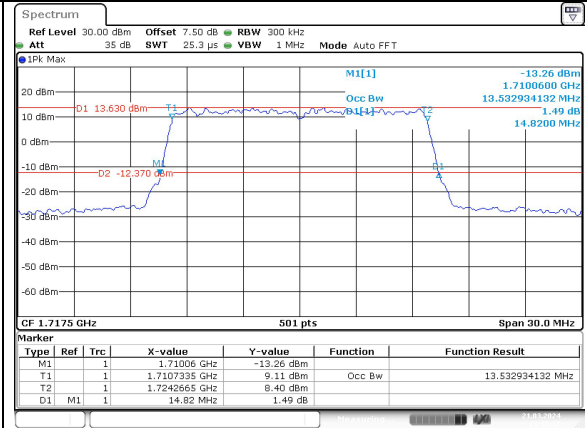
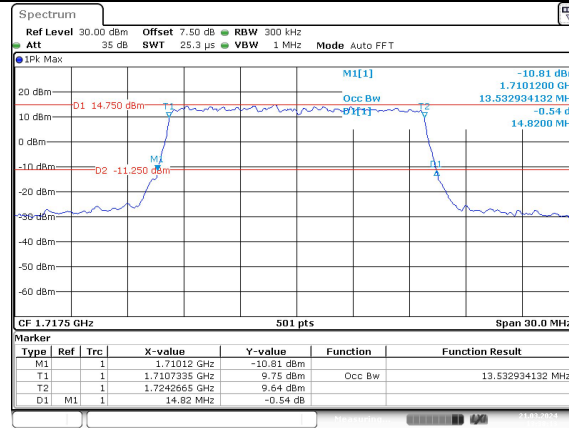
Occupied Bandwidth

Channel

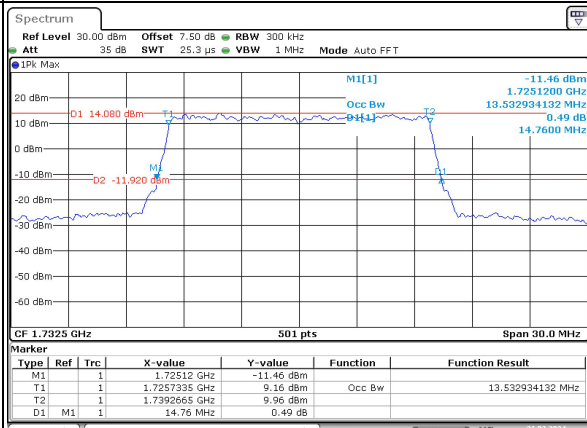
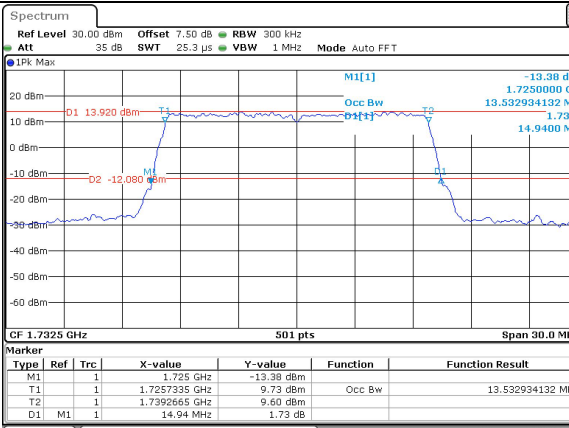
15MHz Bandwidth QPSK

15MHz Bandwidth 16QAM

Lowest



Middle



Highest

